

B1L000S, U1-ch0

calib_packv5_042523_0143.root, FC#3, port B3

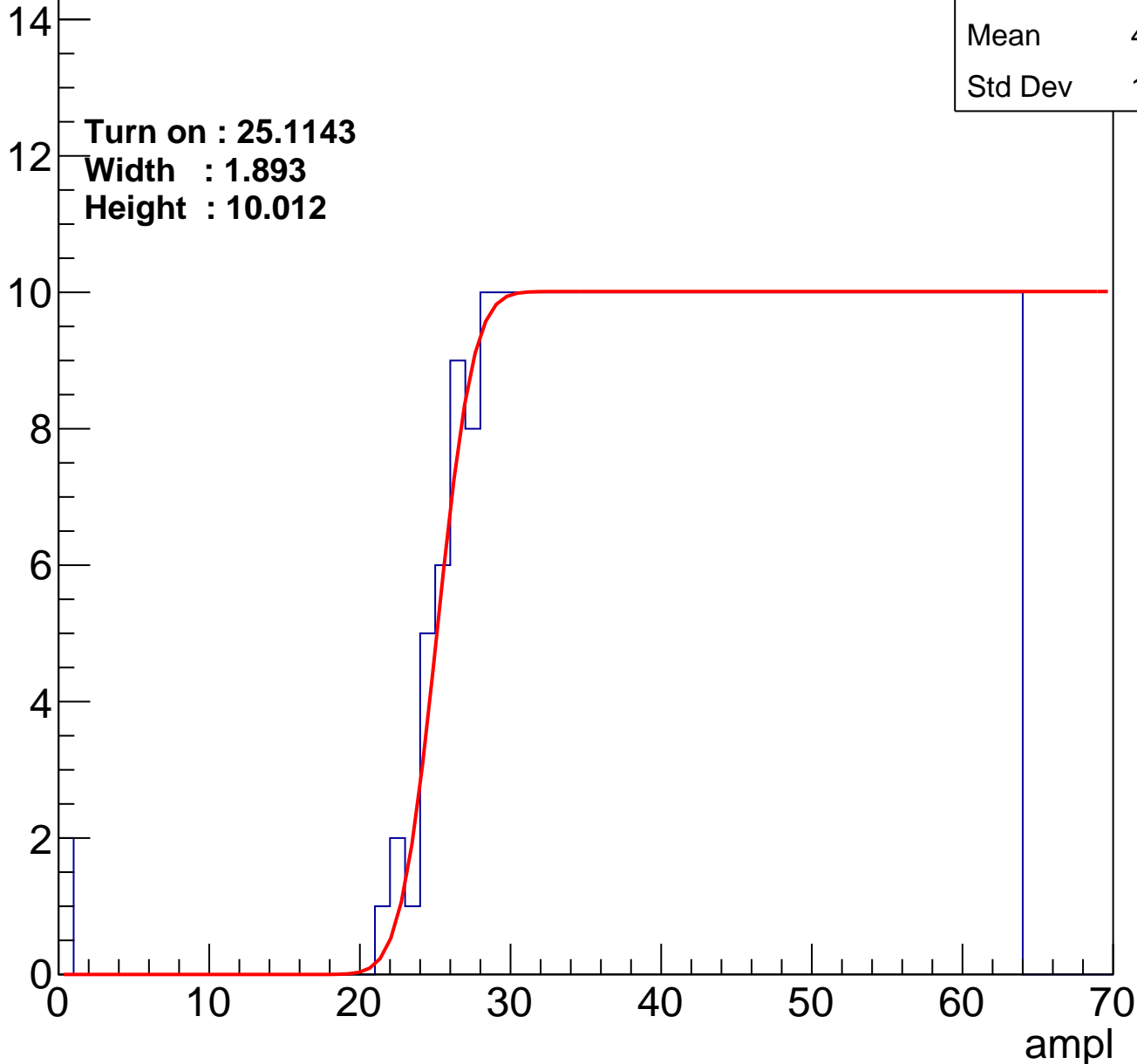
Entry

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.62 |
| Std Dev | 11.79 |

Turn on : 25.1143

Width : 1.893

Height : 10.012



B1L000S, U1-ch1

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.11 |
| Std Dev | 12.47 |

Turn on : 24.8717

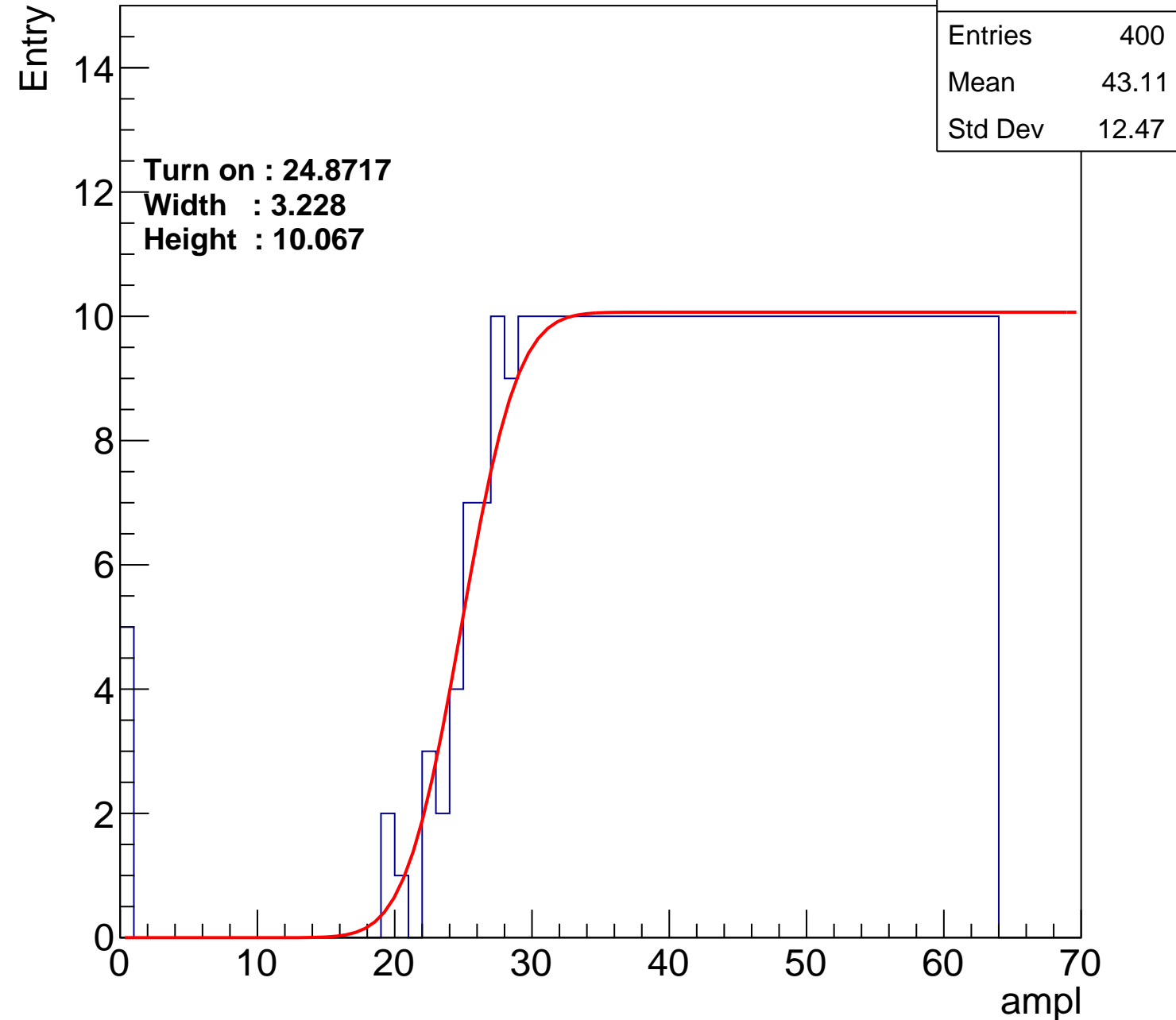
Width : 3.228

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch2

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 406 |
| Mean | 42.84 |
| Std Dev | 12.52 |

Turn on : 24.1604

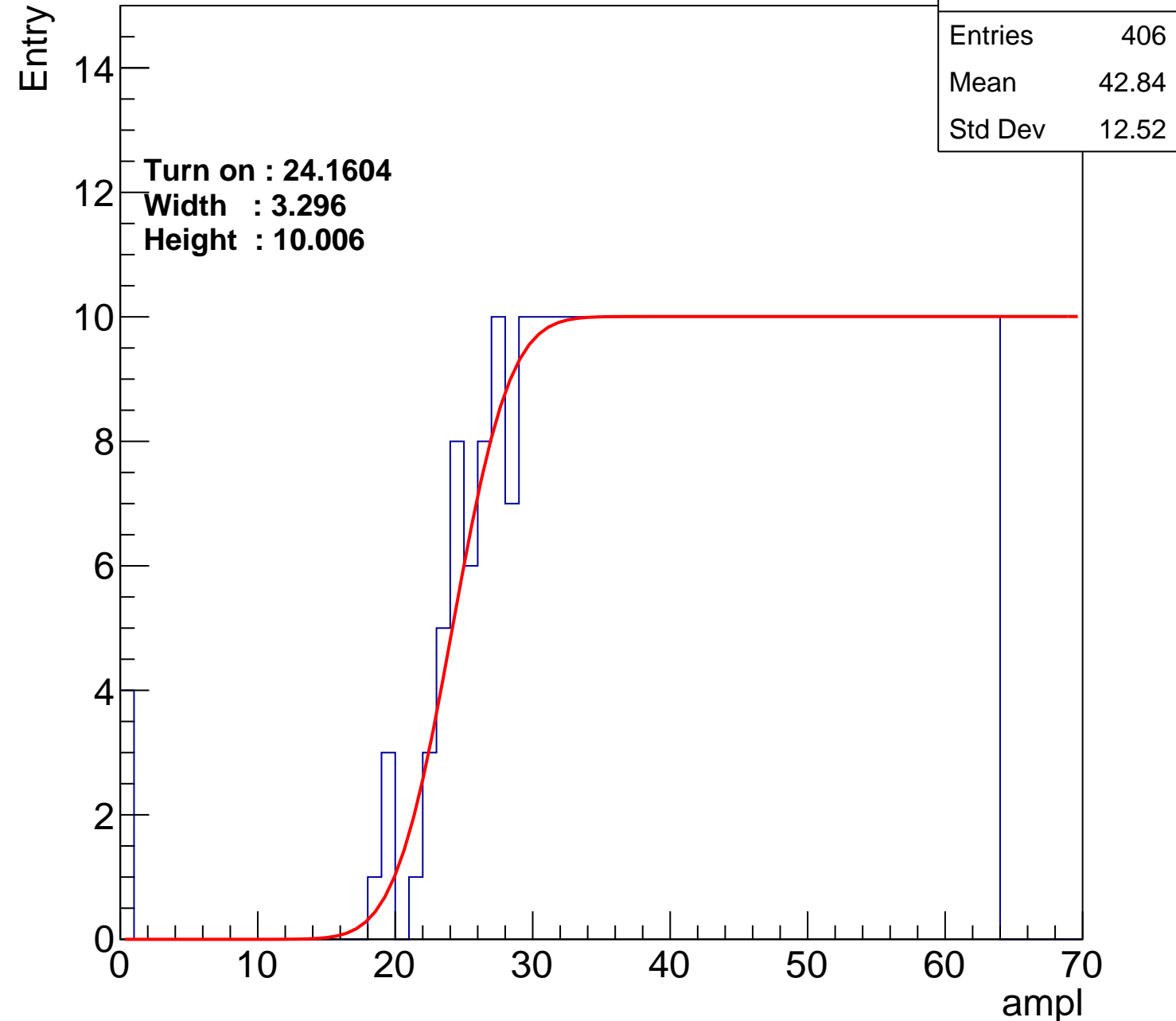
Width : 3.296

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch3

calib_packv5_042523_0143.root, FC#3, port B3

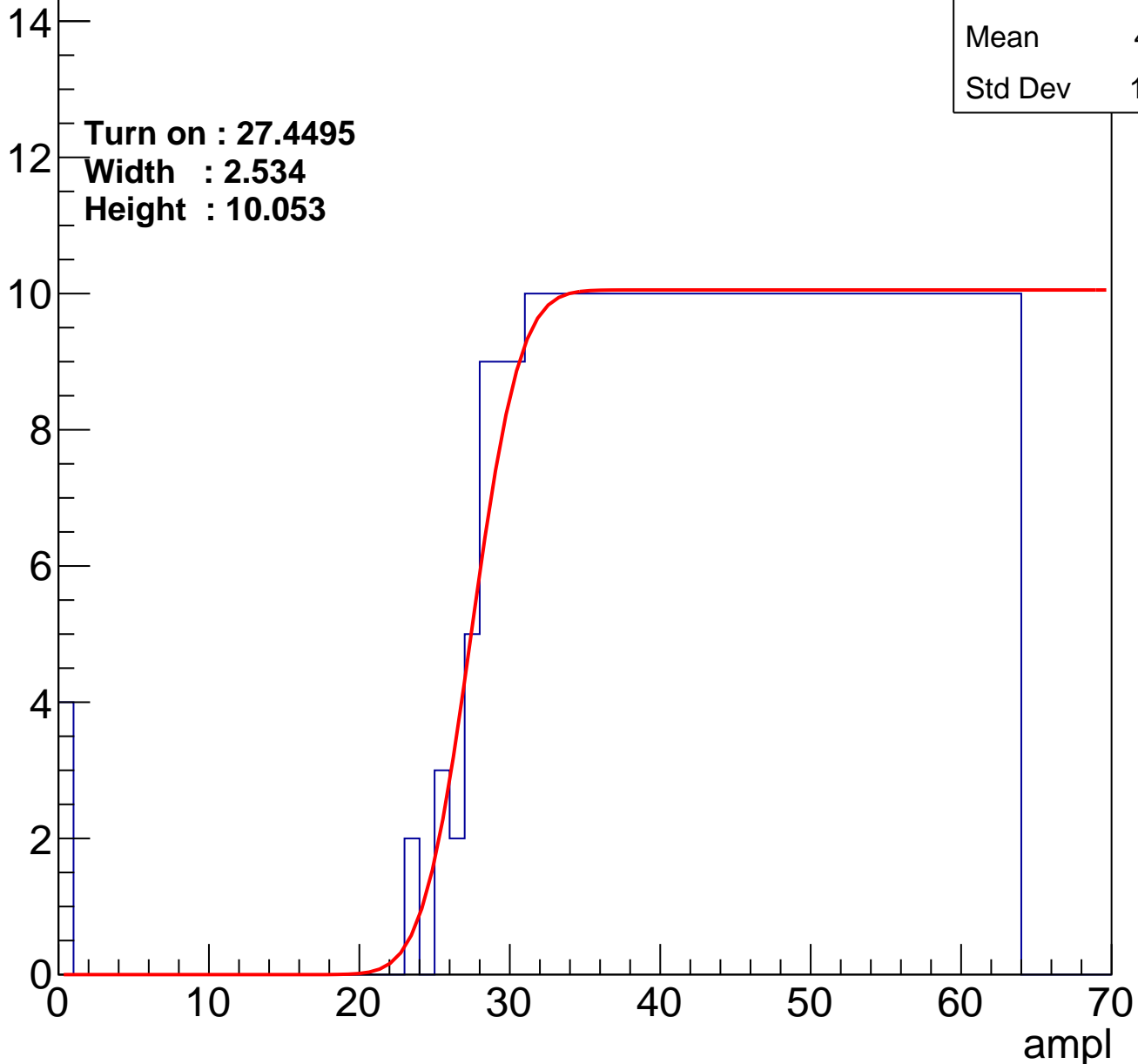
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.51 |
| Std Dev | 11.66 |

Turn on : 27.4495

Width : 2.534

Height : 10.053

Entry



B1L000S, U1-ch4

calib_packv5_042523_0143.root, FC#3, port B3

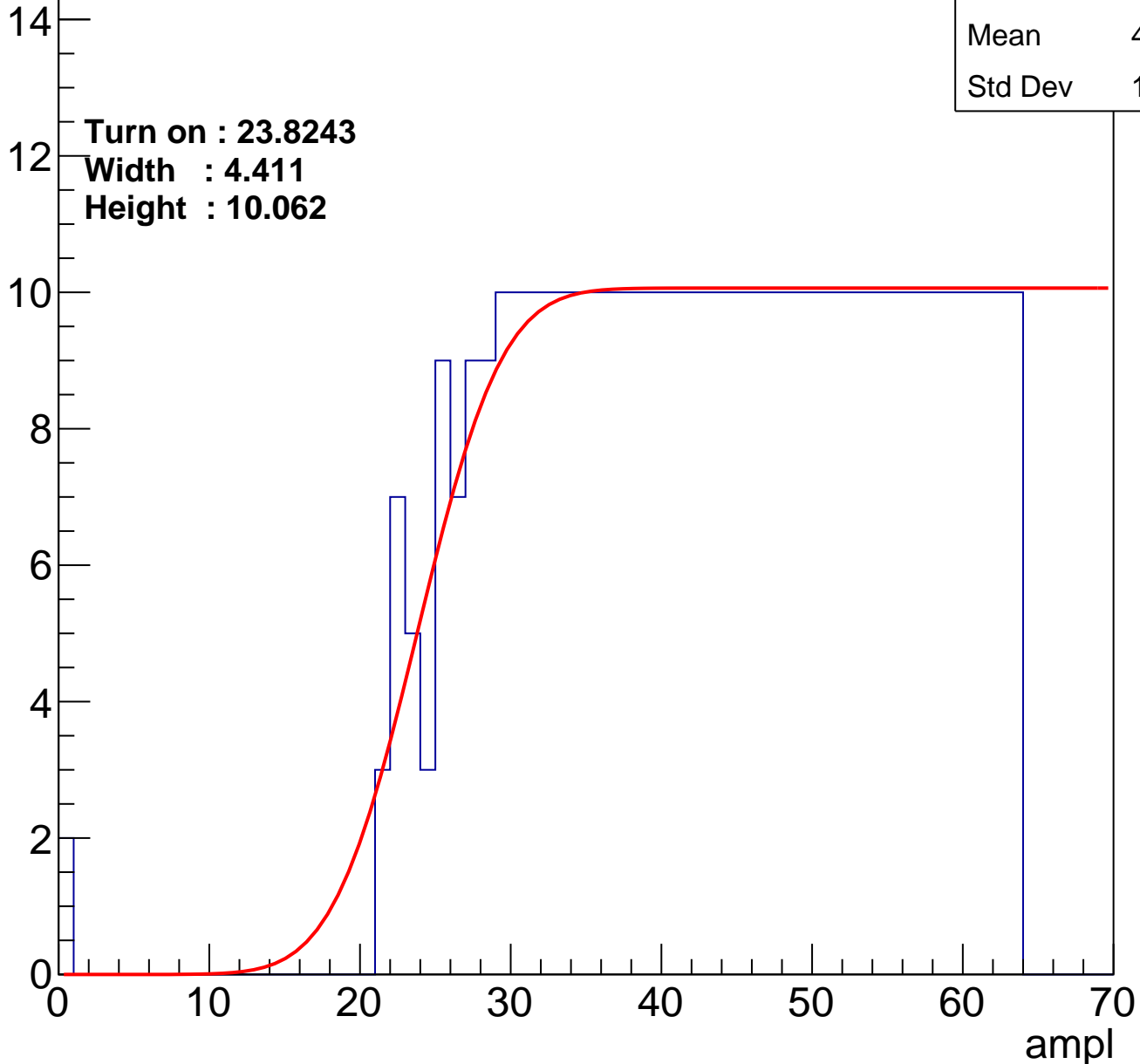
| | |
|---------|-------|
| Entries | 404 |
| Mean | 43.08 |
| Std Dev | 12.13 |

Turn on : 23.8243

Width : 4.411

Height : 10.062

Entry



B1L000S, U1-ch5

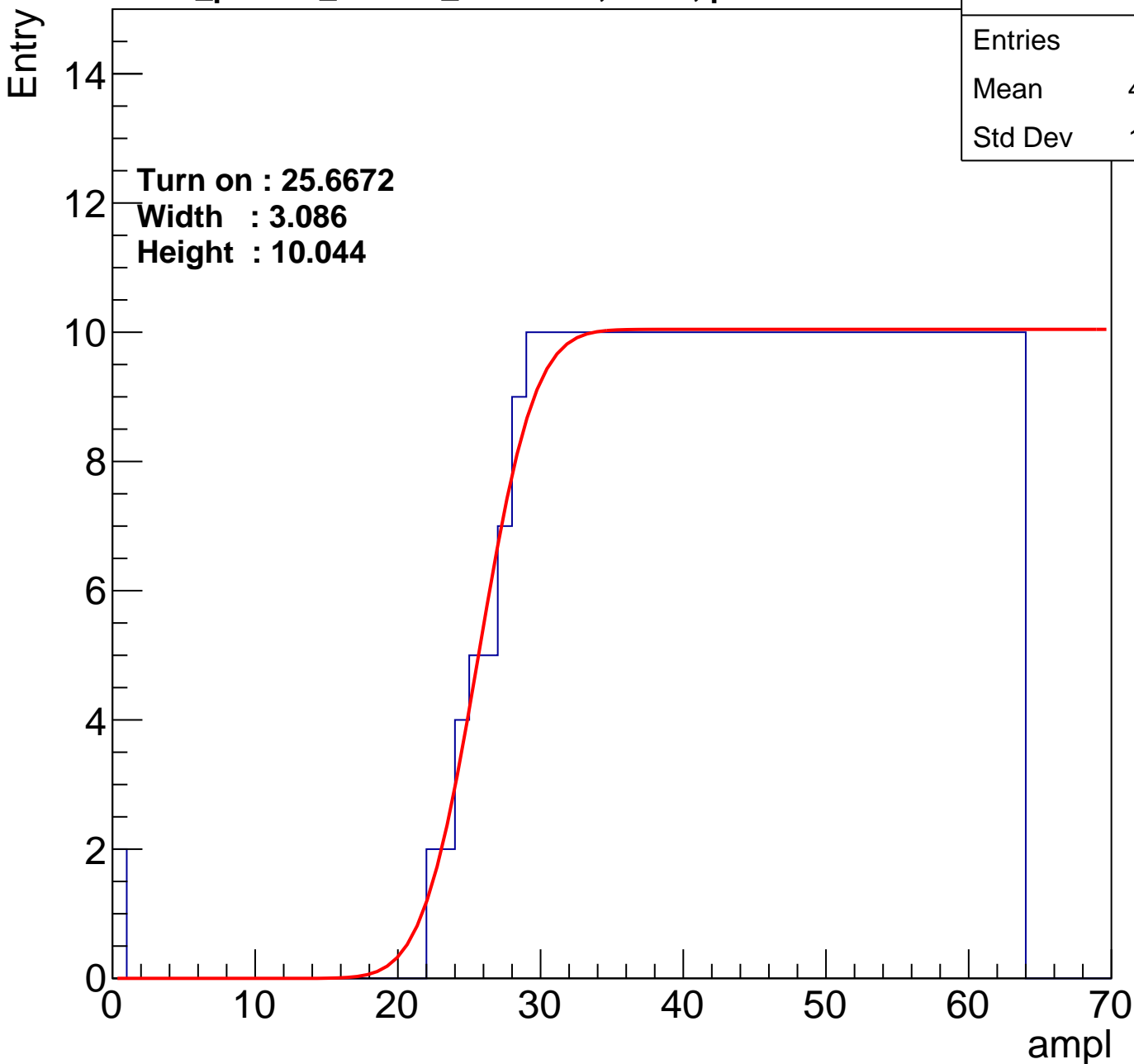
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.99 |
| Std Dev | 11.63 |

Turn on : 25.6672

Width : 3.086

Height : 10.044



B1L000S, U1-ch6

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.55 |
| Std Dev | 11.87 |

Turn on : 24.7716

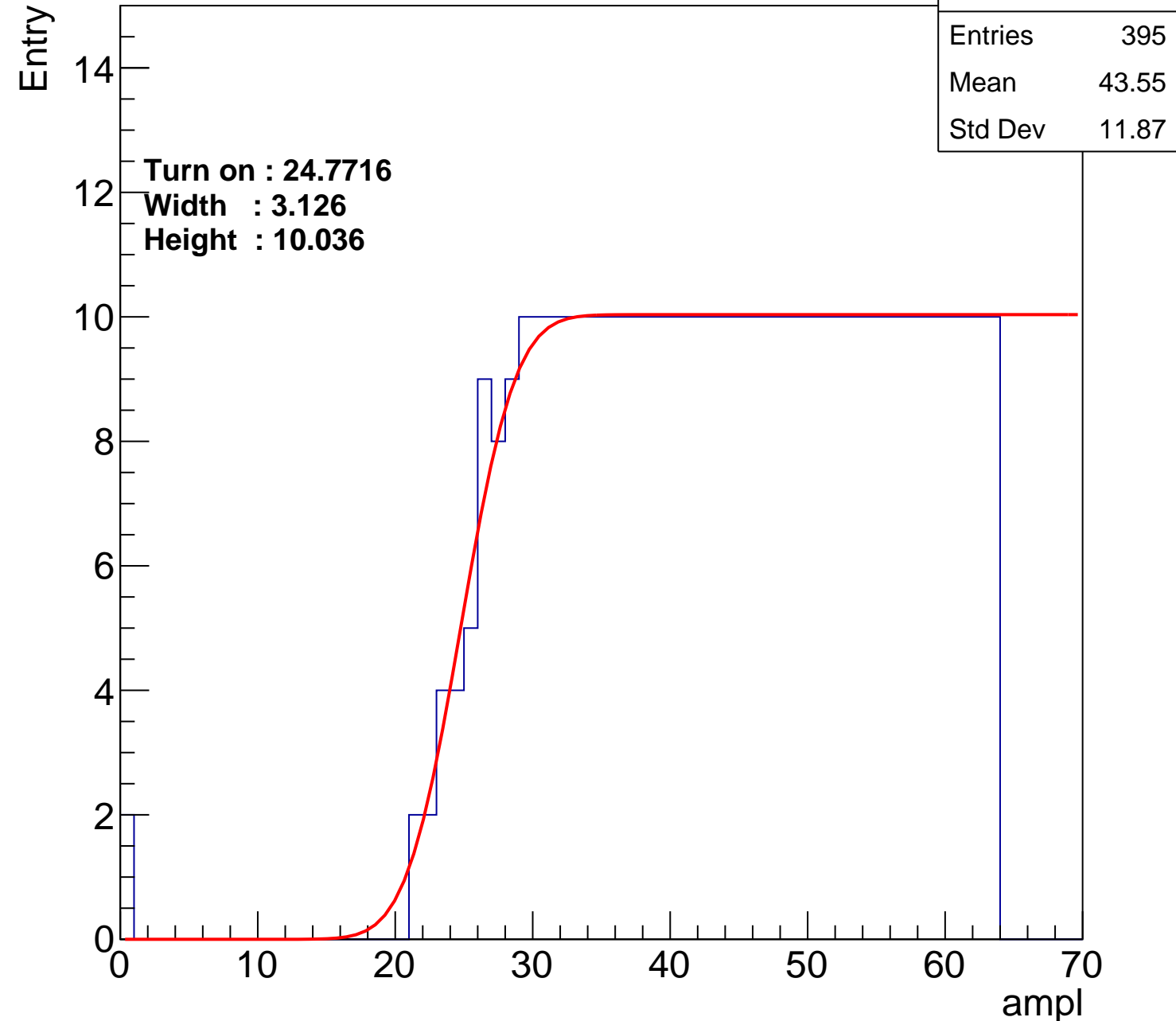
Width : 3.126

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch7

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.98 |
| Std Dev | 11.65 |

Turn on : 25.7352

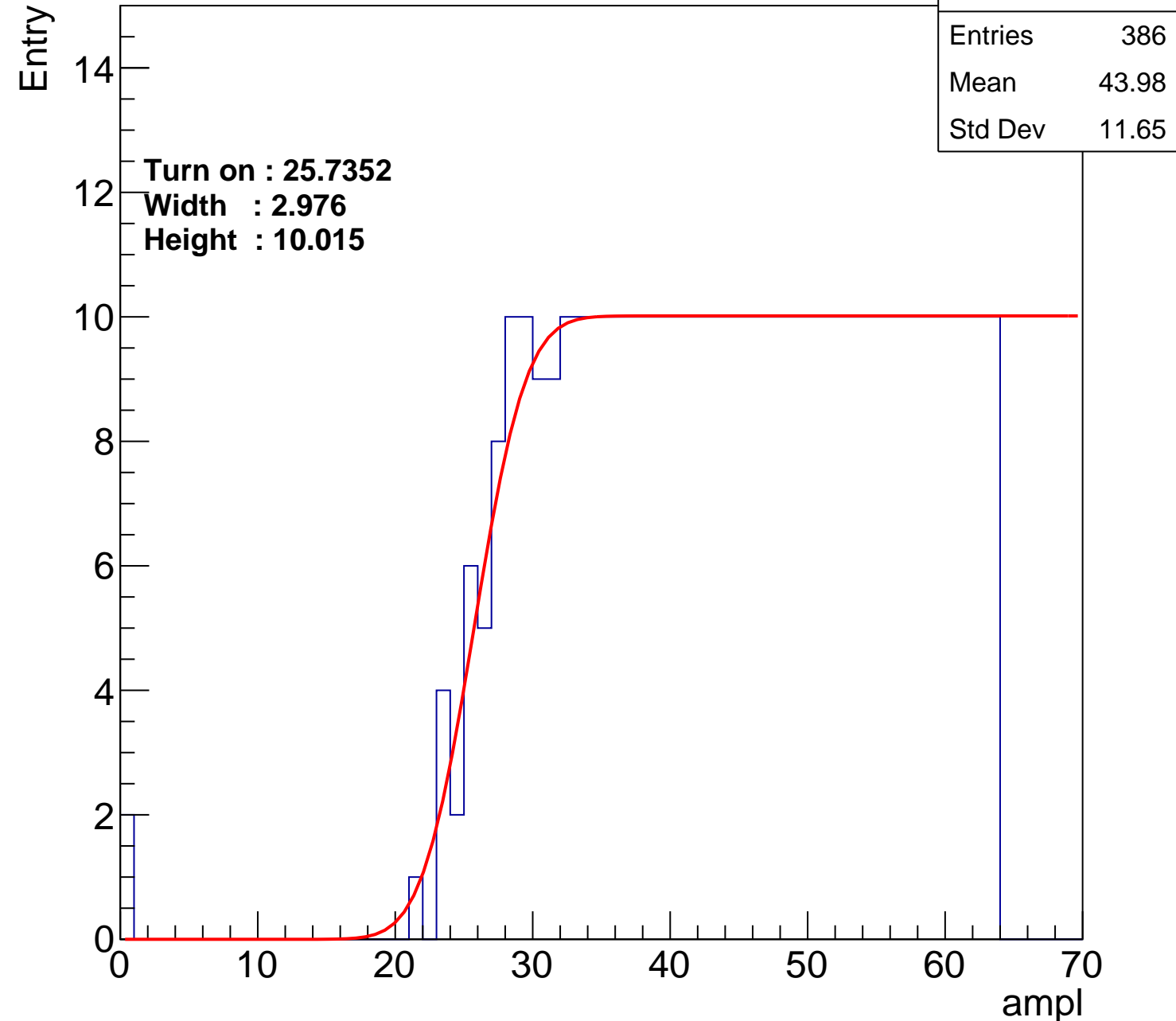
Width : 2.976

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch8

calib_packv5_042523_0143.root, FC#3, port B3

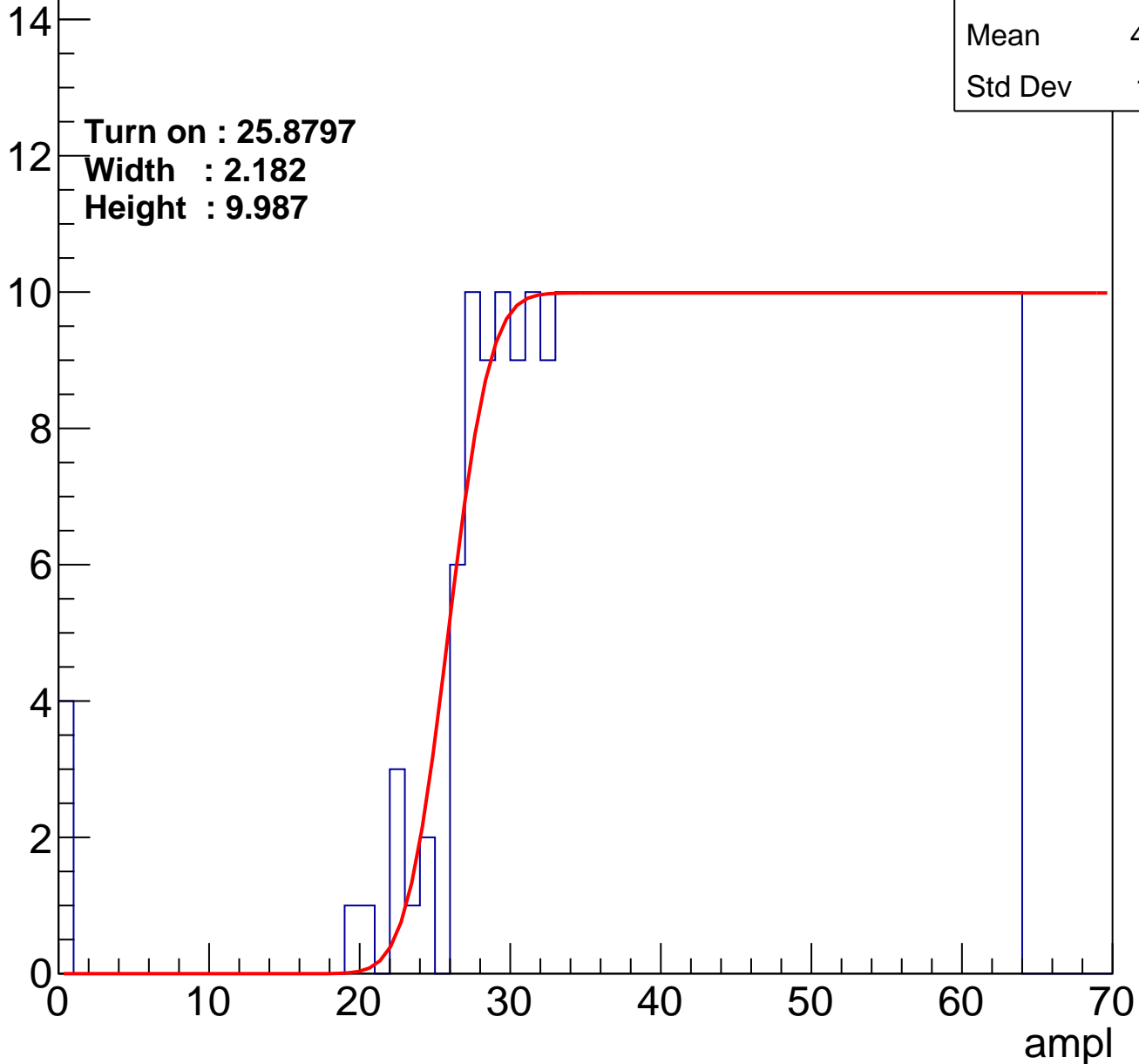
| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.88 |
| Std Dev | 12.01 |

Turn on : 25.8797

Width : 2.182

Height : 9.987

Entry



B1L000S, U1-ch9

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.21 |
| Std Dev | 12.04 |

Turn on : 23.9392

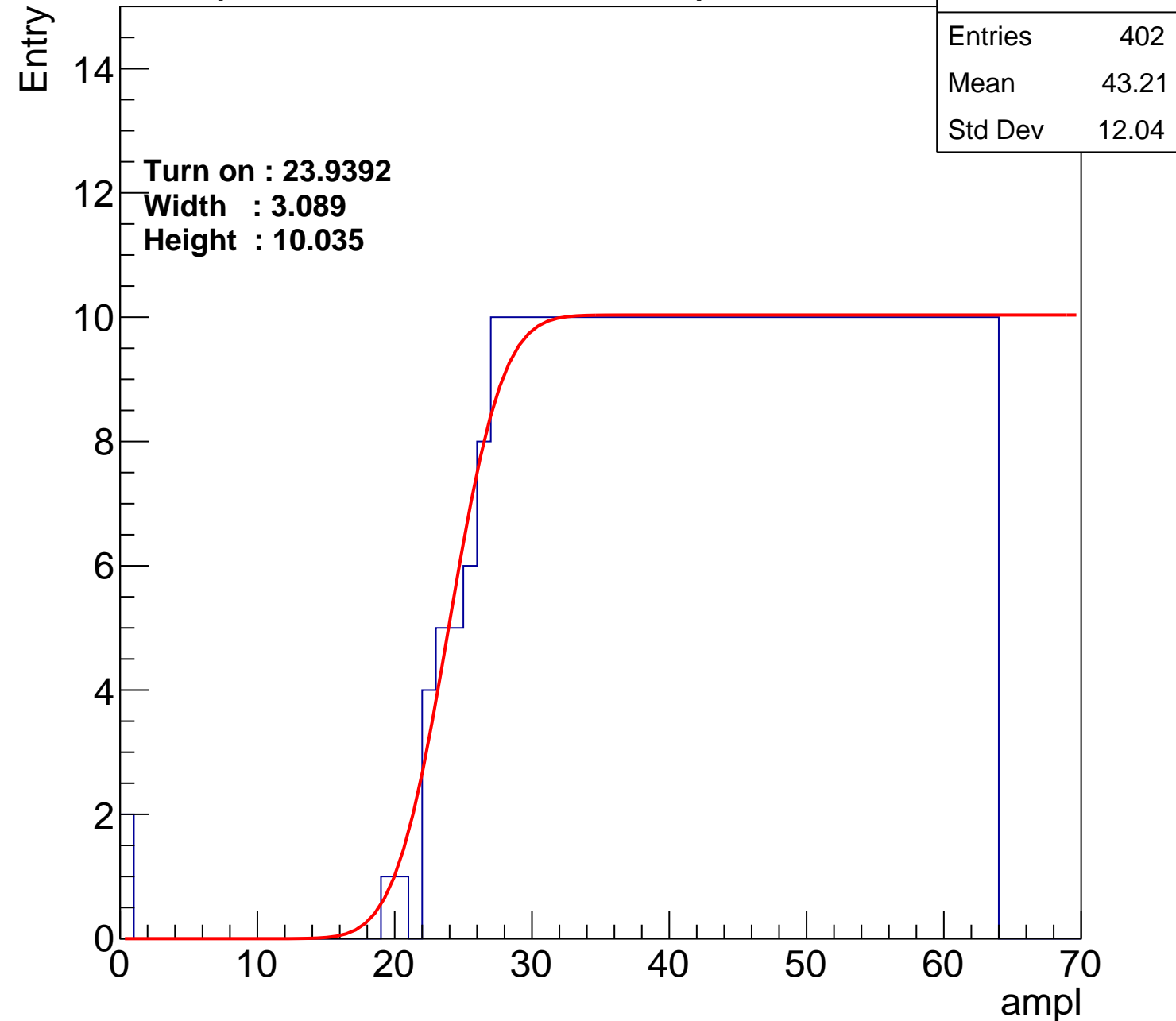
Width : 3.089

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch10

calib_packv5_042523_0143.root, FC#3, port B3

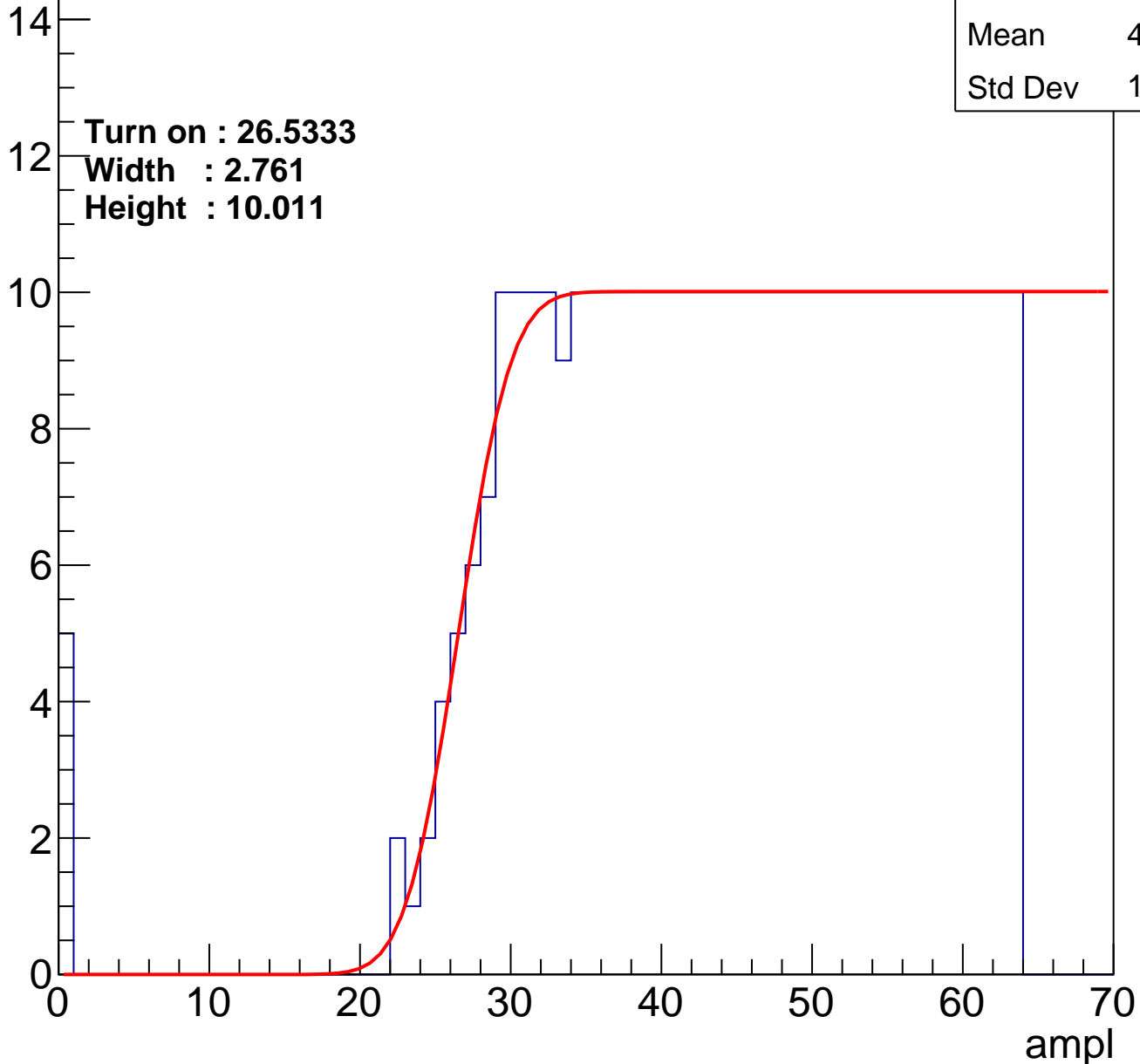
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.02 |
| Std Dev | 12.06 |

Turn on : 26.5333

Width : 2.761

Height : 10.011

Entry



B1L000S, U1-ch11

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.4 |
| Std Dev | 11.45 |

Turn on : 26.6660

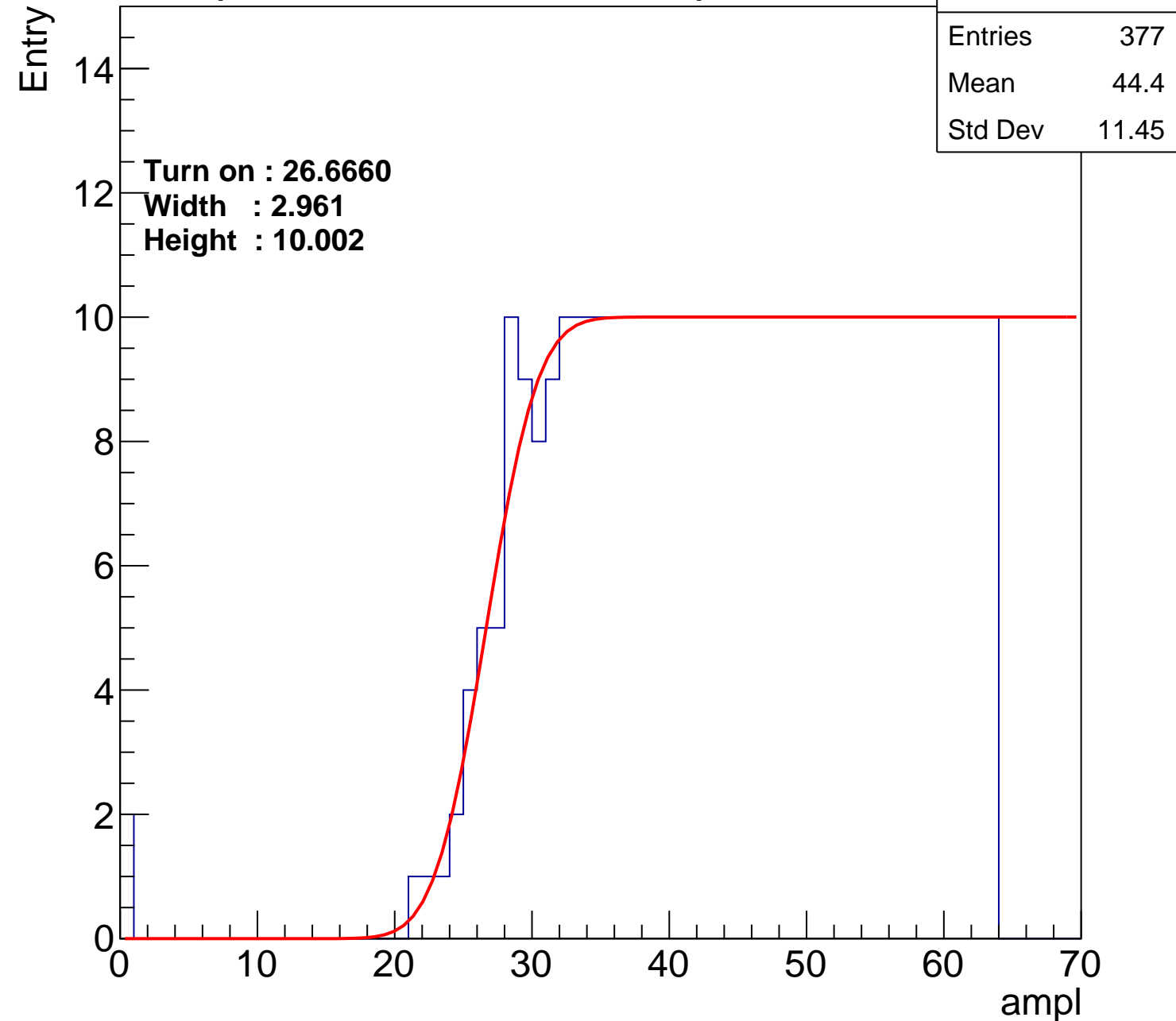
Width : 2.961

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch12

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.25 |
| Std Dev | 11.8 |

Turn on : 26.9482

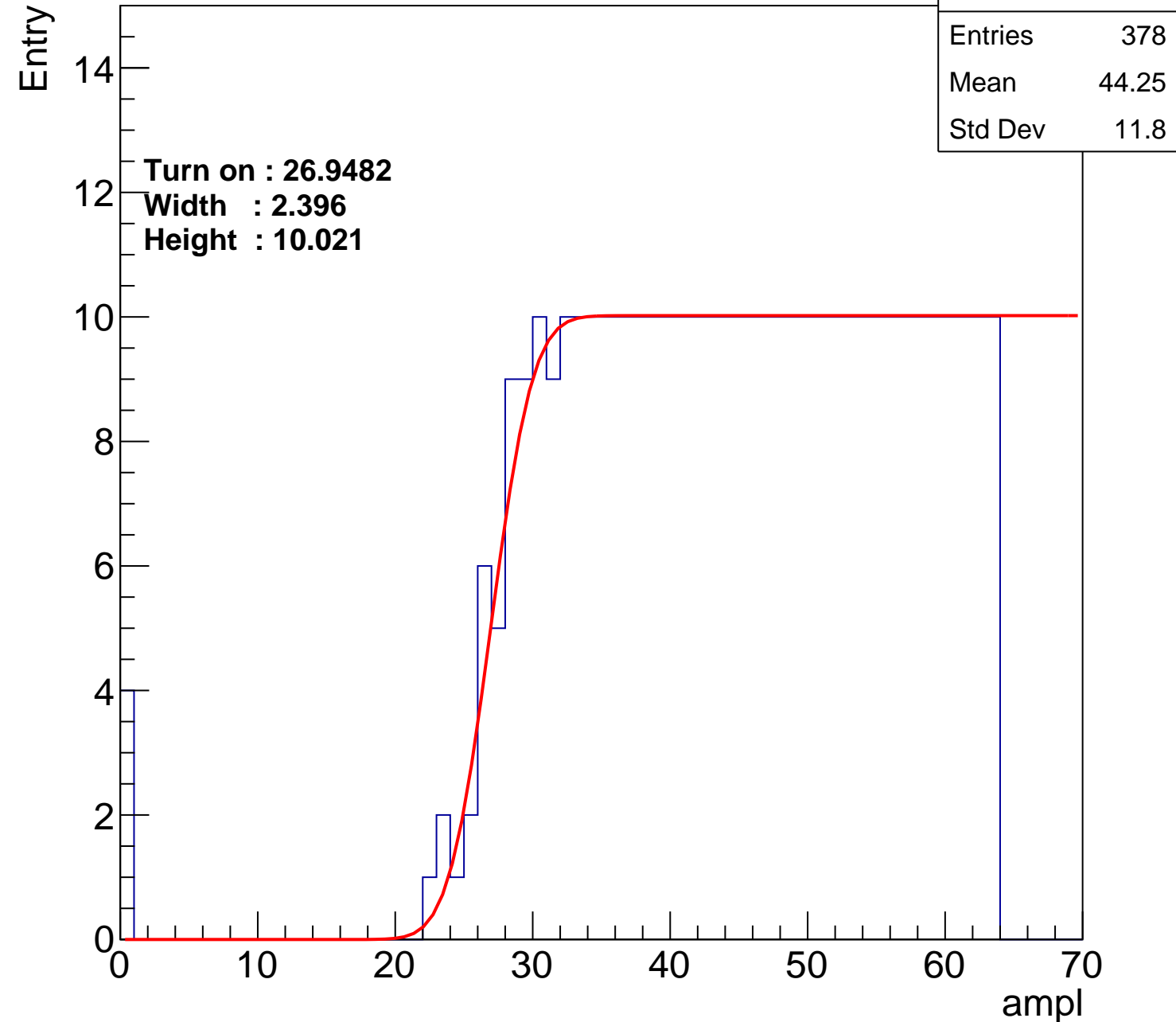
Width : 2.396

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch13

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.17 |
| Std Dev | 12.32 |

Turn on : 24.5686

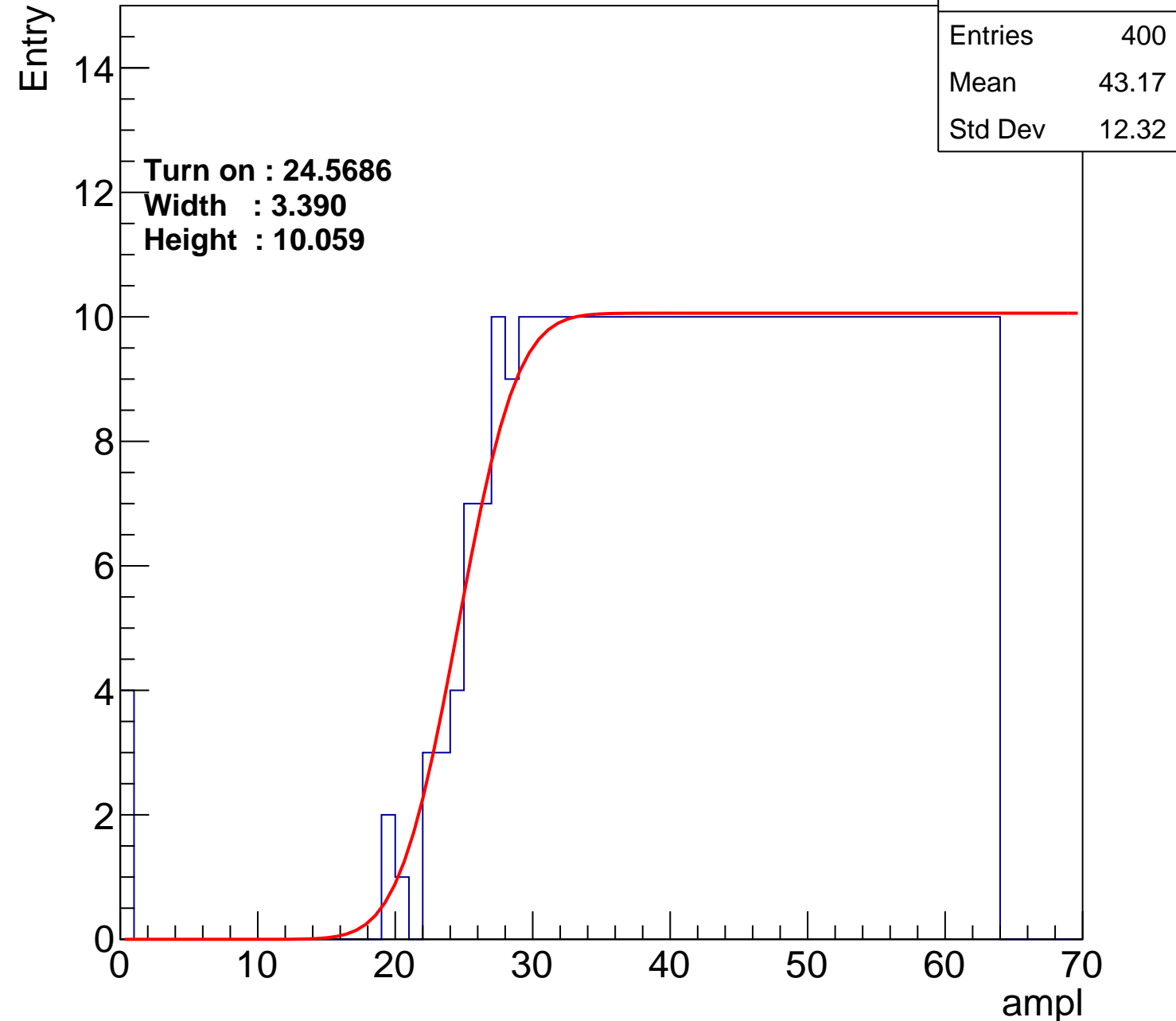
Width : 3.390

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch14

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.74 |
| Std Dev | 11.88 |

Turn on : 26.0170

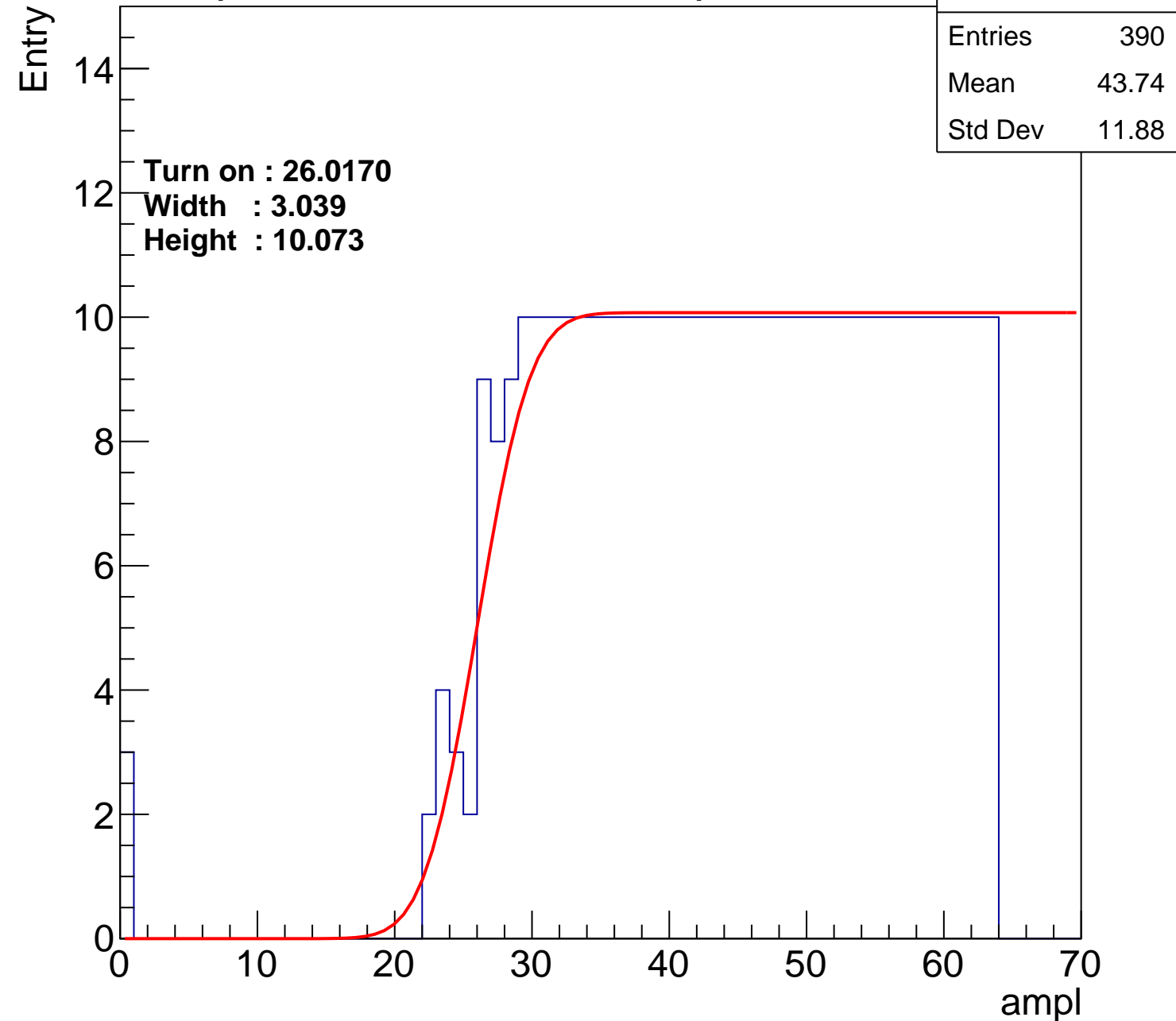
Width : 3.039

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch15

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-----|
| Entries | 393 |
|---------|-----|

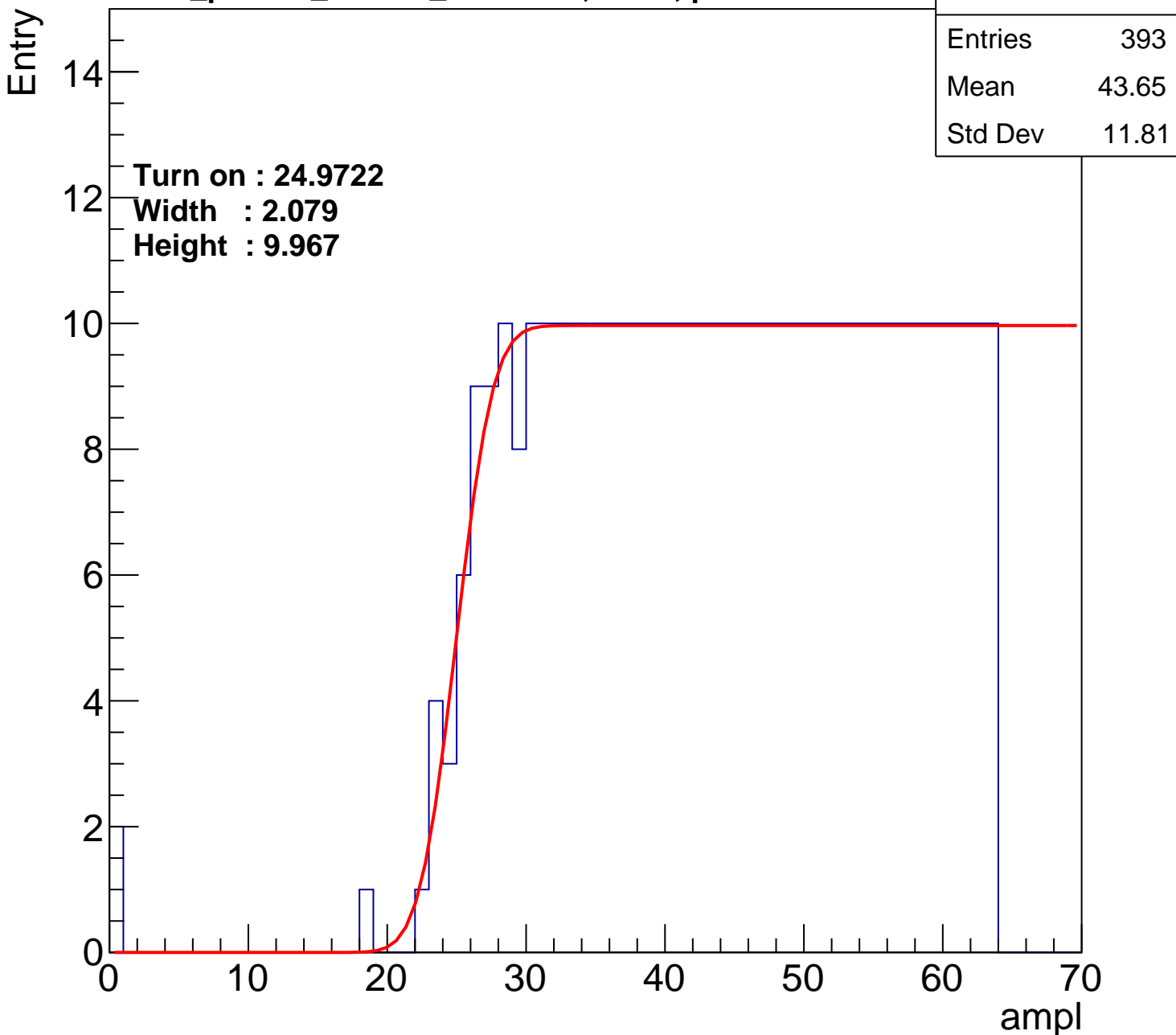
| | |
|------|-------|
| Mean | 43.65 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 11.81 |
|---------|-------|

Turn on : 24.9722

Width : 2.079

Height : 9.967



B1L000S, U1-ch16

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.09 |
| Std Dev | 11.71 |

Turn on : 26.3164

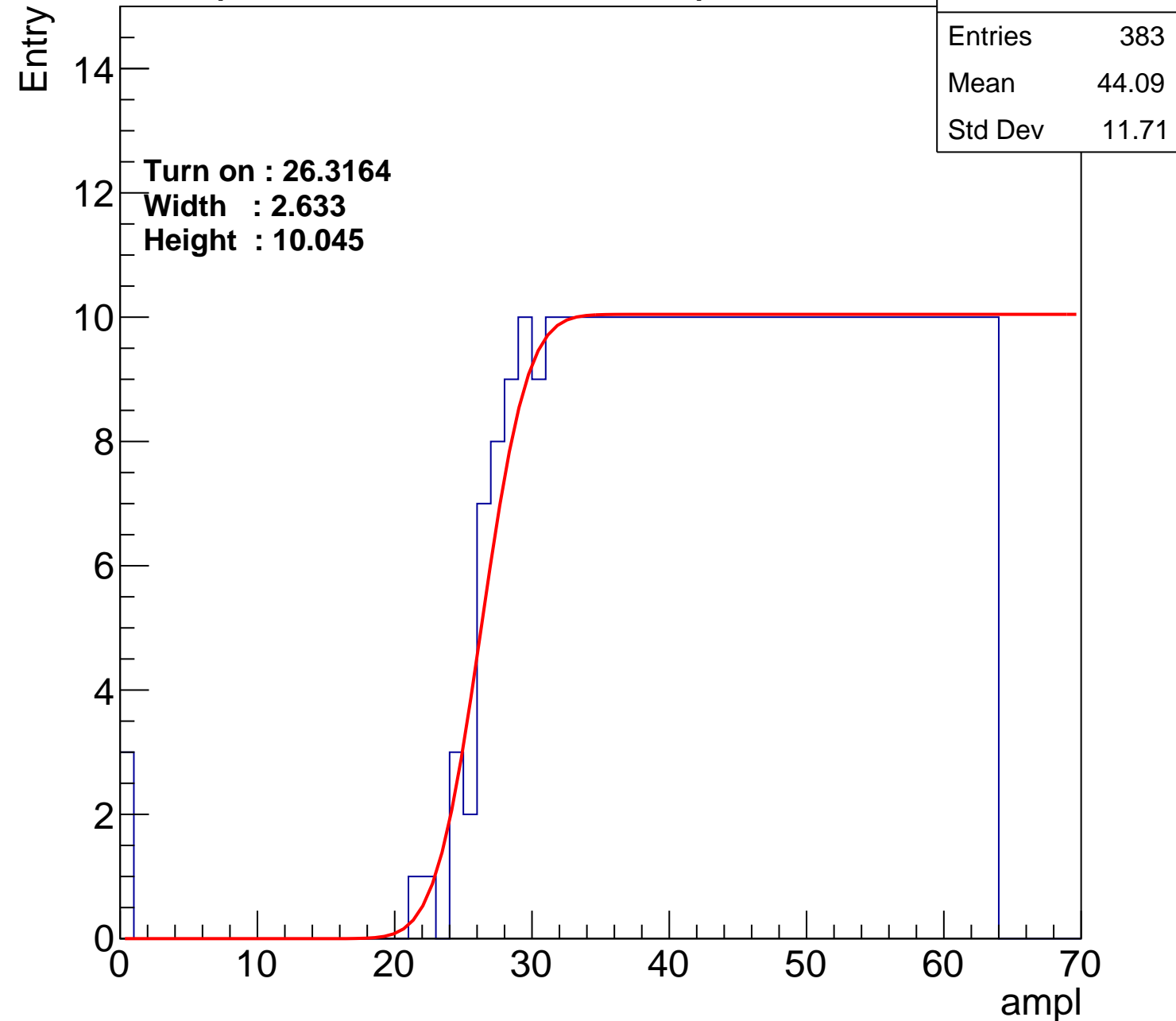
Width : 2.633

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch17

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.57 |
| Std Dev | 11.21 |

Turn on : 26.9088

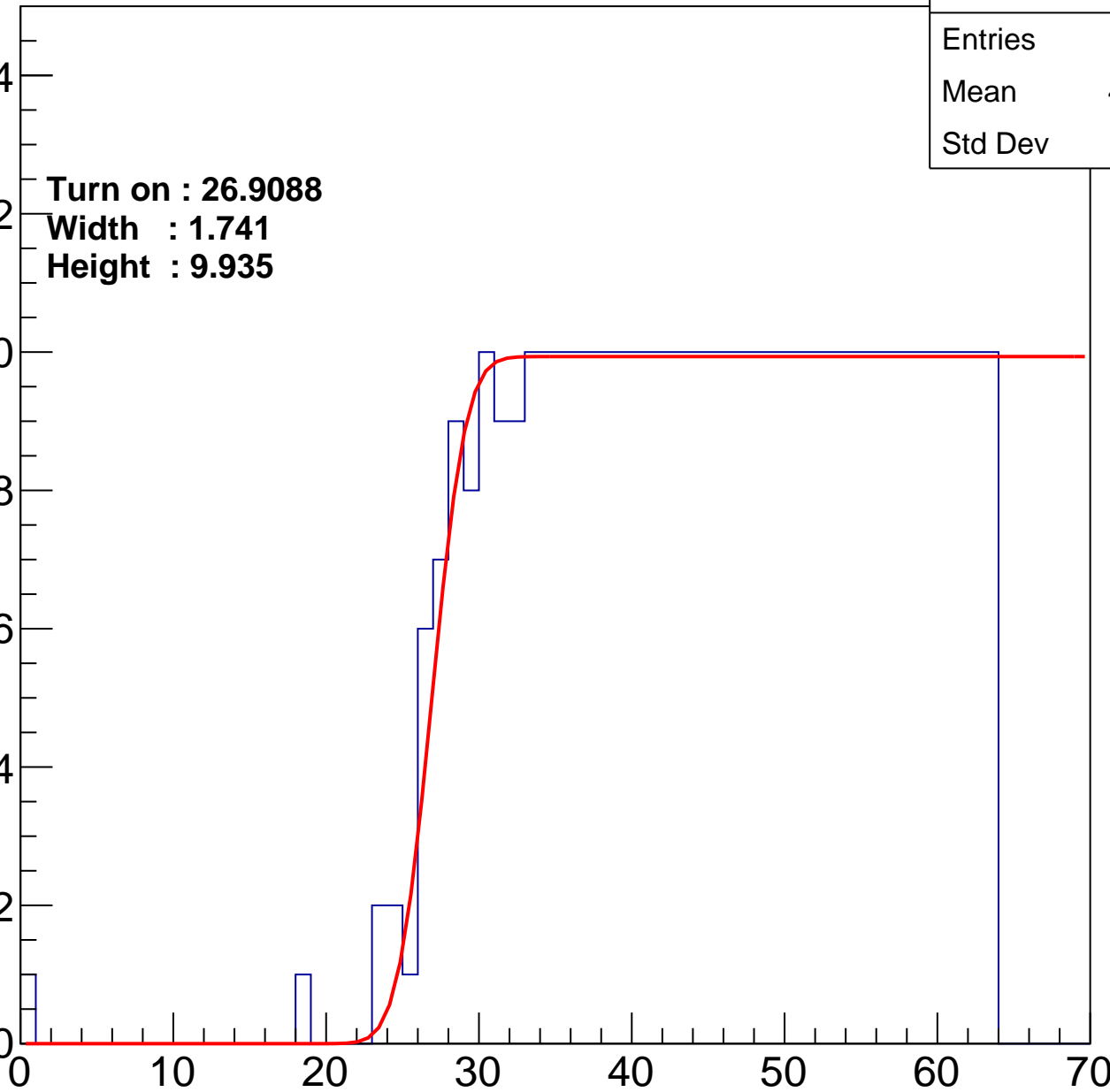
Width : 1.741

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch18

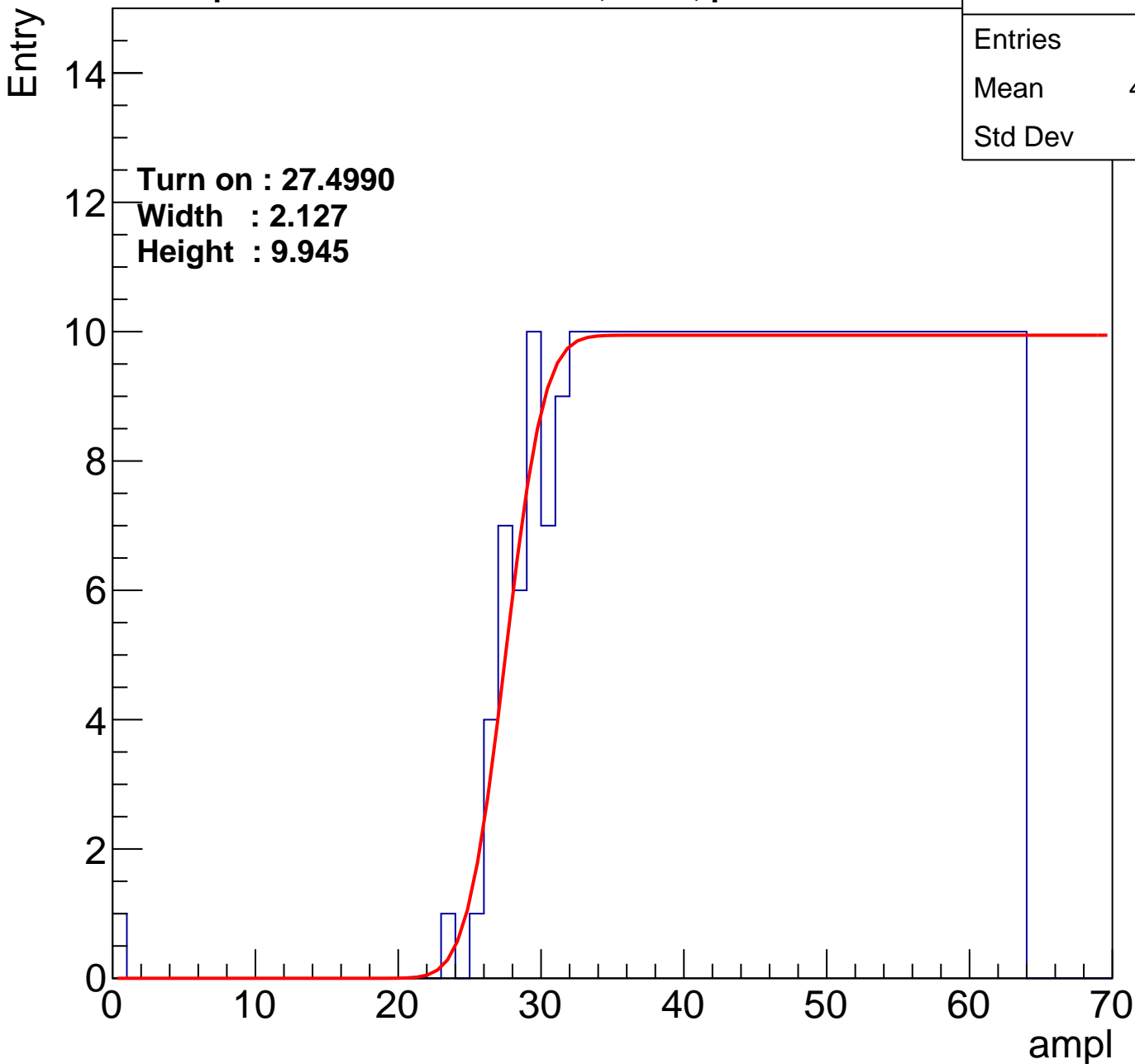
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.05 |
| Std Dev | 10.91 |

Turn on : 27.4990

Width : 2.127

Height : 9.945



B1L000S, U1-ch19

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.5 |
| Std Dev | 11.71 |

Turn on : 24.4998

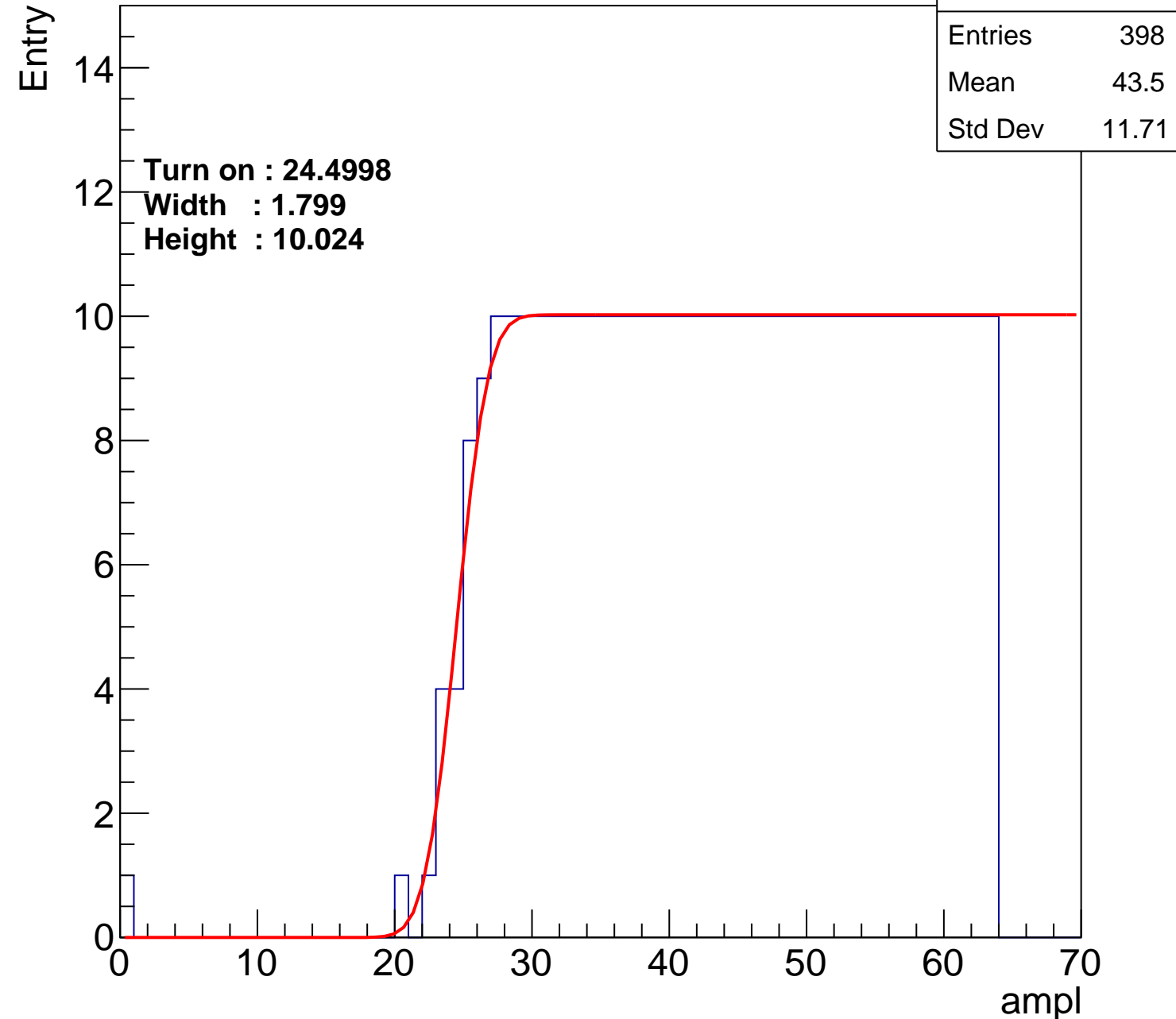
Width : 1.799

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch20

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.02 |
| Std Dev | 12.72 |

Turn on : 24.6122

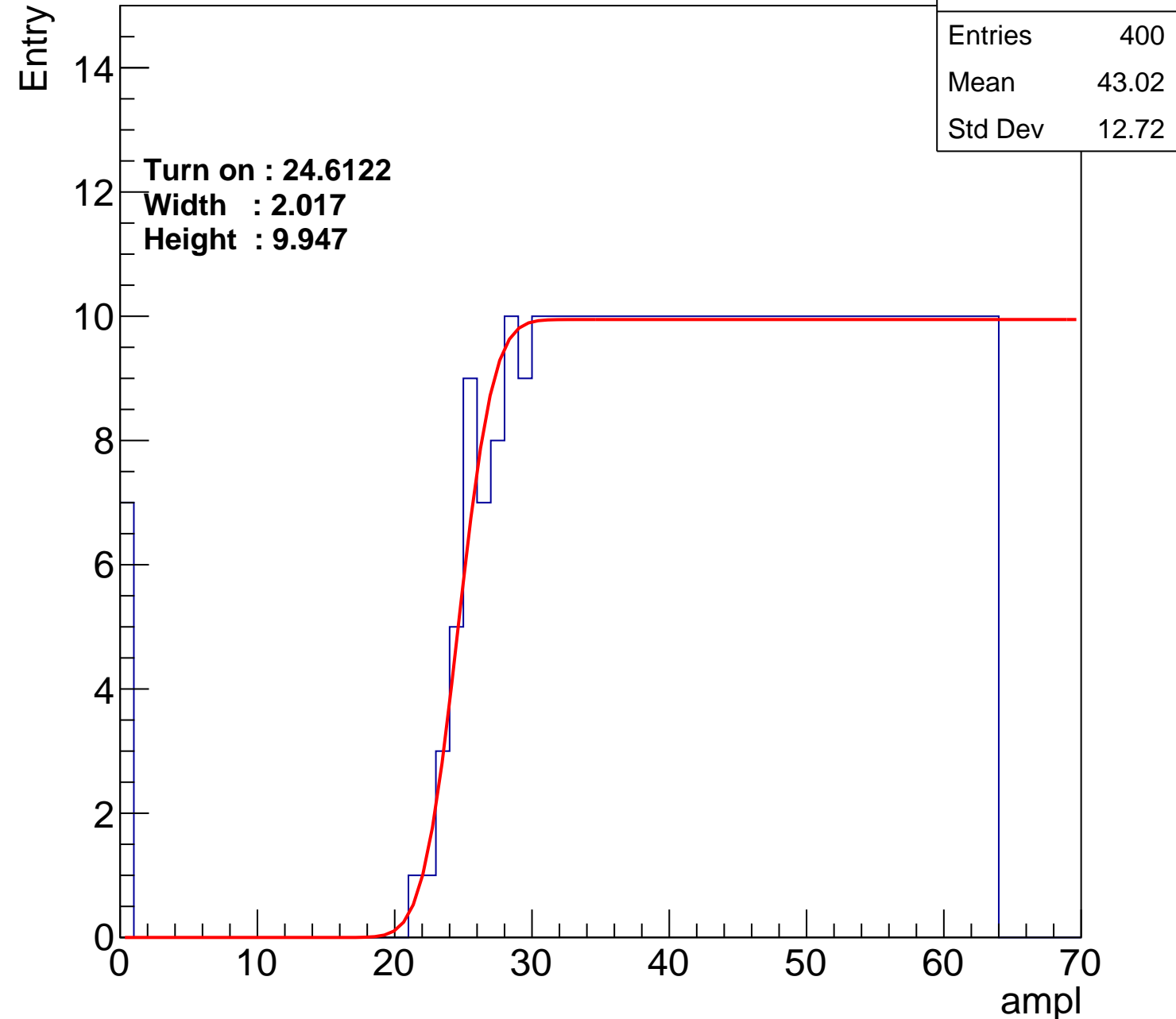
Width : 2.017

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch21

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.49 |
| Std Dev | 12.3 |

Turn on : 25.7799

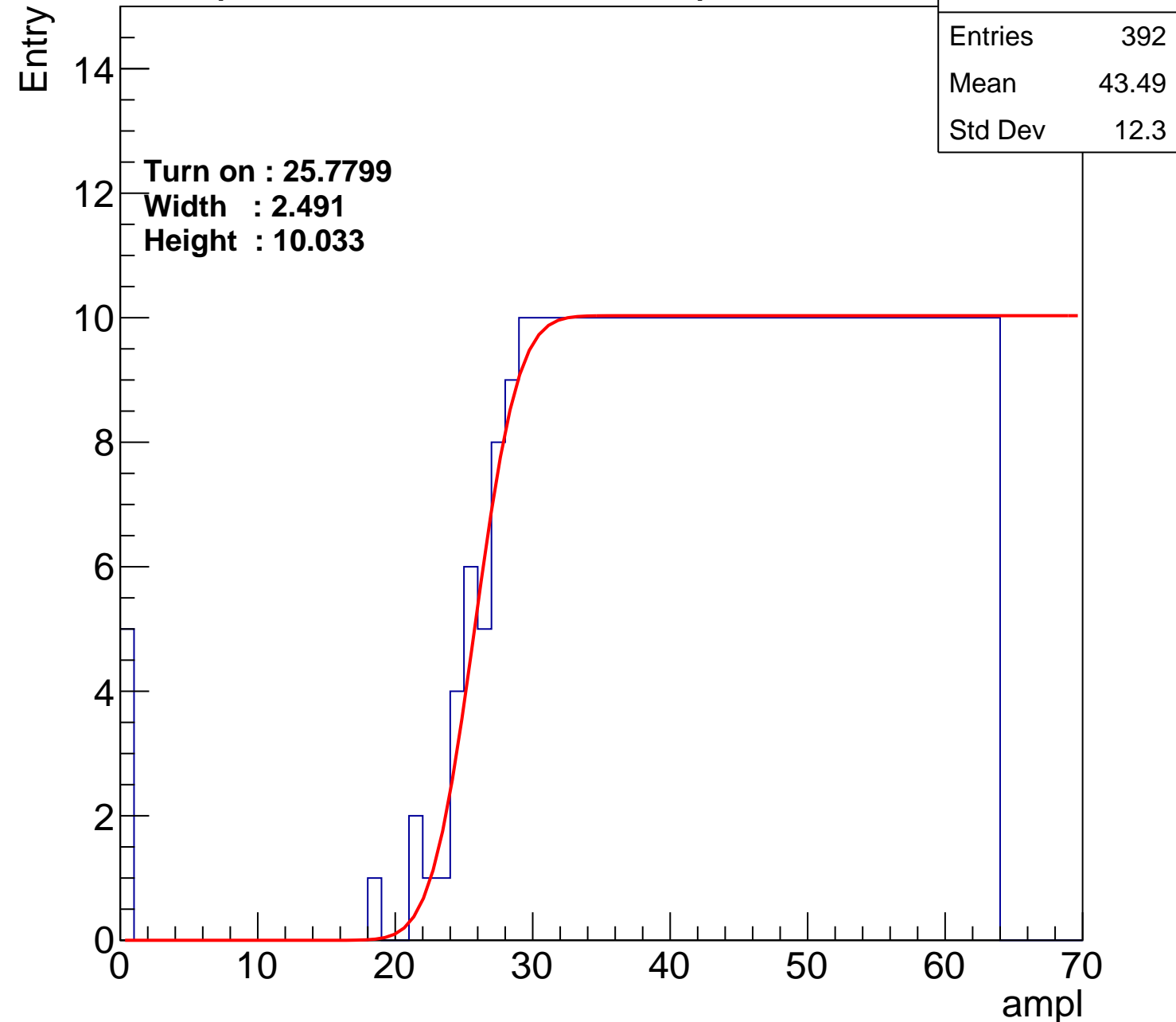
Width : 2.491

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch22

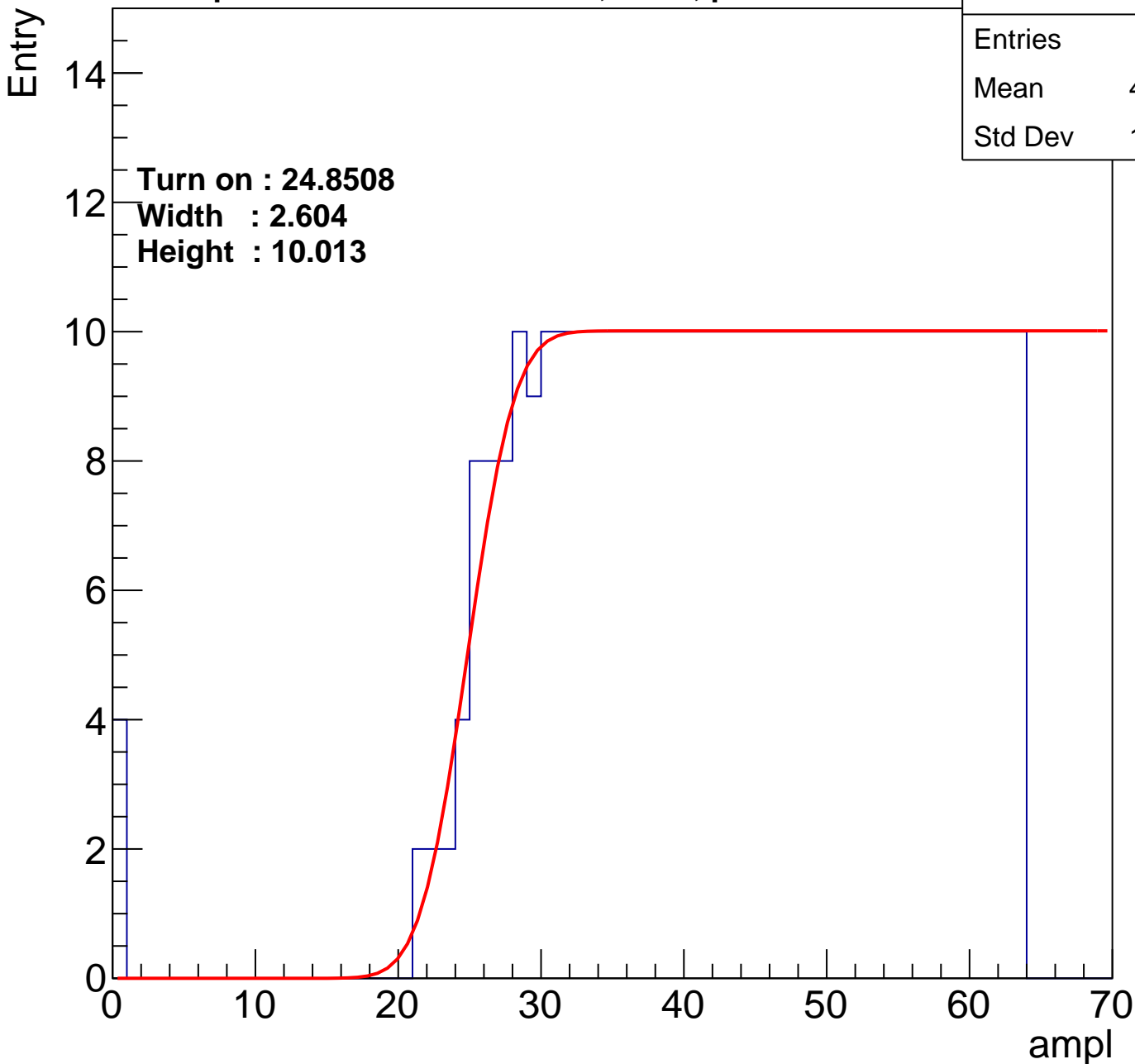
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.33 |
| Std Dev | 12.22 |

Turn on : 24.8508

Width : 2.604

Height : 10.013



B1L000S, U1-ch23

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.15 |
| Std Dev | 12.46 |

Turn on : 25.2836

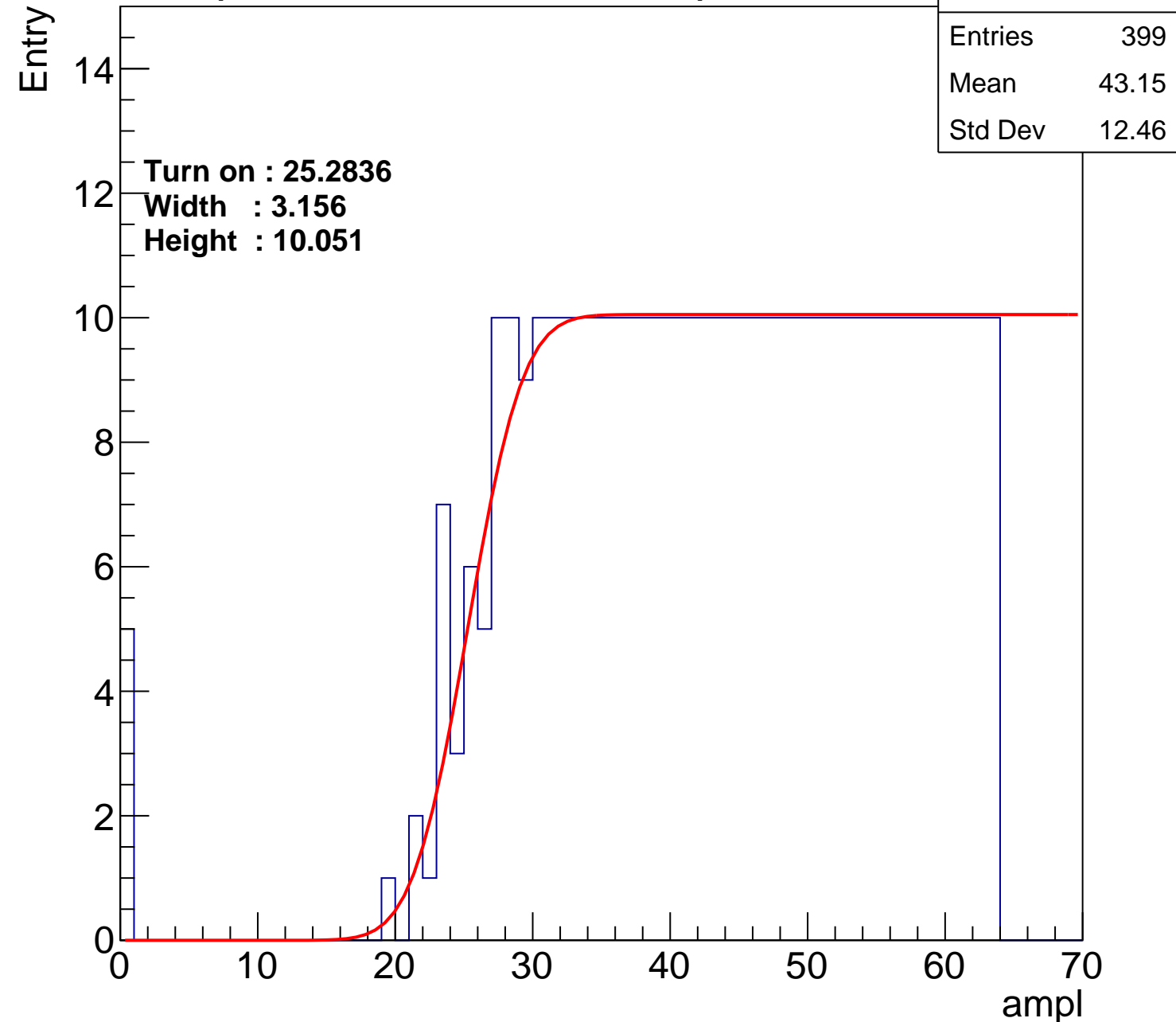
Width : 3.156

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch24

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.84 |
| Std Dev | 11.72 |

Turn on : 25.7332

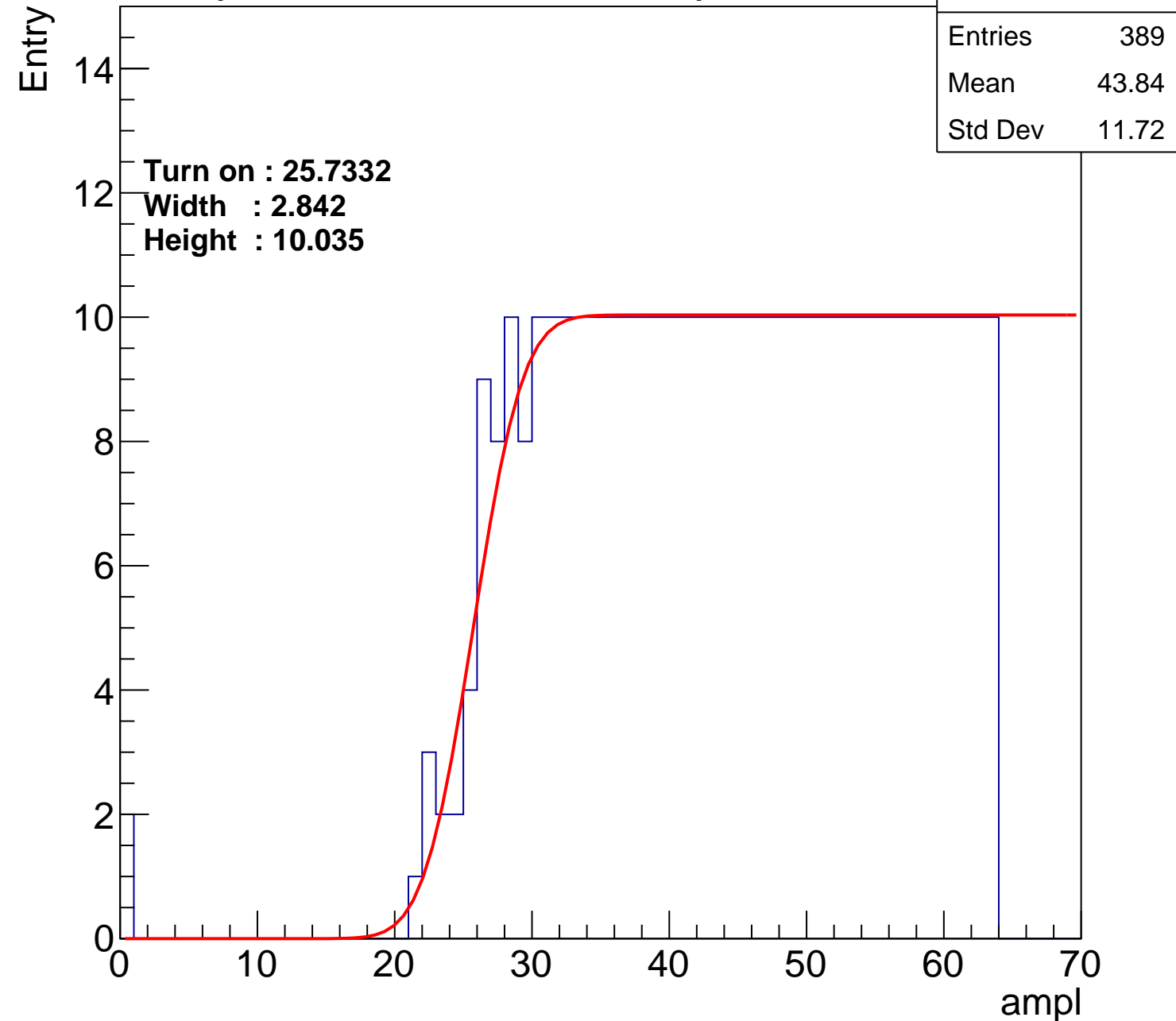
Width : 2.842

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch25

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.66 |
| Std Dev | 11.72 |

Turn on : 25.5517

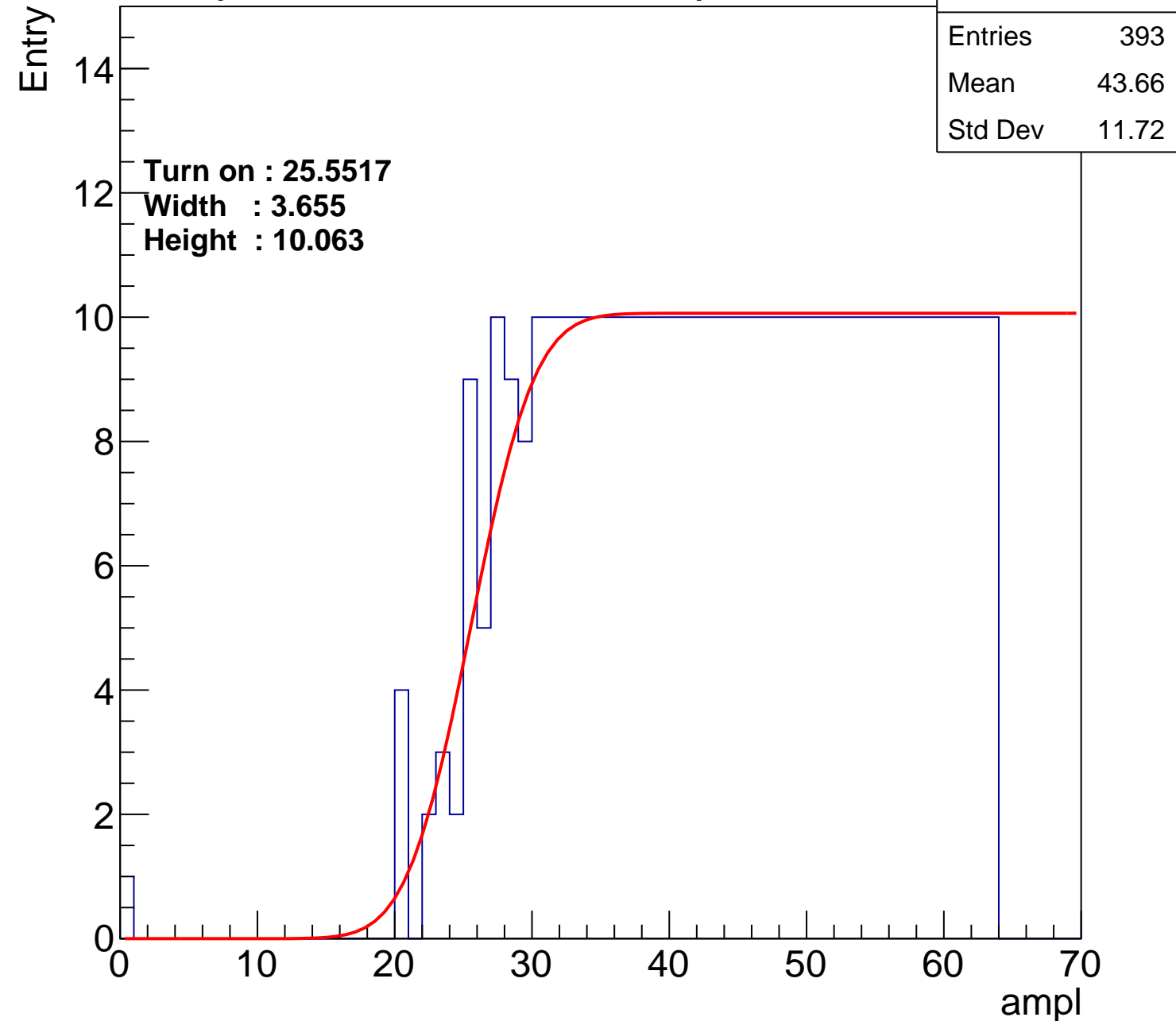
Width : 3.655

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch26

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.27 |
| Std Dev | 12 |

Turn on : 23.8974

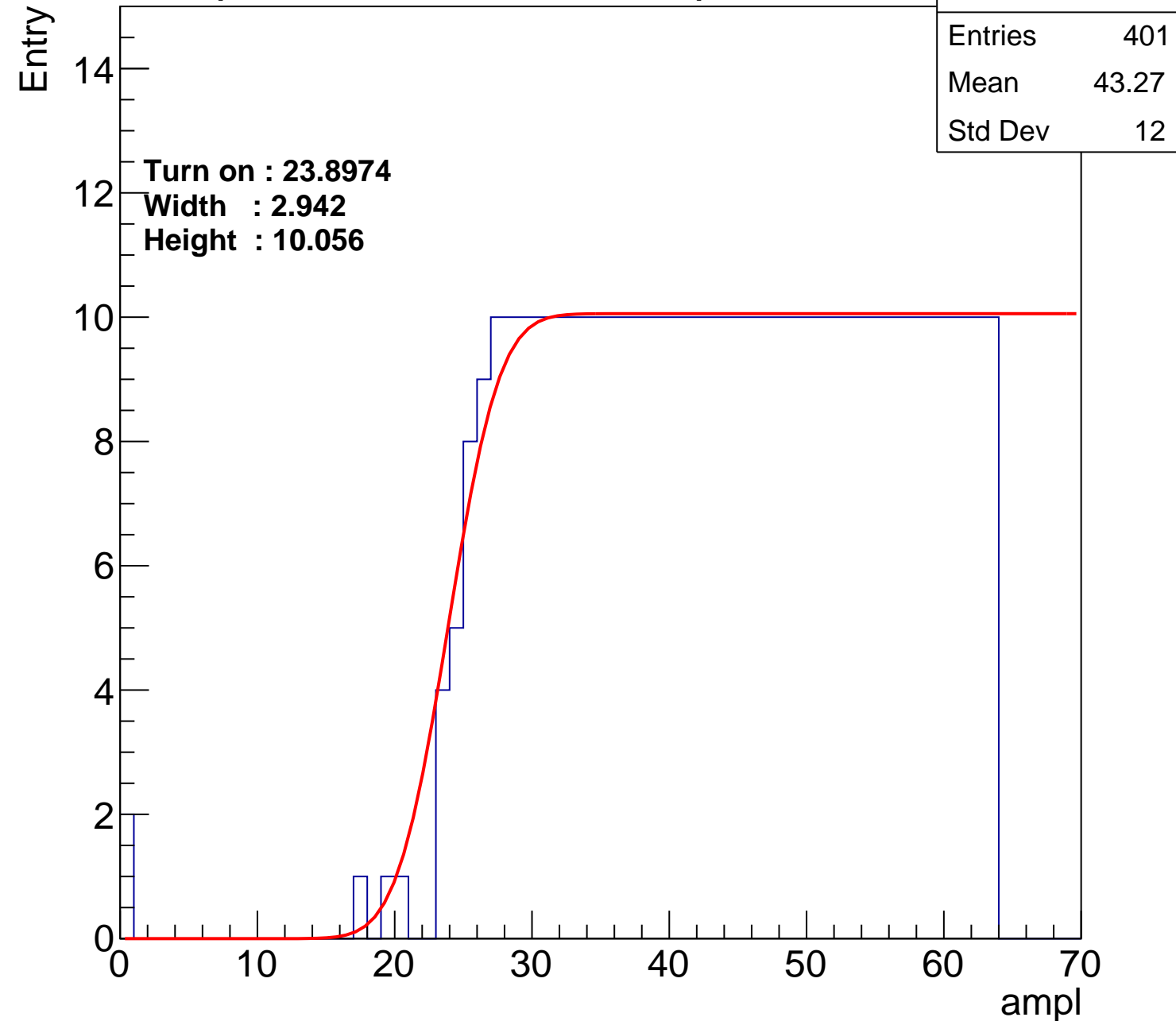
Width : 2.942

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch27

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.74 |
| Std Dev | 12.17 |

Turn on : 26.0605

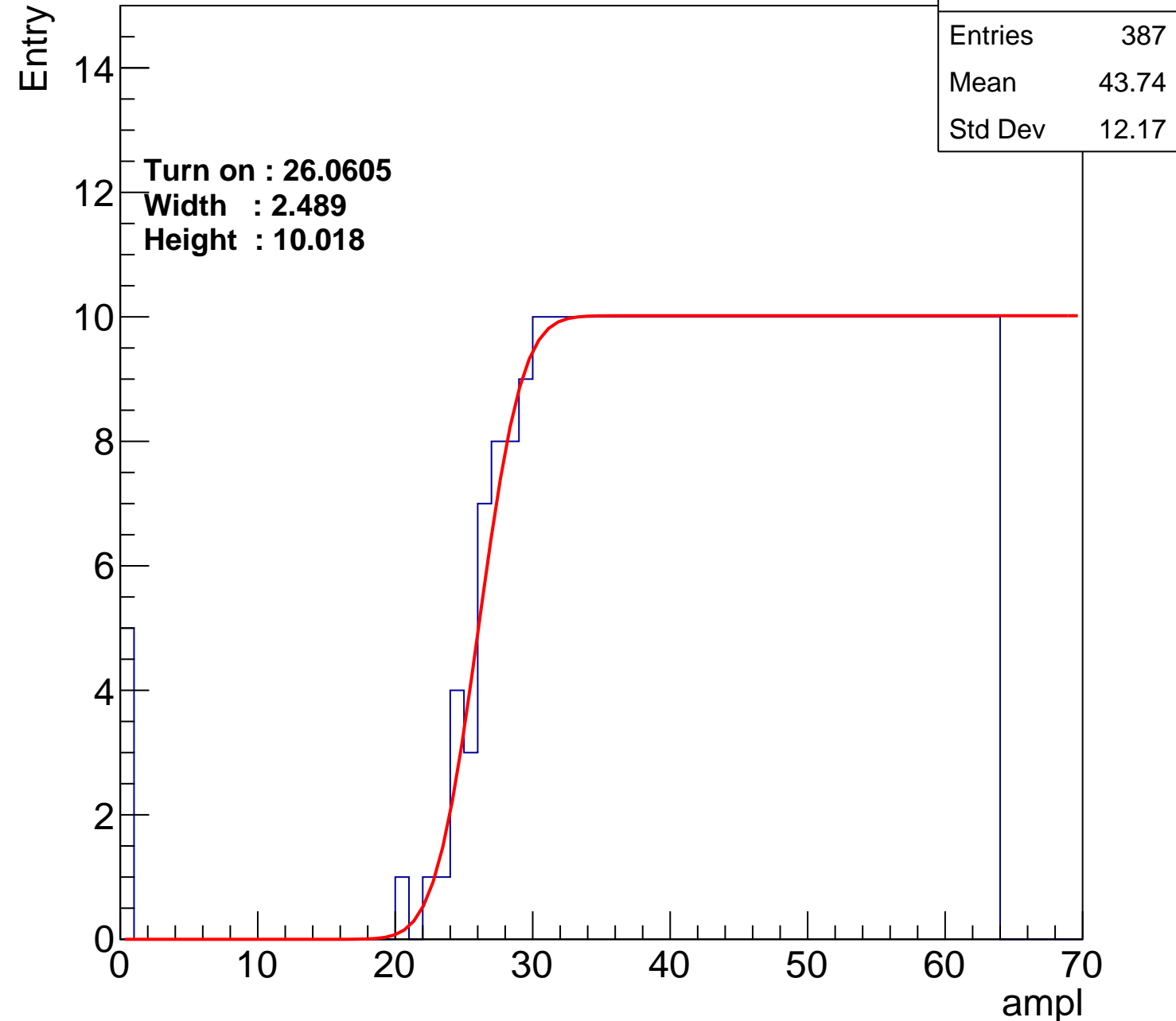
Width : 2.489

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch28

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.28 |
| Std Dev | 12.14 |

Turn on : 25.1067

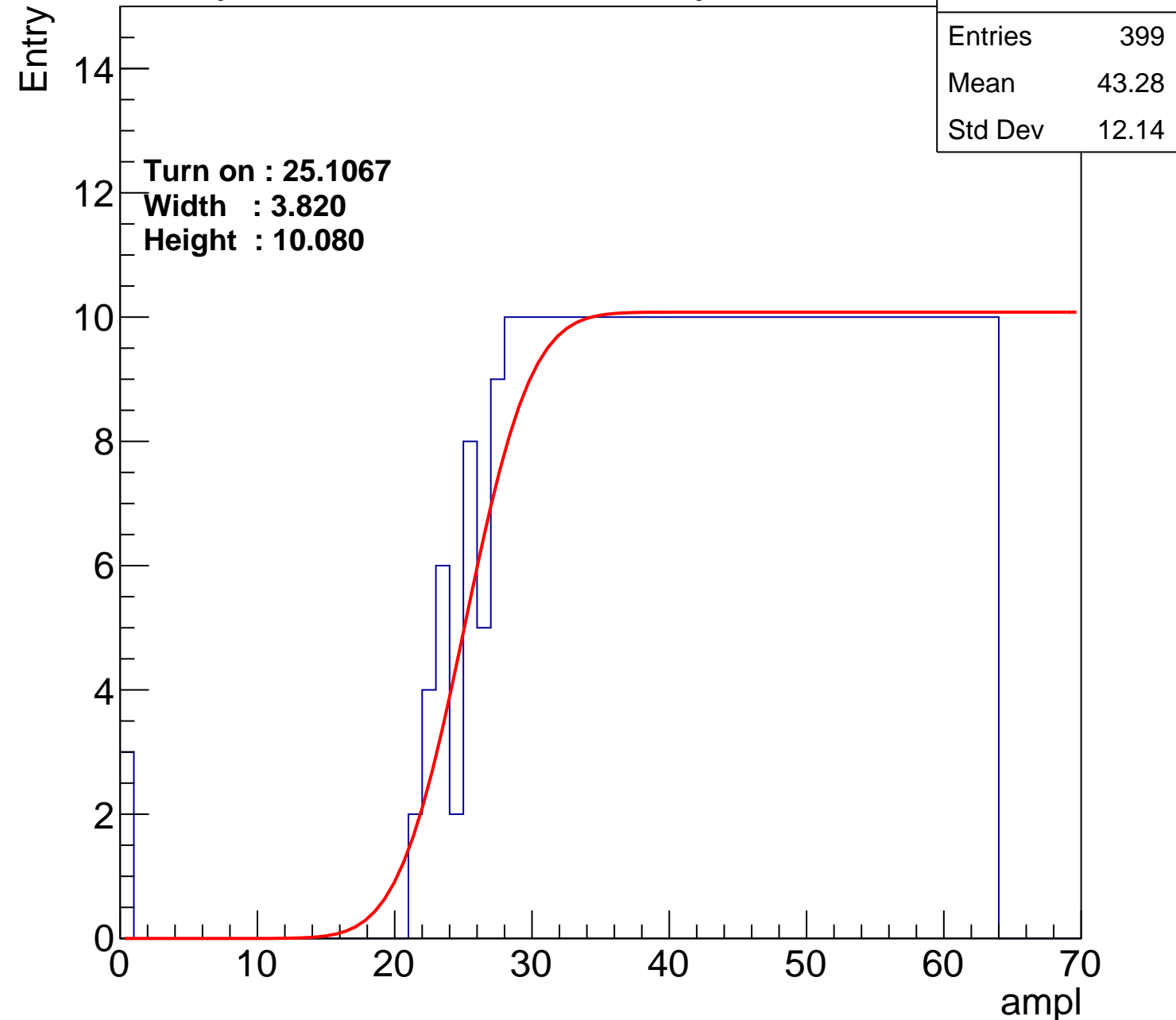
Width : 3.820

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch29

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.75 |
| Std Dev | 11.82 |

Turn on : 25.0193

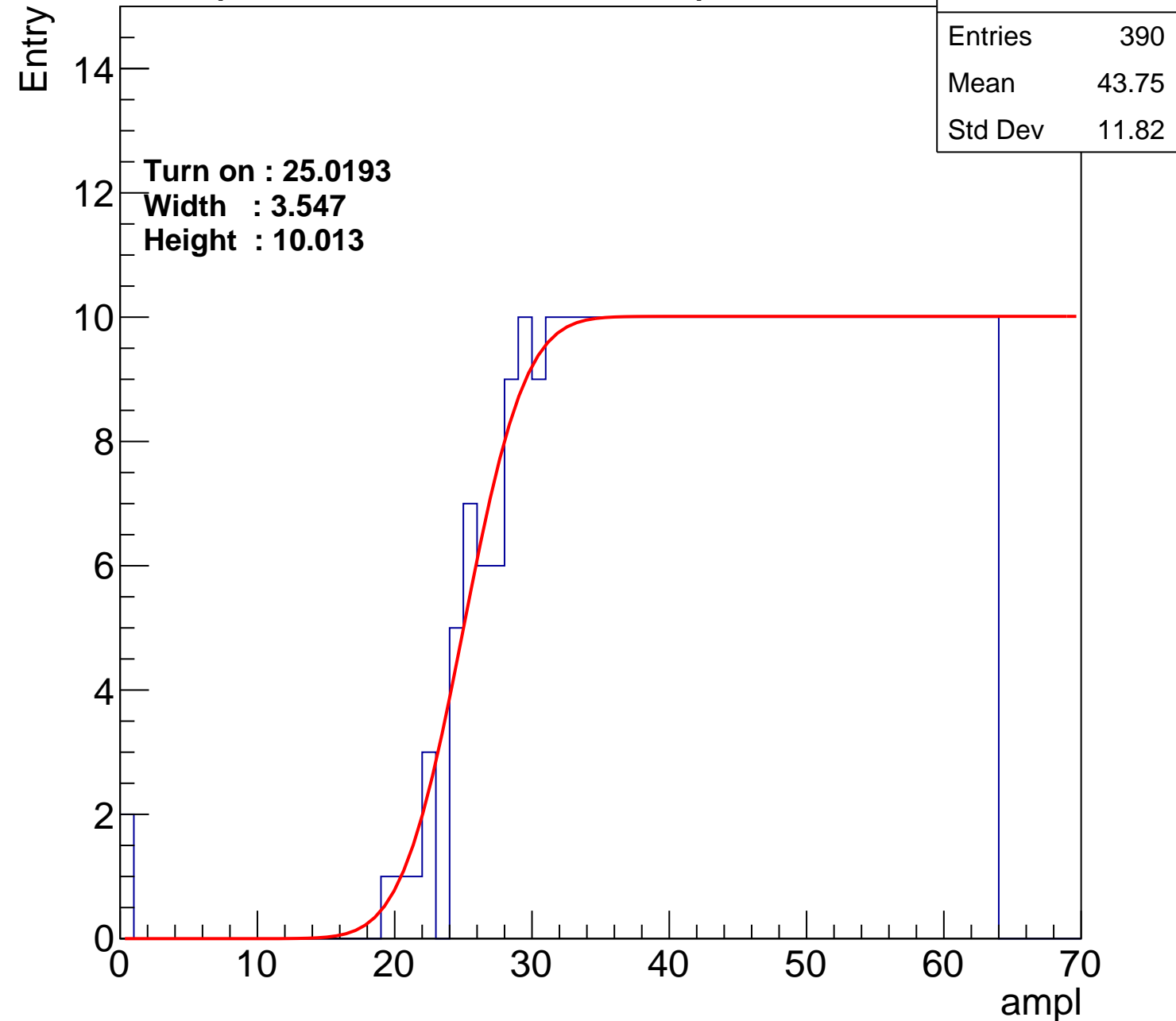
Width : 3.547

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch30

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.65 |
| Std Dev | 11.27 |

Turn on : 26.9330

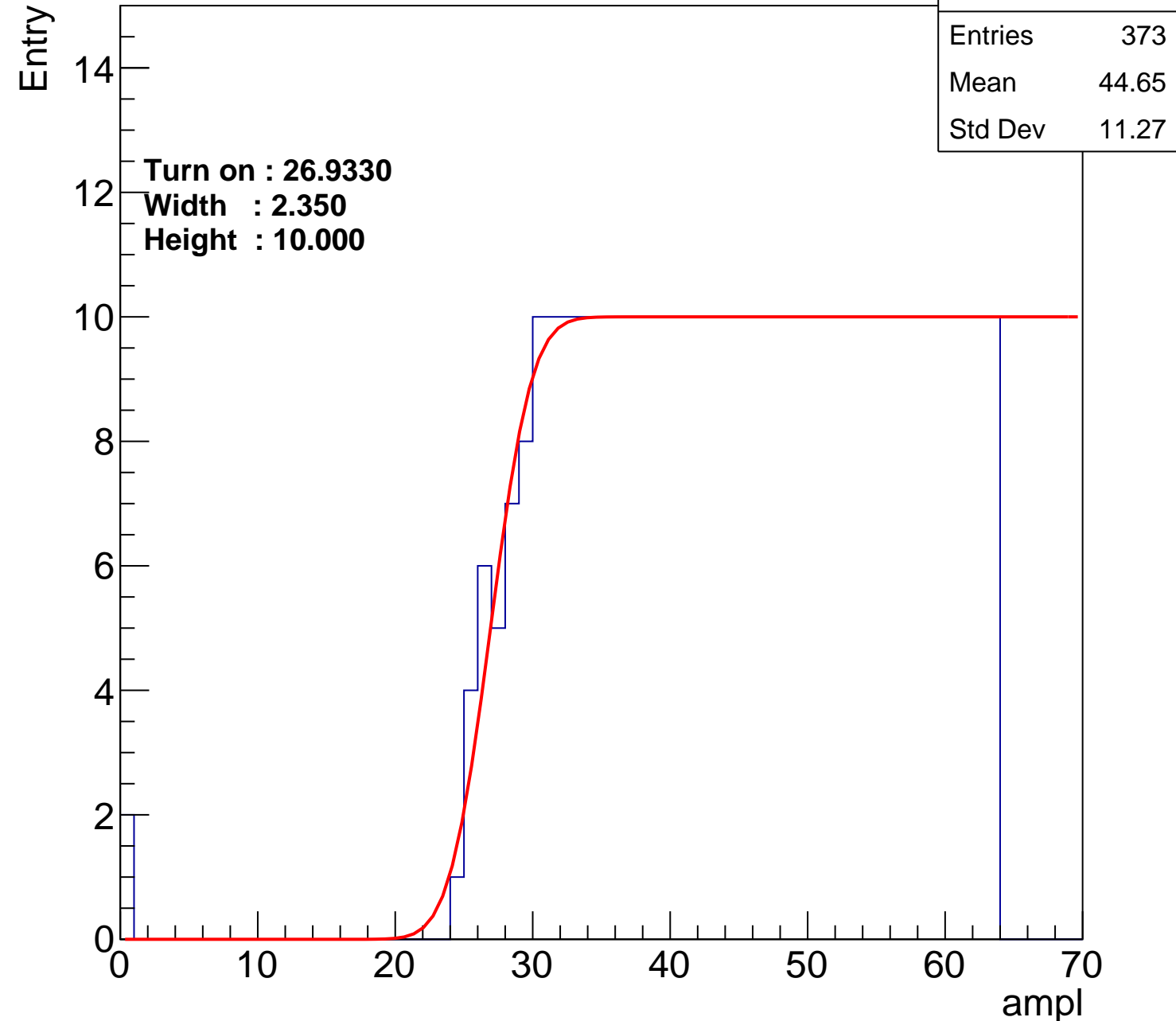
Width : 2.350

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch31

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-----|
| Entries | 388 |
|---------|-----|

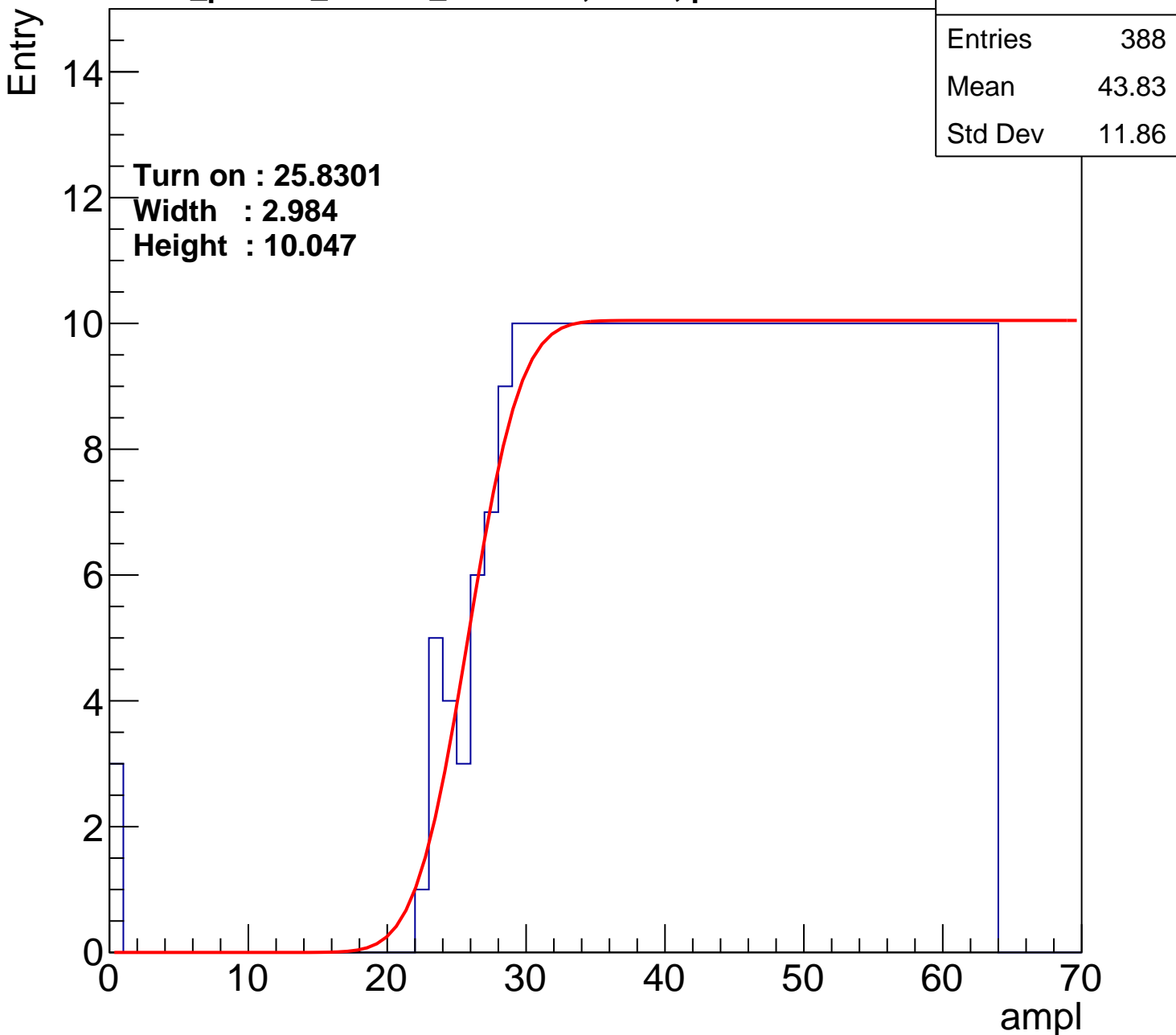
| | |
|------|-------|
| Mean | 43.83 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 11.86 |
|---------|-------|

Turn on : 25.8301

Width : 2.984

Height : 10.047



B1L000S, U1-ch32

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.9 |
| Std Dev | 12.11 |

Turn on : 23.2288

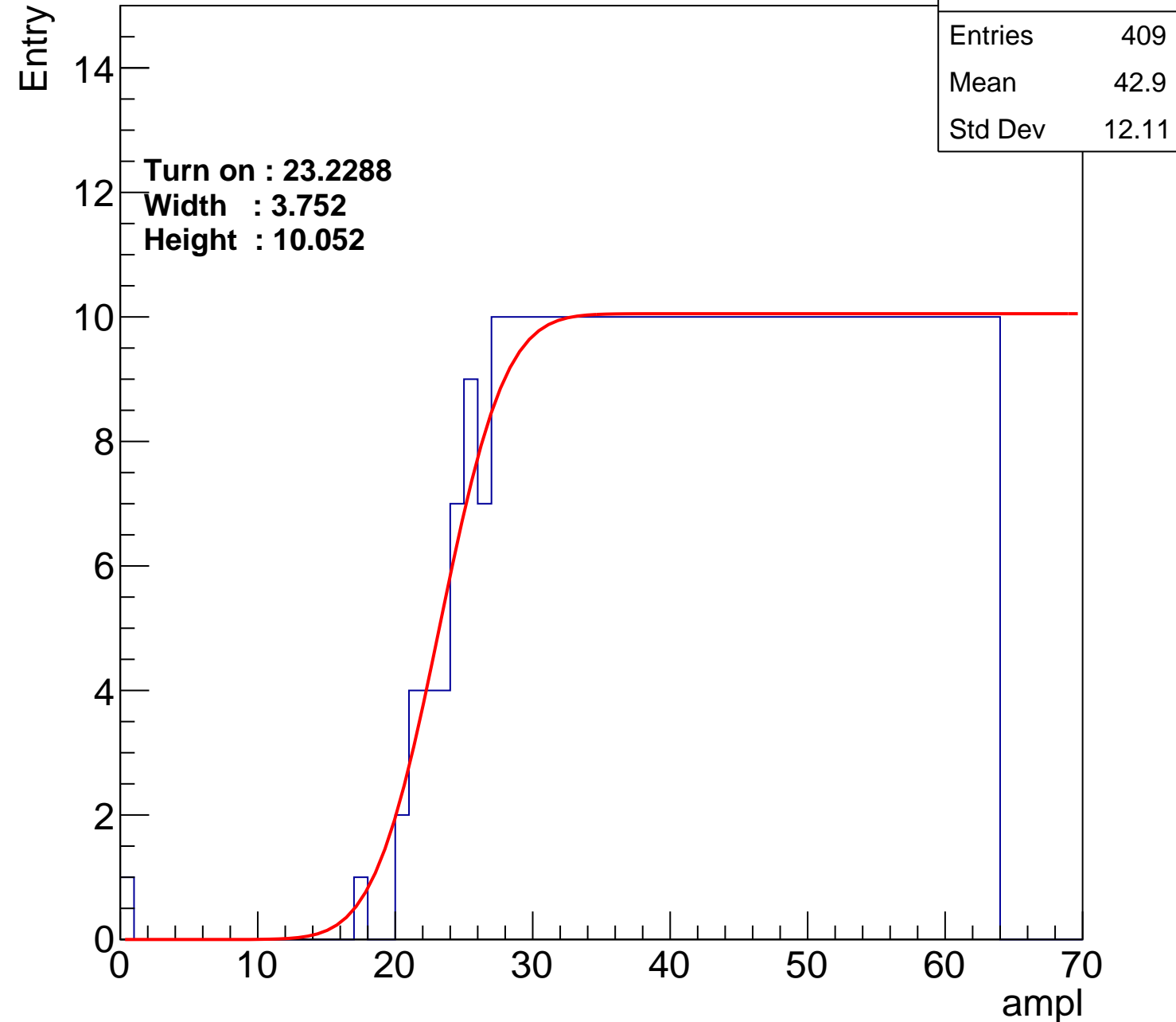
Width : 3.752

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch33

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.19 |
| Std Dev | 11.52 |

Turn on : 25.9414

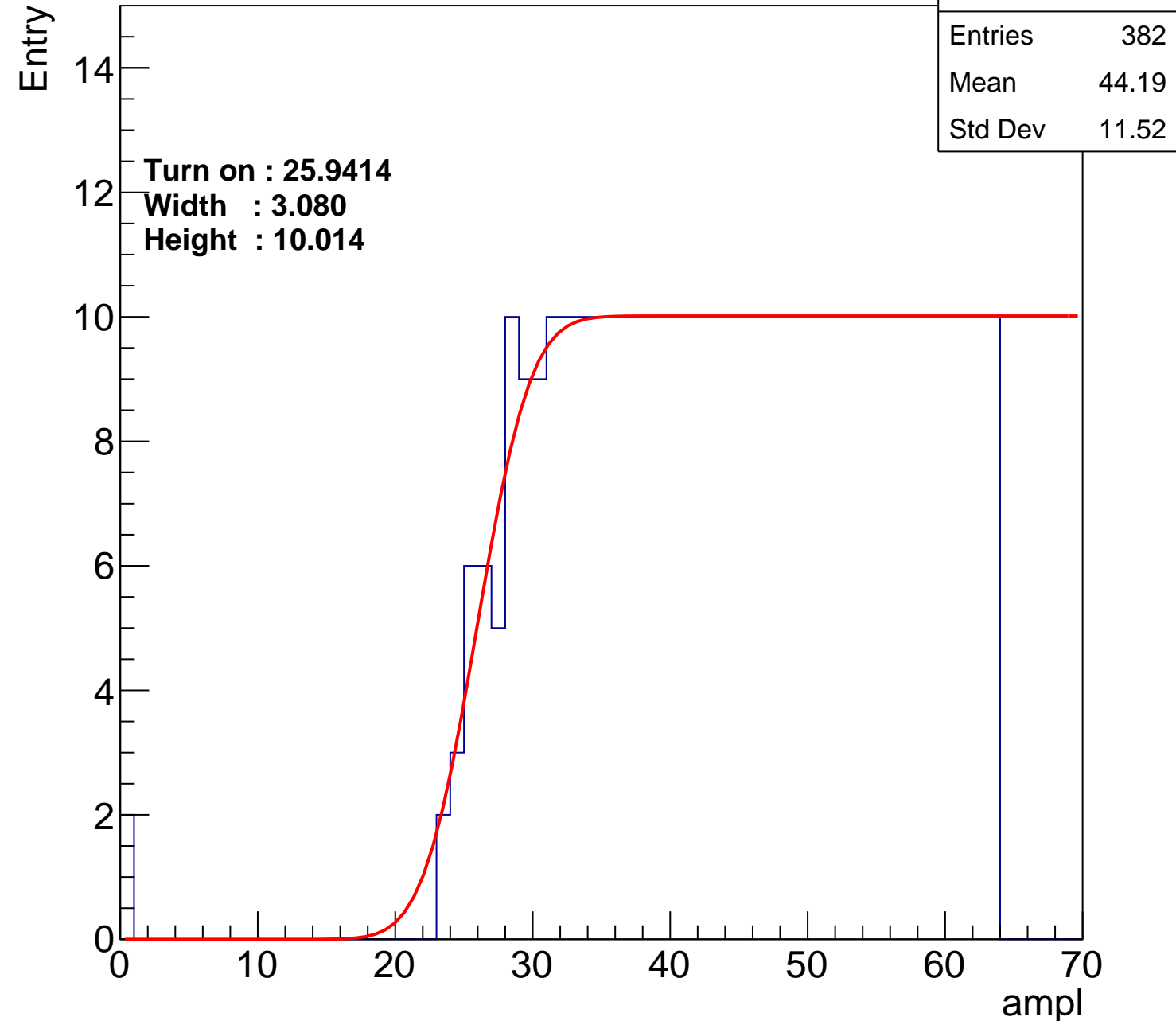
Width : 3.080

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch34

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.27 |
| Std Dev | 11.87 |

Turn on : 23.6965

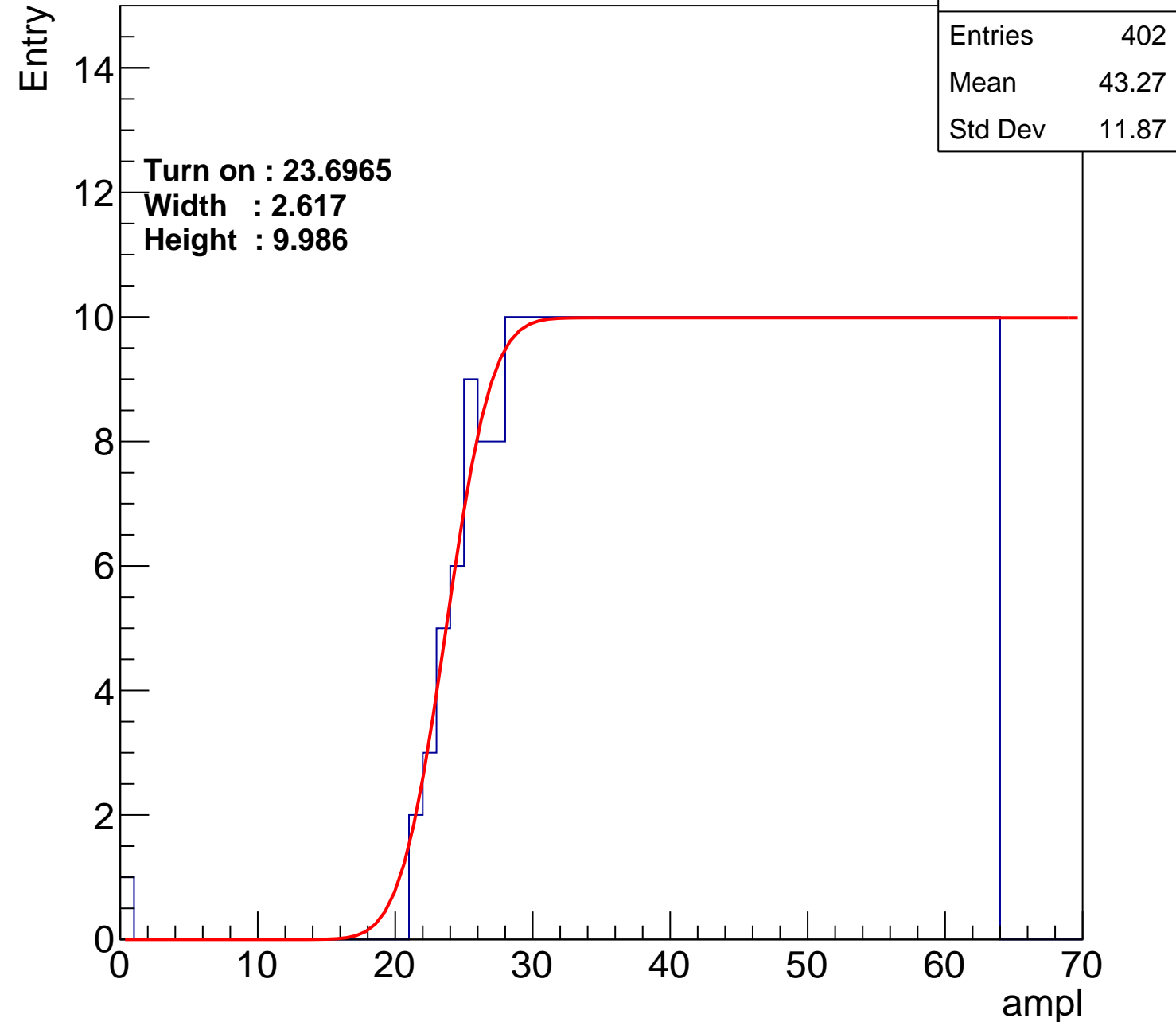
Width : 2.617

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch35

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.36 |
| Std Dev | 11.78 |

Turn on : 27.2093

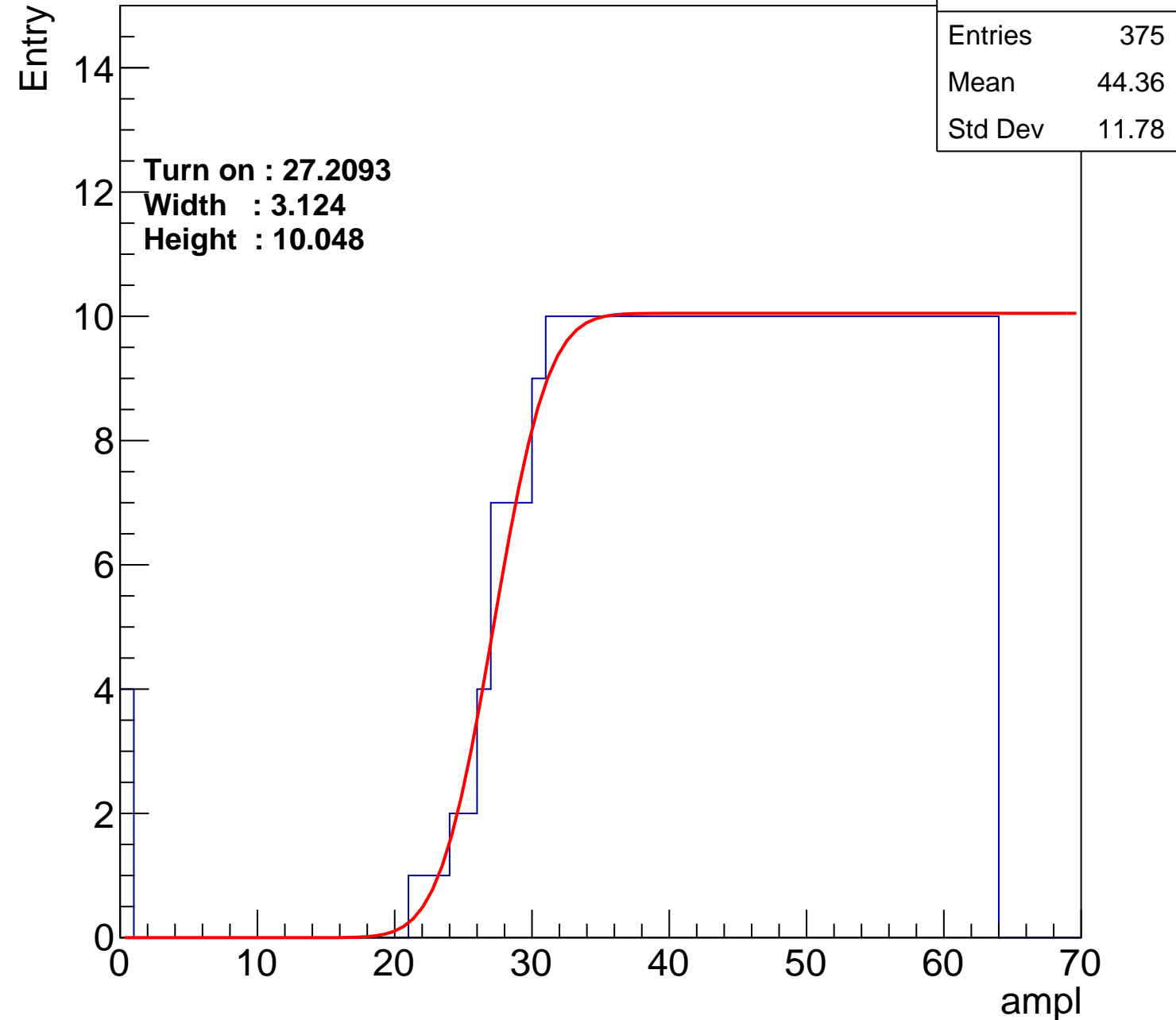
Width : 3.124

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch36

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.65 |
| Std Dev | 11.81 |

Turn on : 25.1057

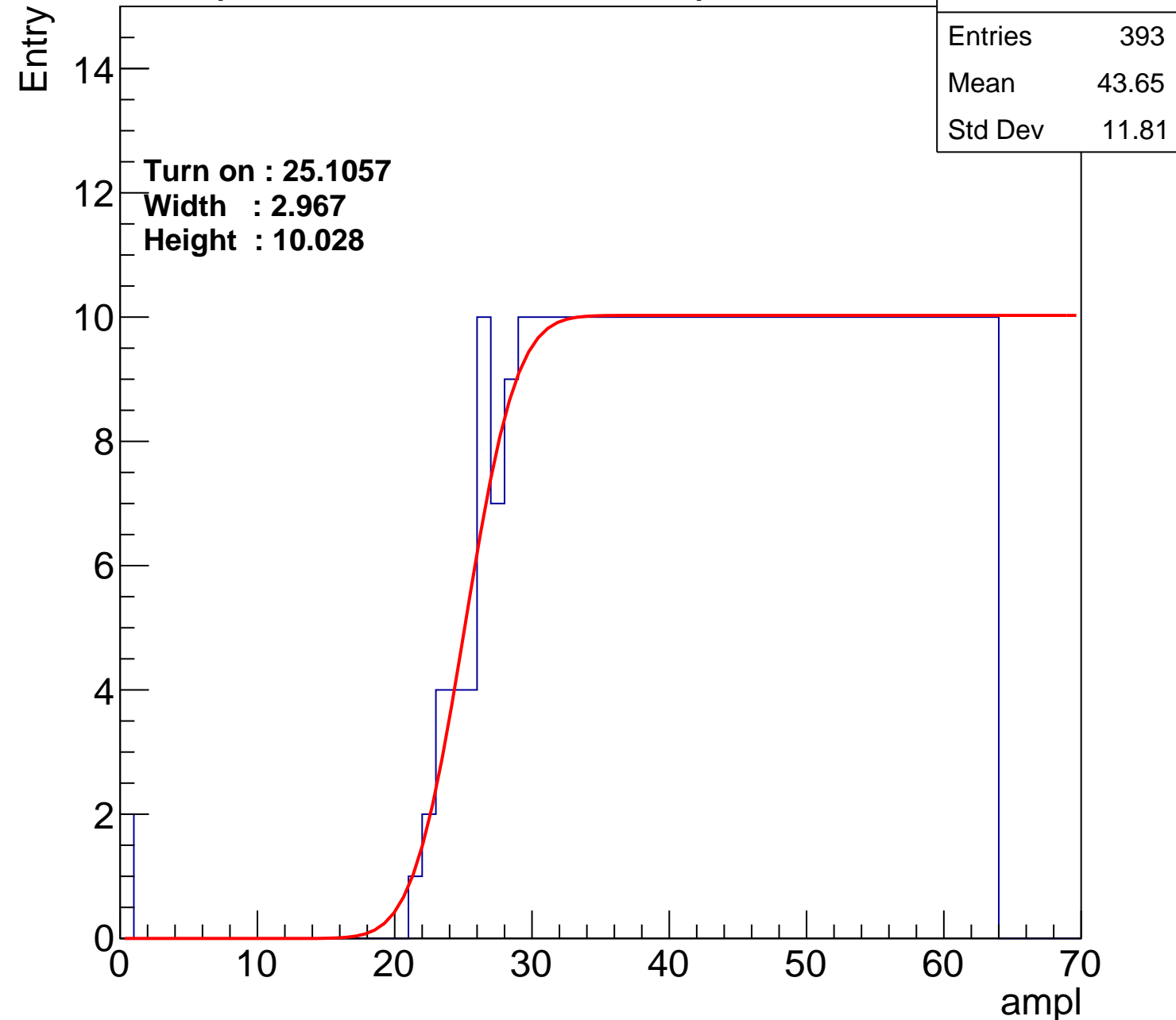
Width : 2.967

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch37

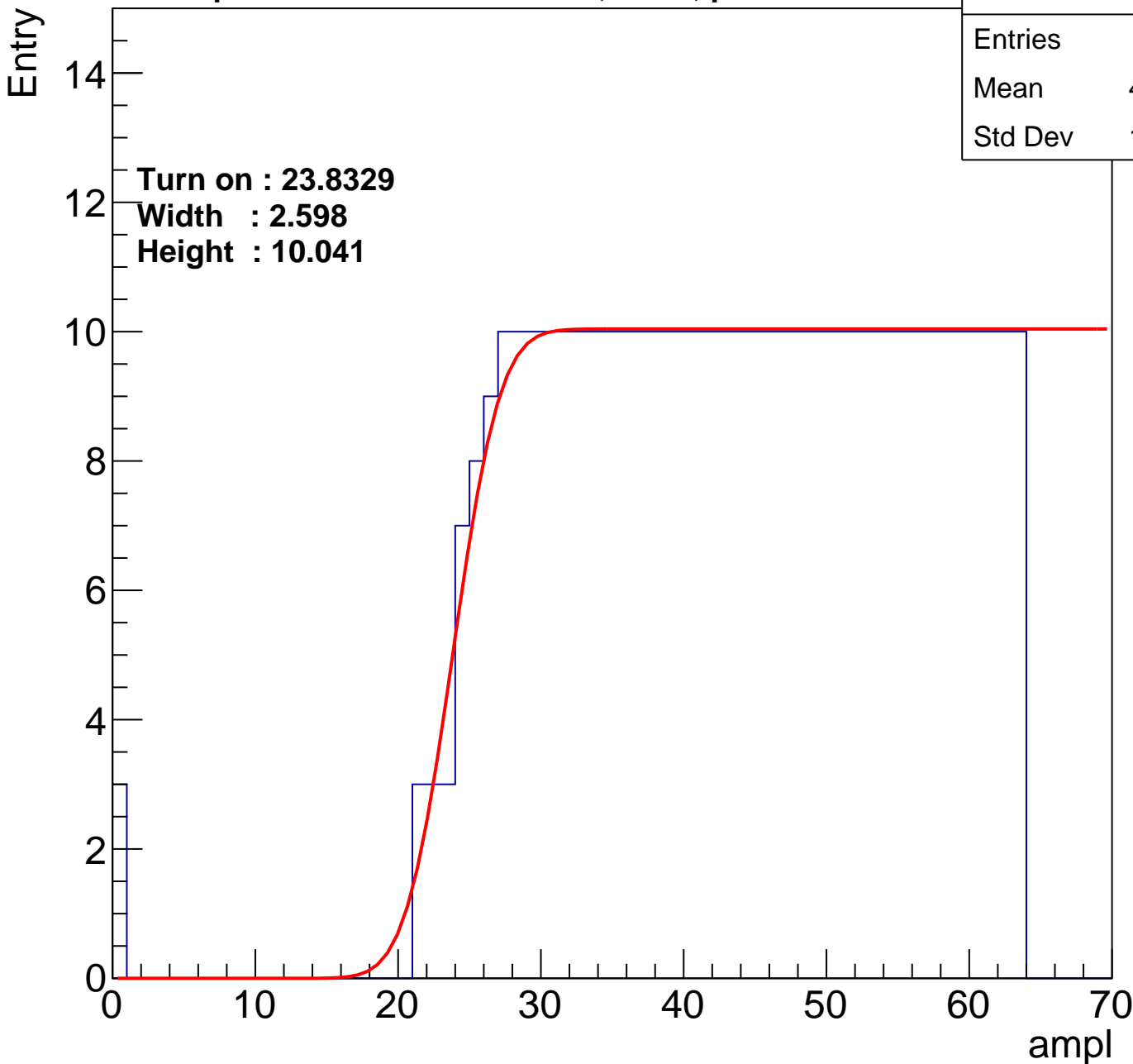
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 406 |
| Mean | 42.98 |
| Std Dev | 12.25 |

Turn on : 23.8329

Width : 2.598

Height : 10.041



B1L000S, U1-ch38

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.78 |
| Std Dev | 12.28 |

Turn on : 26.3265

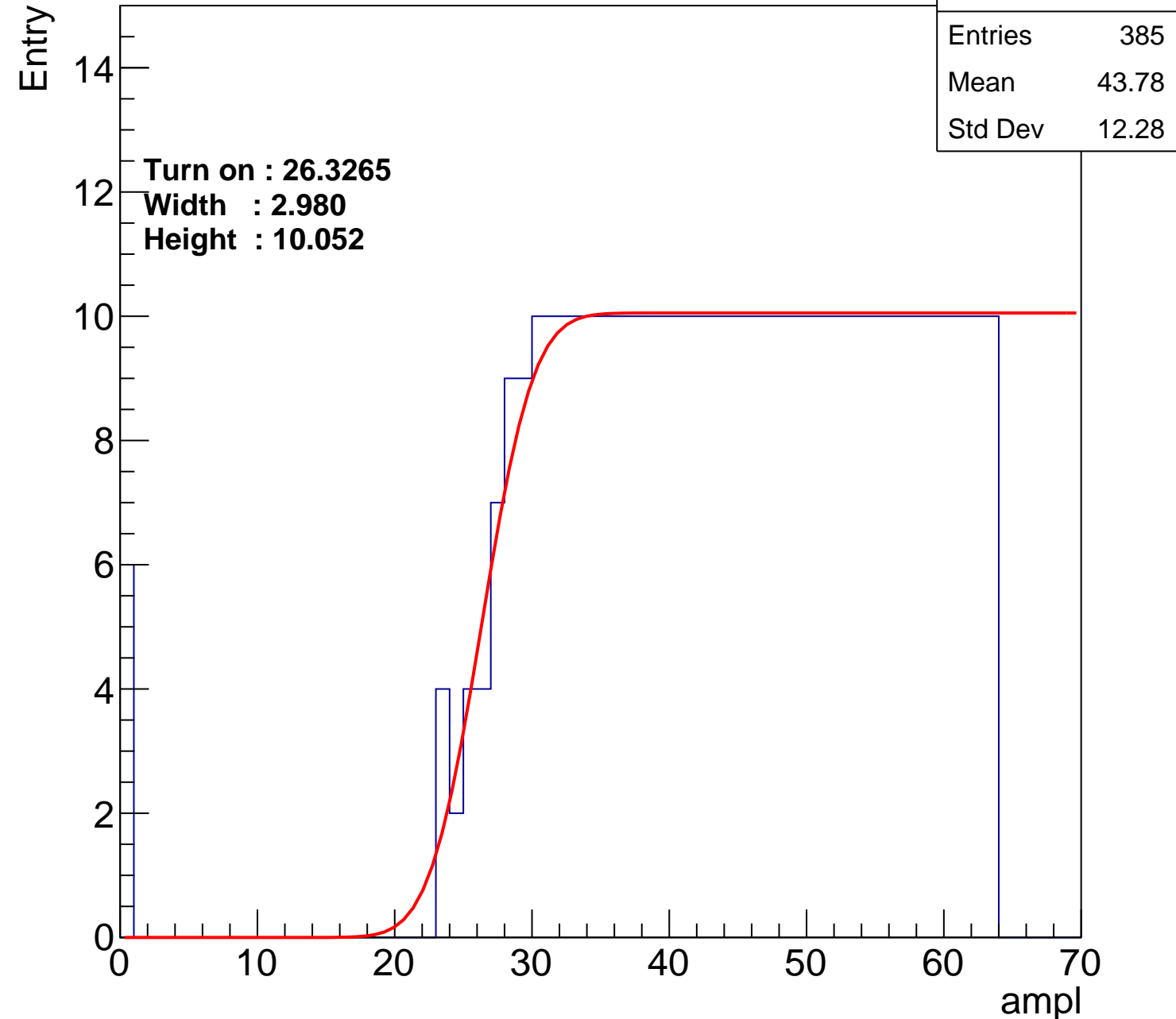
Width : 2.980

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch39

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.4 |
| Std Dev | 12.06 |

Turn on : 24.9901

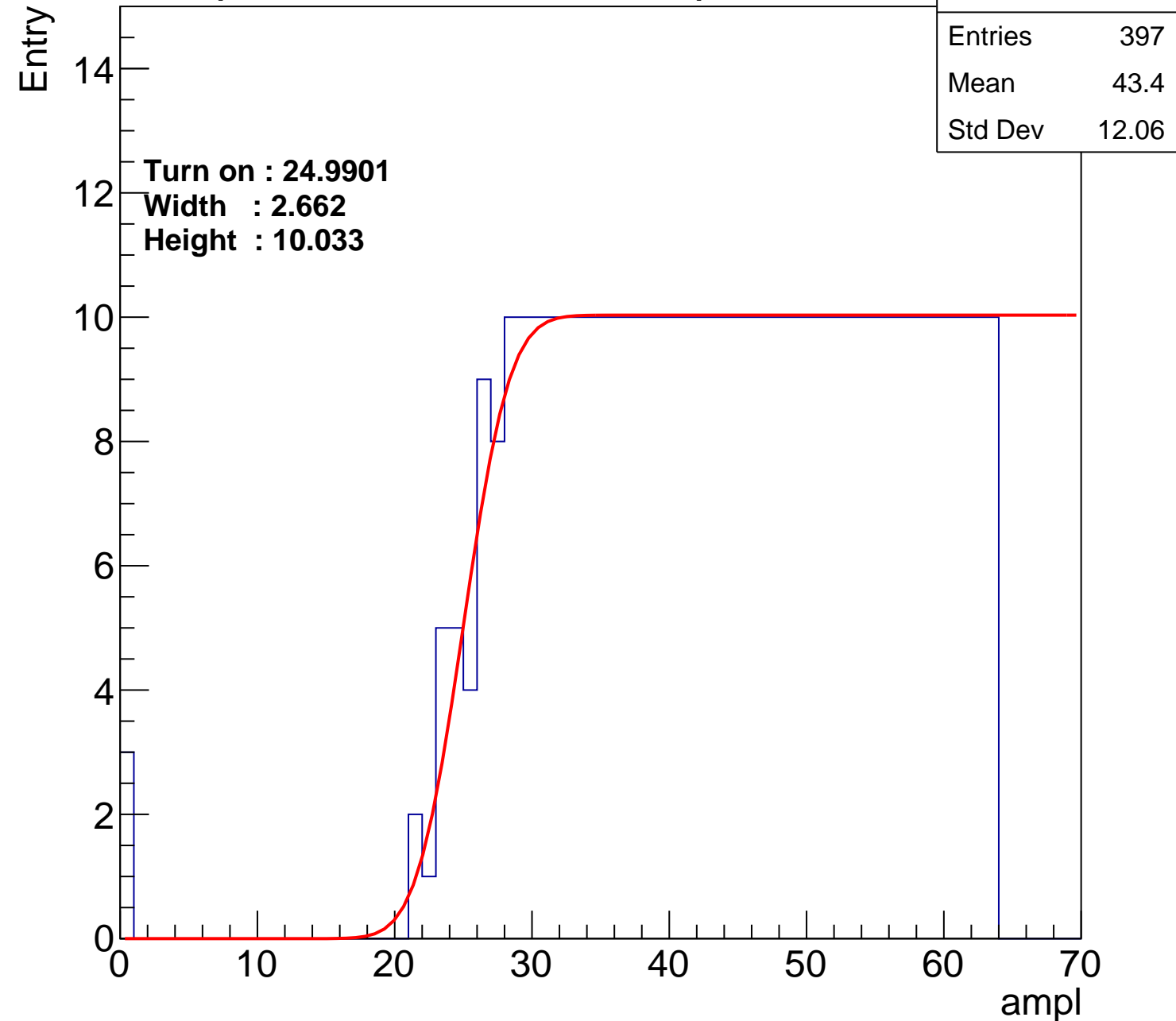
Width : 2.662

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch40

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.28 |
| Std Dev | 11.62 |

Turn on : 26.6410

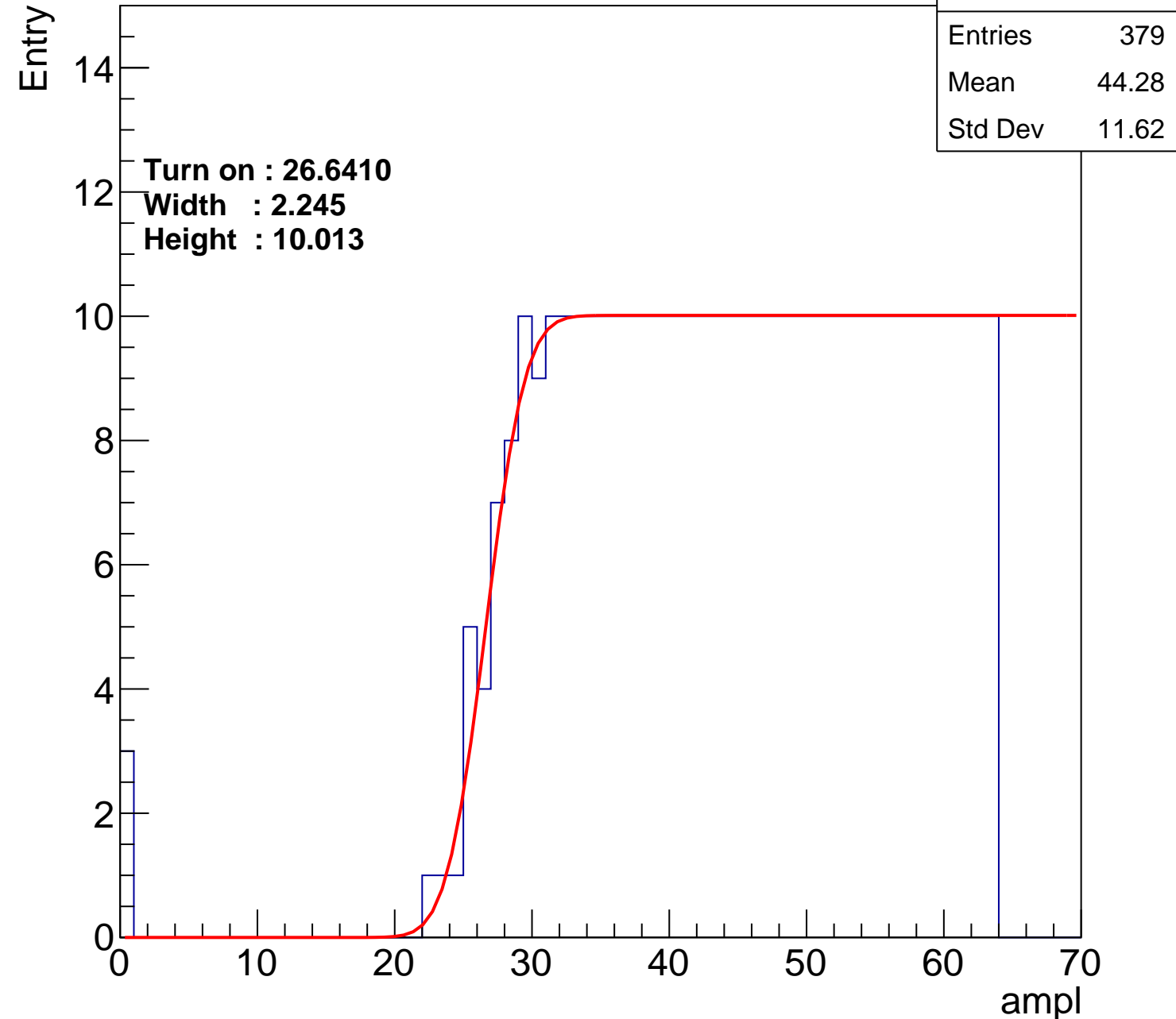
Width : 2.245

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch41

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.27 |
| Std Dev | 11.93 |

Turn on : 24.1412

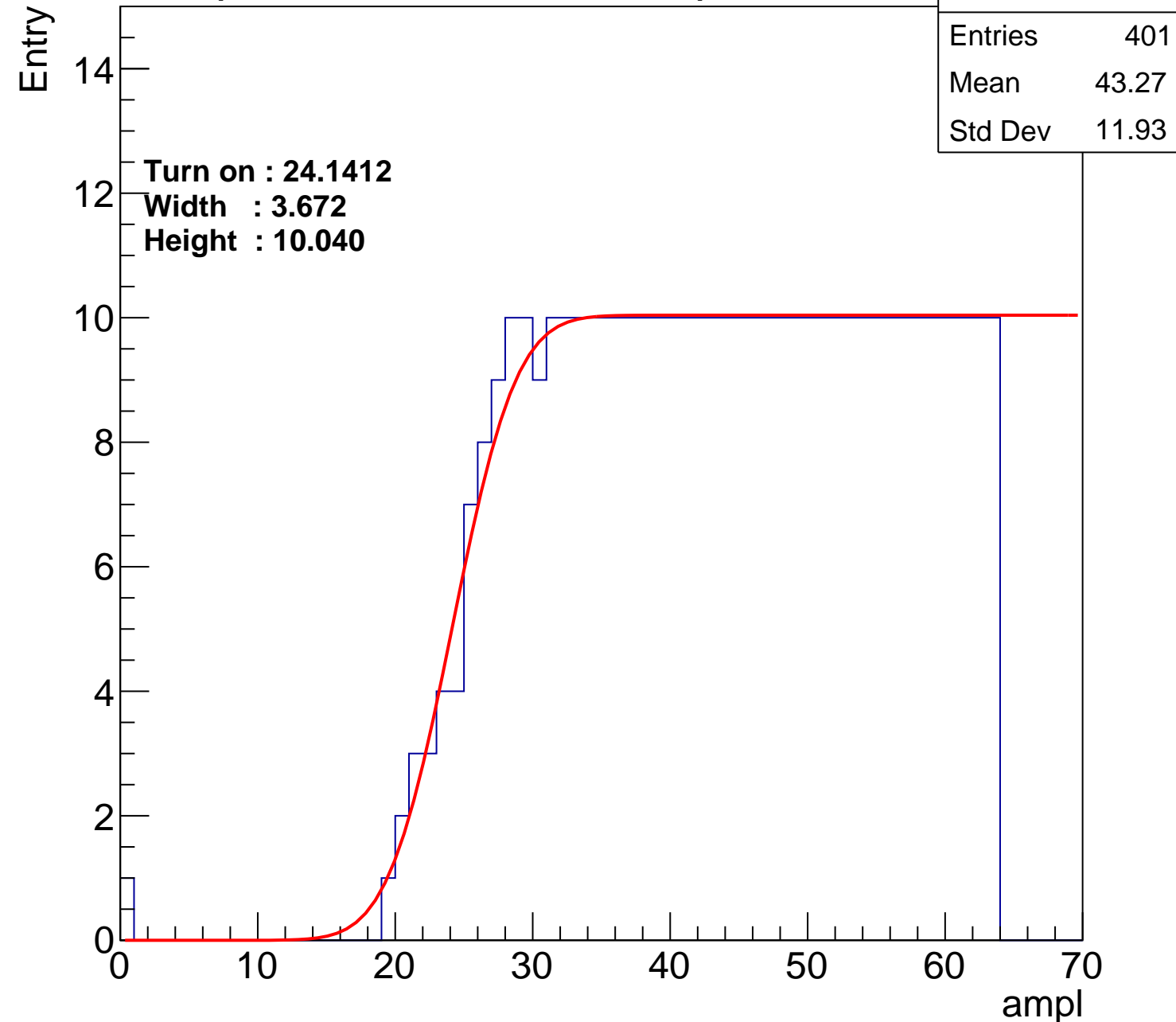
Width : 3.672

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch42

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.77 |
| Std Dev | 11.93 |

Turn on : 25.1891

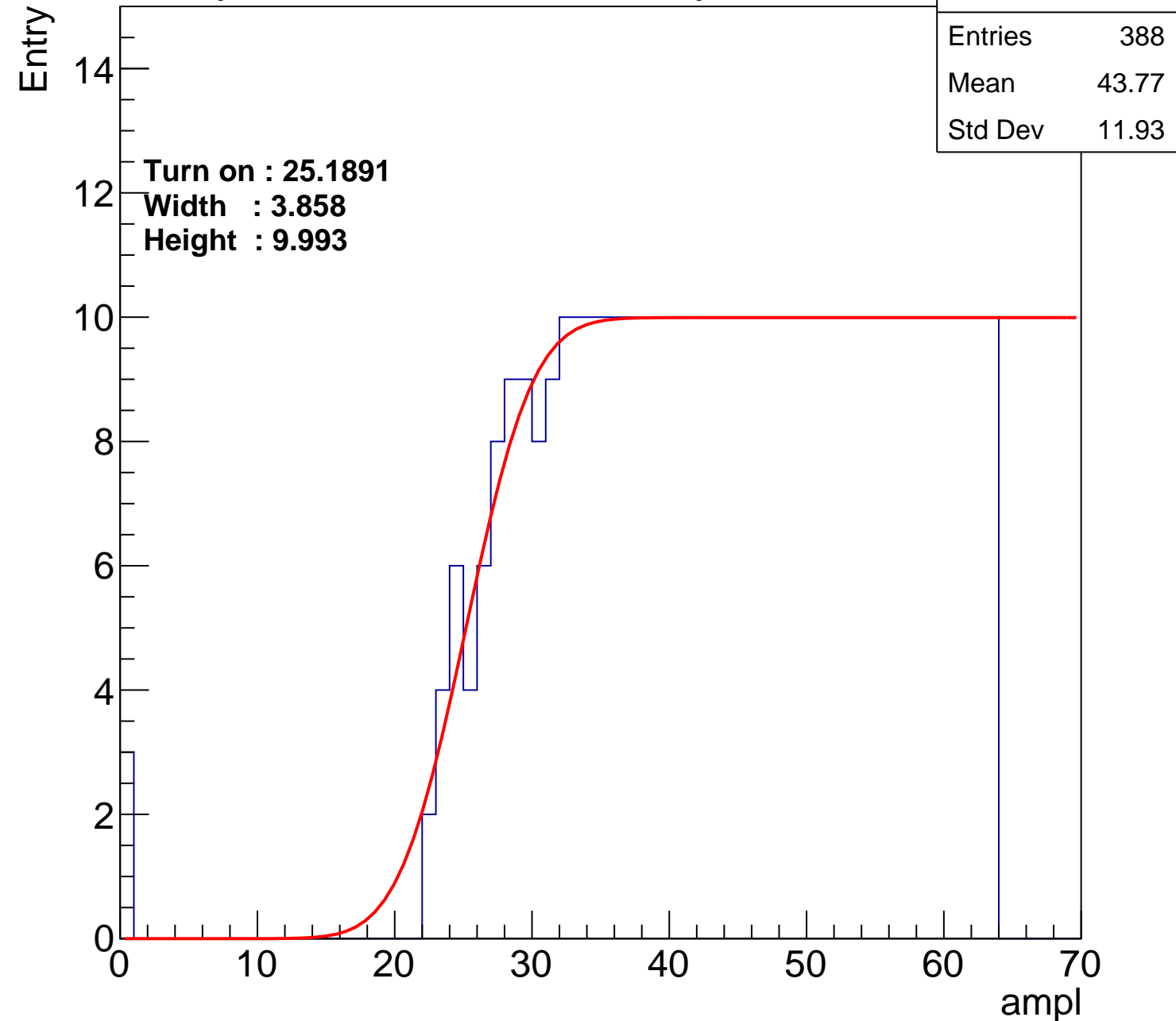
Width : 3.858

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch43

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.7 |
| Std Dev | 12.06 |

Turn on : 25.2335

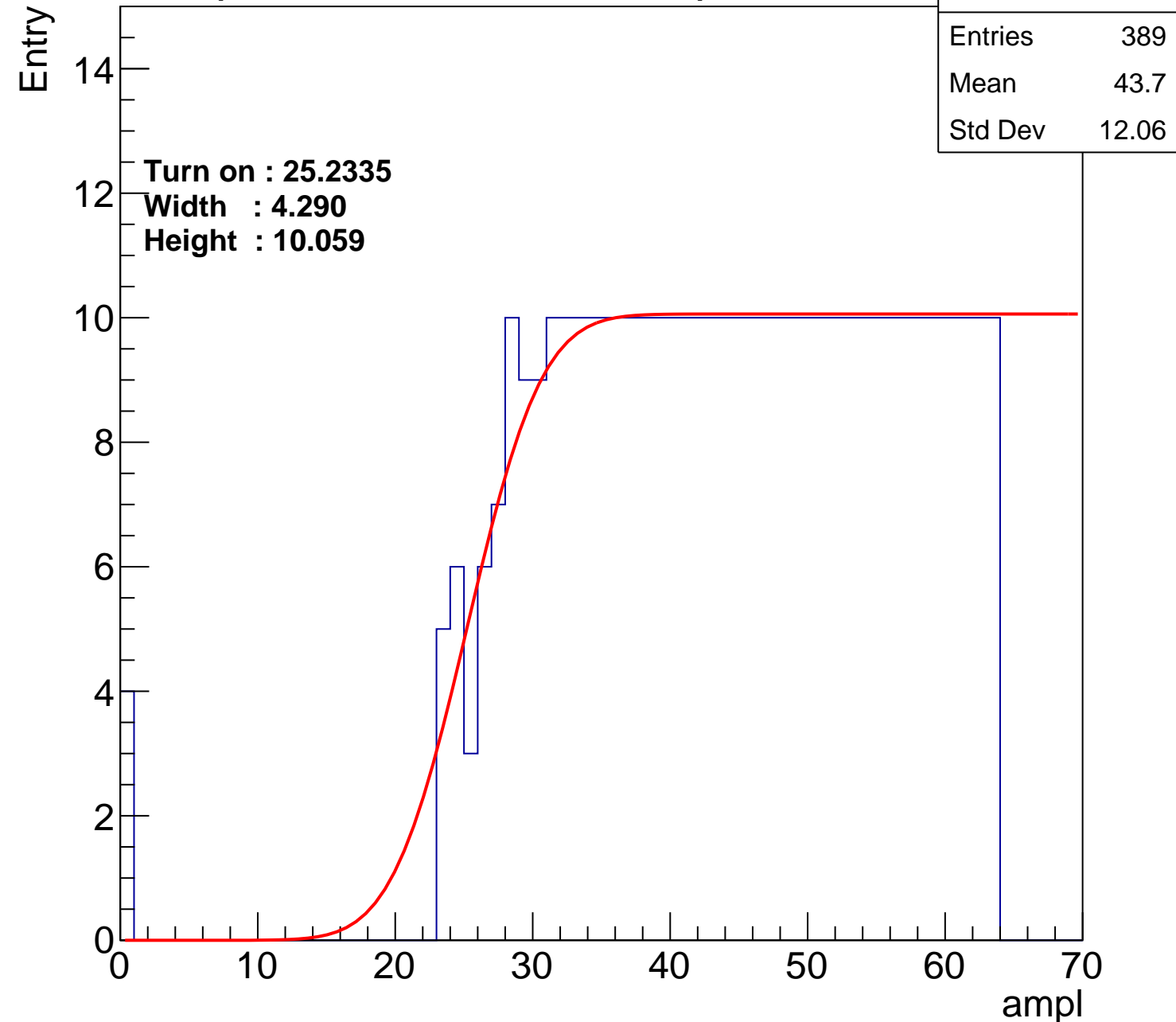
Width : 4.290

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch44

calib_packv5_042523_0143.root, FC#3, port B3

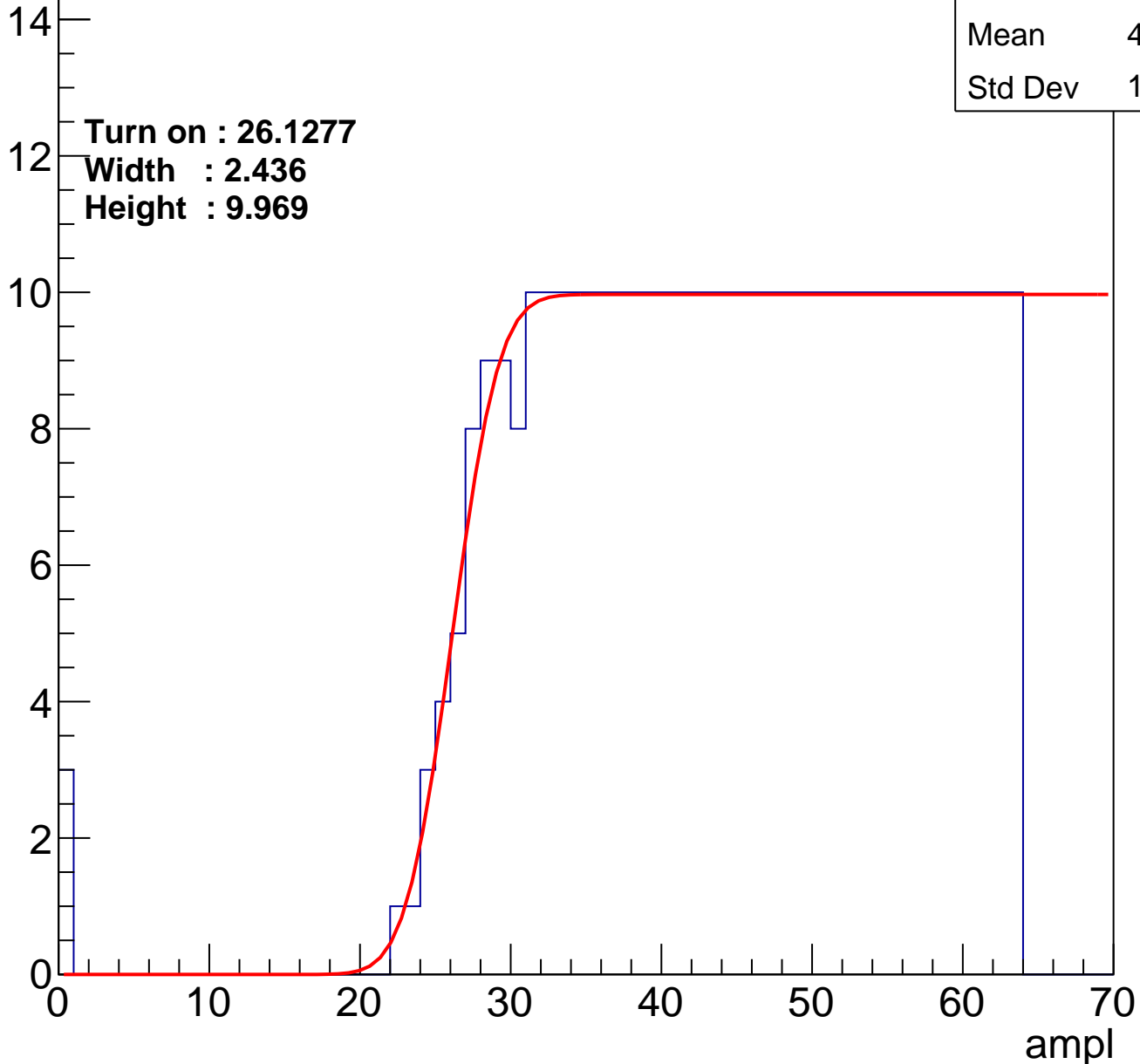
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.16 |
| Std Dev | 11.69 |

Turn on : 26.1277

Width : 2.436

Height : 9.969

Entry



B1L000S, U1-ch45

calib_packv5_042523_0143.root, FC#3, port B3

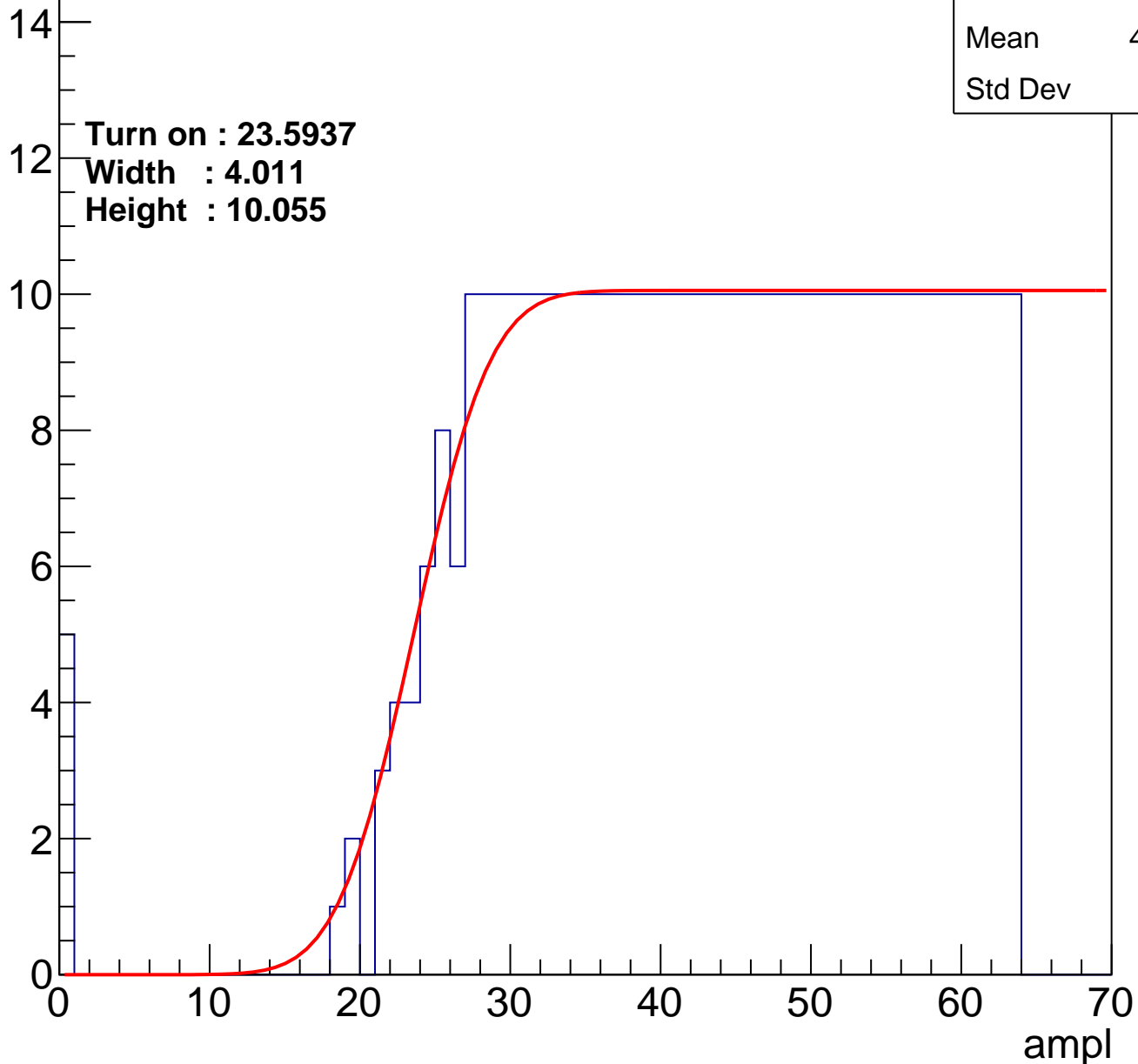
| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.66 |
| Std Dev | 12.7 |

Turn on : 23.5937

Width : 4.011

Height : 10.055

Entry



B1L000S, U1-ch46

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.11 |
| Std Dev | 11.55 |

Turn on : 25.7225

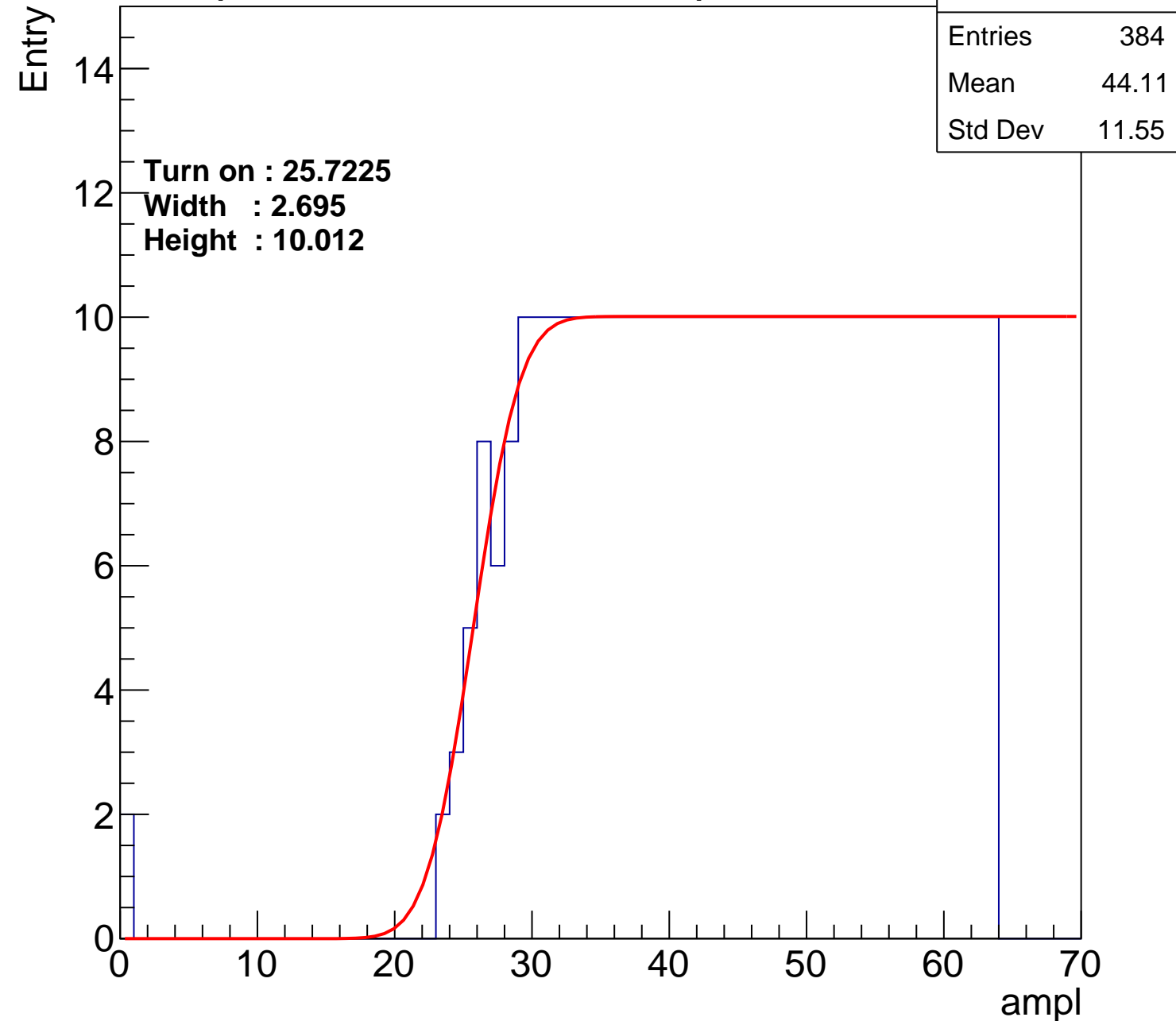
Width : 2.695

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch47

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.61 |
| Std Dev | 12.62 |

Turn on : 26.1728

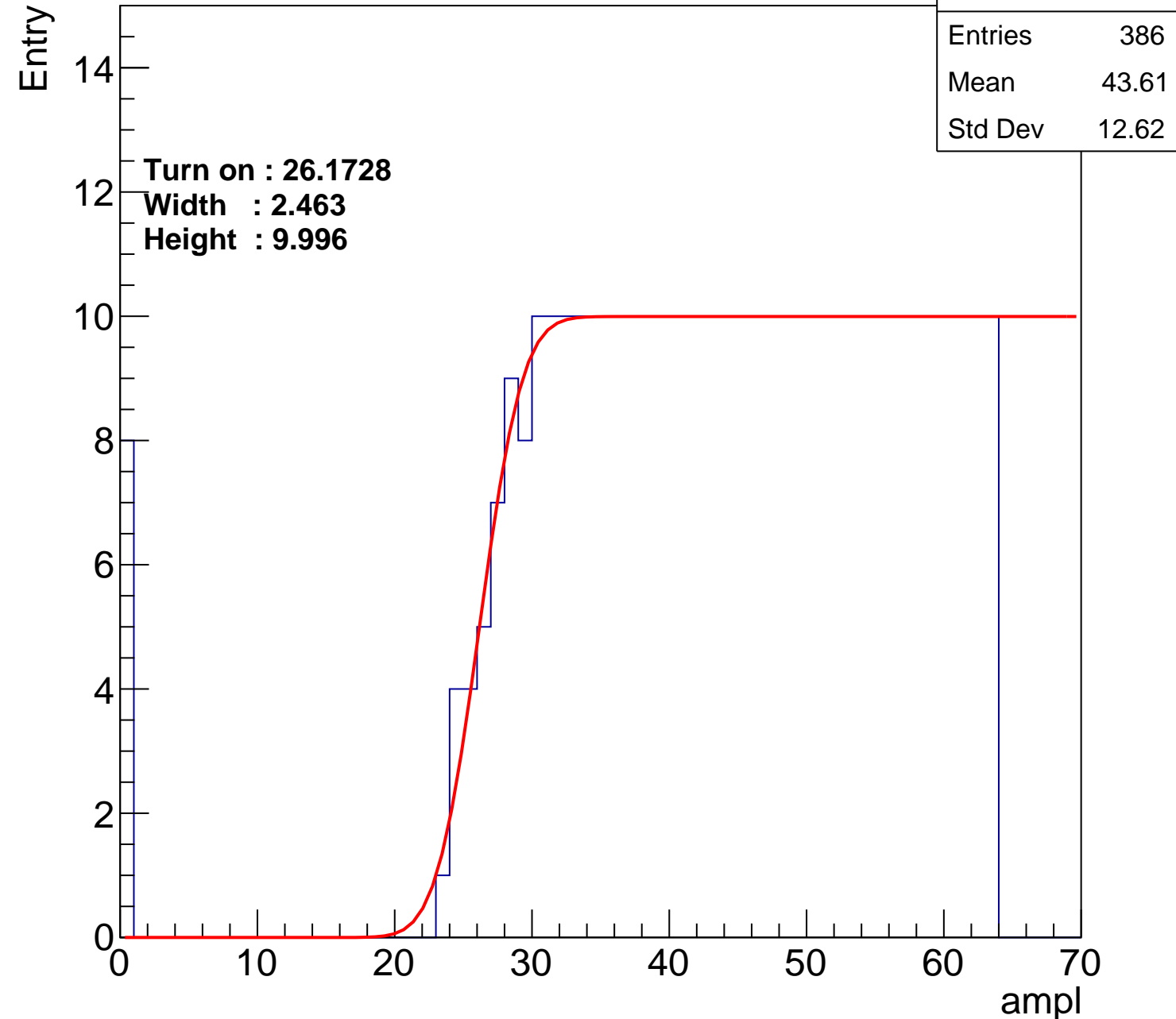
Width : 2.463

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch48

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.44 |
| Std Dev | 11.39 |

Turn on : 26.8813

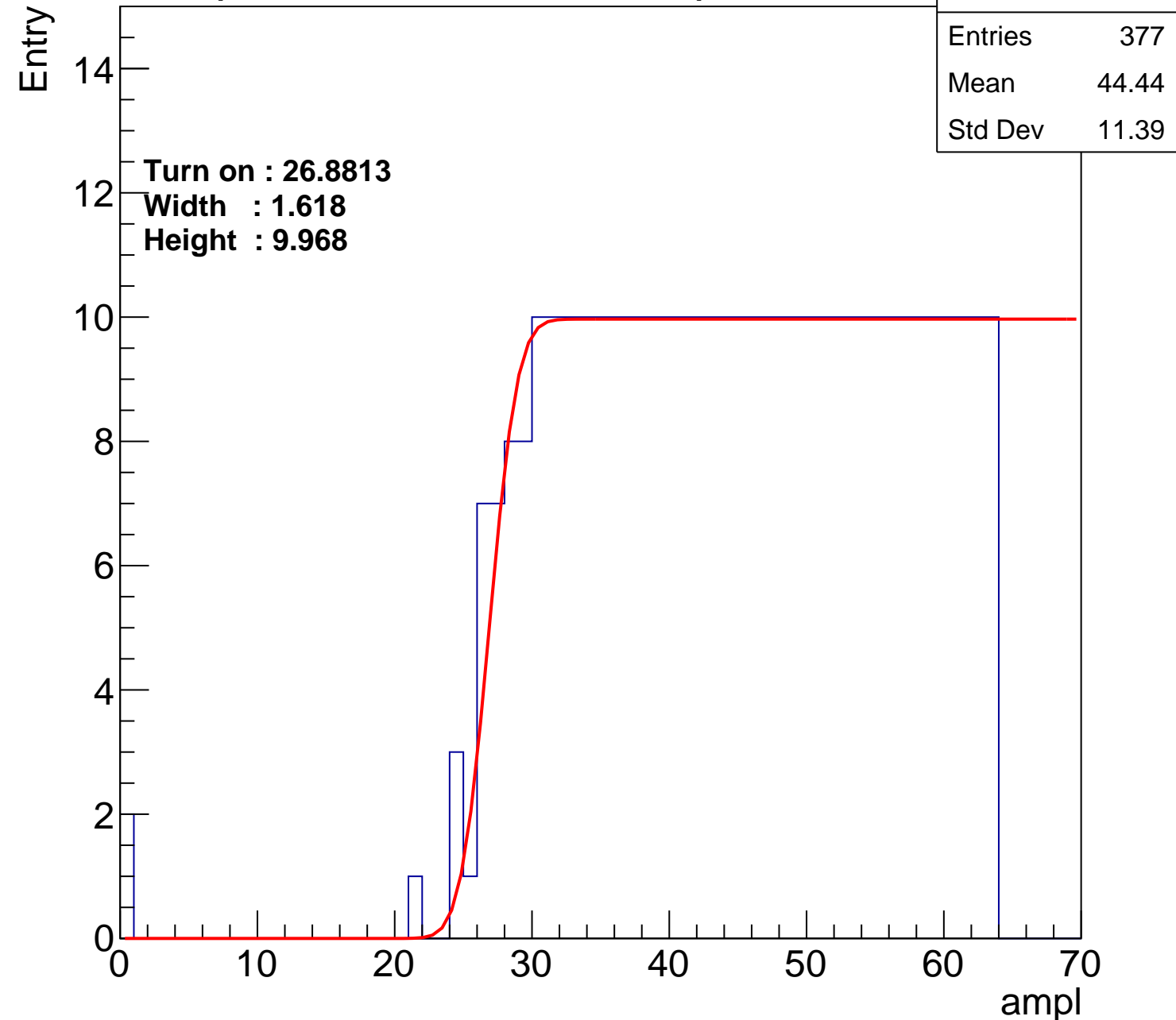
Width : 1.618

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch49

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.96 |
| Std Dev | 11.8 |

Turn on : 25.6737

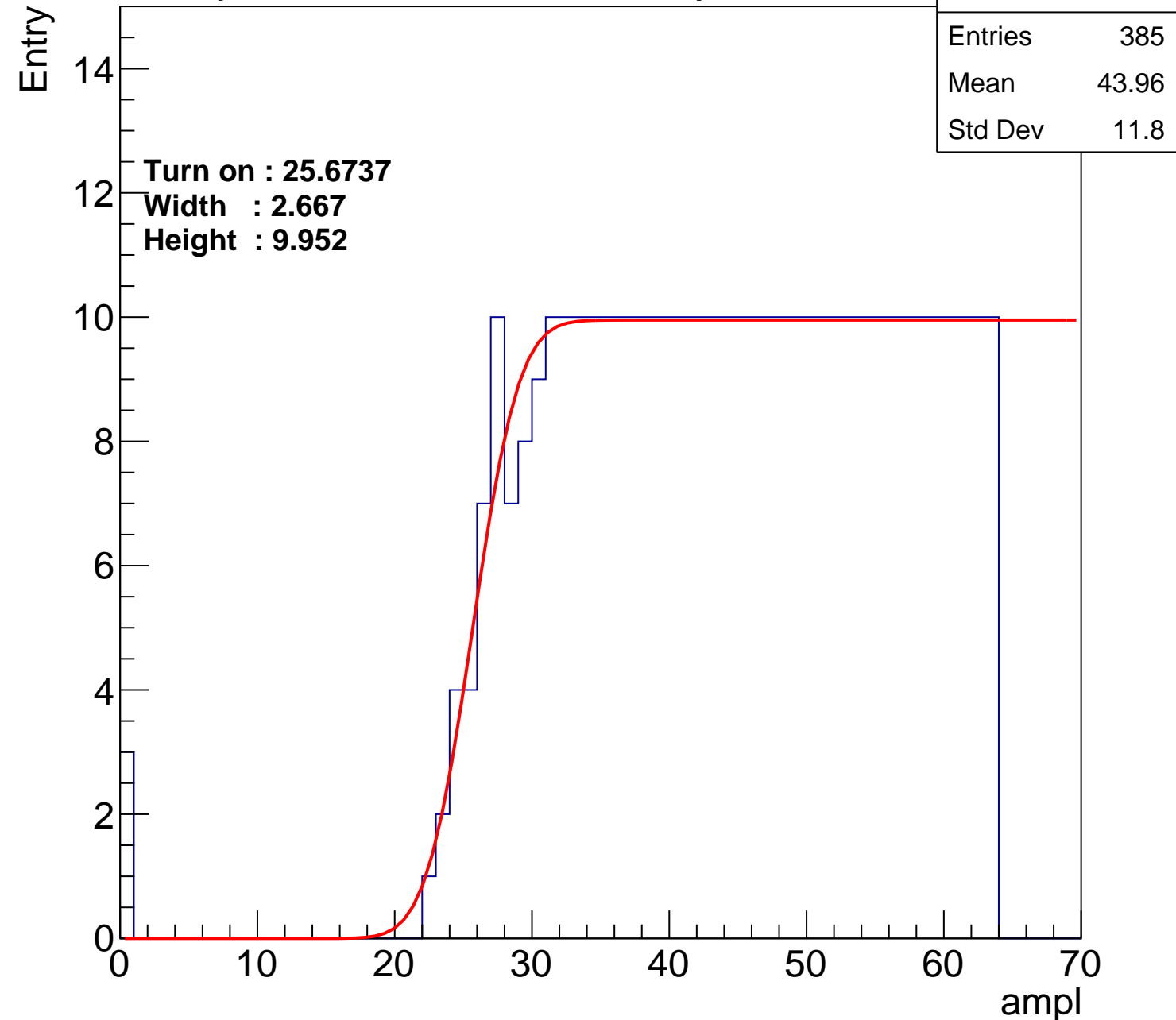
Width : 2.667

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch50

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.55 |
| Std Dev | 12.03 |

Turn on : 25.0890

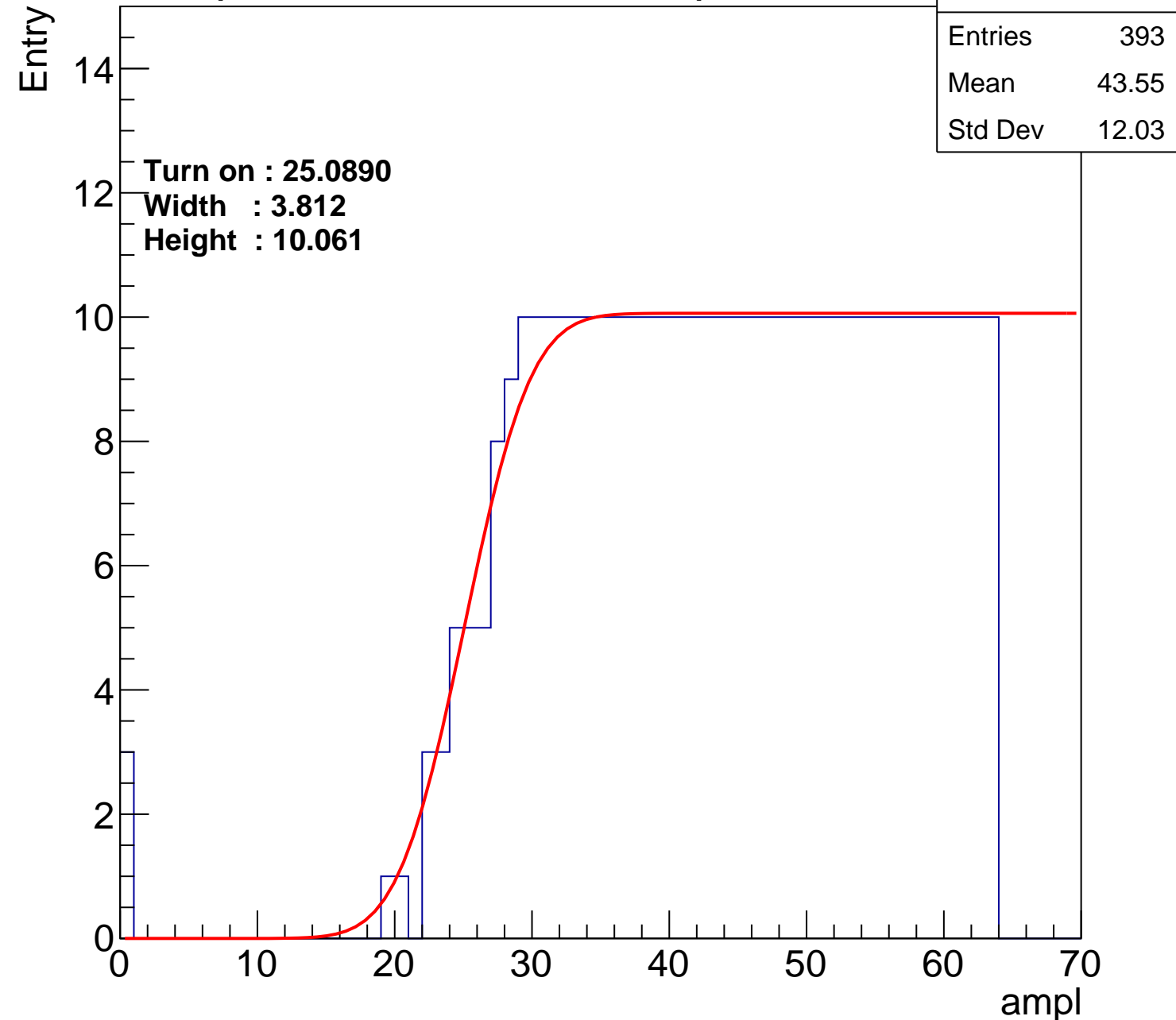
Width : 3.812

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch51

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.43 |
| Std Dev | 11.83 |

Turn on : 24.2733

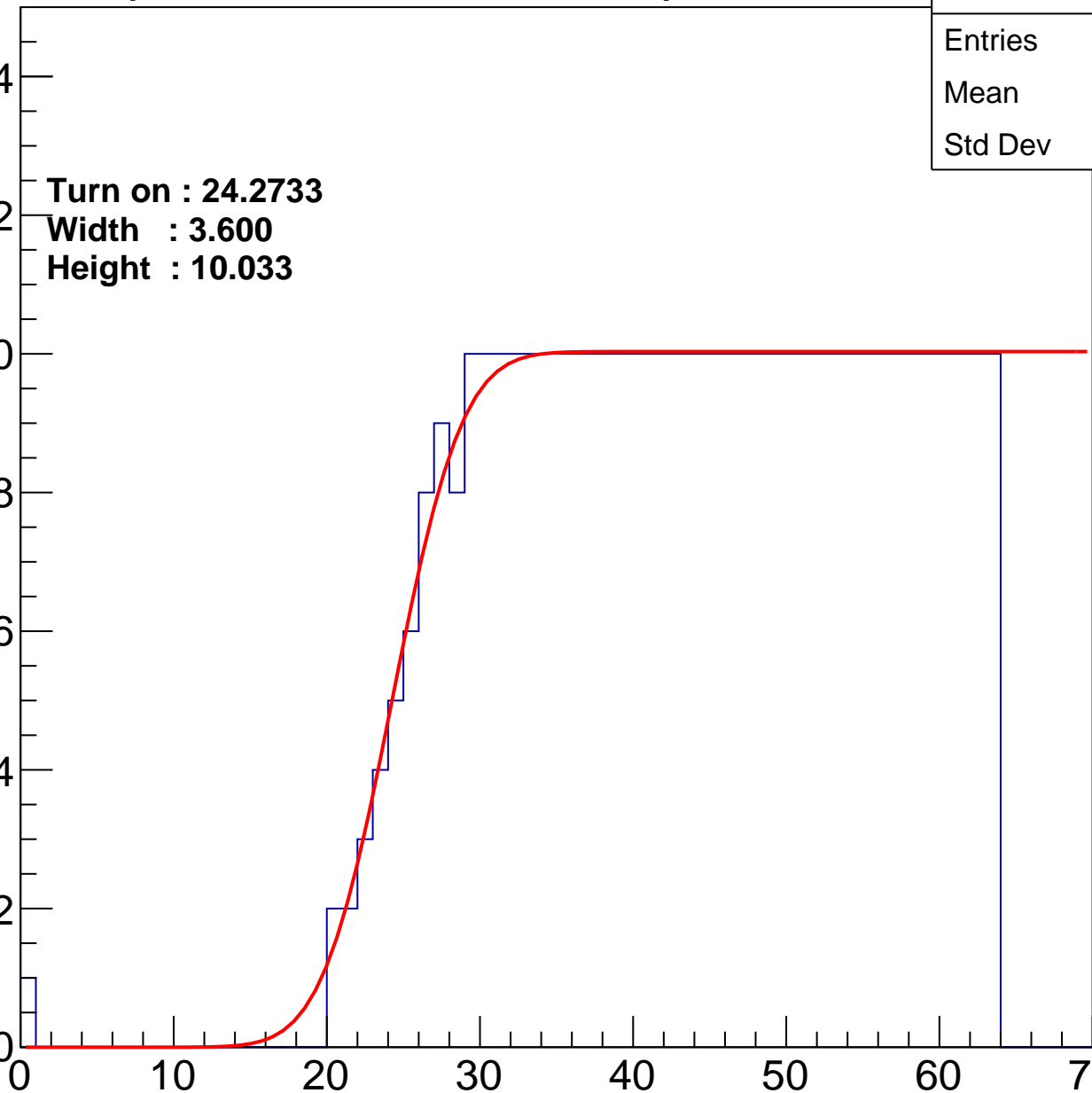
Width : 3.600

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch52

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 407 |
| Mean | 42.85 |
| Std Dev | 12.46 |

Turn on : 24.0519

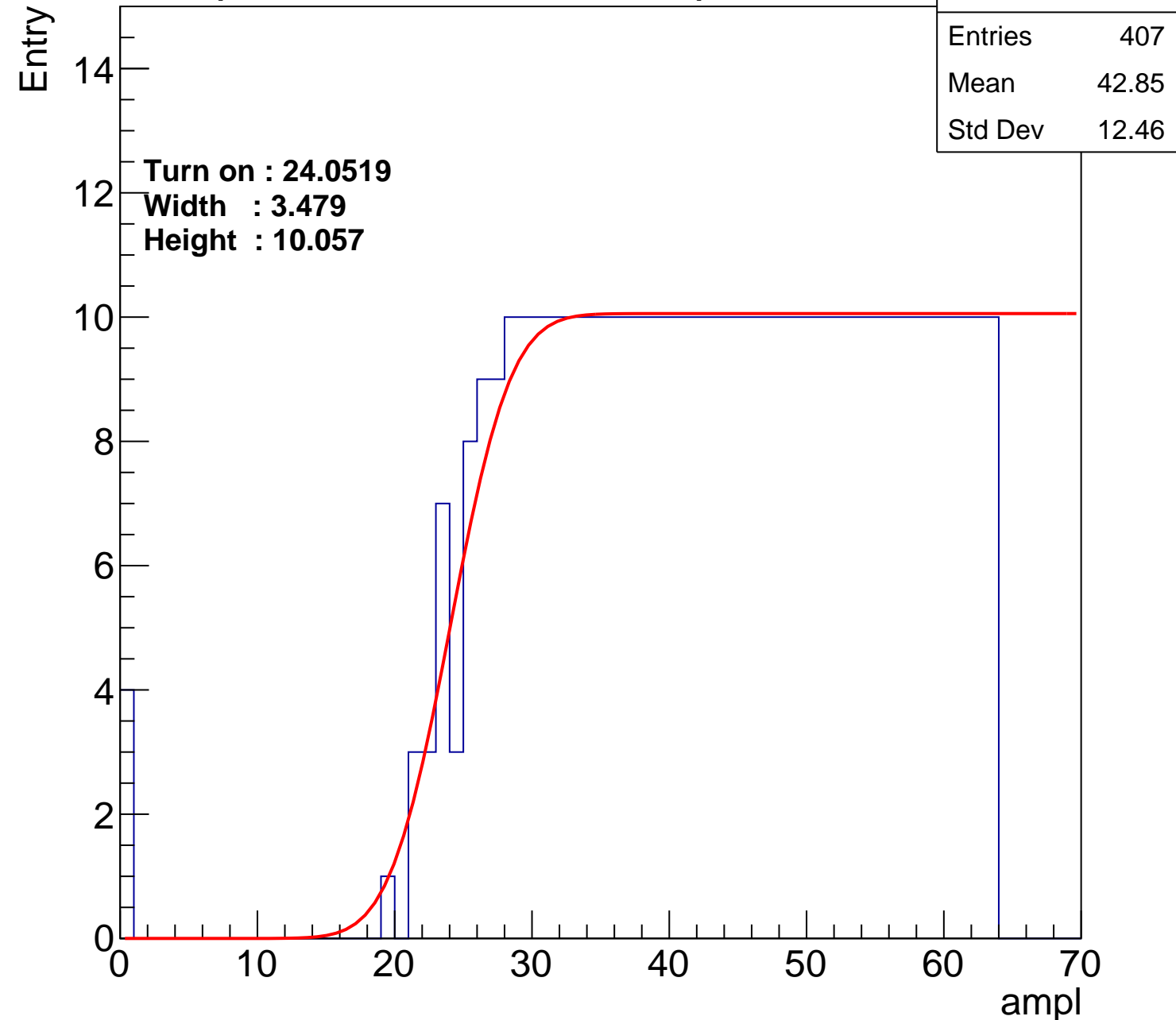
Width : 3.479

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch53

calib_packv5_042523_0143.root, FC#3, port B3

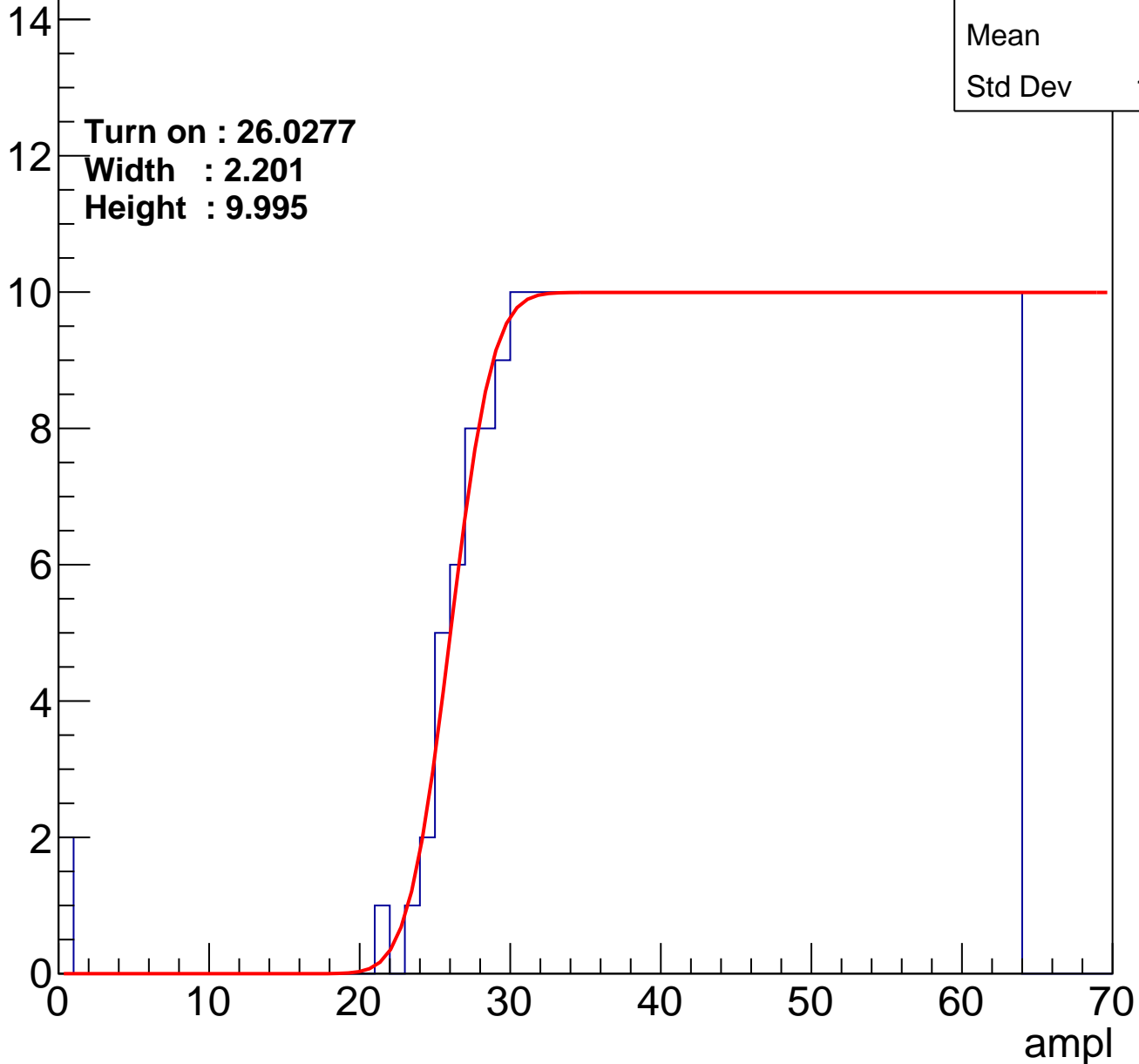
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.2 |
| Std Dev | 11.51 |

Turn on : 26.0277

Width : 2.201

Height : 9.995

Entry



B1L000S, U1-ch54

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 408 |
| Mean | 42.83 |
| Std Dev | 12.44 |

Turn on : 23.3104

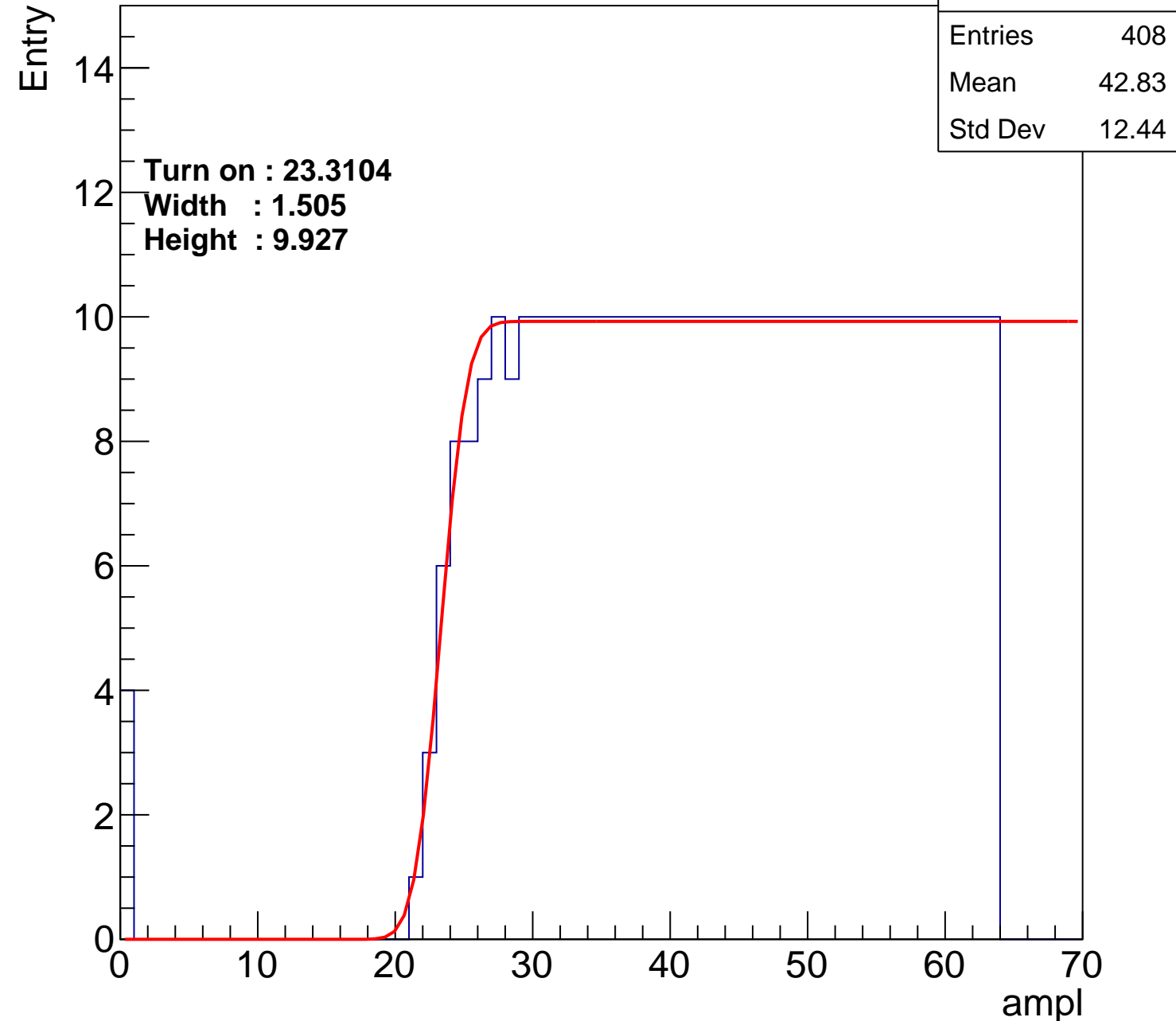
Width : 1.505

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch55

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.46 |
| Std Dev | 11.68 |

Turn on : 27.3016

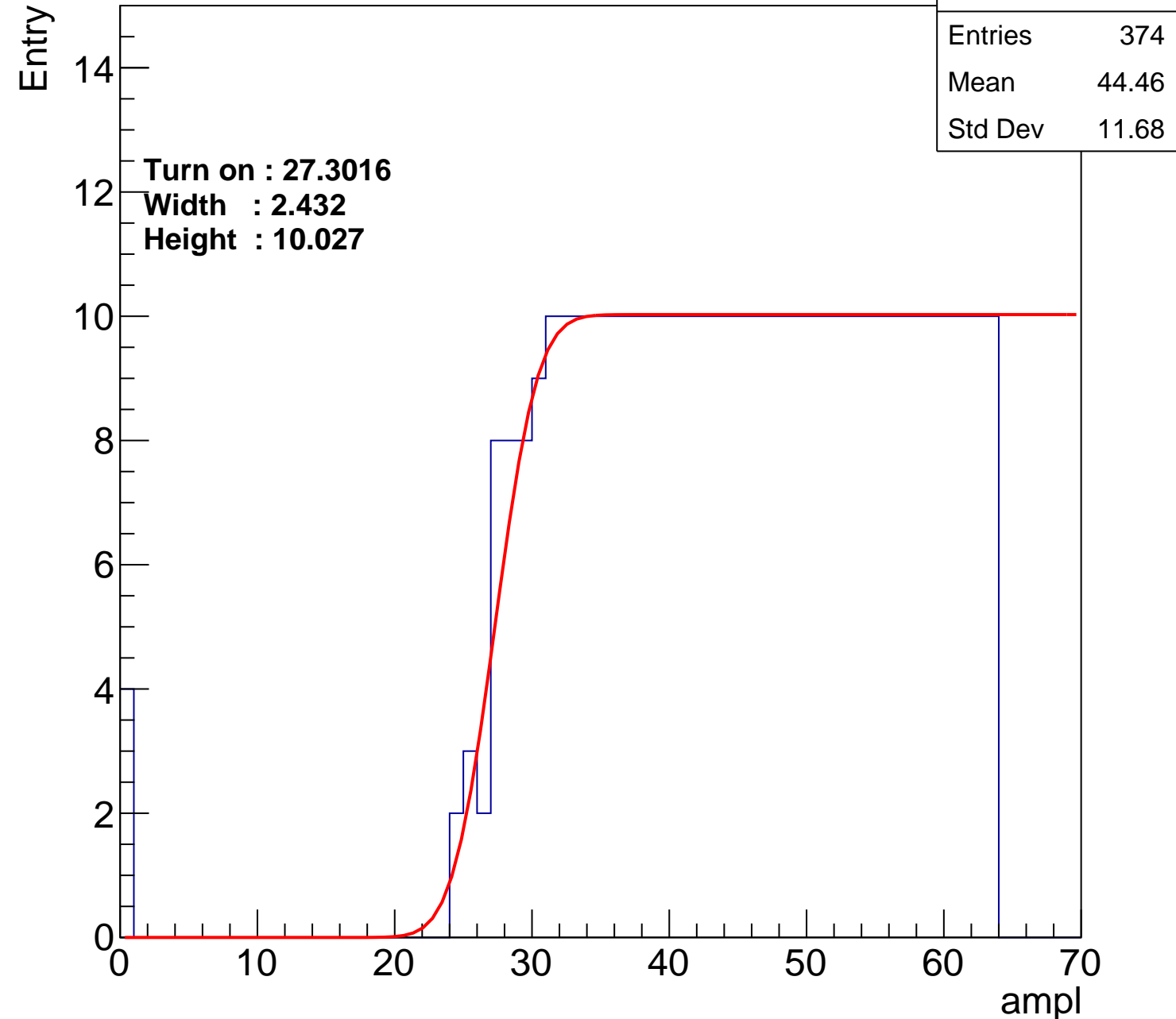
Width : 2.432

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch56

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.76 |
| Std Dev | 11.78 |

Turn on : 24.9185

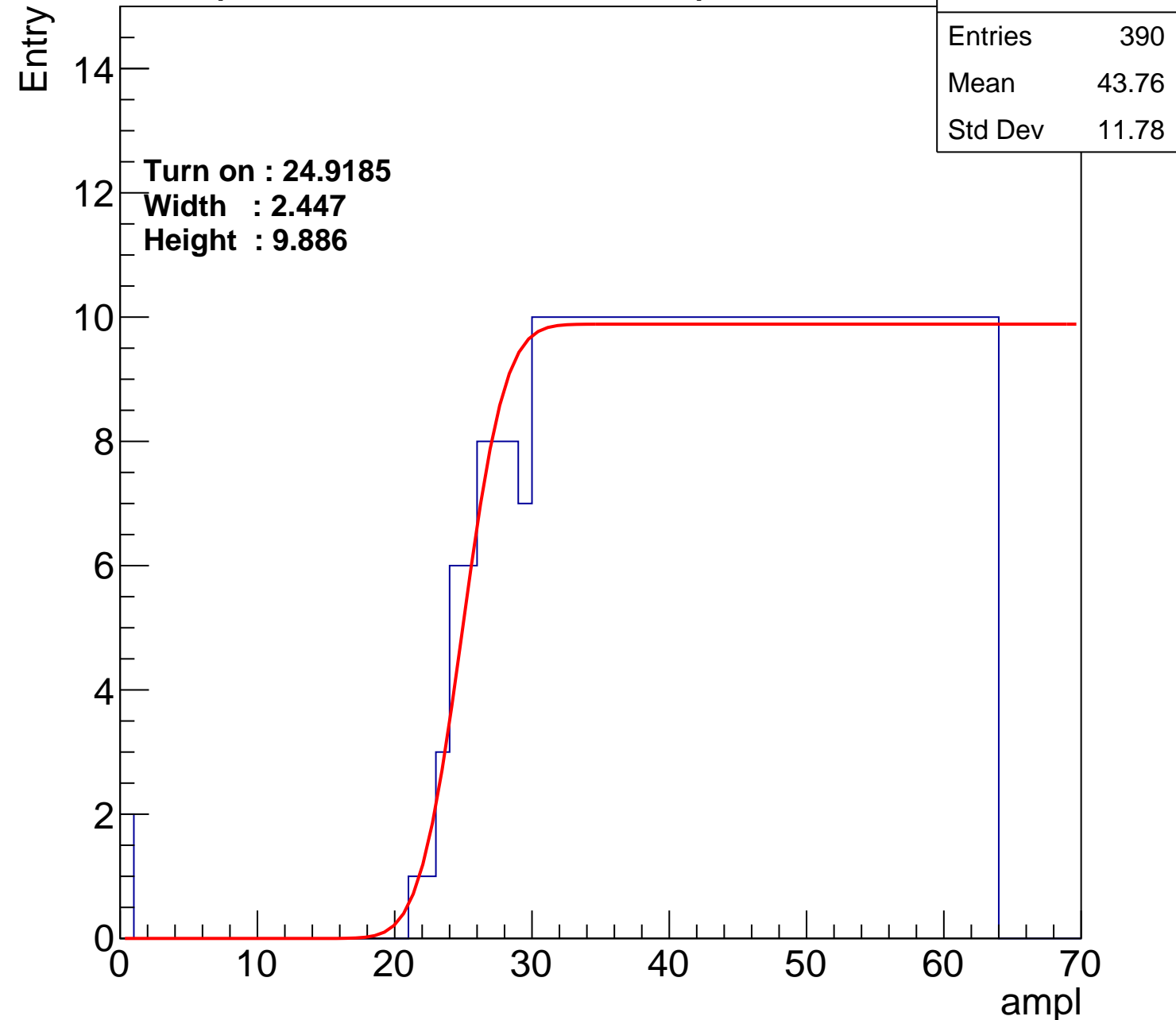
Width : 2.447

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch57

calib_packv5_042523_0143.root, FC#3, port B3

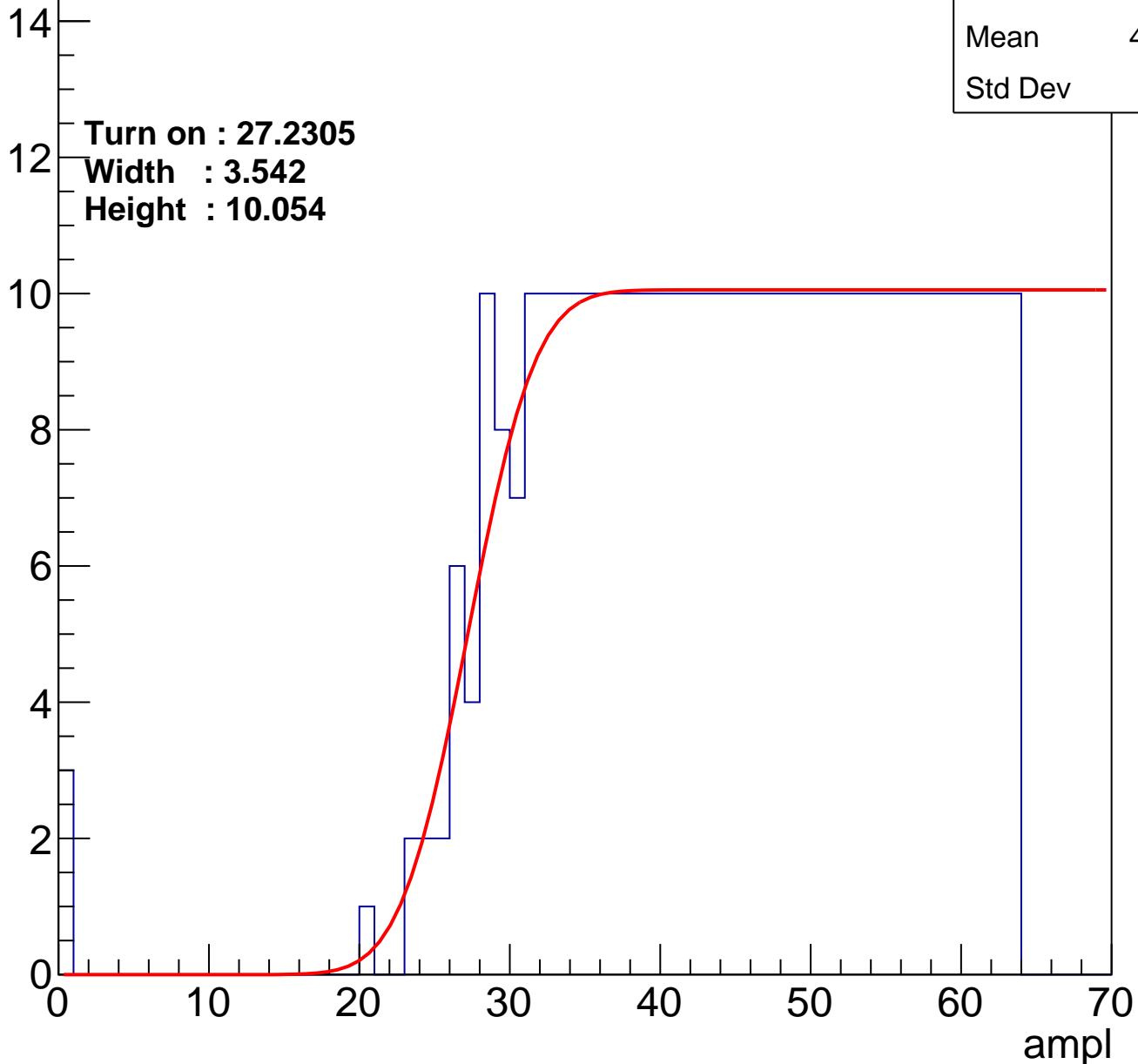
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.43 |
| Std Dev | 11.6 |

Turn on : 27.2305

Width : 3.542

Height : 10.054

Entry



B1L000S, U1-ch58

calib_packv5_042523_0143.root, FC#3, port B3

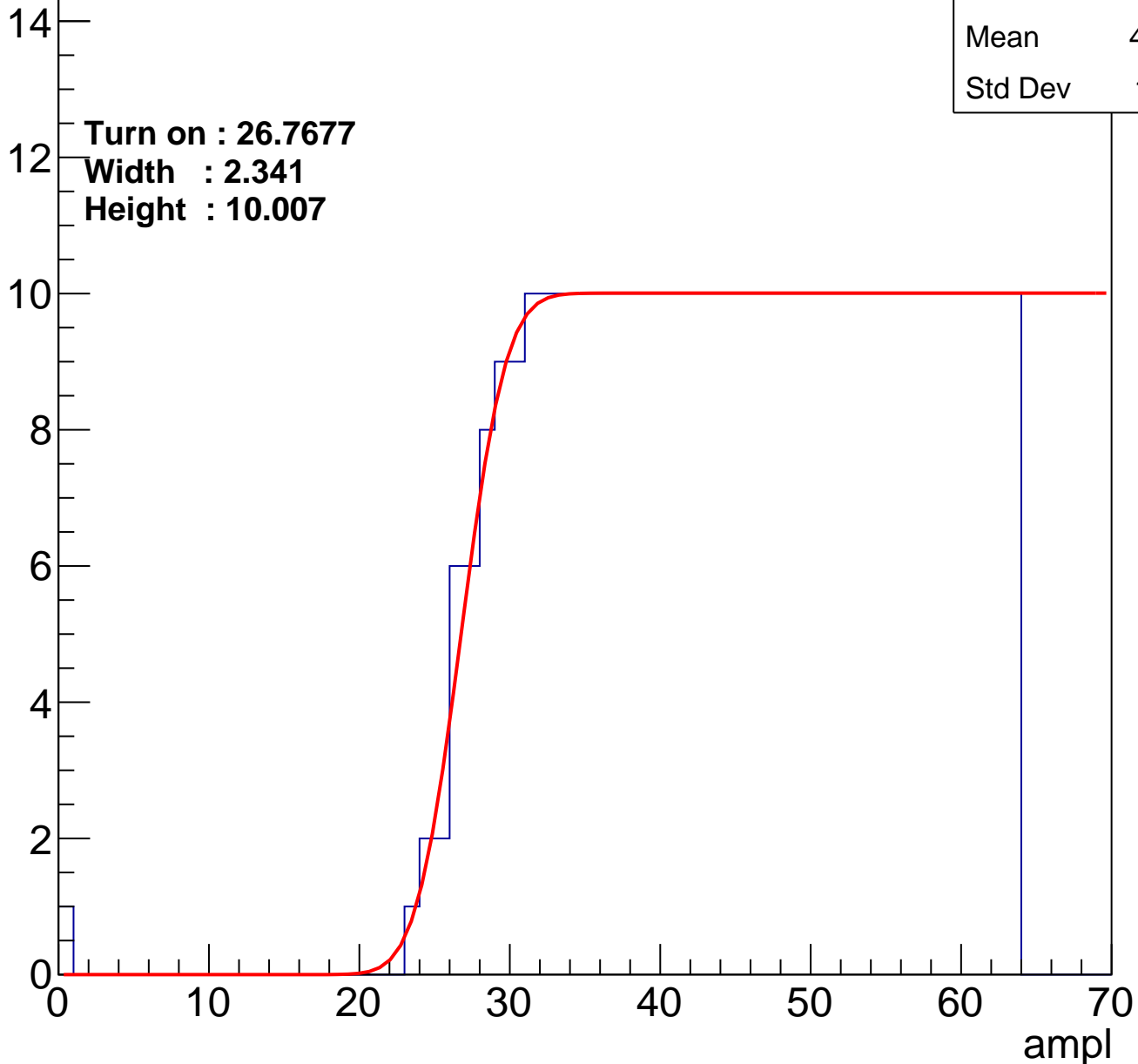
Entry

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.66 |
| Std Dev | 11.11 |

Turn on : 26.7677

Width : 2.341

Height : 10.007



B1L000S, U1-ch59

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.28 |
| Std Dev | 12.15 |

Turn on : 25.1822

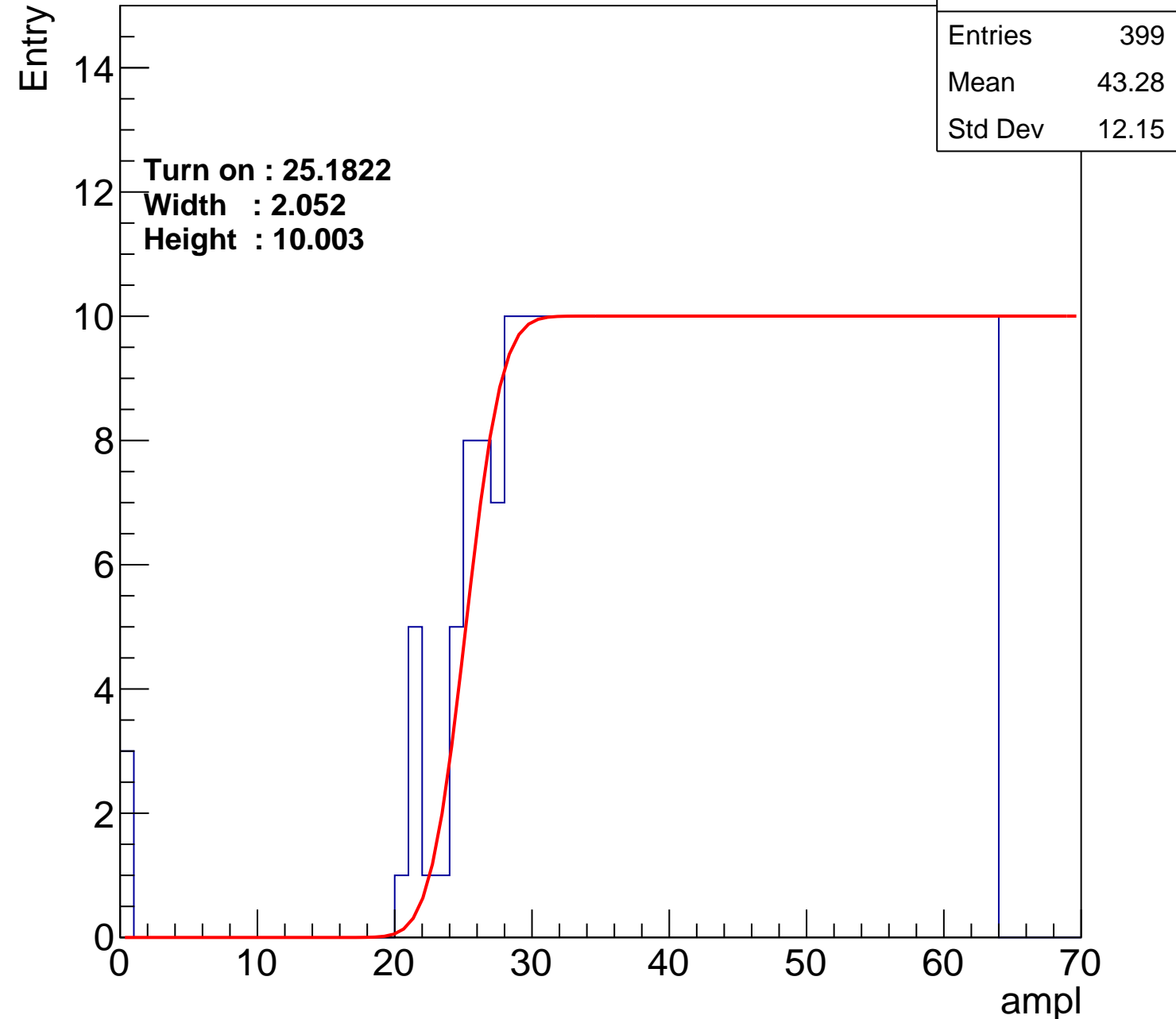
Width : 2.052

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch60

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 408 |
| Mean | 42.93 |
| Std Dev | 12.17 |

Turn on : 23.4480

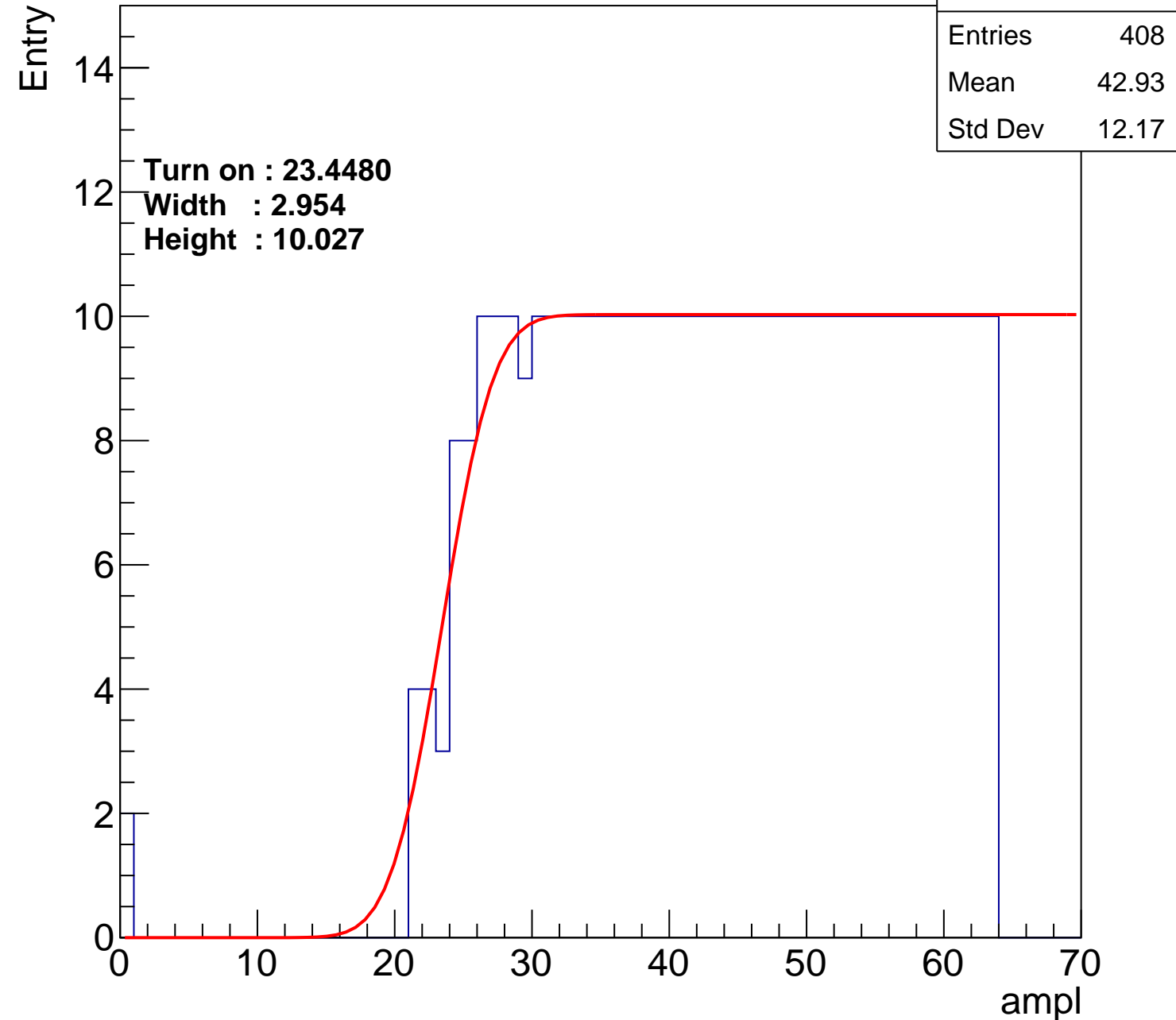
Width : 2.954

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch61

calib_packv5_042523_0143.root, FC#3, port B3

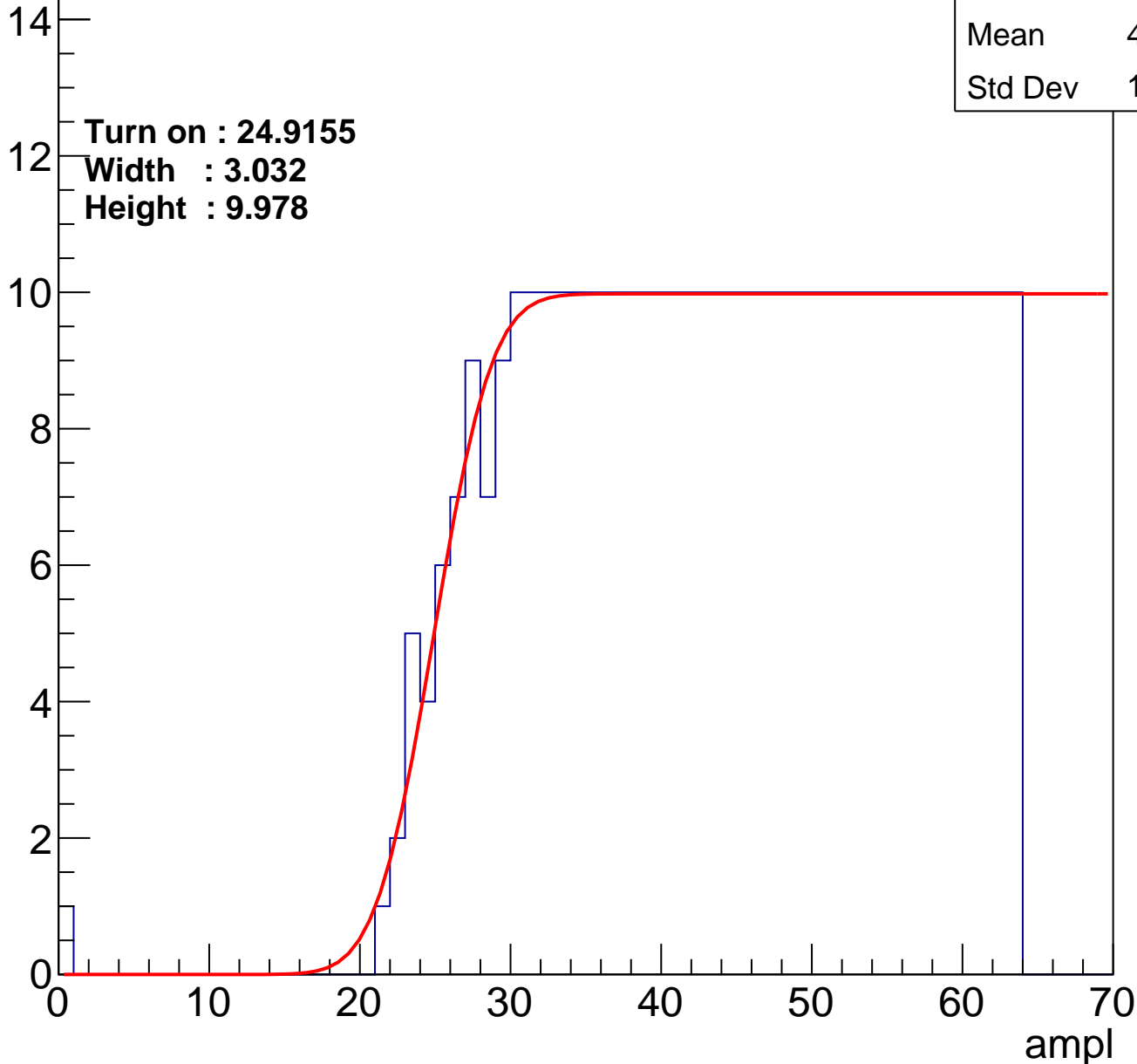
| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.78 |
| Std Dev | 11.63 |

Turn on : 24.9155

Width : 3.032

Height : 9.978

Entry



B1L000S, U1-ch62

calib_packv5_042523_0143.root, FC#3, port B3

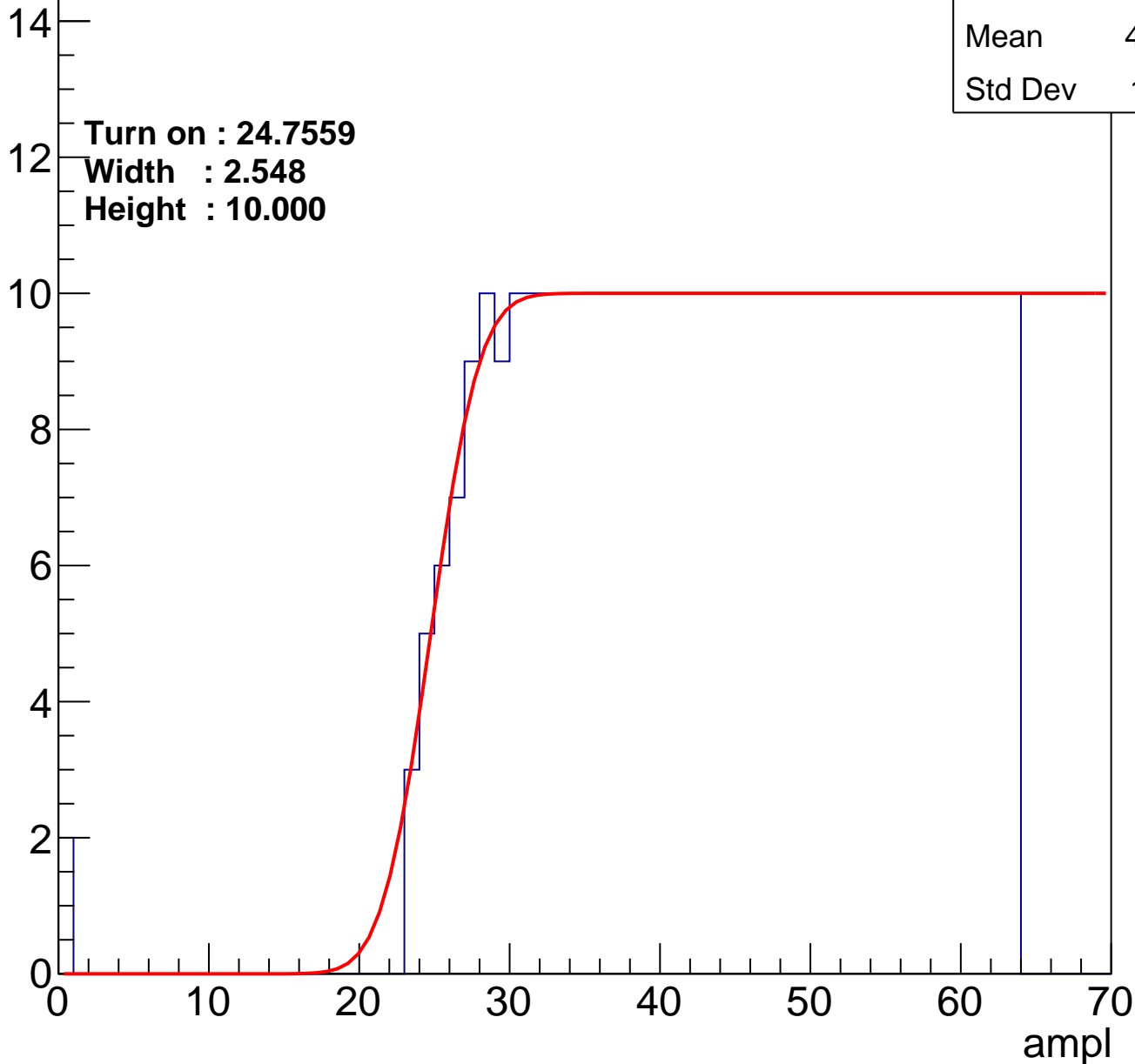
Entry

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.77 |
| Std Dev | 11.71 |

Turn on : 24.7559

Width : 2.548

Height : 10.000



B1L000S, U1-ch63

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.6 |
| Std Dev | 11.72 |

Turn on : 25.5841

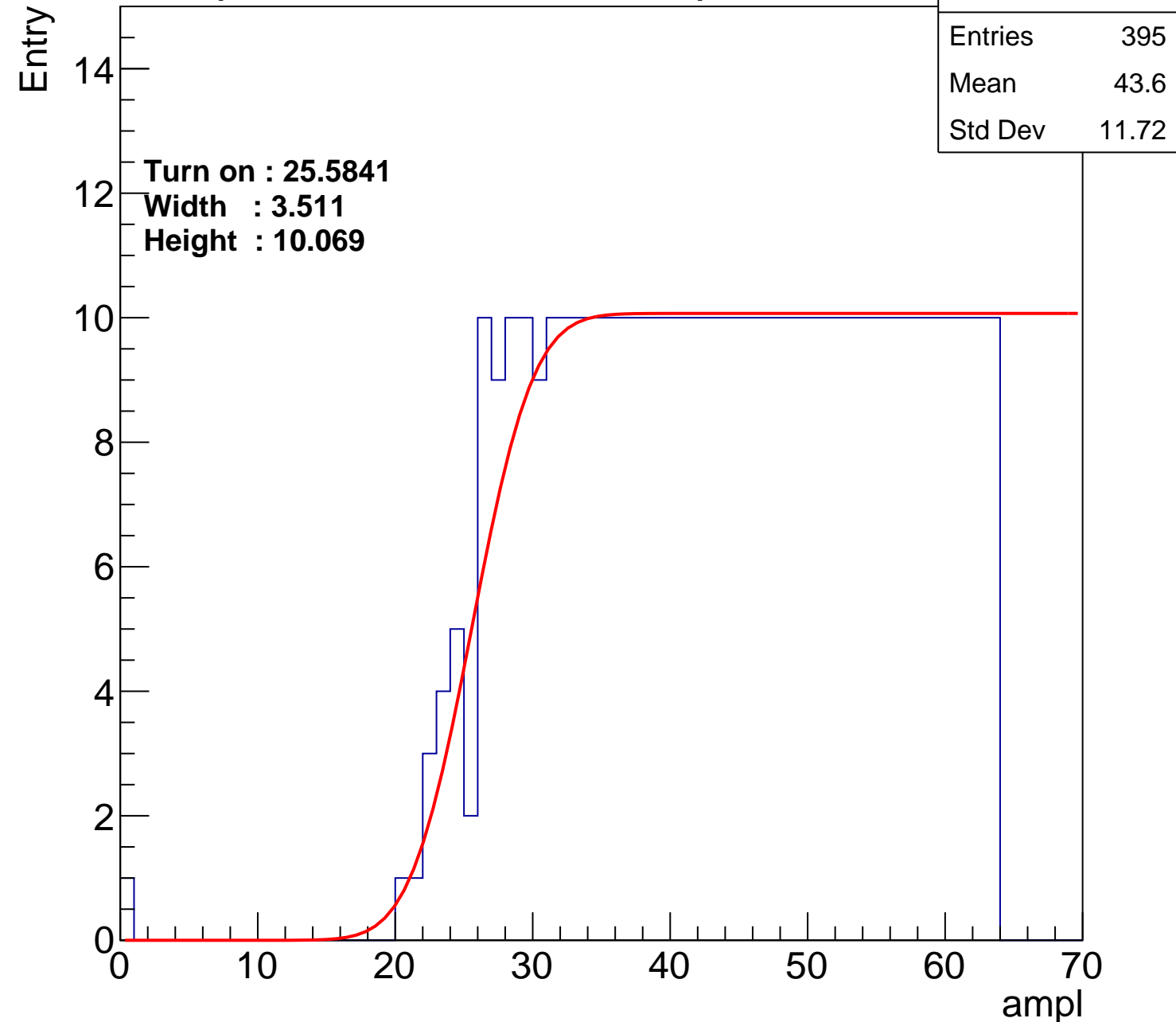
Width : 3.511

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch64

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.38 |
| Std Dev | 12.09 |

Turn on : 24.6678

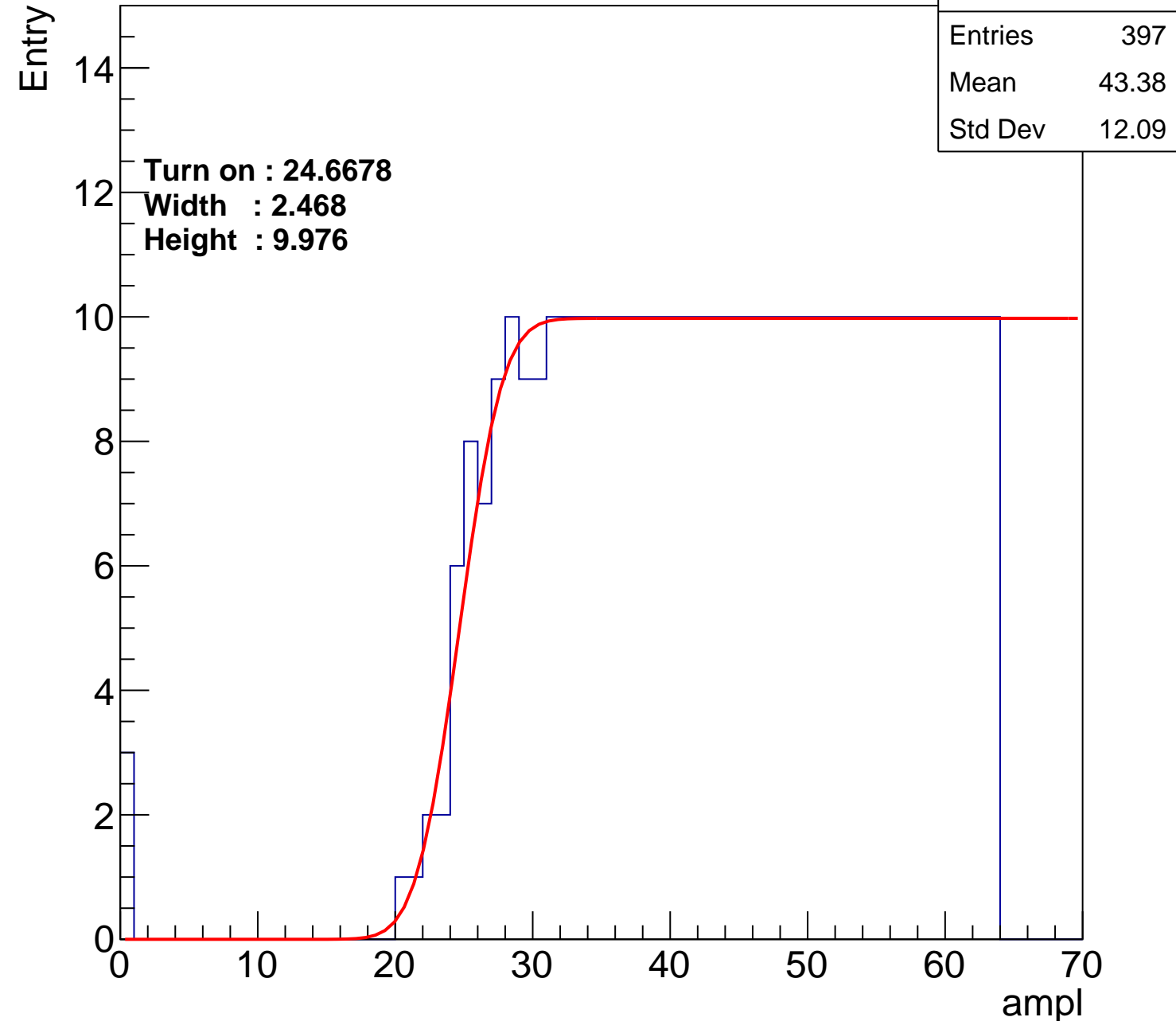
Width : 2.468

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch65

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.38 |
| Std Dev | 12.37 |

Turn on : 25.3202

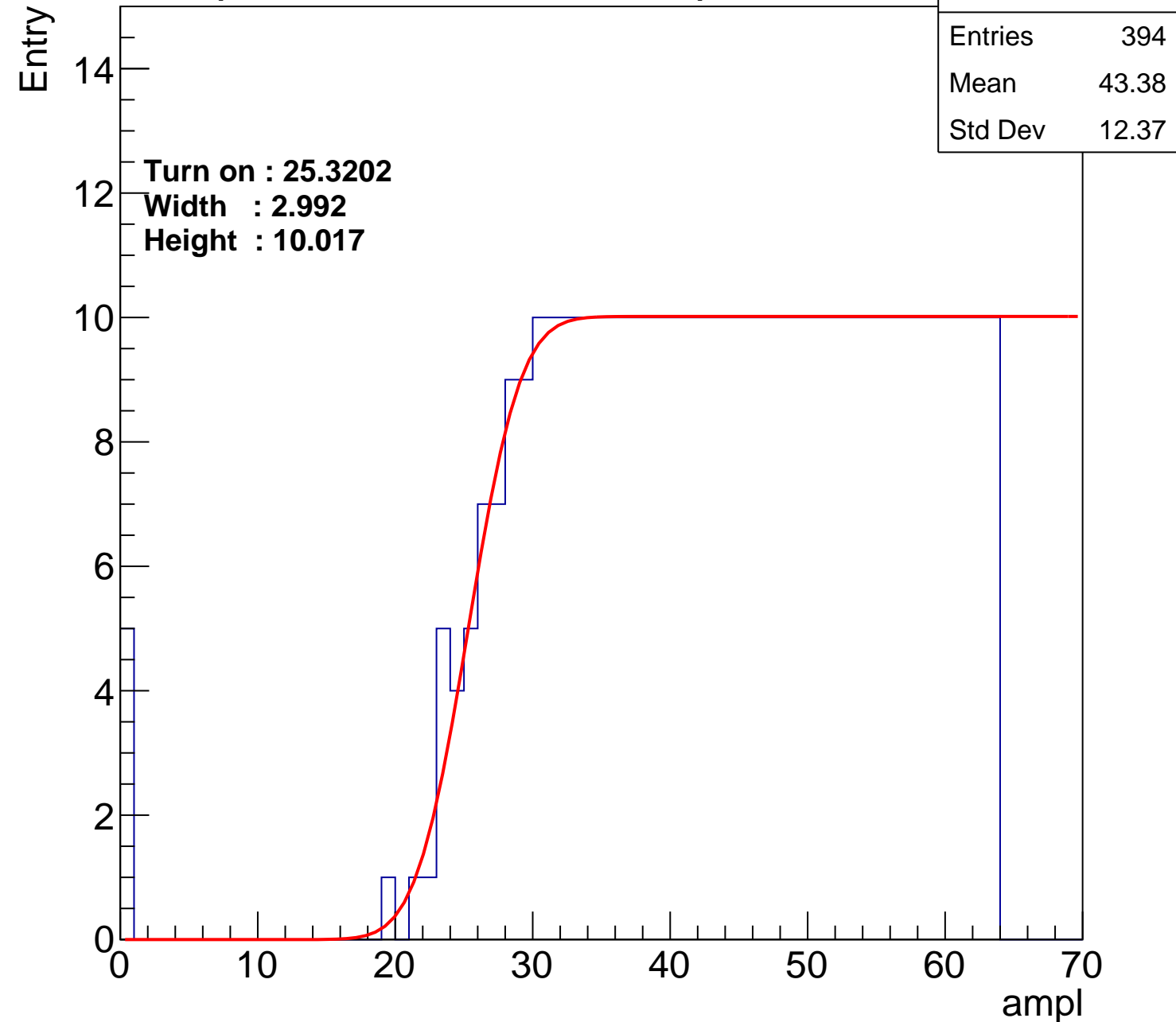
Width : 2.992

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch66

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 413 |
| Mean | 42.48 |
| Std Dev | 12.78 |

Turn on : 23.7297

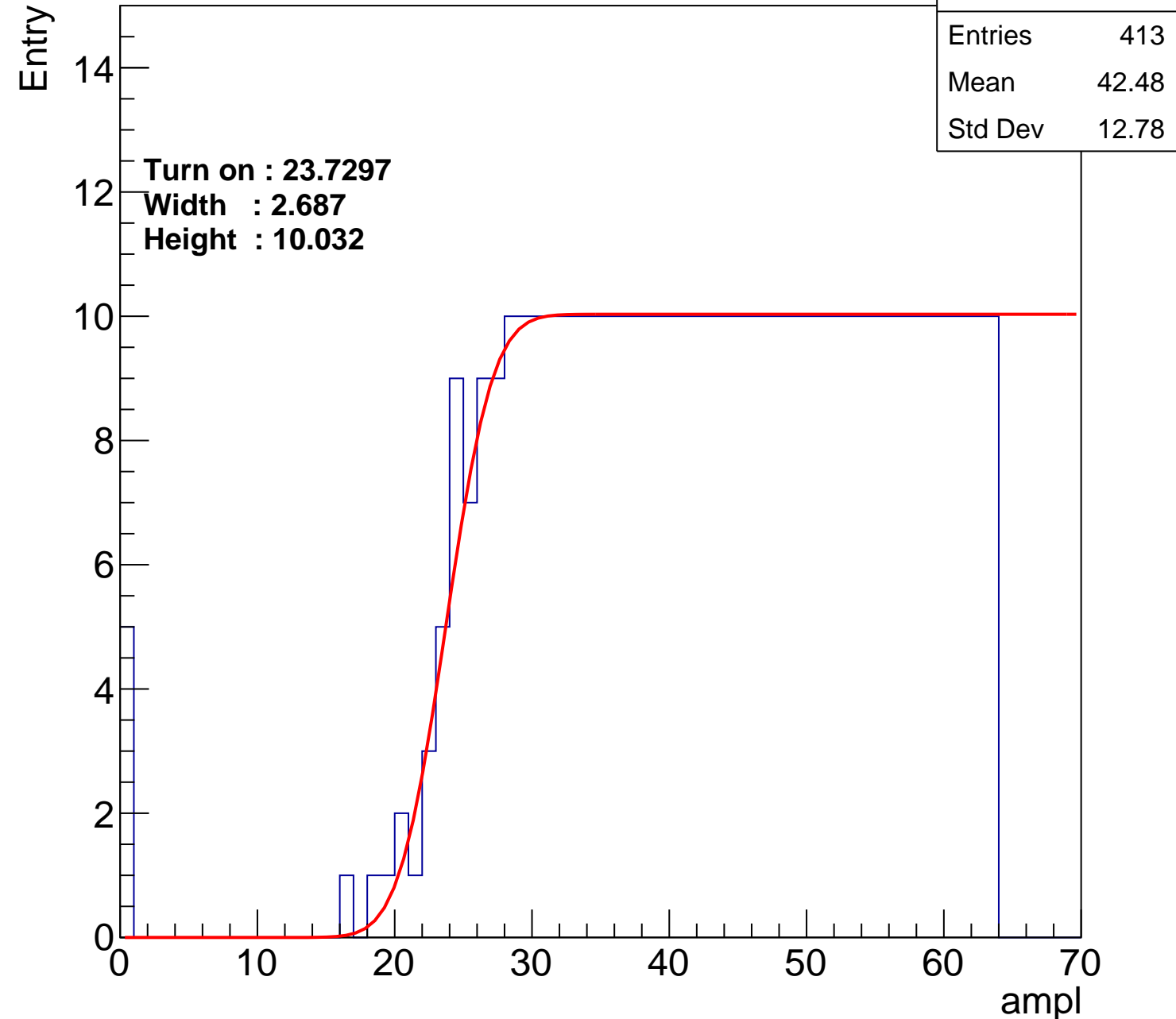
Width : 2.687

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch67

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.35 |
| Std Dev | 11.67 |

Turn on : 26.8005

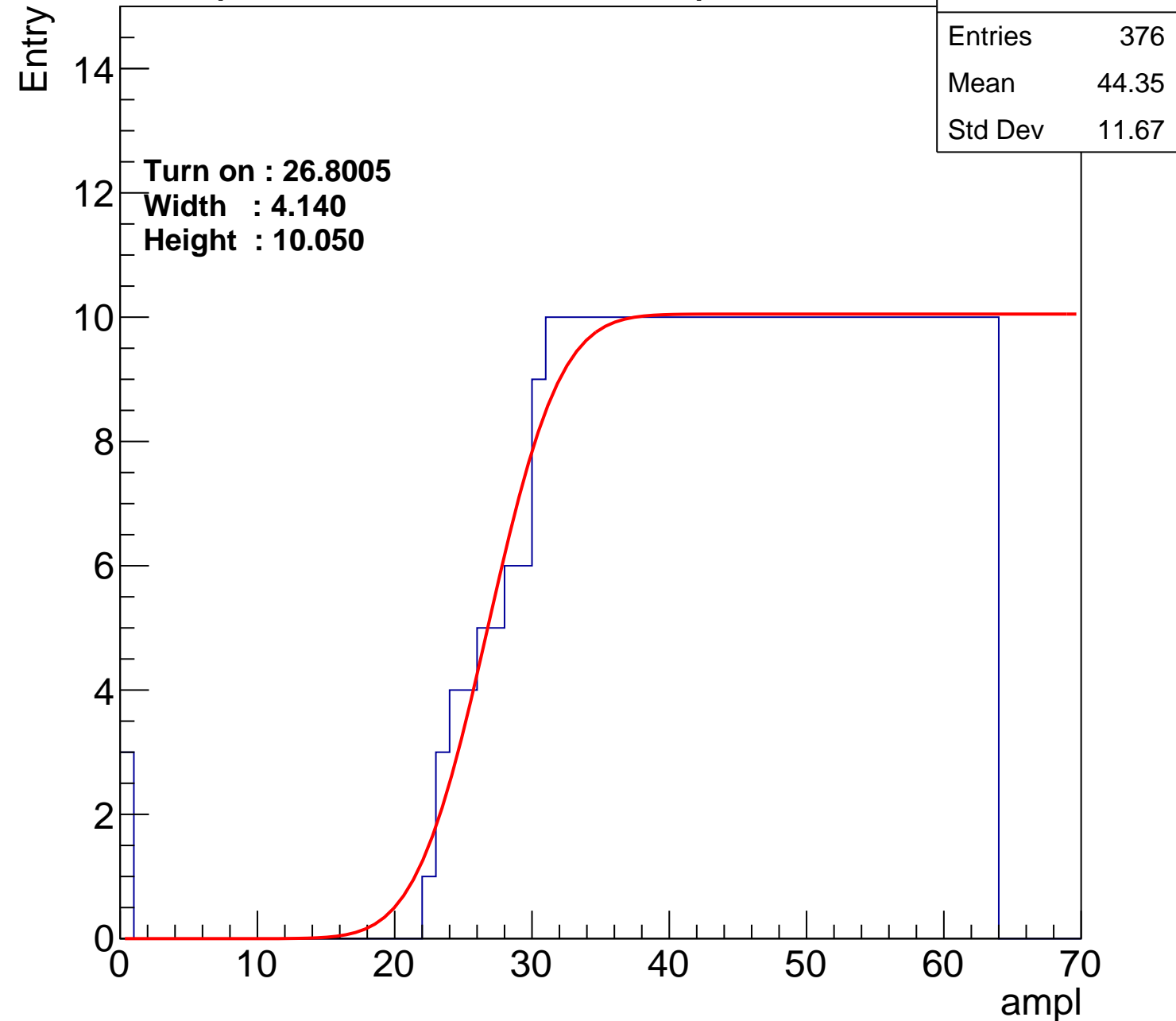
Width : 4.140

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch68

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.89 |
| Std Dev | 11.19 |

Turn on : 27.3764

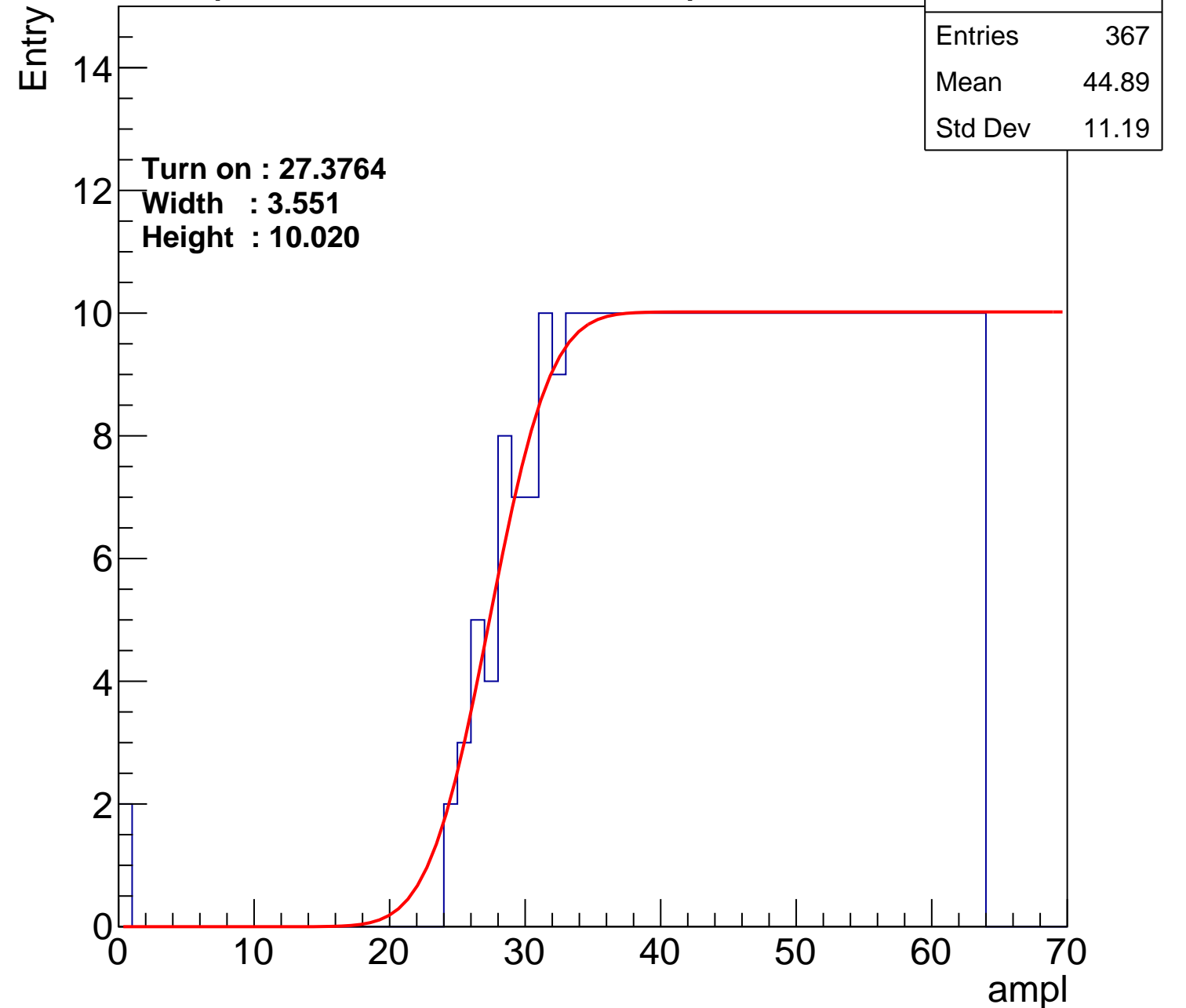
Width : 3.551

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch69

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.4 |
| Std Dev | 11.83 |

Turn on : 24.4664

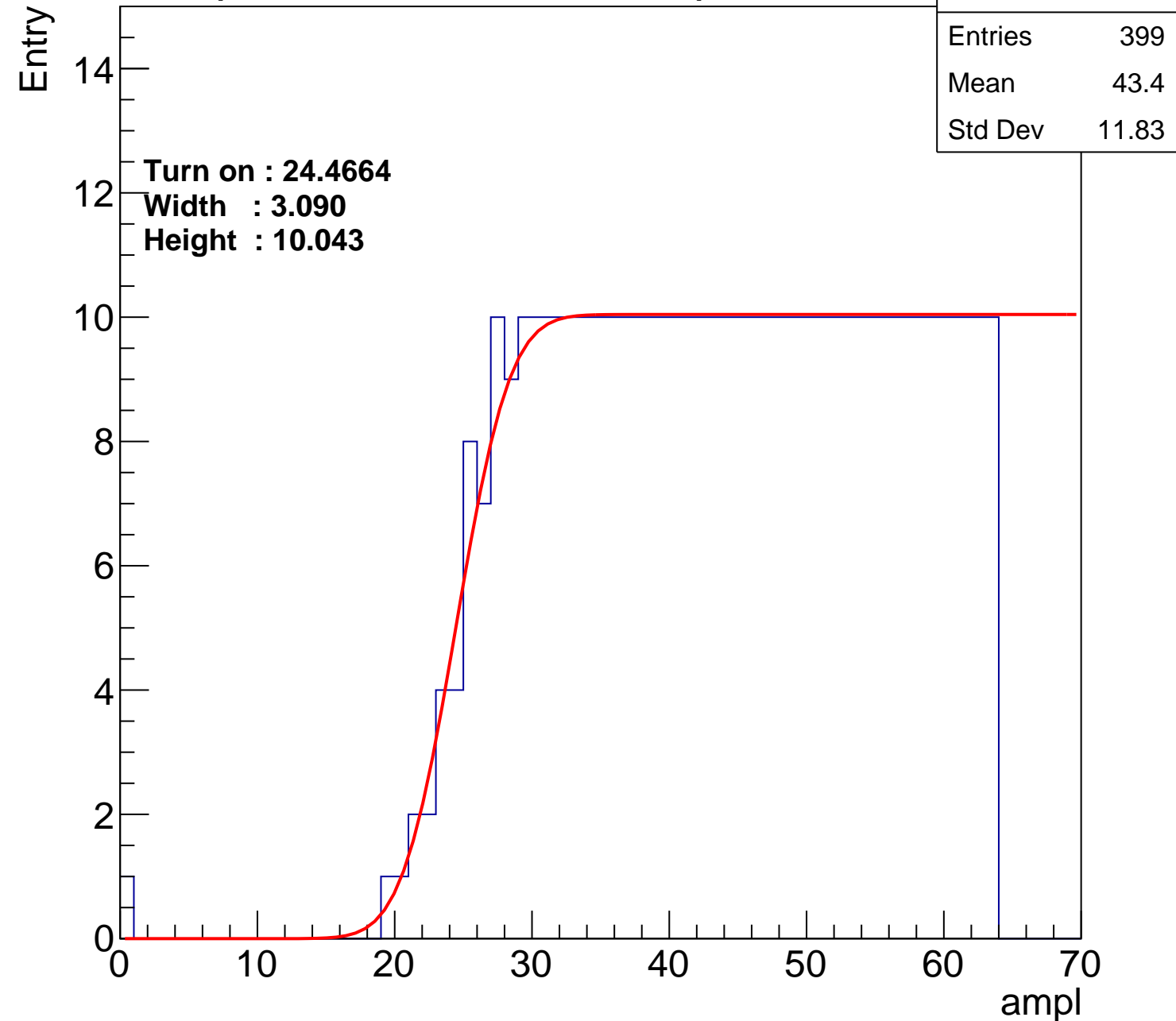
Width : 3.090

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch70

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.29 |
| Std Dev | 12.24 |

Turn on : 25.0969

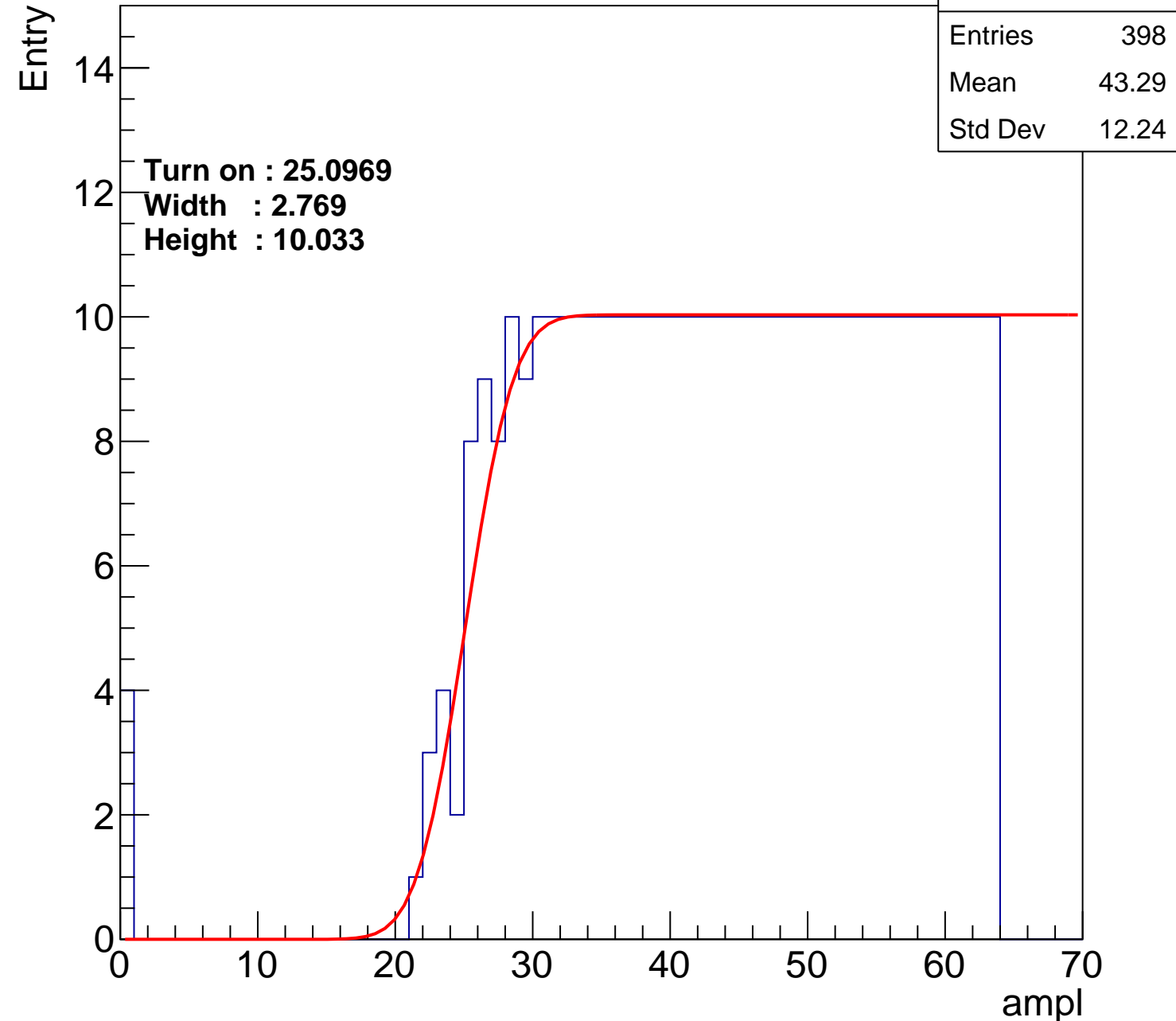
Width : 2.769

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch71

calib_packv5_042523_0143.root, FC#3, port B3

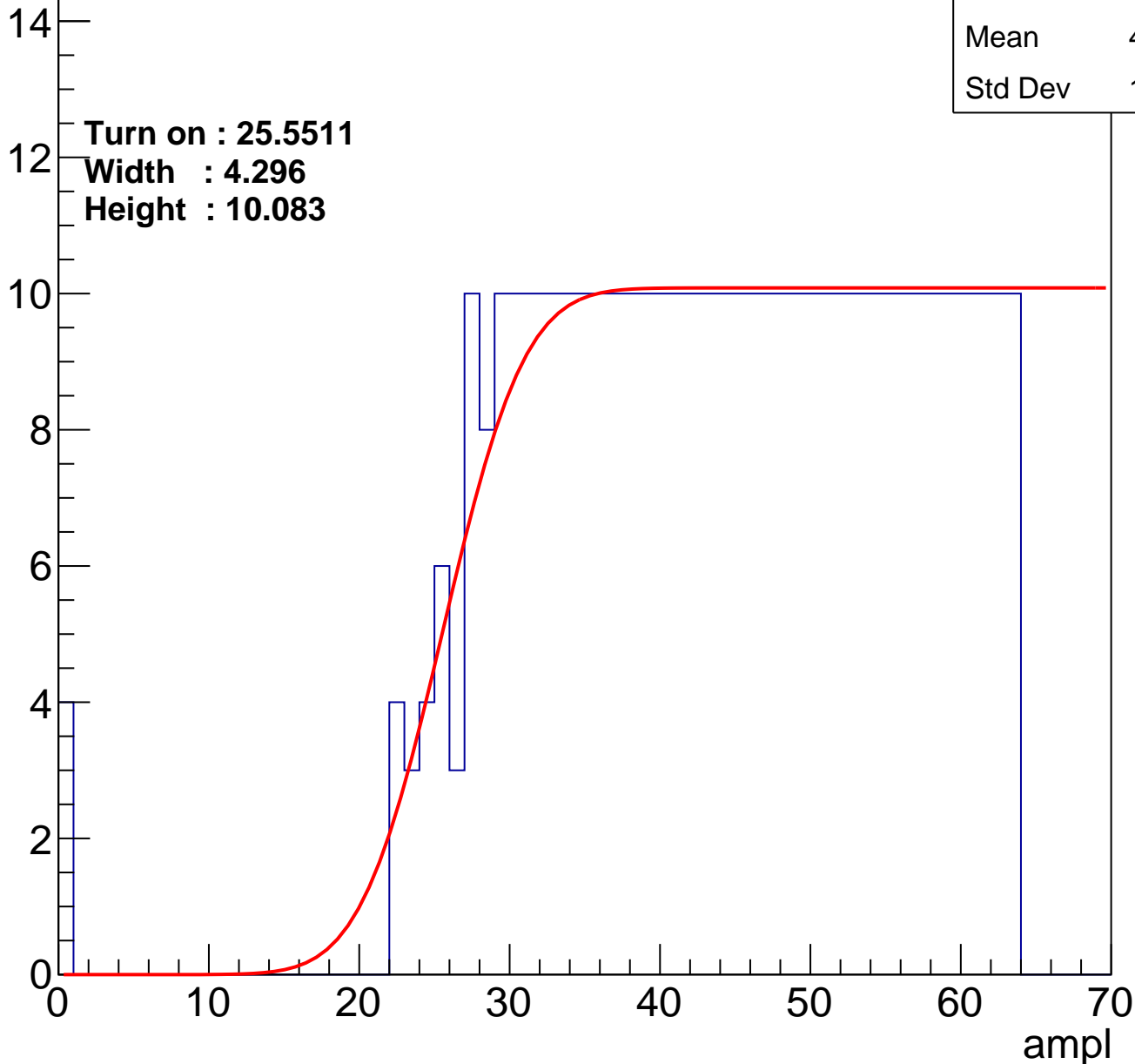
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.56 |
| Std Dev | 12.13 |

Turn on : 25.5511

Width : 4.296

Height : 10.083

Entry



B1L000S, U1-ch72

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.88 |
| Std Dev | 12 |

Turn on : 25.4541

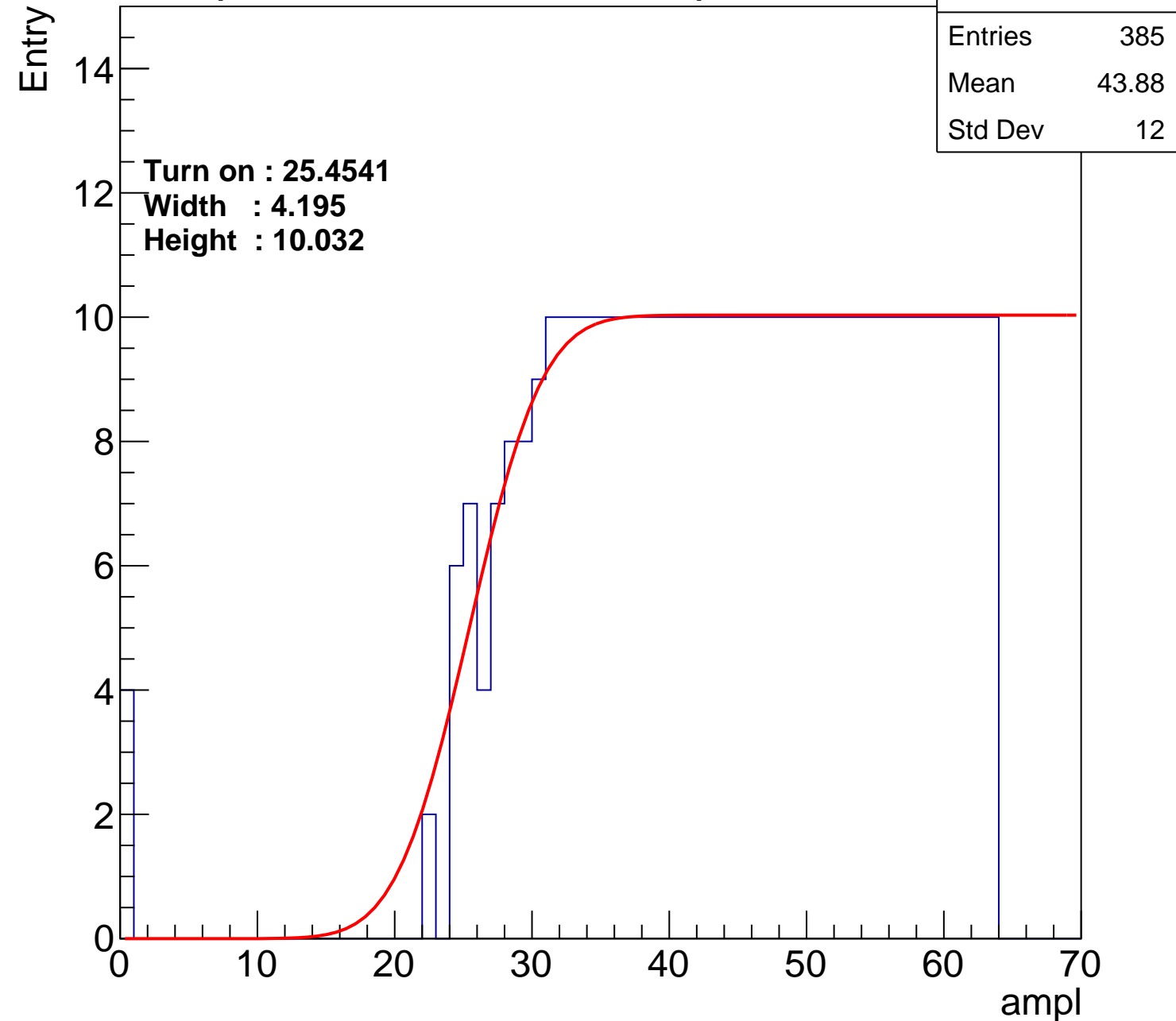
Width : 4.195

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch73

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.81 |
| Std Dev | 11.58 |

Turn on : 25.0240

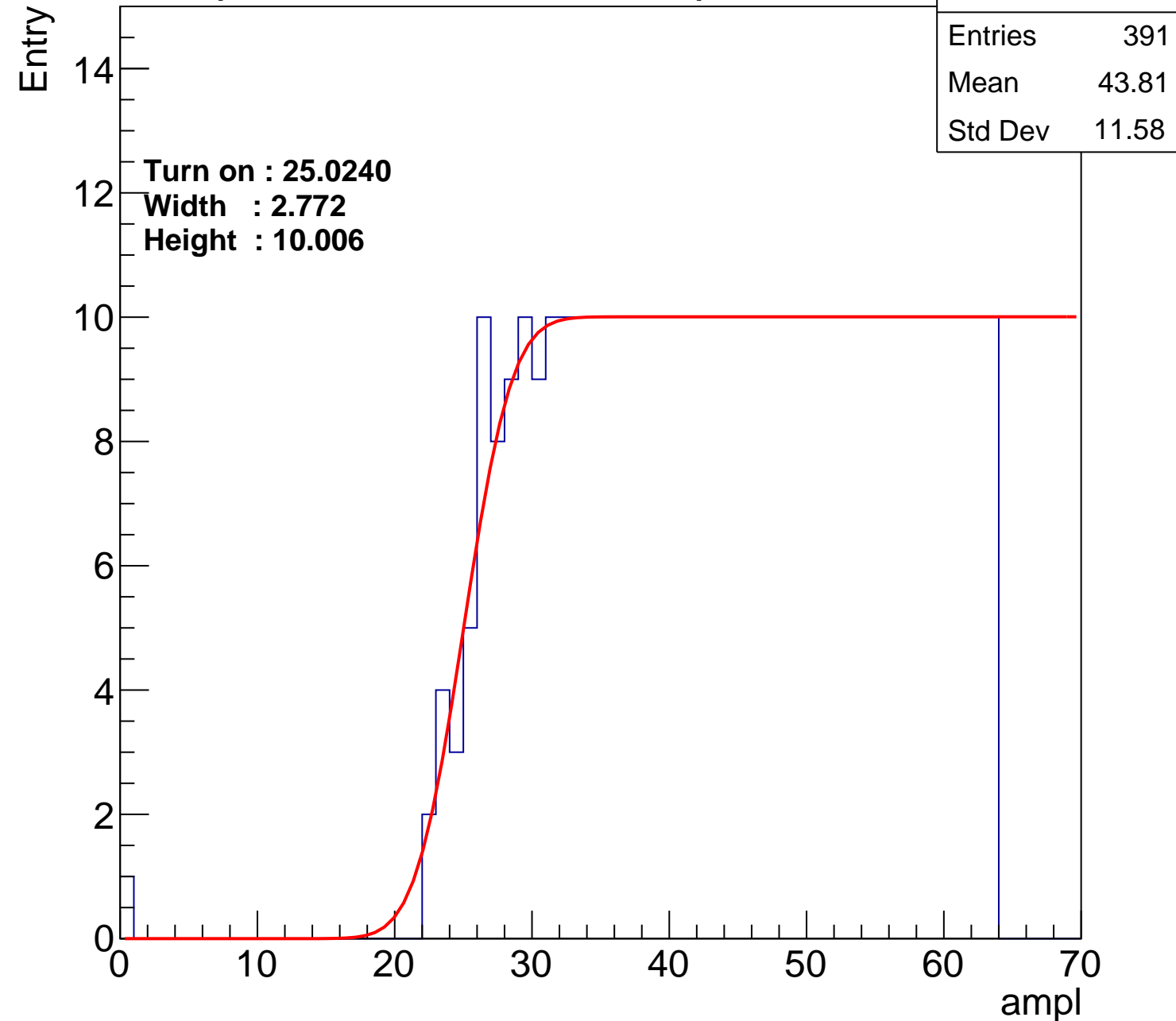
Width : 2.772

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch74

calib_packv5_042523_0143.root, FC#3, port B3

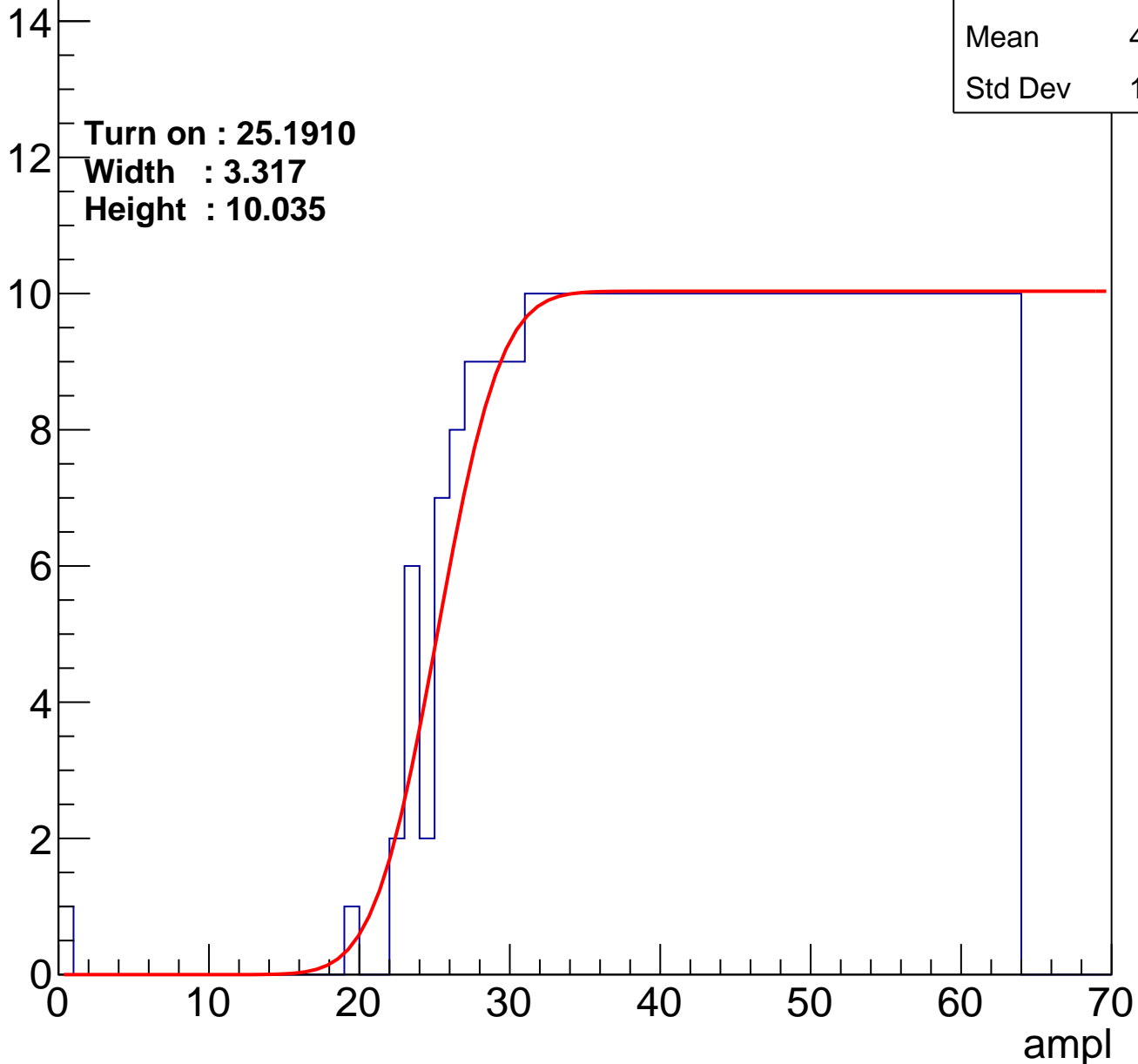
| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.68 |
| Std Dev | 11.68 |

Turn on : 25.1910

Width : 3.317

Height : 10.035

Entry



B1L000S, U1-ch75

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 403 |
| Mean | 43.2 |
| Std Dev | 11.94 |

Turn on : 23.9331

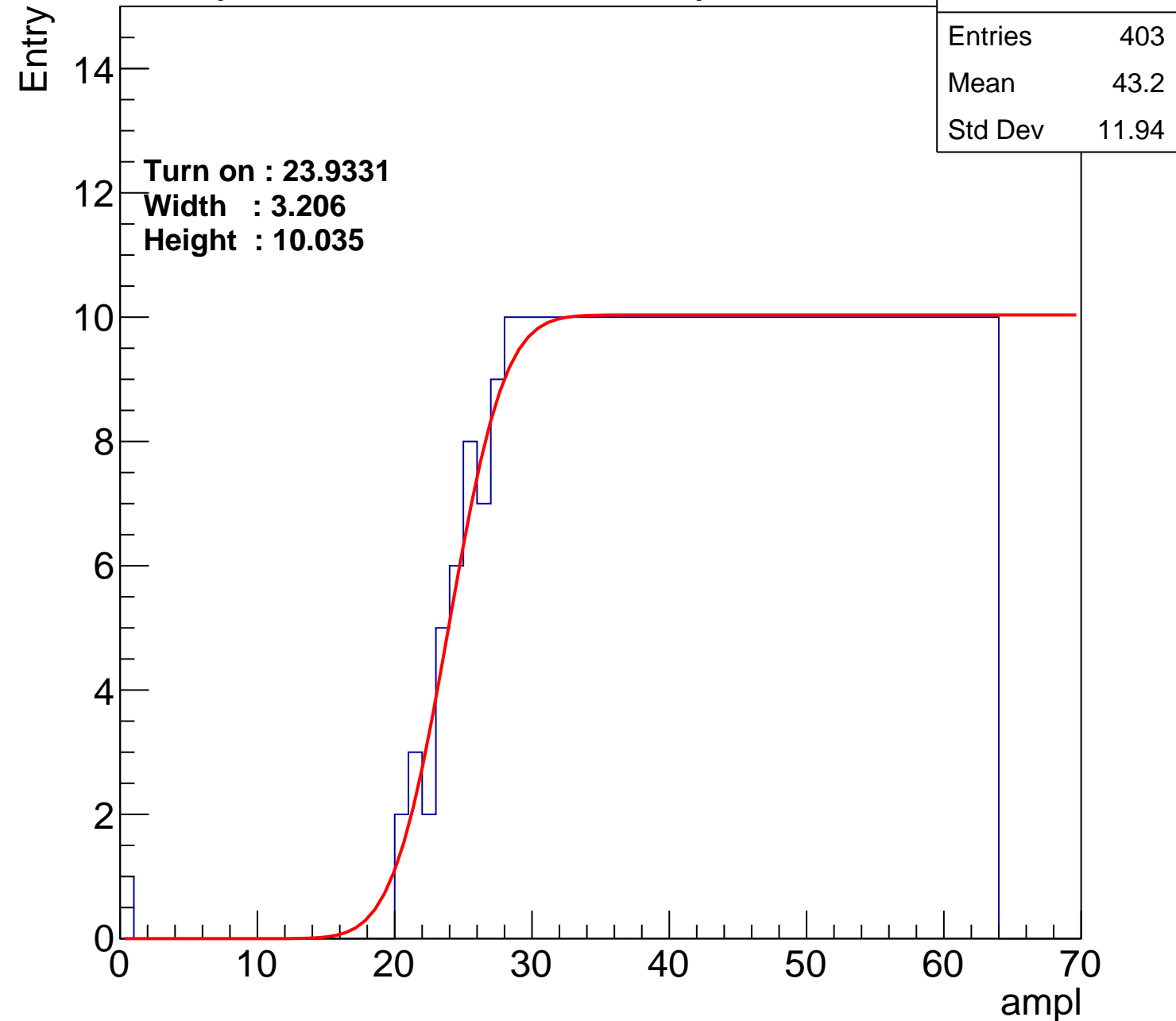
Width : 3.206

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch76

calib_packv5_042523_0143.root, FC#3, port B3

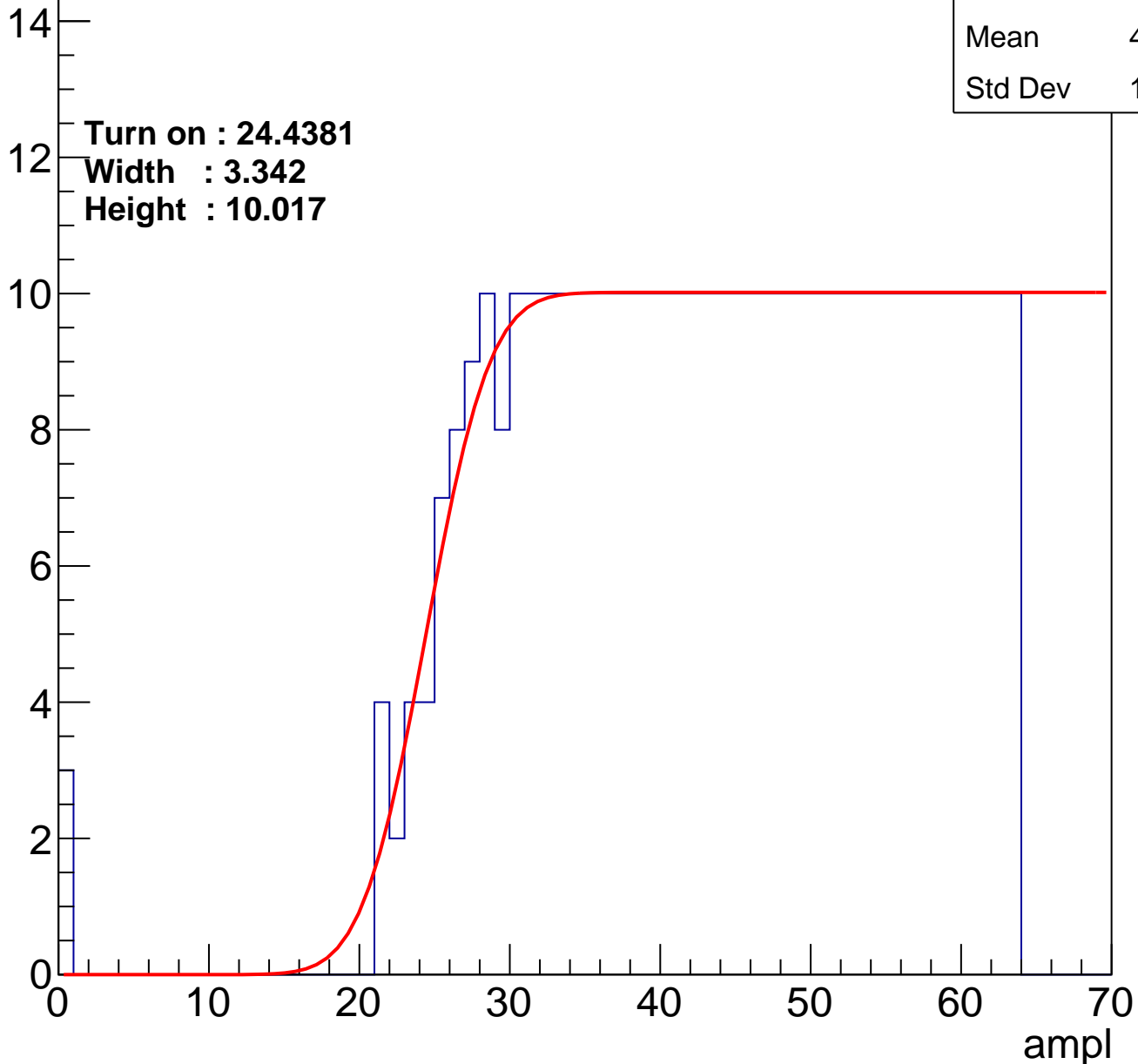
| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.27 |
| Std Dev | 12.16 |

Turn on : 24.4381

Width : 3.342

Height : 10.017

Entry



B1L000S, U1-ch77

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.32 |
| Std Dev | 12.24 |

Turn on : 24.7904

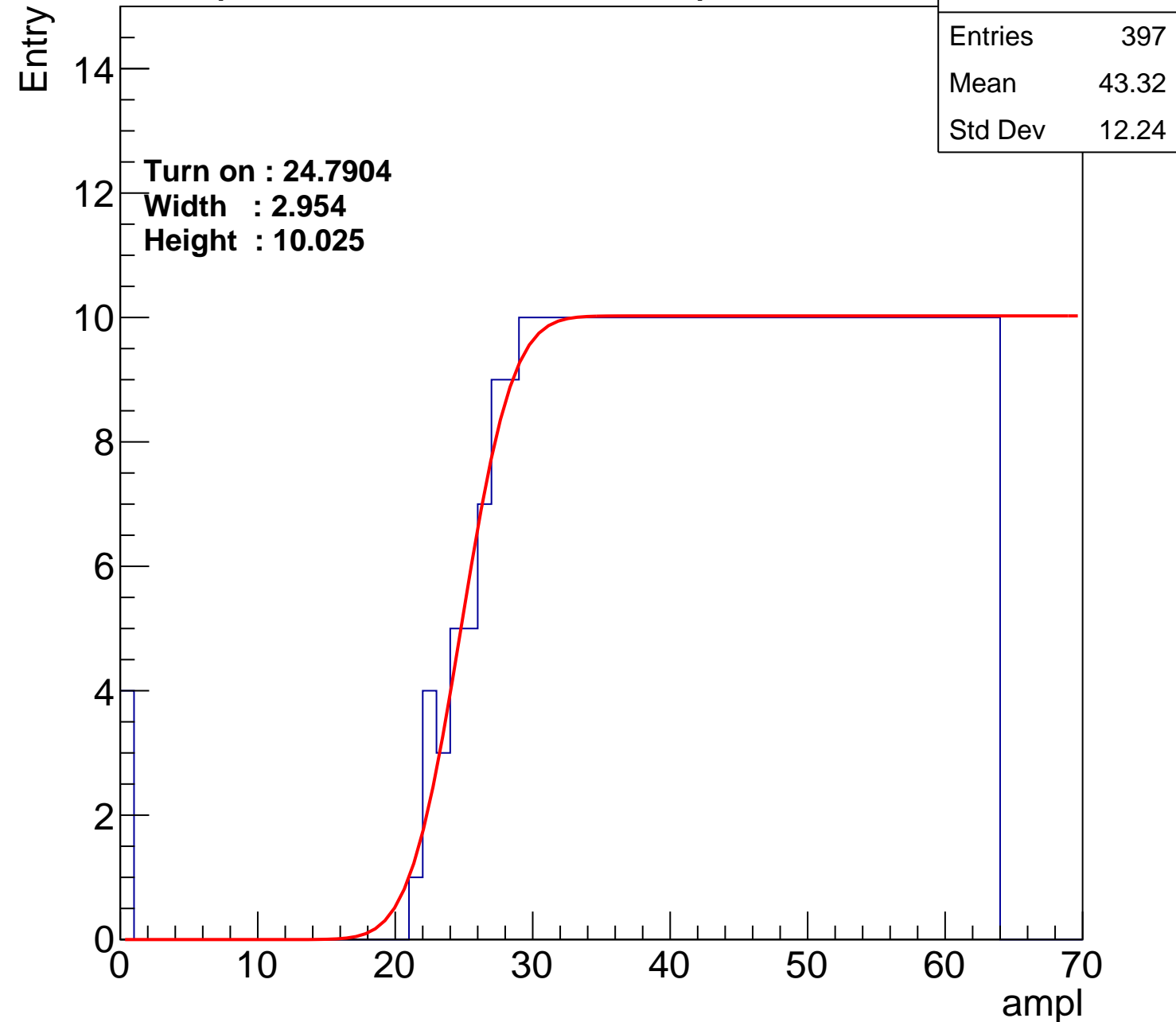
Width : 2.954

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch78

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.63 |
| Std Dev | 11.84 |

Turn on : 24.7861

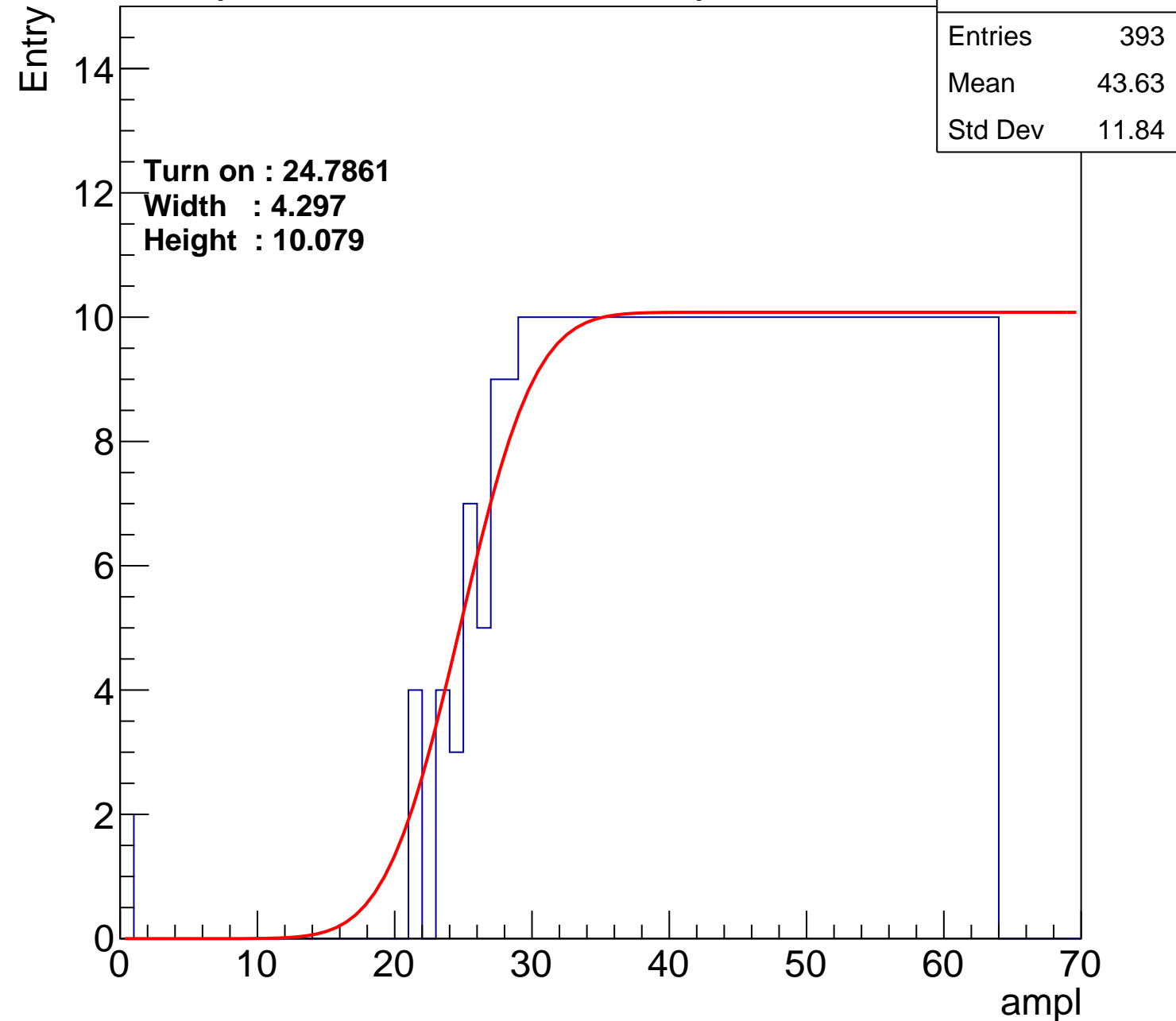
Width : 4.297

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch79

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.93 |
| Std Dev | 11.92 |

Turn on : 26.9470

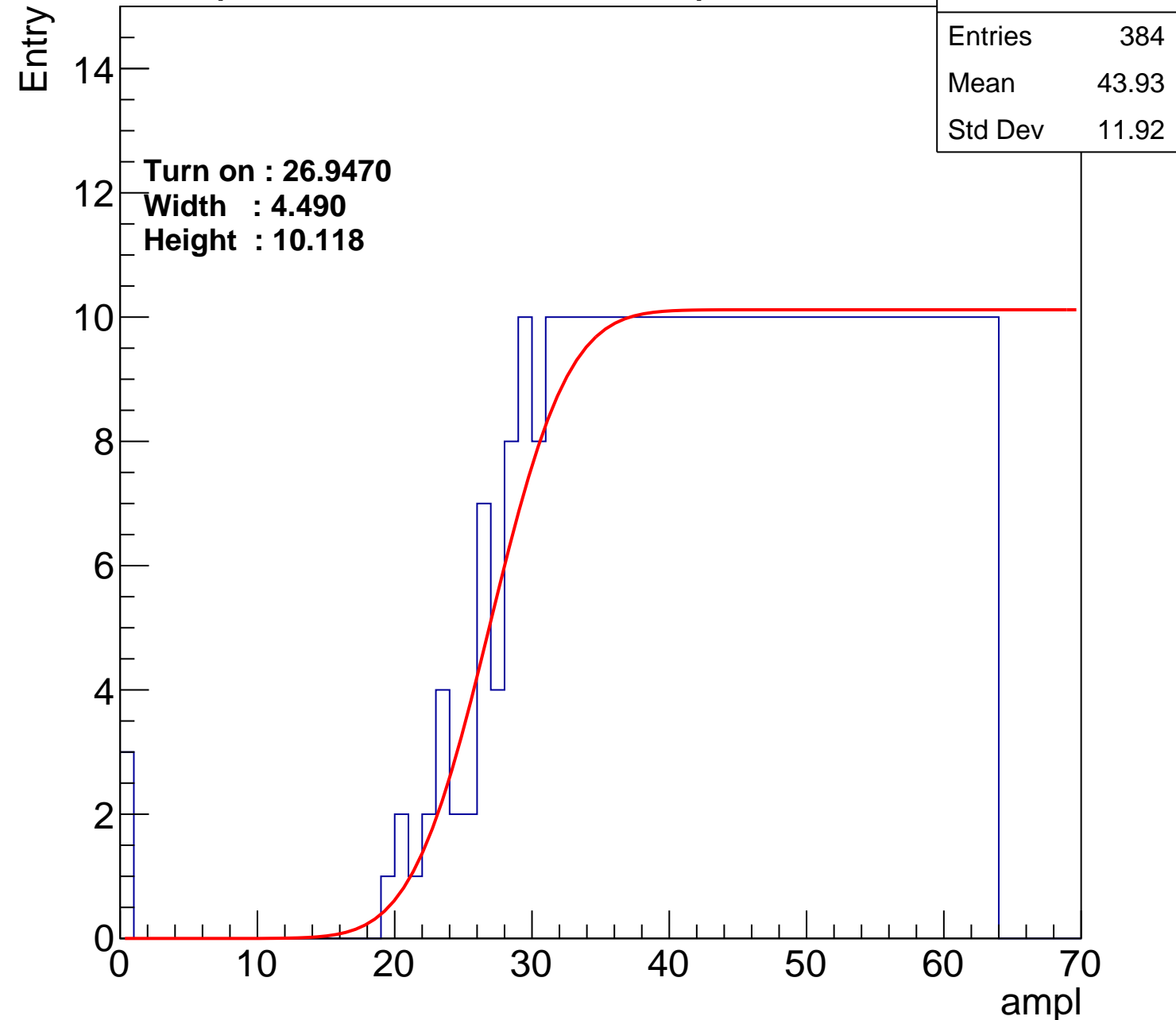
Width : 4.490

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch80

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.19 |
| Std Dev | 12.18 |

Turn on : 24.0546

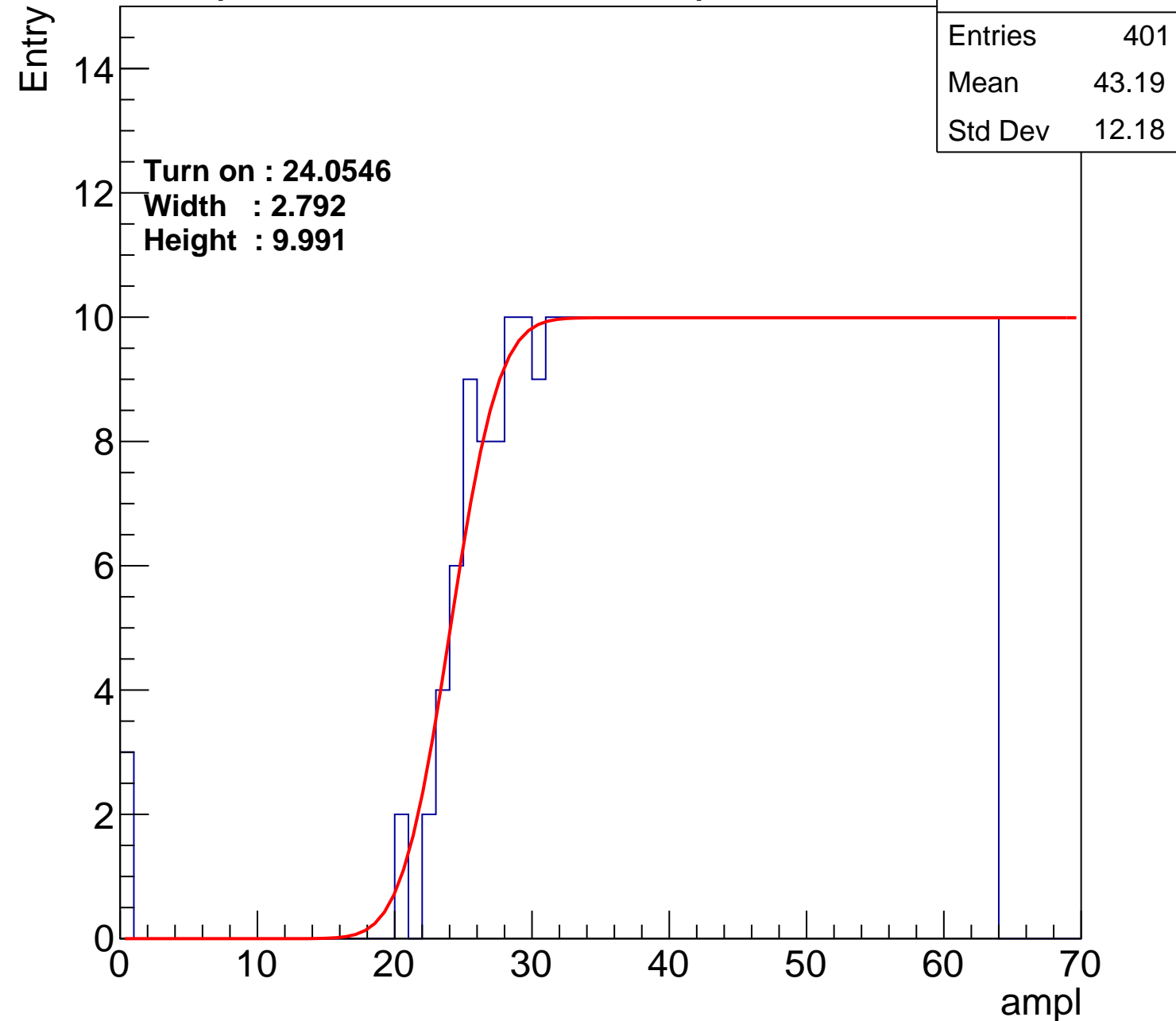
Width : 2.792

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch81

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.83 |
| Std Dev | 11.9 |

Turn on : 25.7412

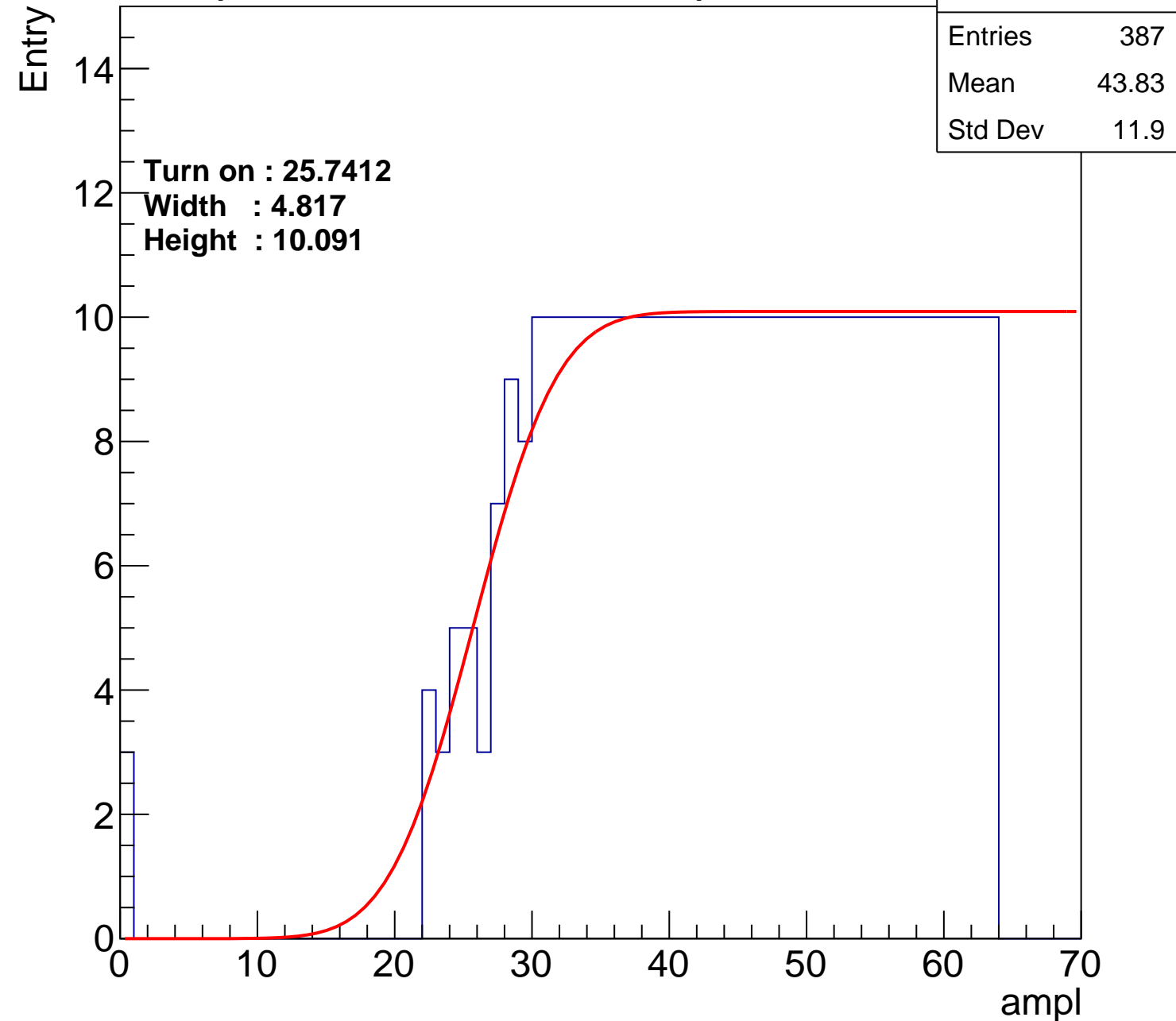
Width : 4.817

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch82

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 44.01 |
| Std Dev | 11.48 |

Turn on : 25.7851

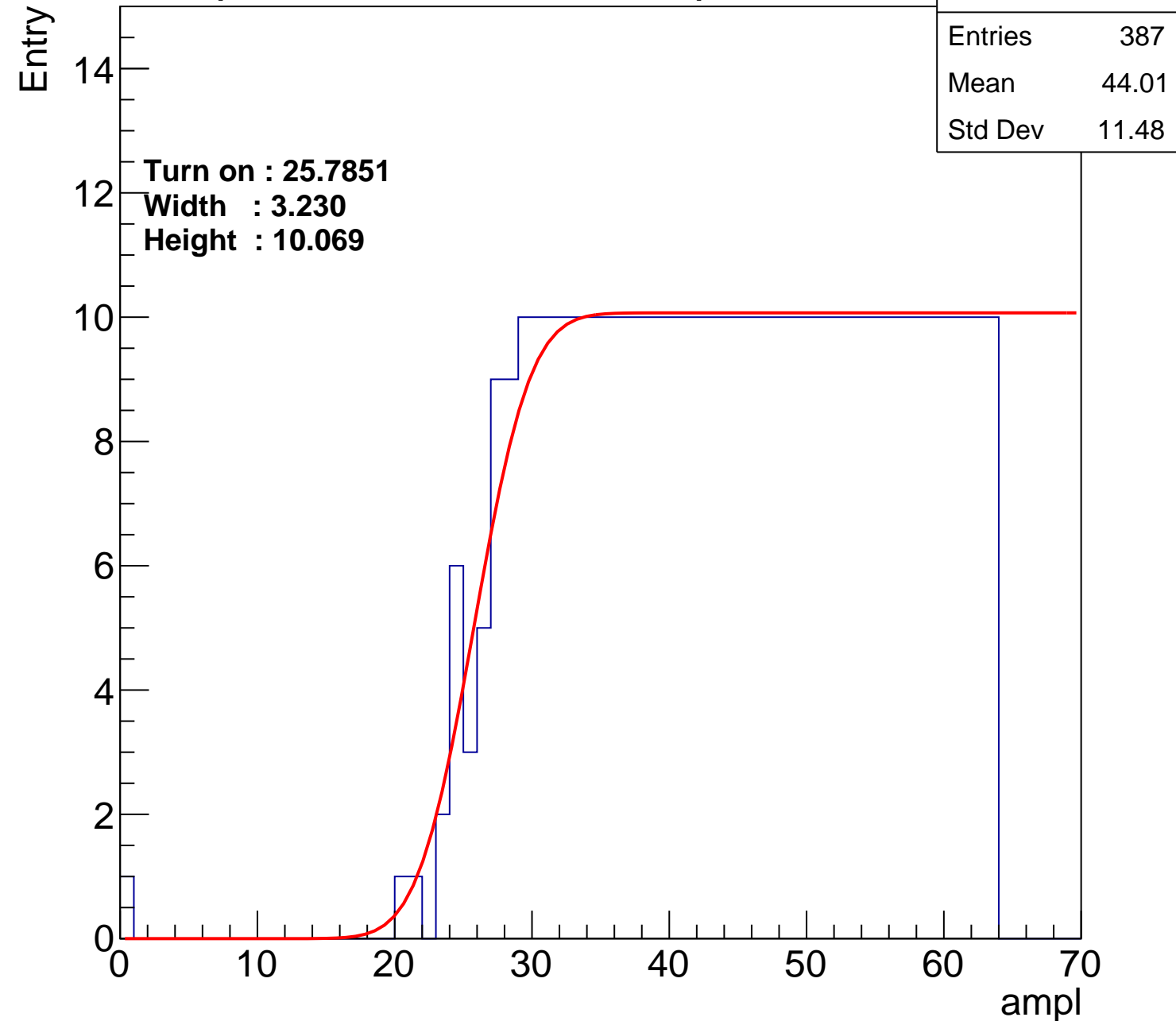
Width : 3.230

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch83

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 406 |
| Mean | 42.89 |
| Std Dev | 12.44 |

Turn on : 23.7417

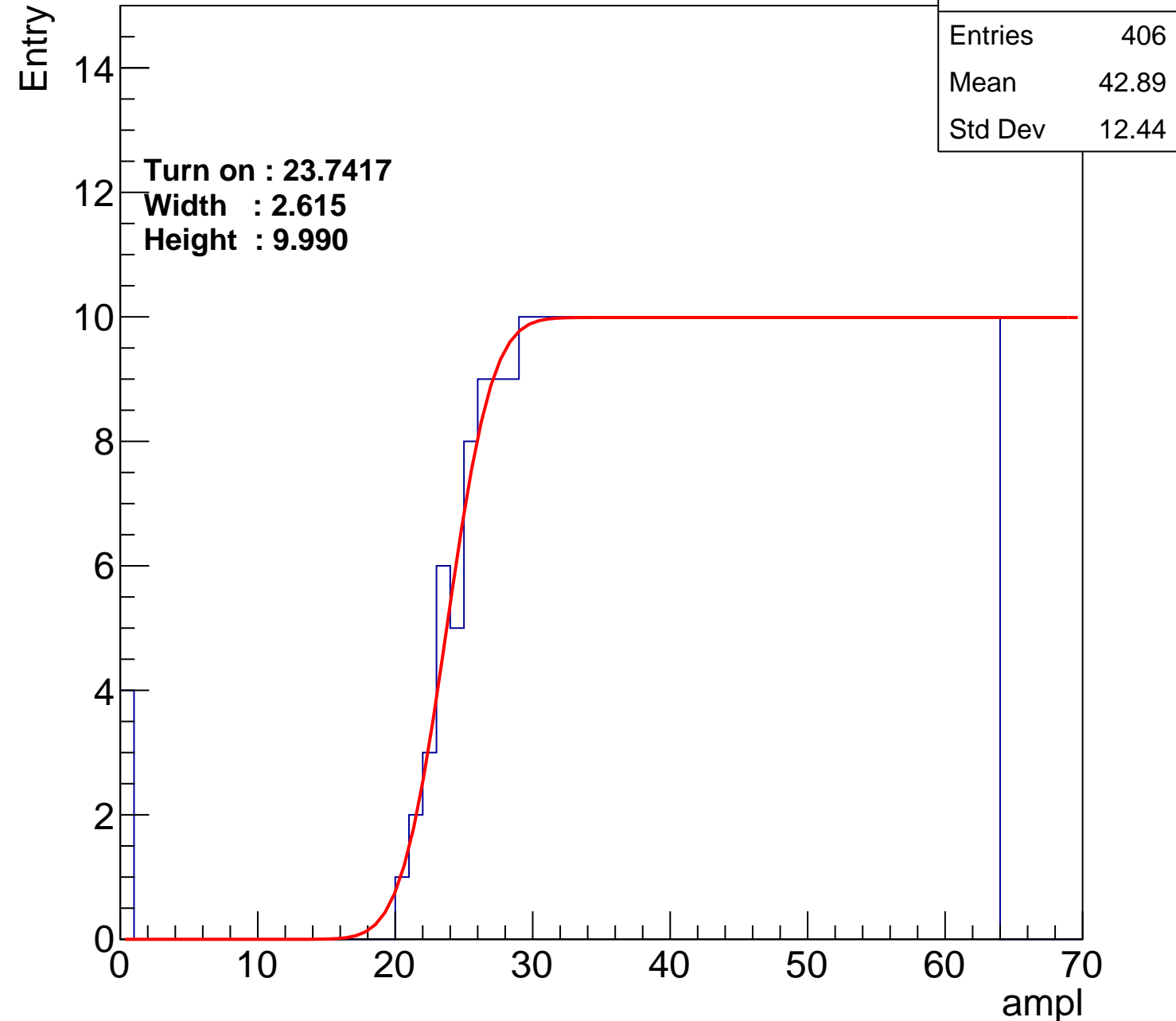
Width : 2.615

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch84

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.81 |
| Std Dev | 11.59 |

Turn on : 25.3978

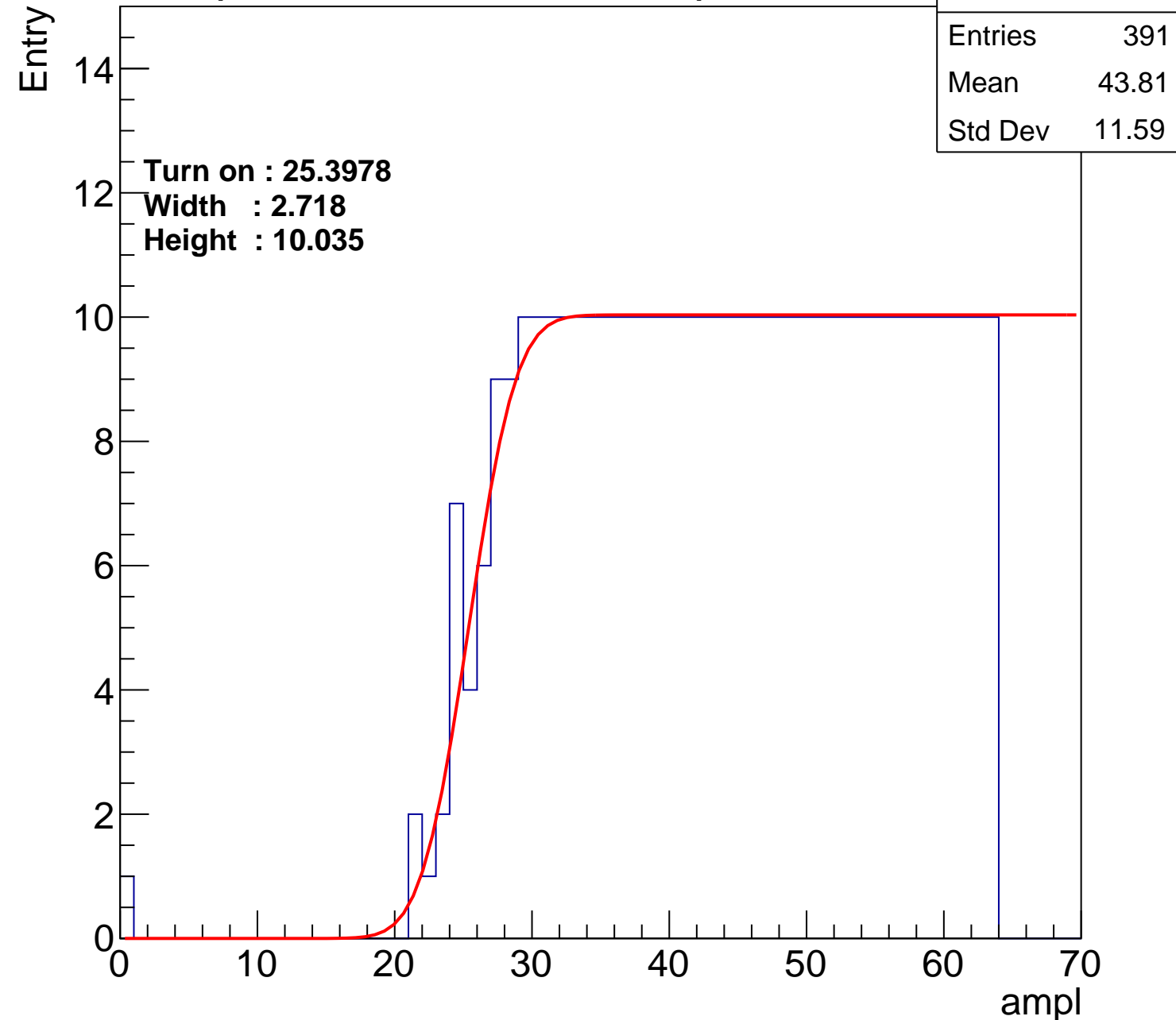
Width : 2.718

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch85

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.33 |
| Std Dev | 12.17 |

Turn on : 24.7862

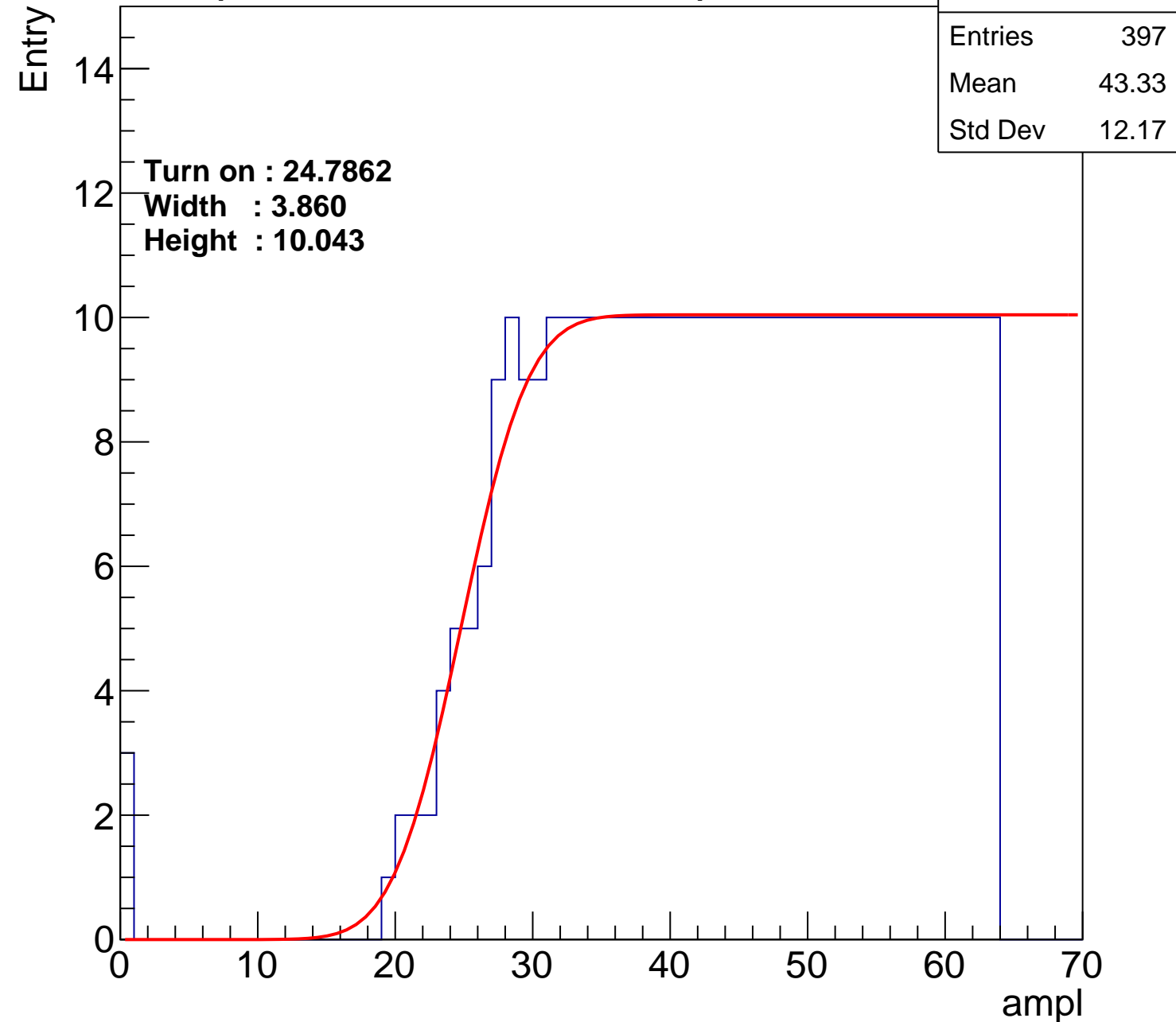
Width : 3.860

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch86

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.72 |
| Std Dev | 11.69 |

Turn on : 25.2764

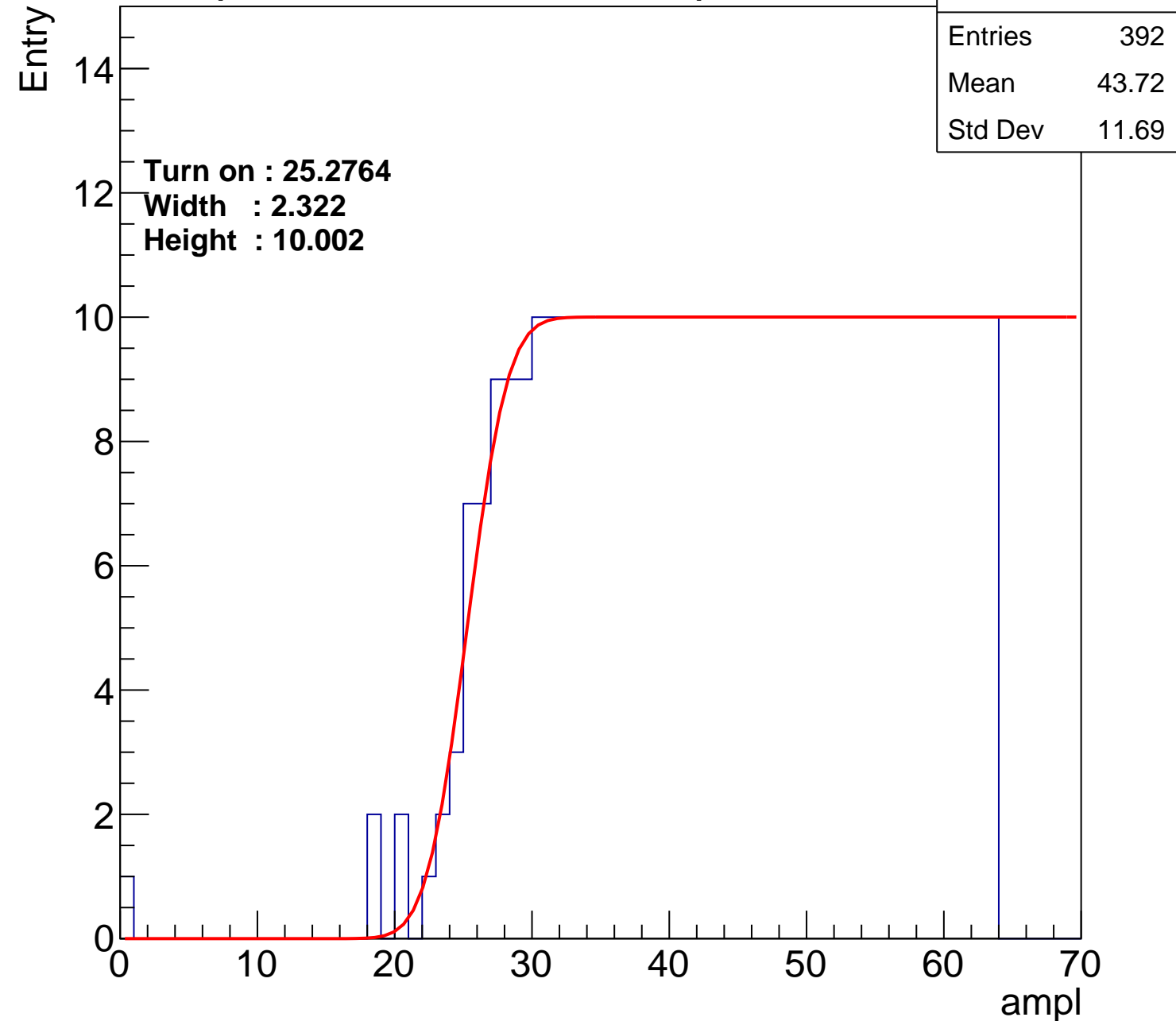
Width : 2.322

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch87

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.42 |
| Std Dev | 11.41 |

Turn on : 26.5672

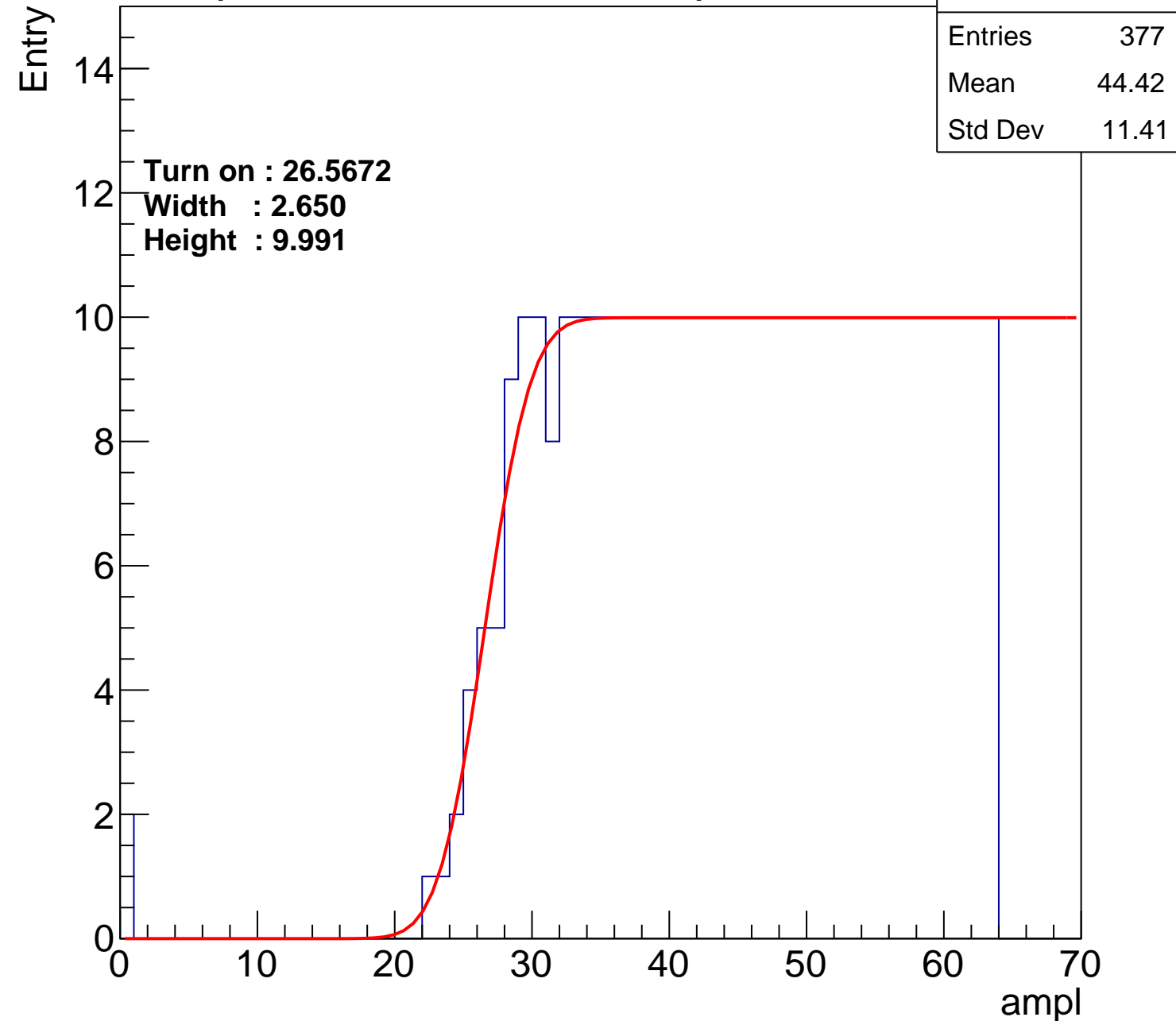
Width : 2.650

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch88

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.48 |
| Std Dev | 11.81 |

Turn on : 24.6436

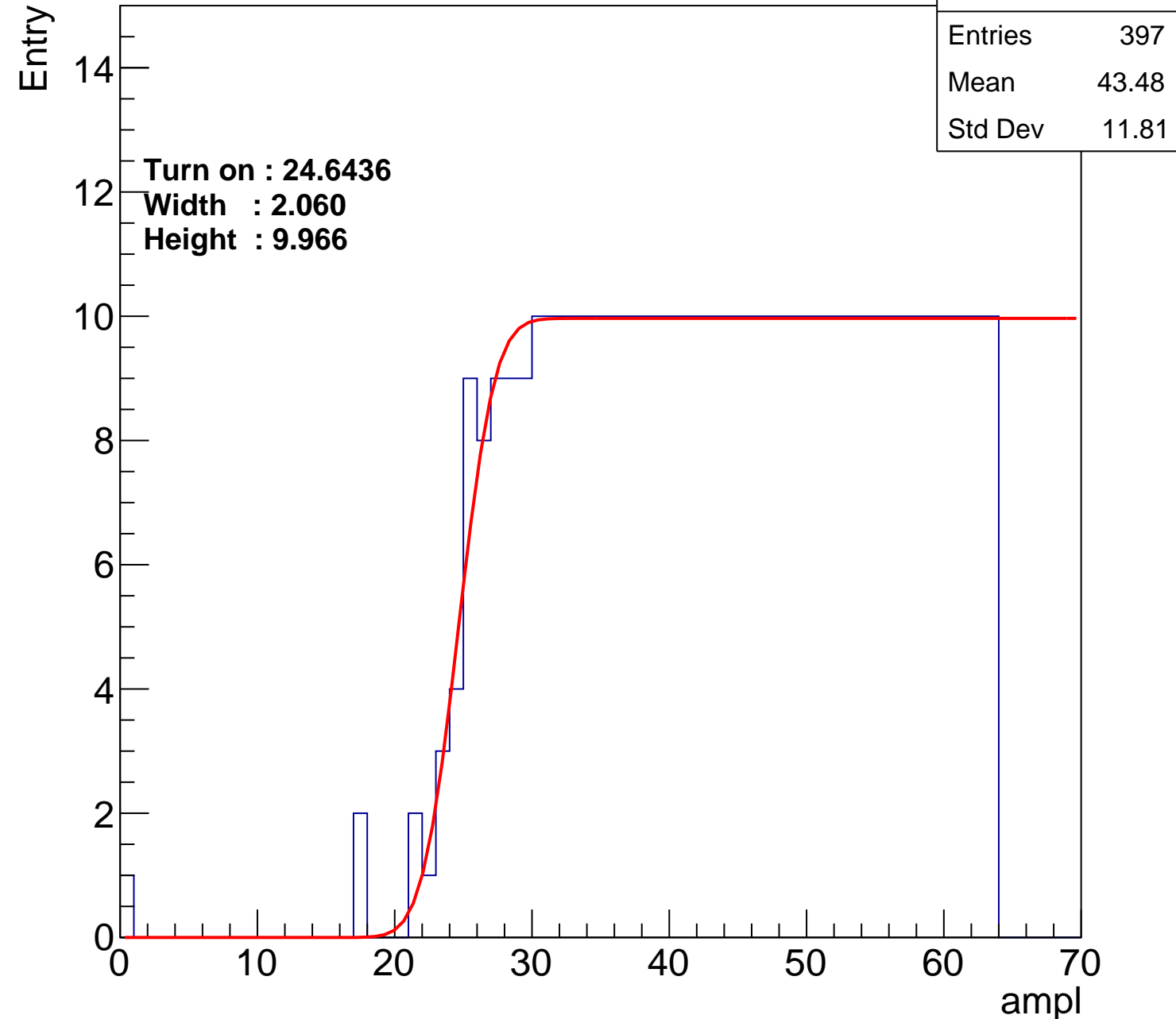
Width : 2.060

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch89

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.99 |
| Std Dev | 11.62 |

Turn on : 25.7786

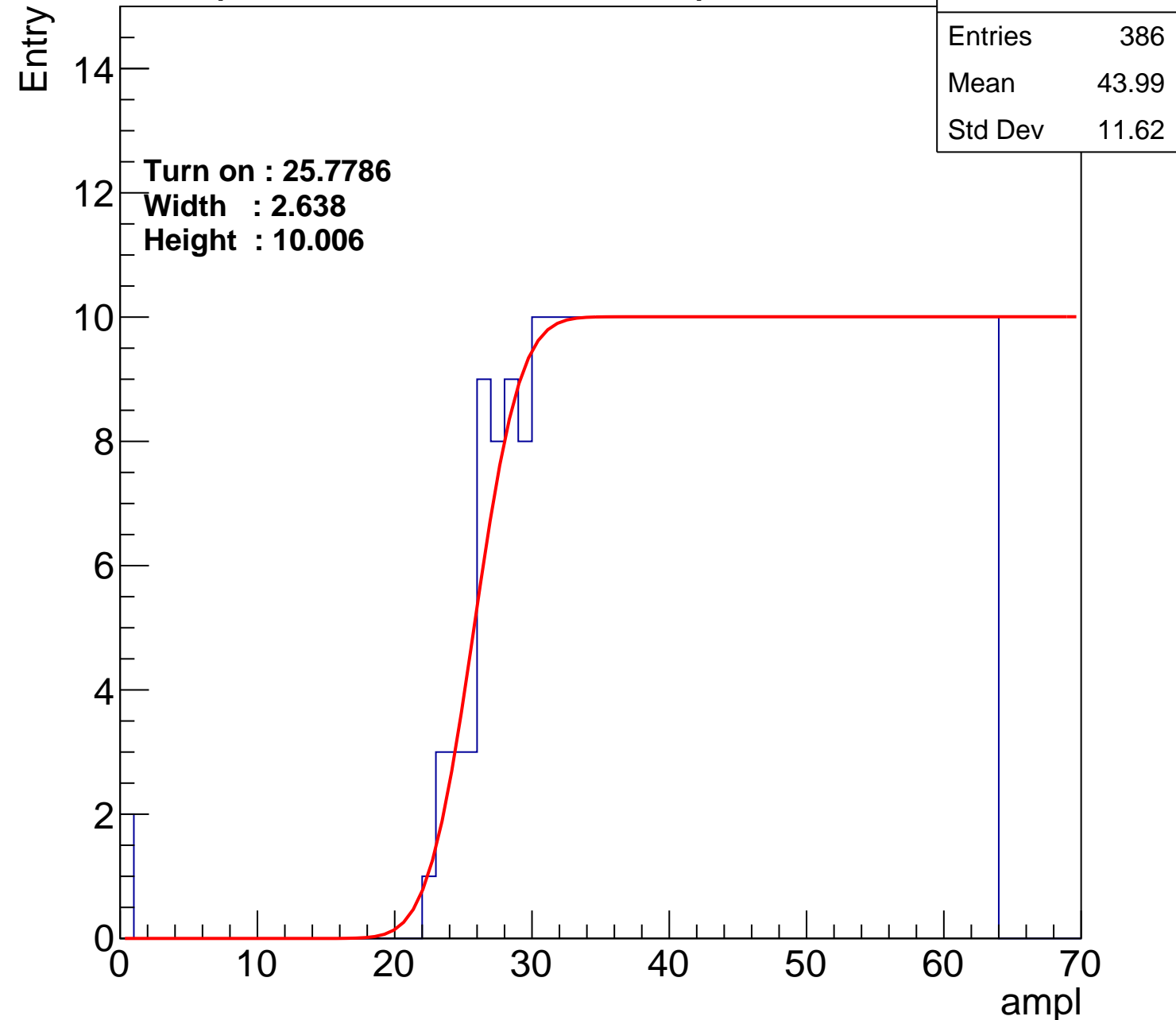
Width : 2.638

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch90

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.79 |
| Std Dev | 11.21 |

Turn on : 27.4809

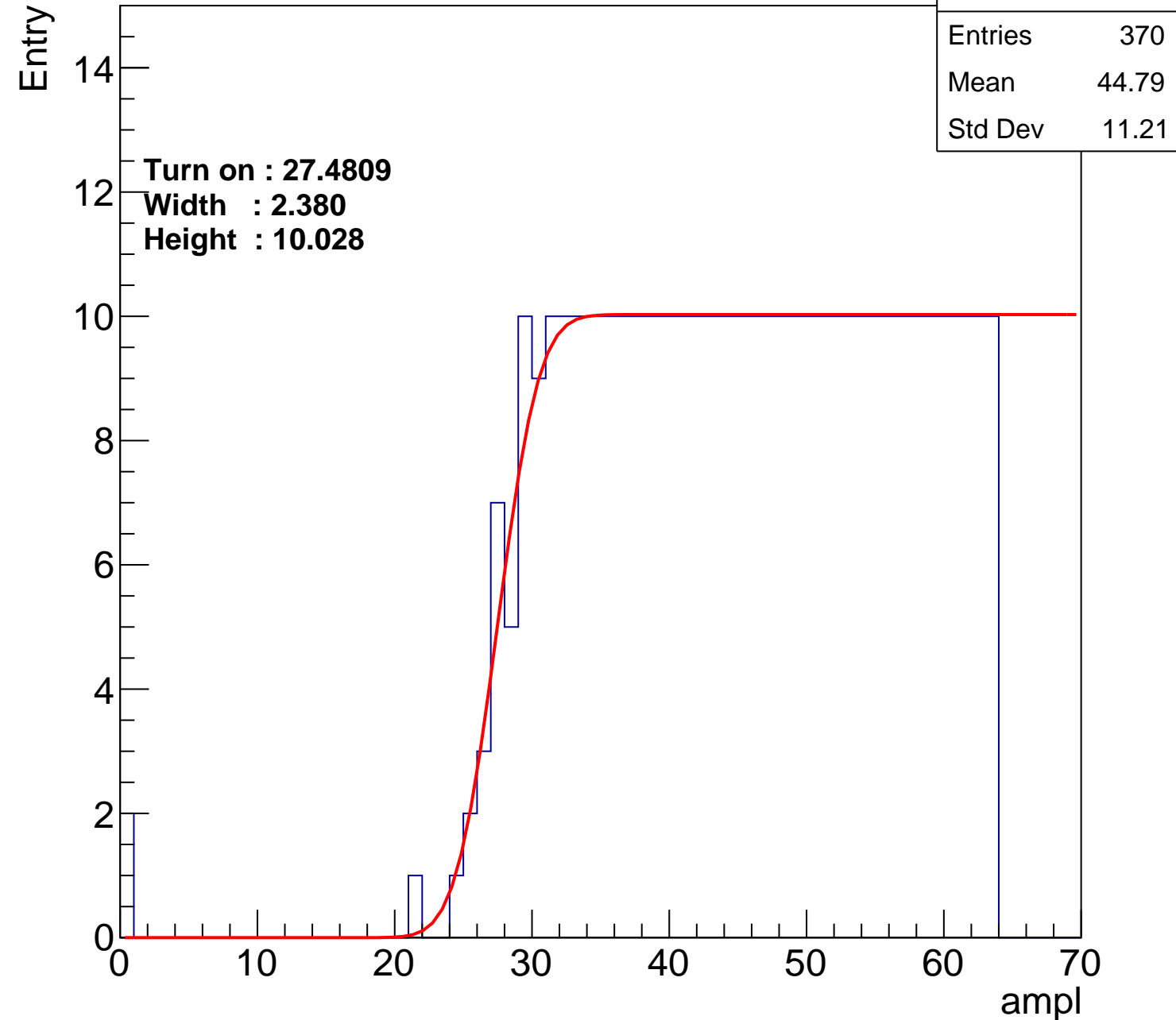
Width : 2.380

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch91

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.3 |
| Std Dev | 11.94 |

Turn on : 24.3330

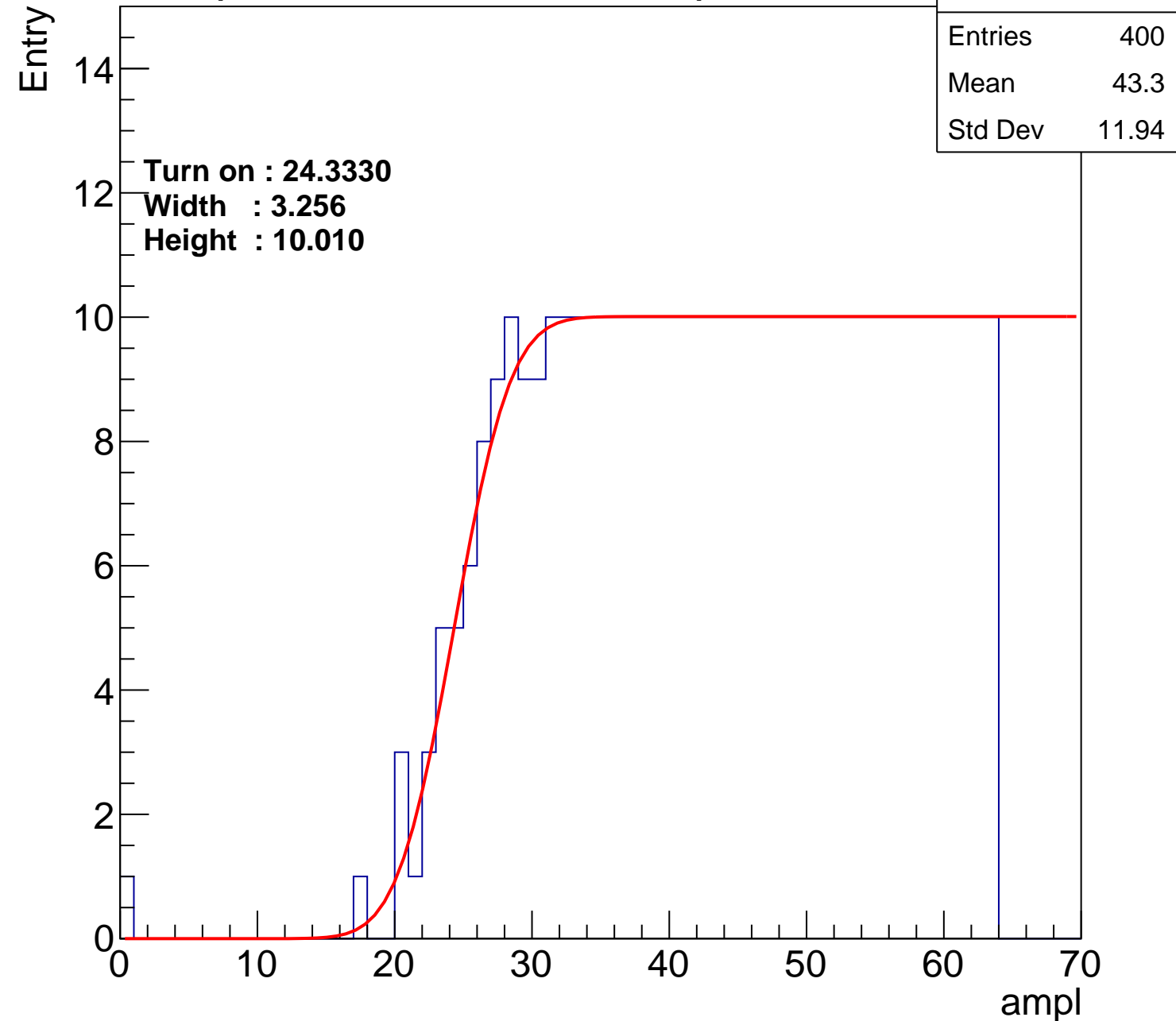
Width : 3.256

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch92

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.36 |
| Std Dev | 11.96 |

Turn on : 24.2685

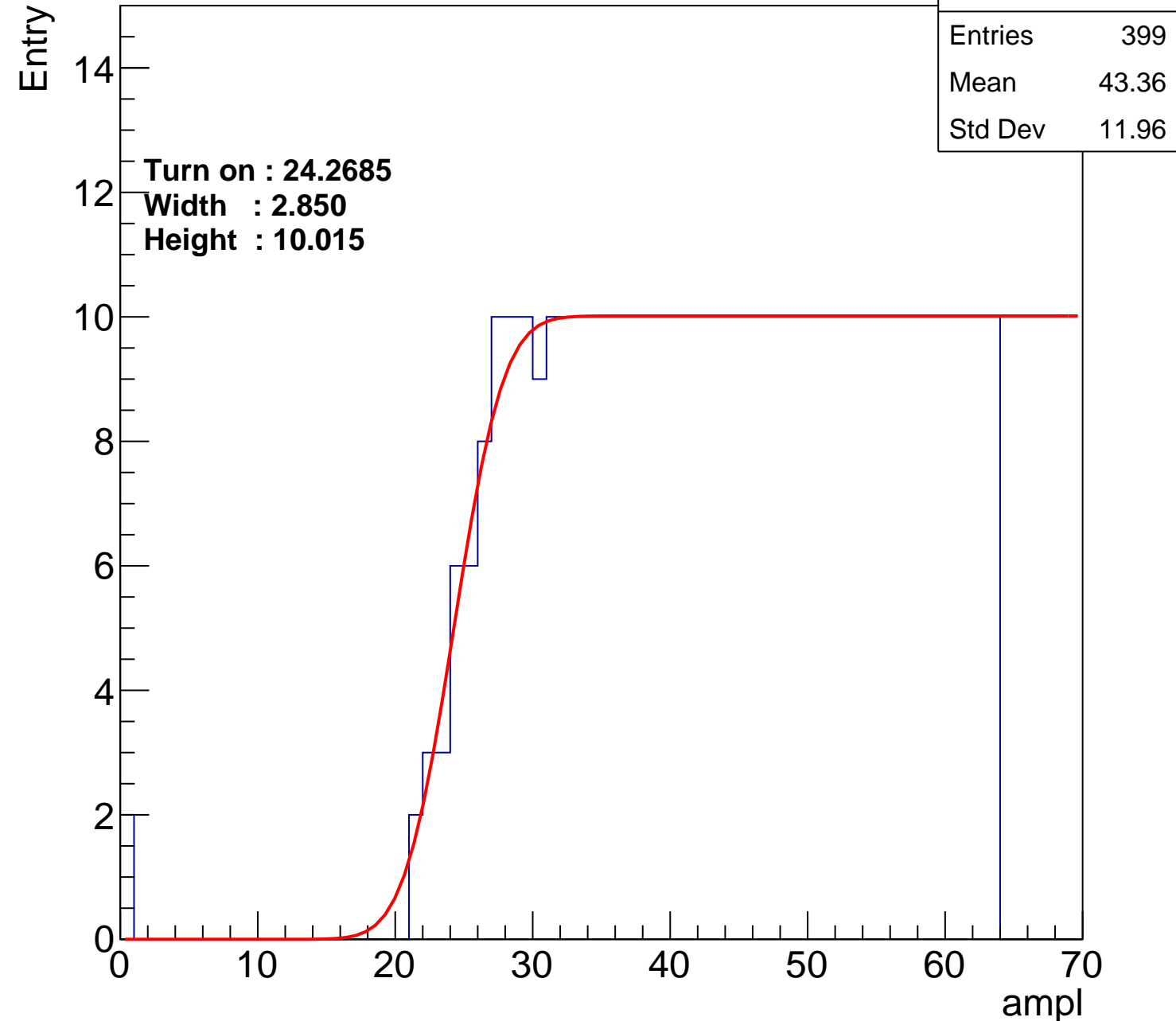
Width : 2.850

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch93

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.52 |
| Std Dev | 11.37 |

Turn on : 27.3219

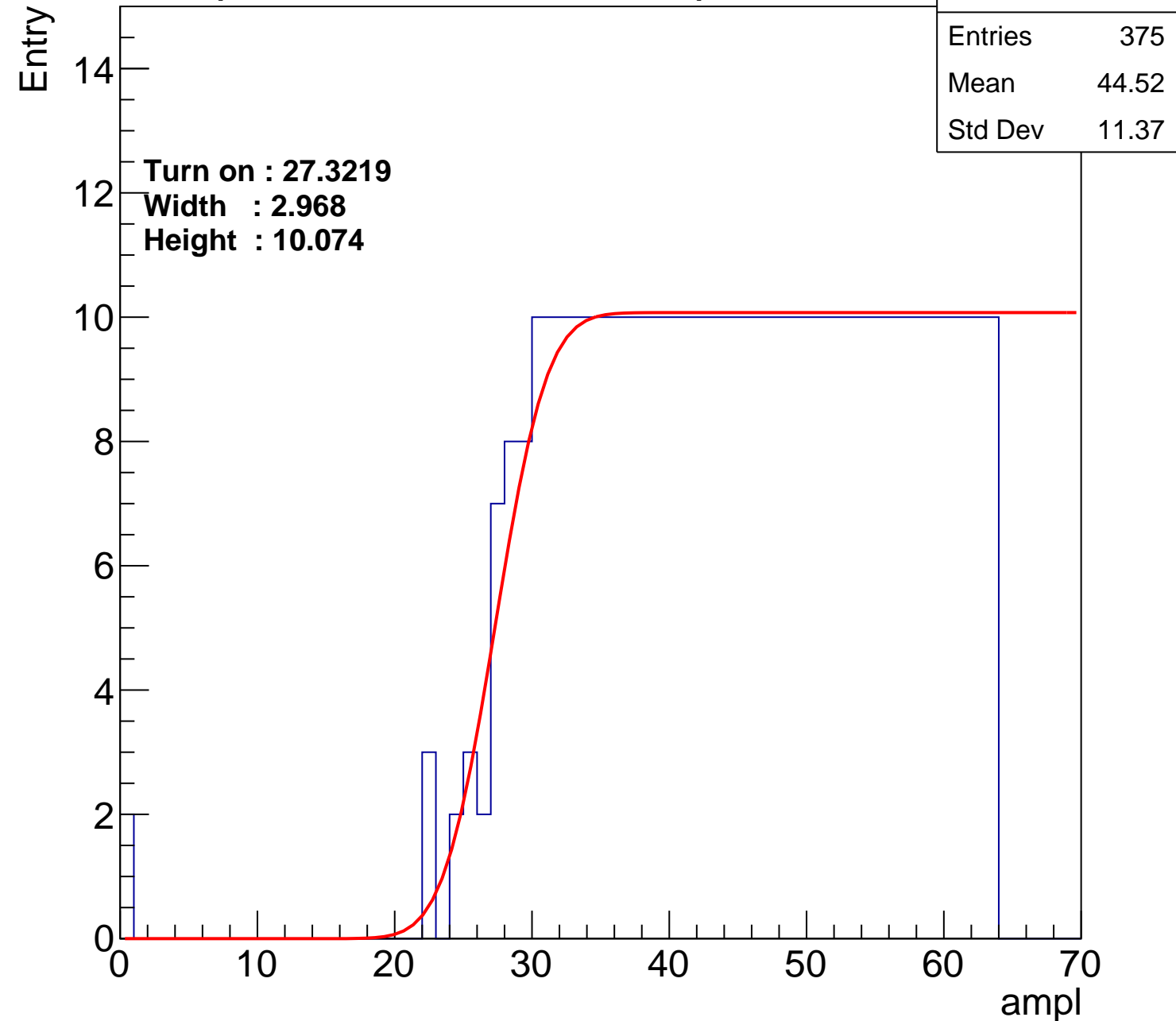
Width : 2.968

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch94

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.85 |
| Std Dev | 11.75 |

Turn on : 25.6573

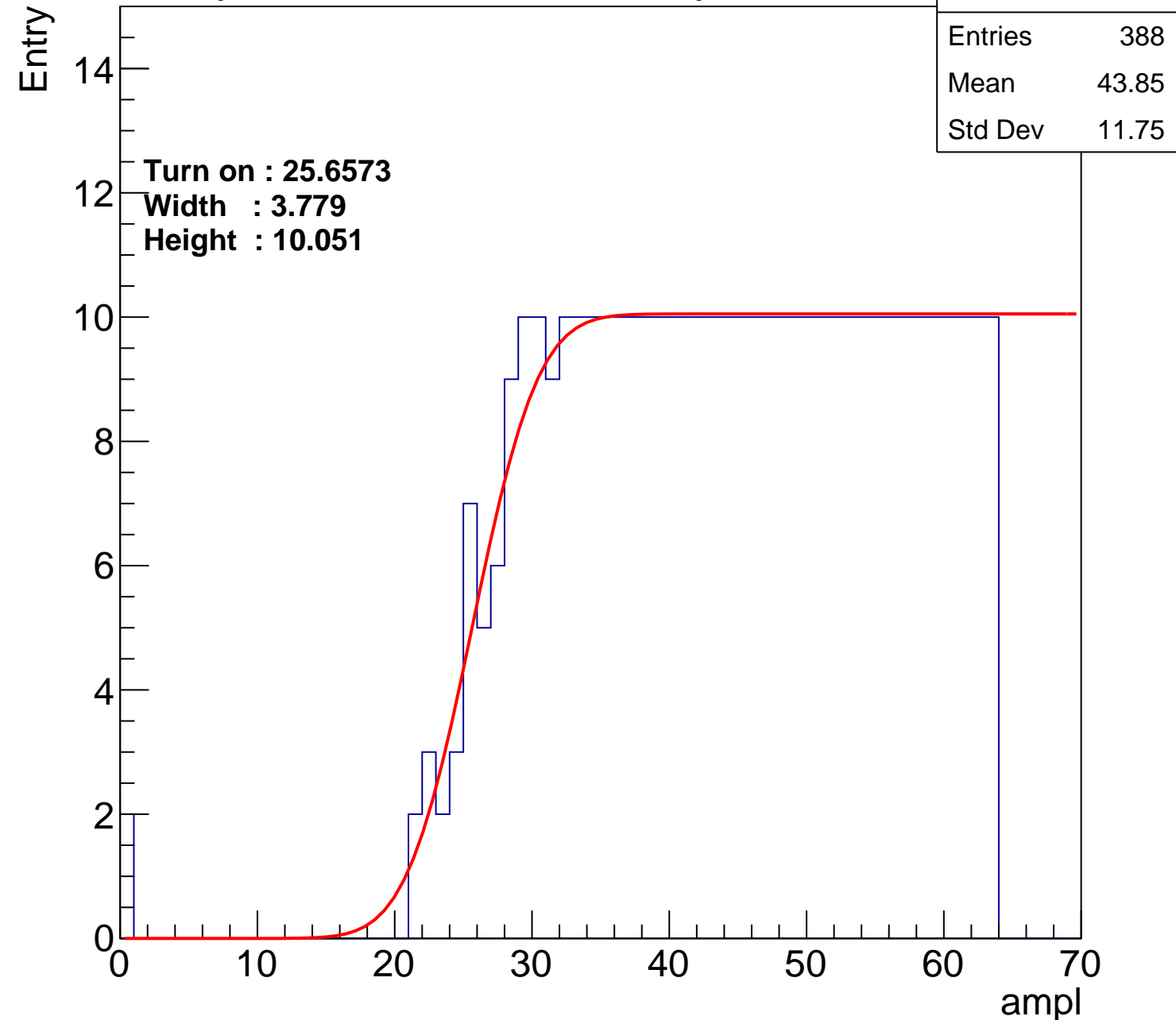
Width : 3.779

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch95

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 407 |
| Mean | 42.91 |
| Std Dev | 12.25 |

Turn on : 24.1581

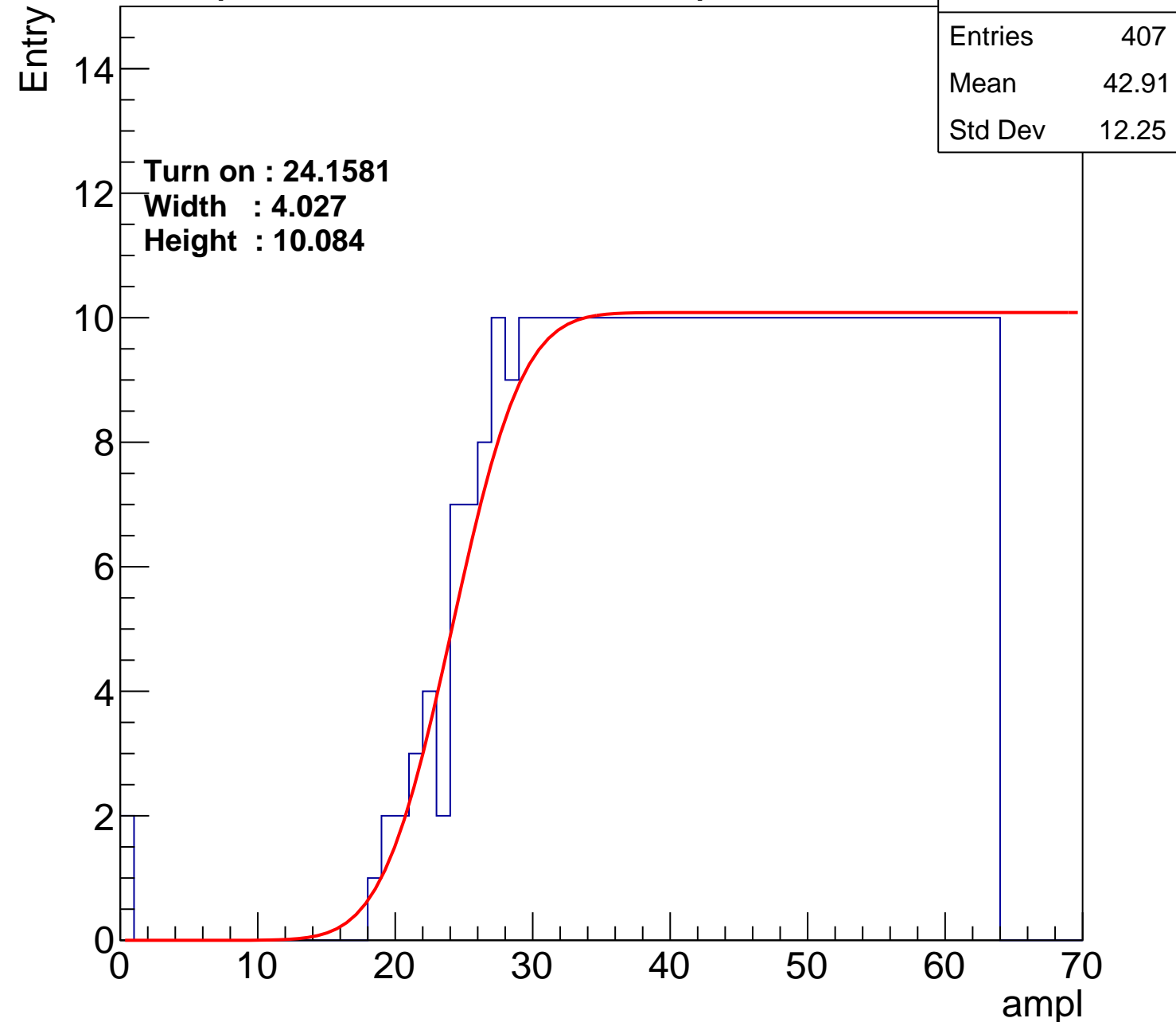
Width : 4.027

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch96

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.67 |
| Std Dev | 11.43 |

Turn on : 27.3425

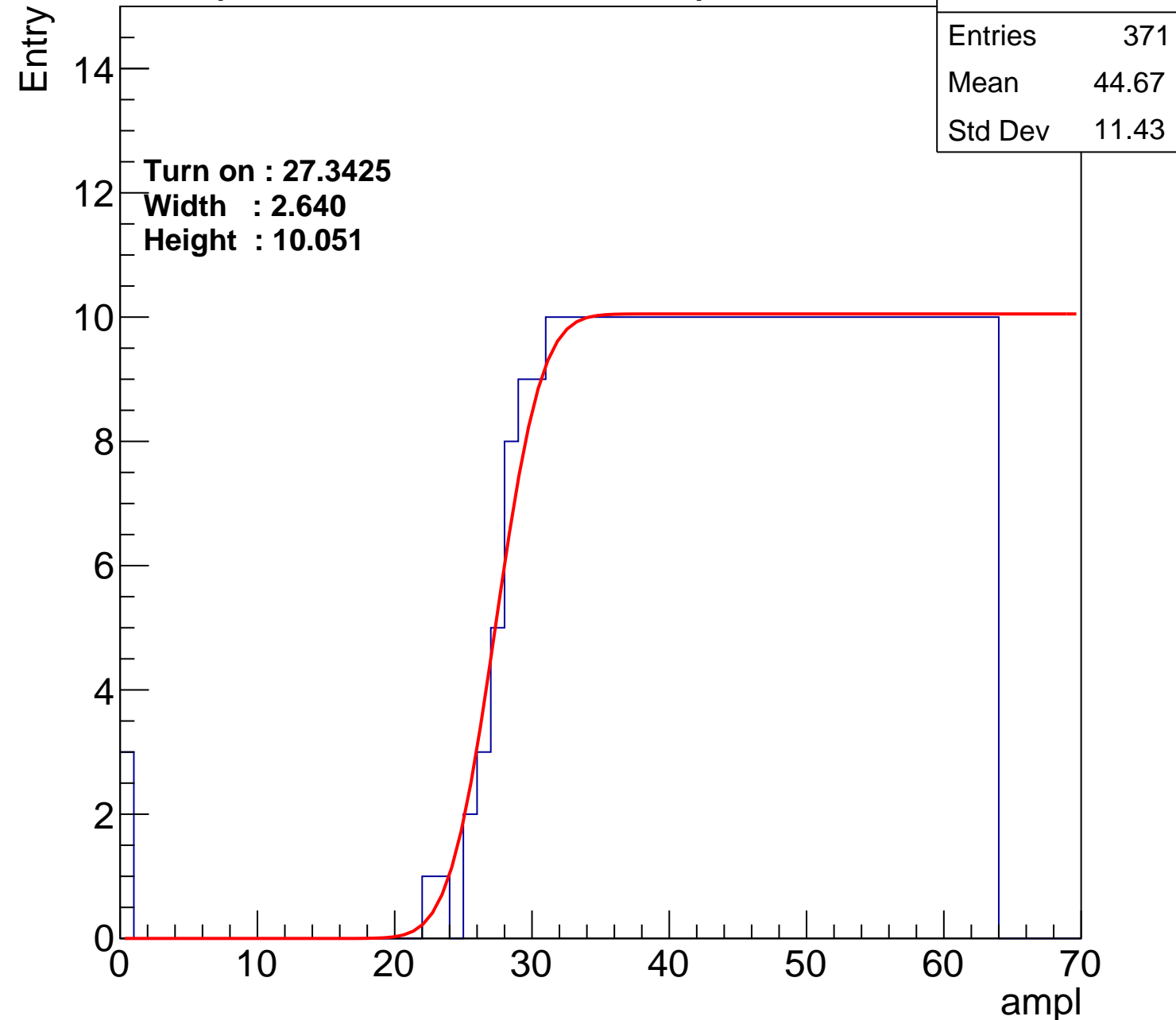
Width : 2.640

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch97

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.54 |
| Std Dev | 11.16 |

Turn on : 26.5912

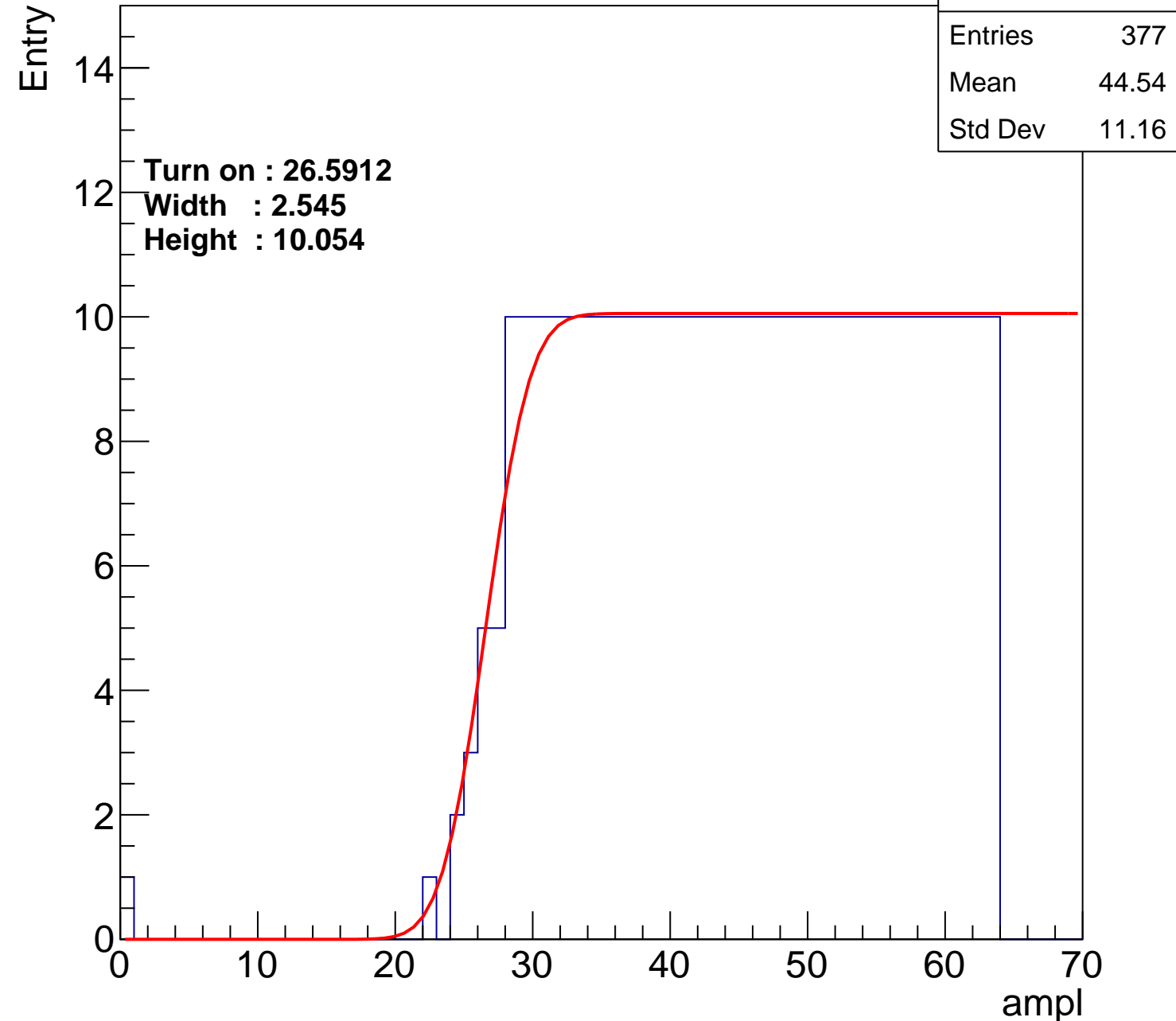
Width : 2.545

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch98

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 406 |
| Mean | 42.89 |
| Std Dev | 12.44 |

Turn on : 23.7867

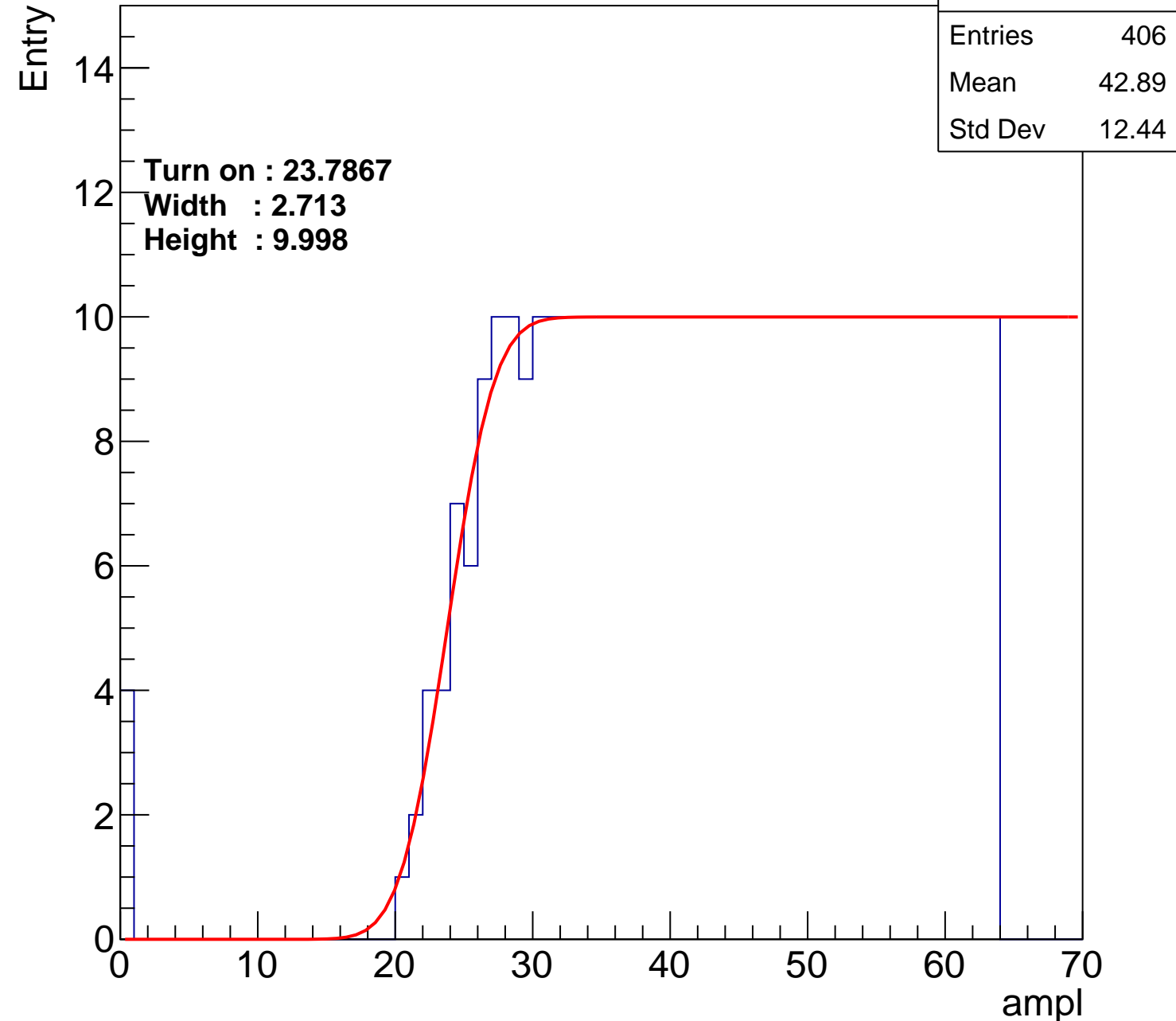
Width : 2.713

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch99

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.74 |
| Std Dev | 11.65 |

Turn on : 25.2342

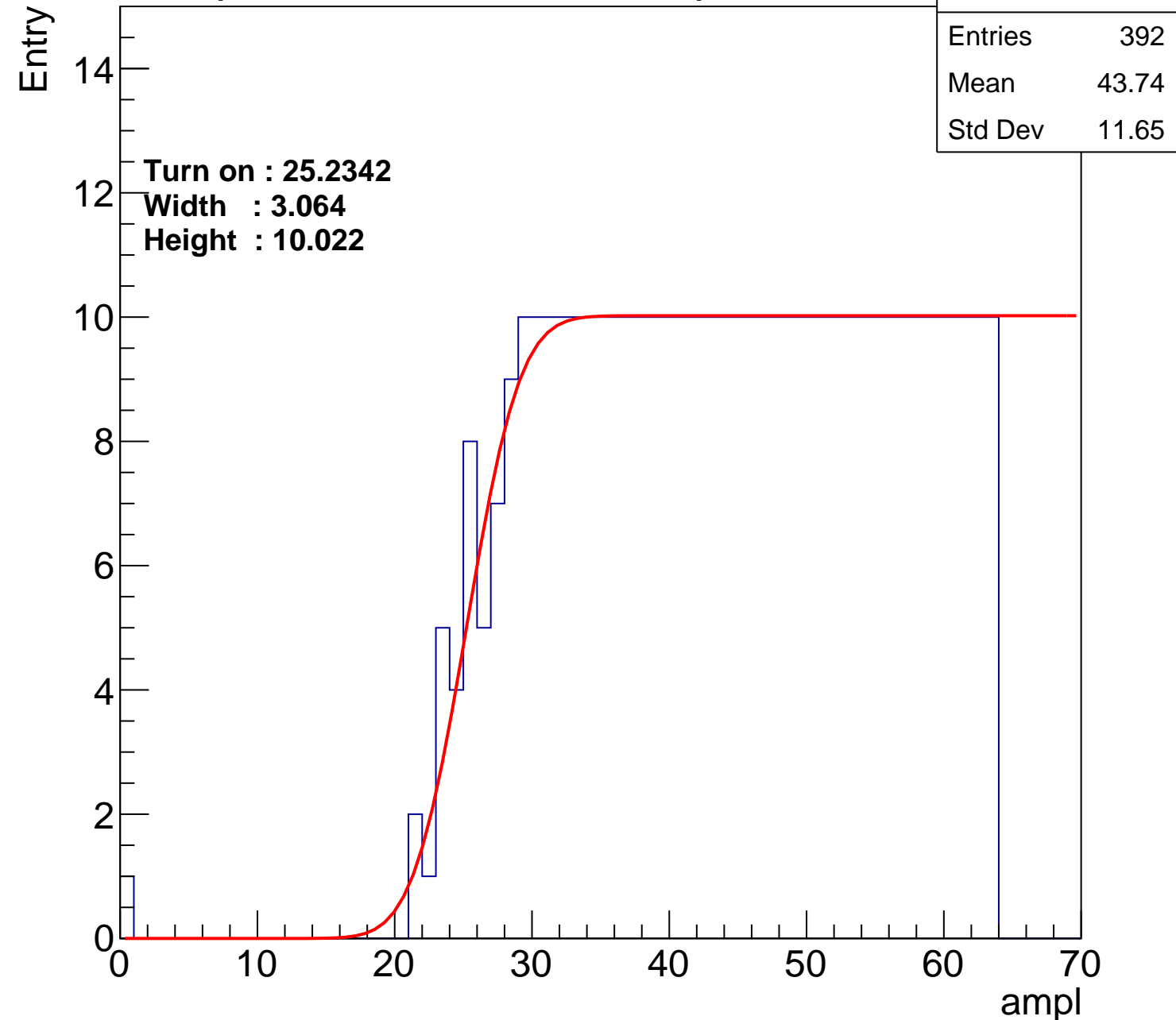
Width : 3.064

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch100

calib_packv5_042523_0143.root, FC#3, port B3

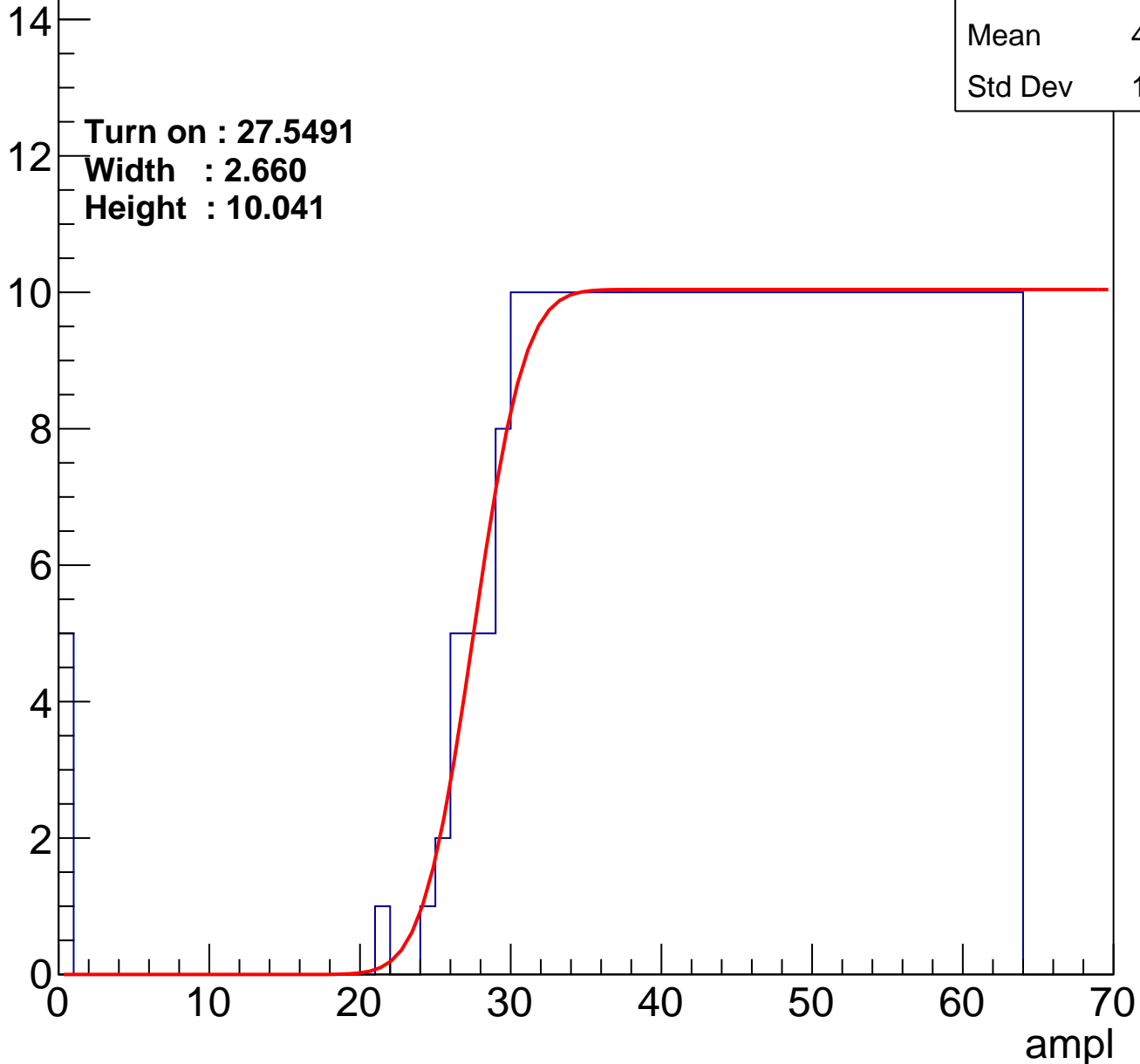
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.47 |
| Std Dev | 11.85 |

Turn on : 27.5491

Width : 2.660

Height : 10.041

Entry



B1L000S, U1-ch101

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.08 |
| Std Dev | 11.52 |

Turn on : 26.3181

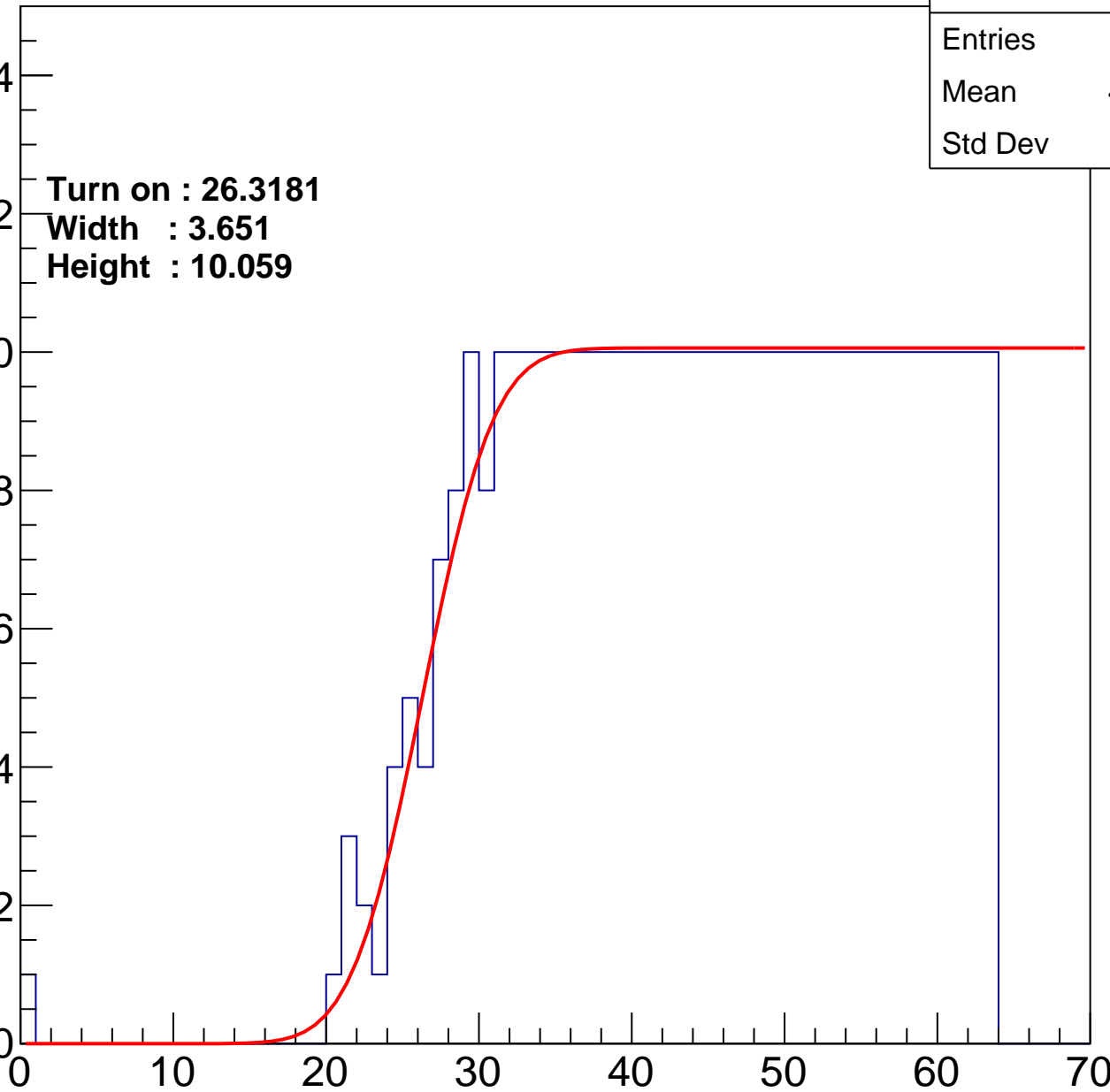
Width : 3.651

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch102

calib_packv5_042523_0143.root, FC#3, port B3

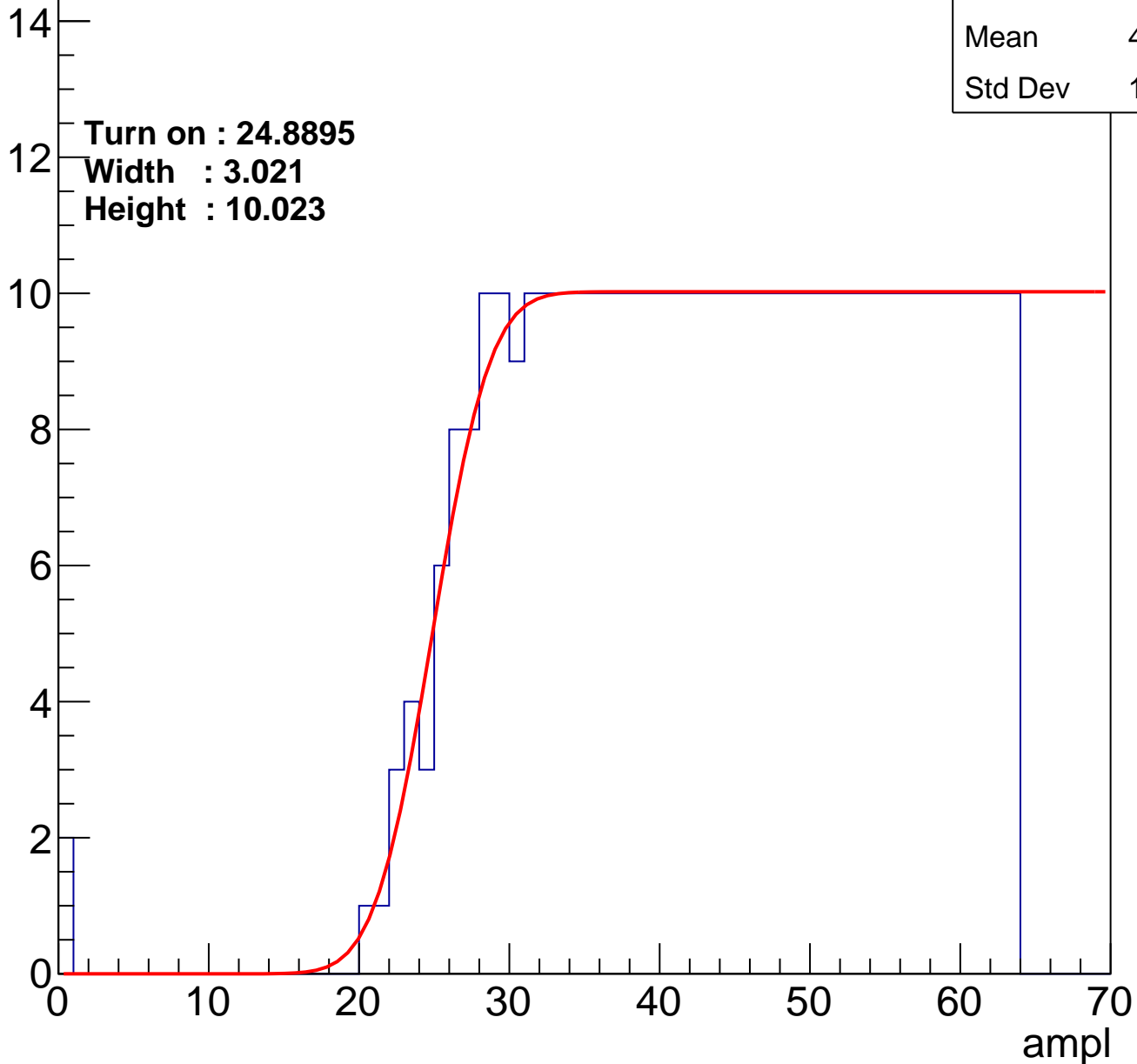
| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.53 |
| Std Dev | 11.89 |

Turn on : 24.8895

Width : 3.021

Height : 10.023

Entry



calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.31 |
| Std Dev | 12.25 |

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.31 |
| Std Dev | 12.25 |

Width : 3.184

Height : 9.999



B1L000S, U1-ch104

calib_packv5_042523_0143.root, FC#3, port B3

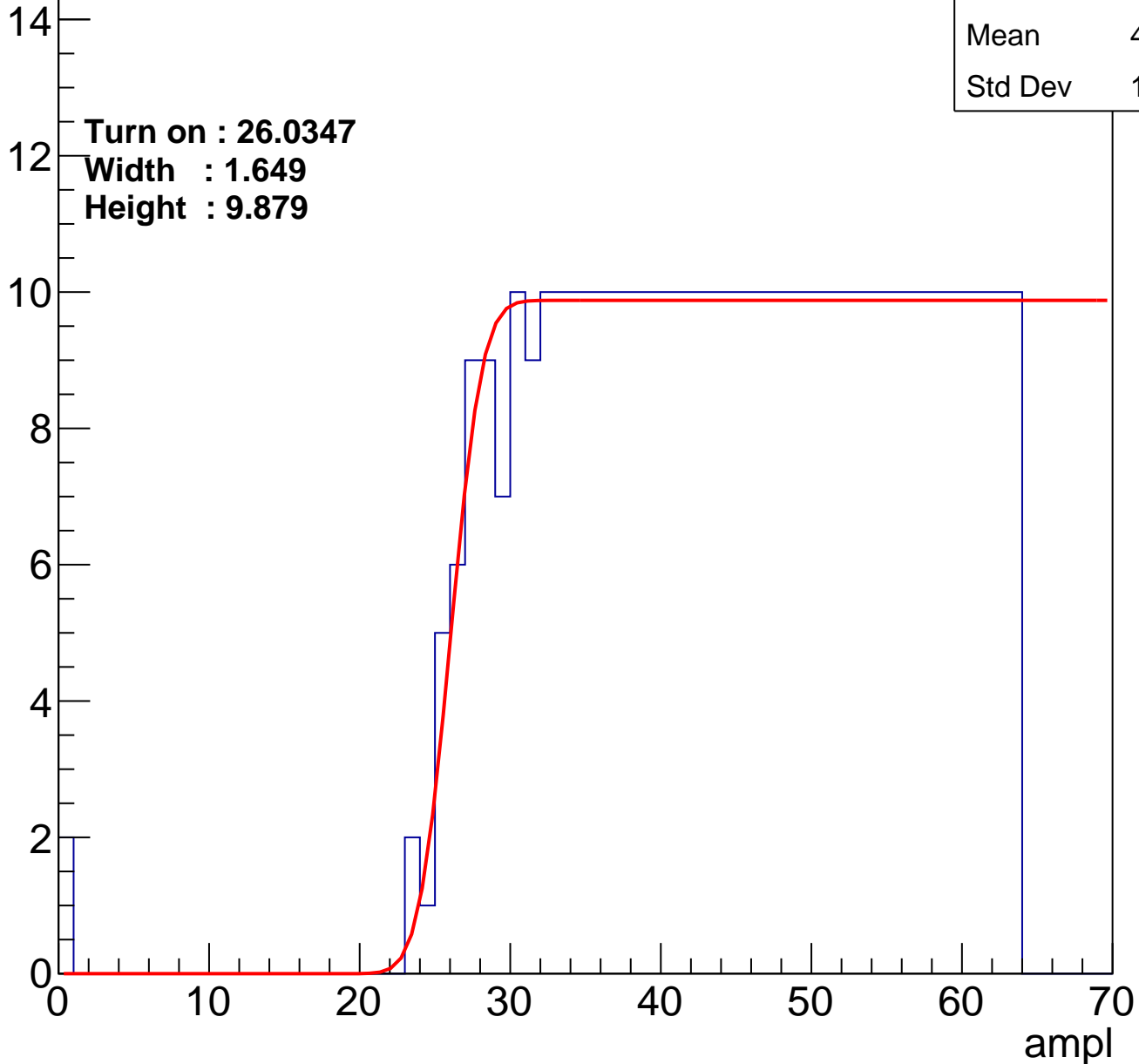
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.28 |
| Std Dev | 11.47 |

Turn on : 26.0347

Width : 1.649

Height : 9.879

Entry



B1L000S, U1-ch105

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.04 |
| Std Dev | 11.8 |

Turn on : 28.1803

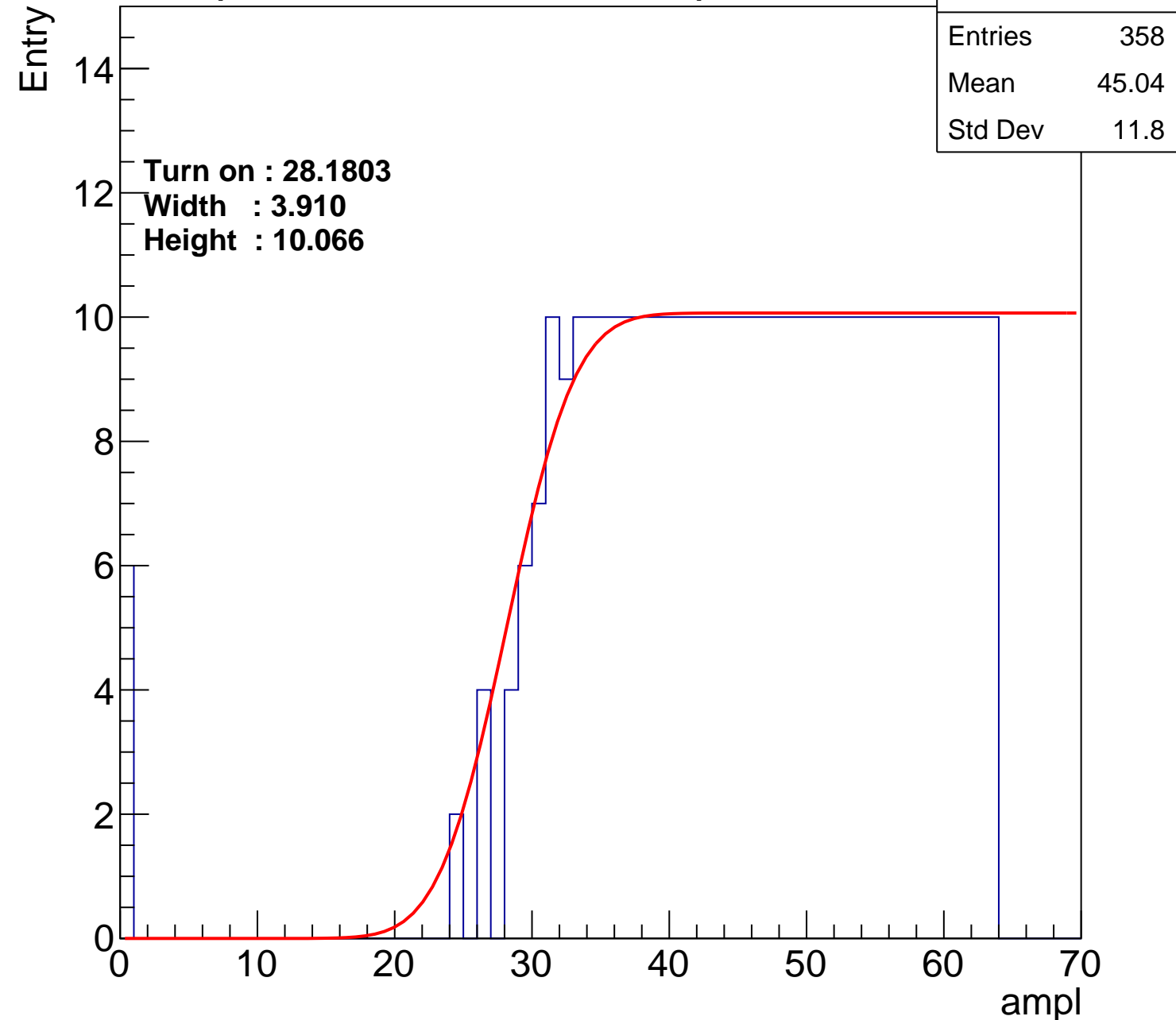
Width : 3.910

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch106

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.52 |
| Std Dev | 11.79 |

Turn on : 24.9797

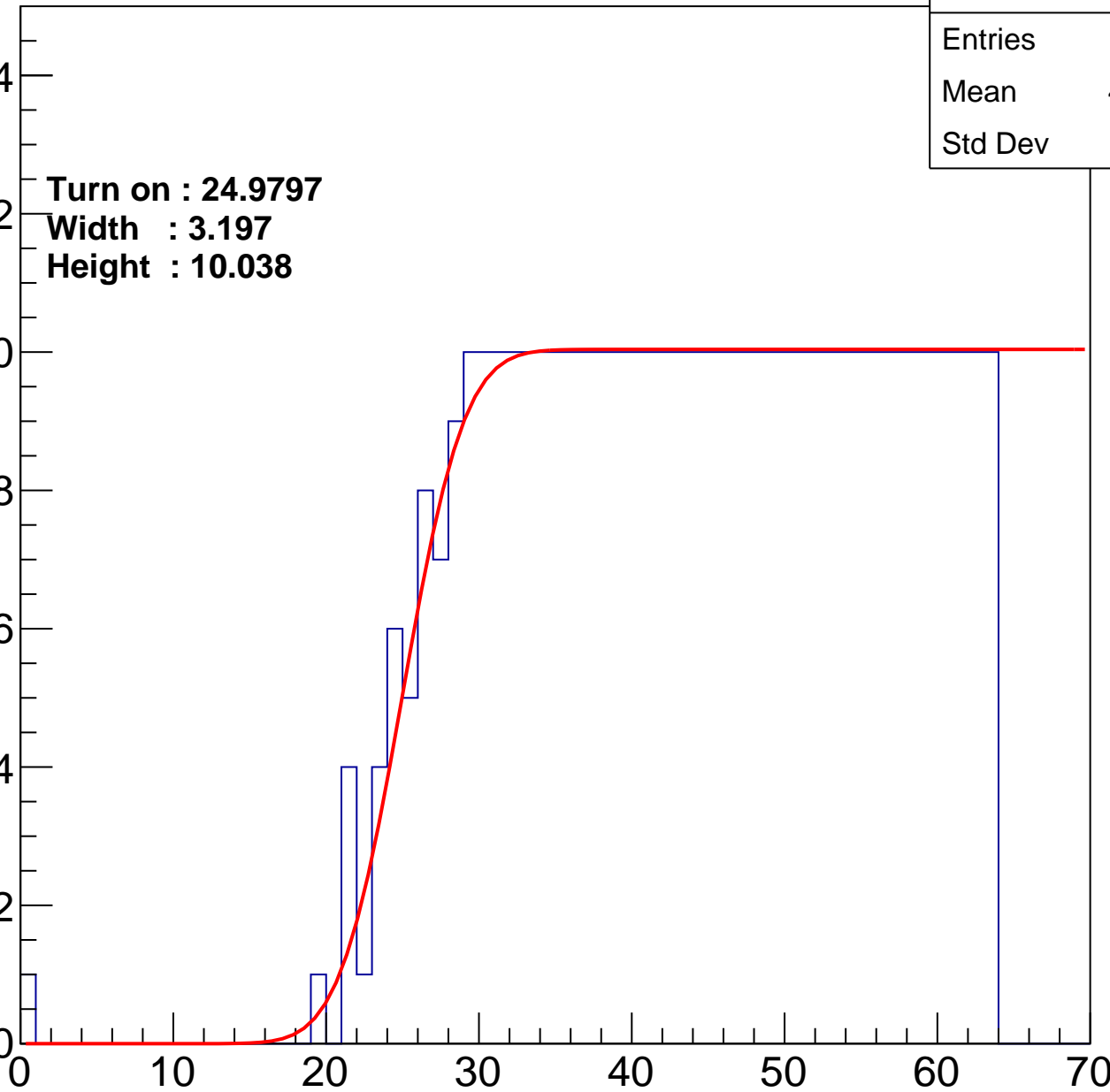
Width : 3.197

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch107

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.35 |
| Std Dev | 12.13 |

Turn on : 24.6541

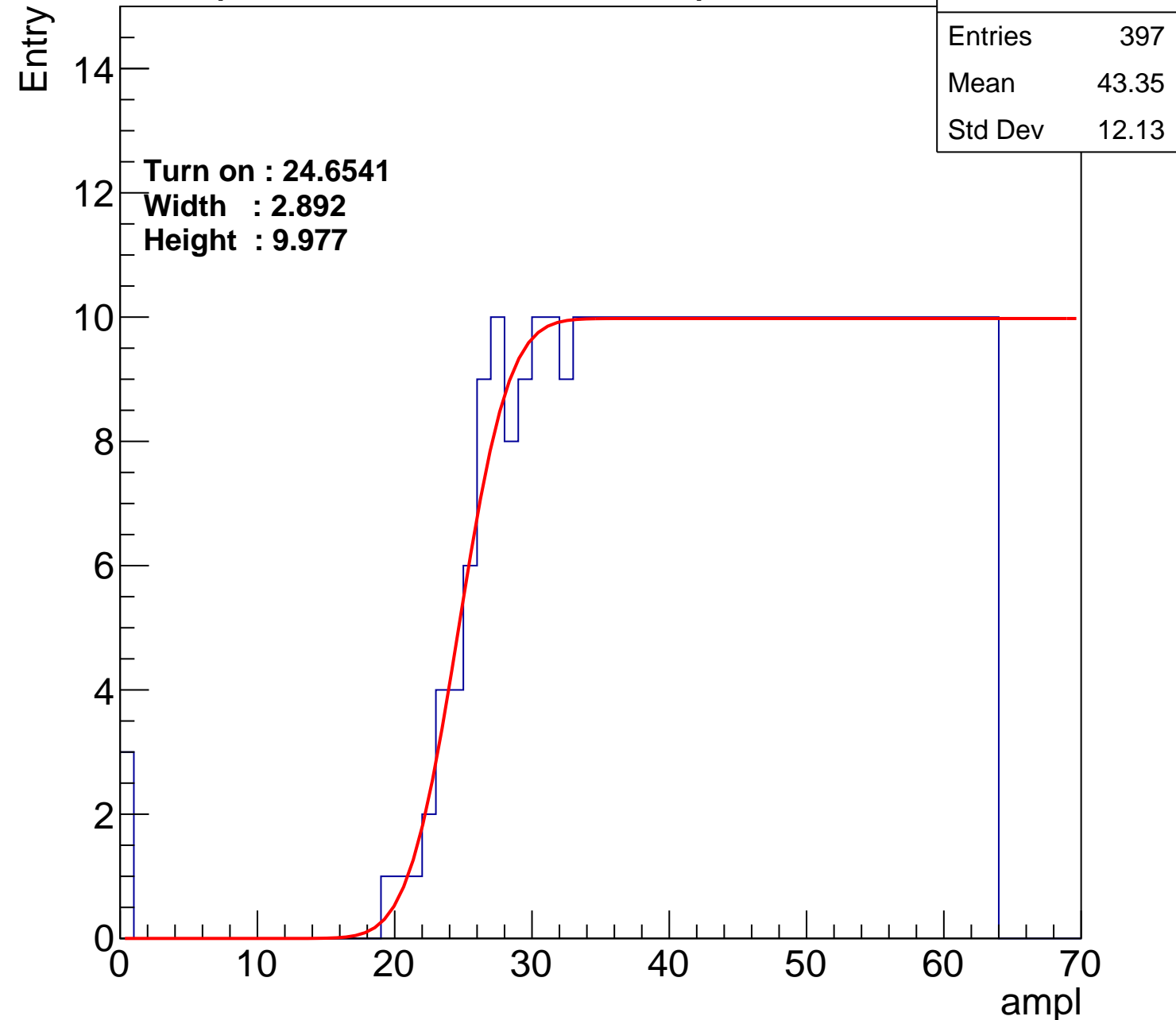
Width : 2.892

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch108

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.93 |
| Std Dev | 11.69 |

Turn on : 26.1531

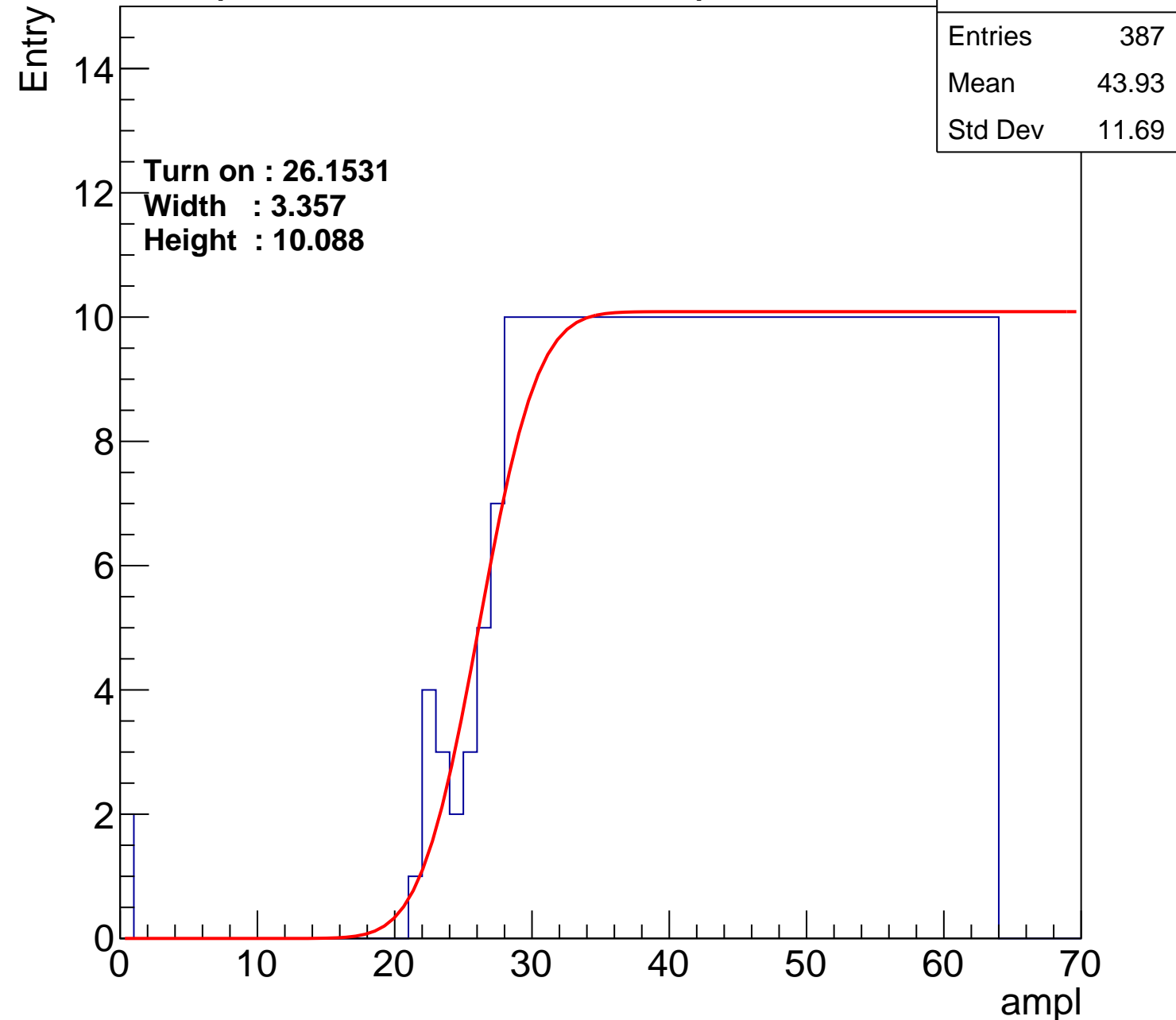
Width : 3.357

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch109

calib_packv5_042523_0143.root, FC#3, port B3

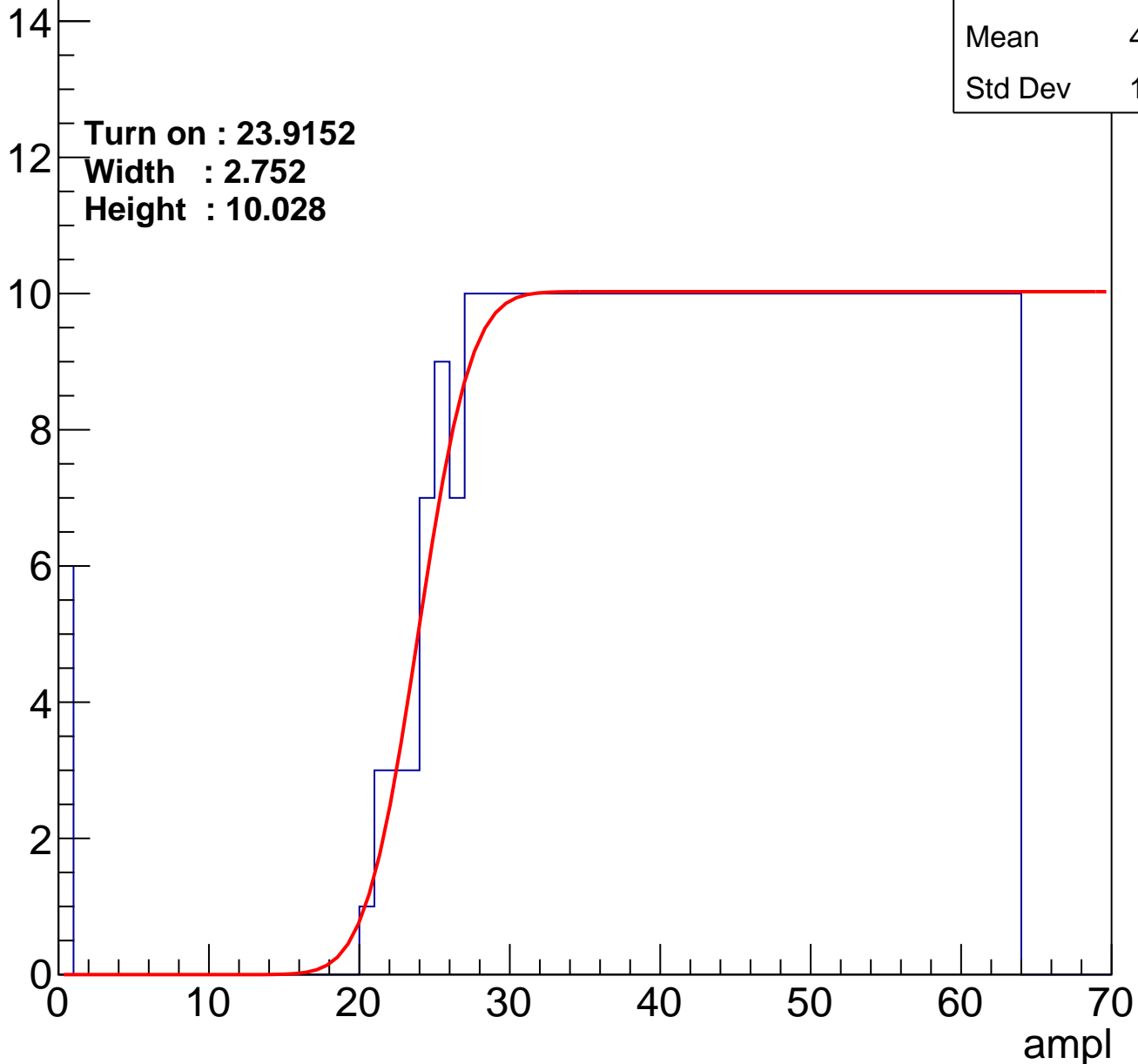
| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.65 |
| Std Dev | 12.77 |

Turn on : 23.9152

Width : 2.752

Height : 10.028

Entry



B1L000S, U1-ch110

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.45 |
| Std Dev | 11.81 |

Turn on : 24.6270

Width : 2.671

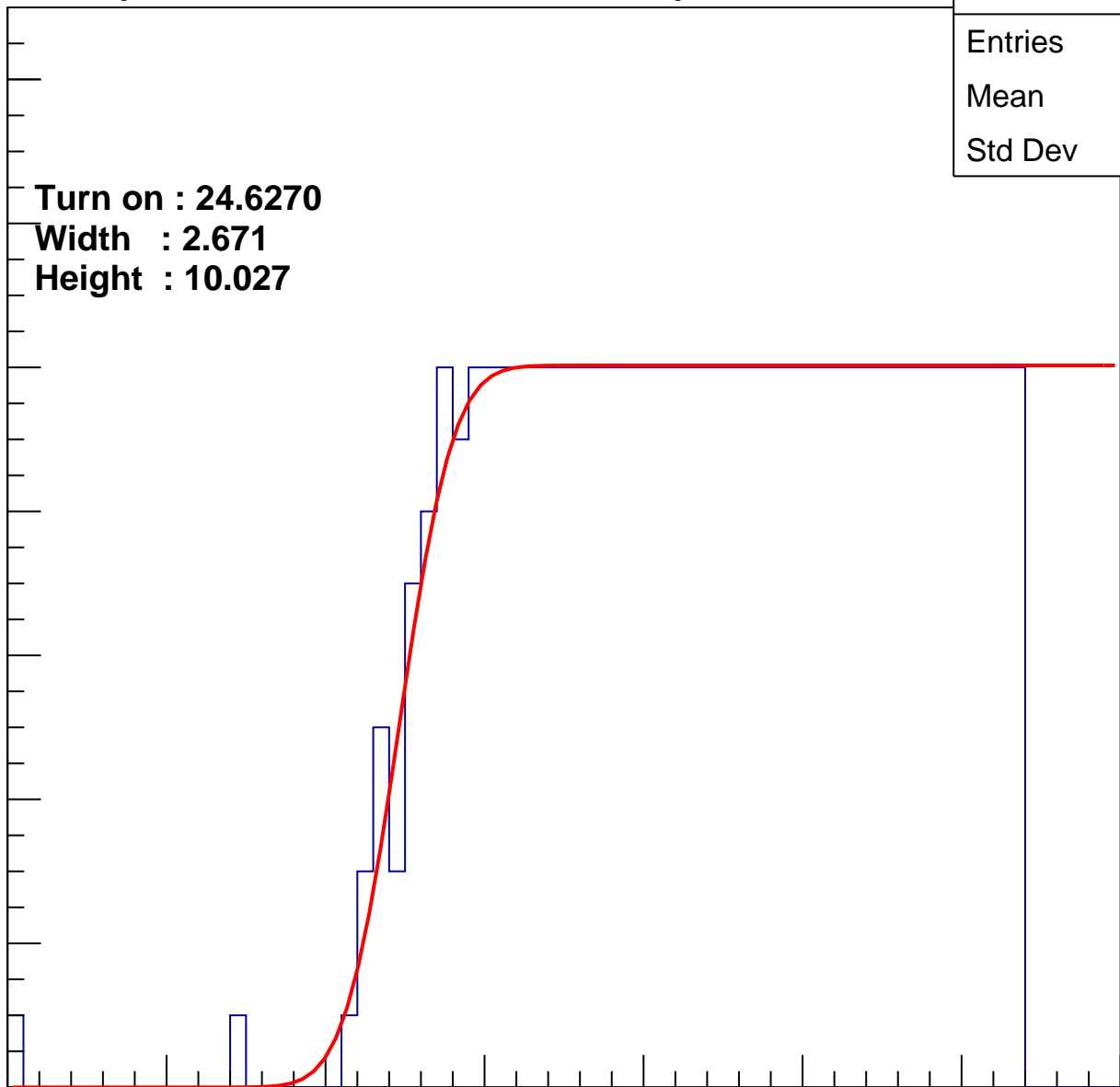
Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U1-ch111

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 407 |
| Mean | 42.77 |
| Std Dev | 12.63 |

Turn on : 24.2490

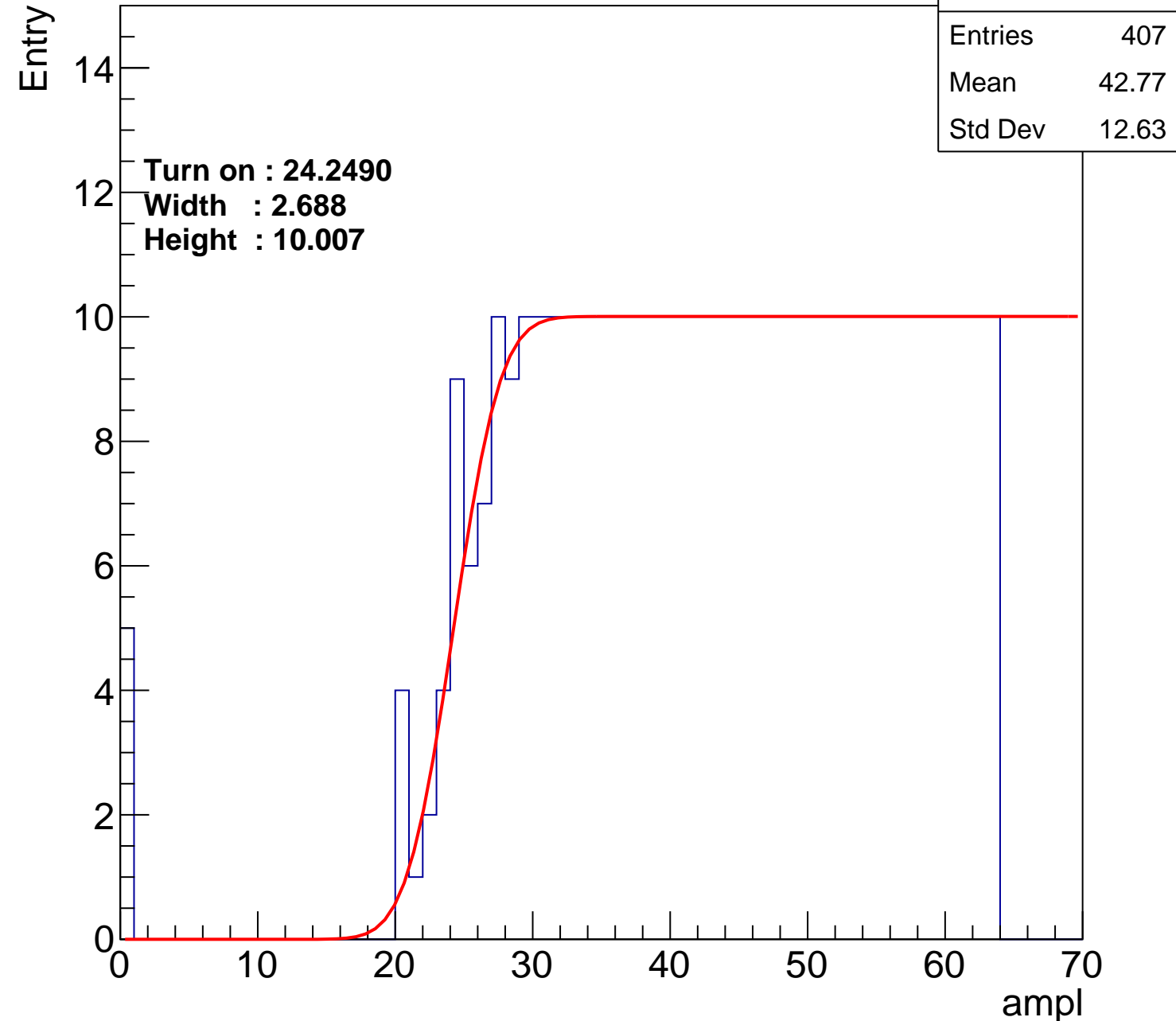
Width : 2.688

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch112

calib_packv5_042523_0143.root, FC#3, port B3

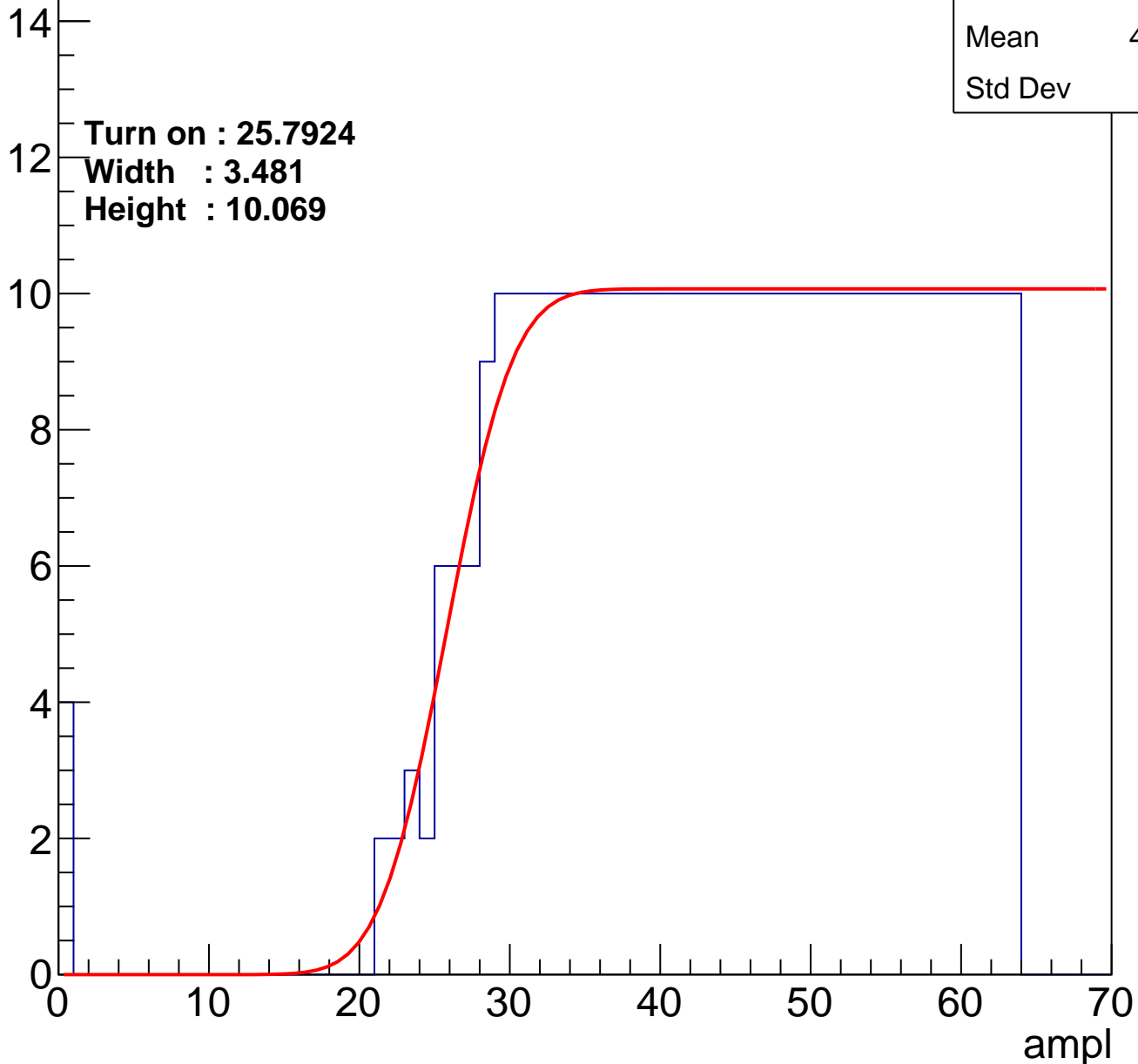
| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.65 |
| Std Dev | 12.1 |

Turn on : 25.7924

Width : 3.481

Height : 10.069

Entry



B1L000S, U1-ch113

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.43 |
| Std Dev | 11.9 |

Turn on : 24.6690

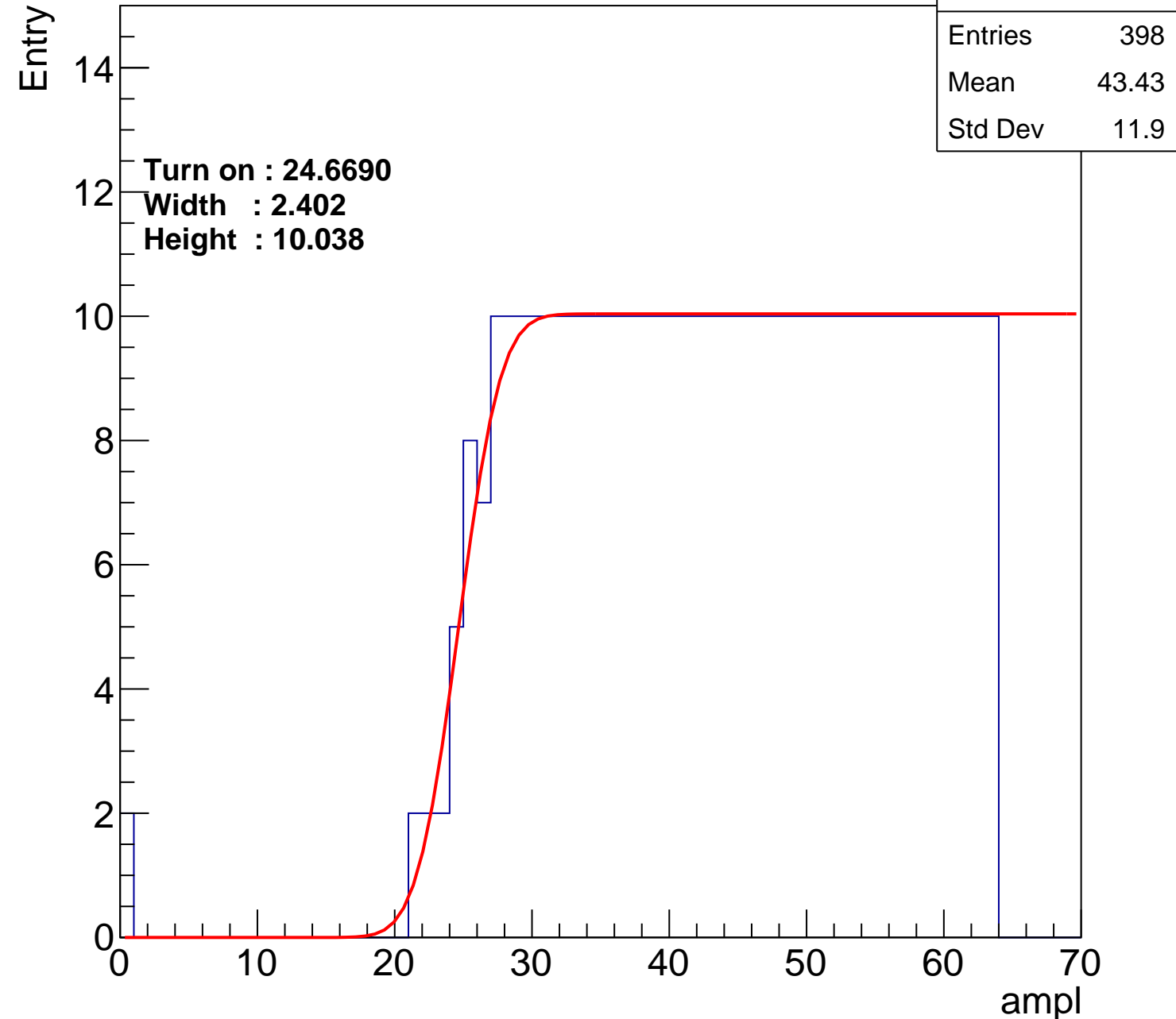
Width : 2.402

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch114

calib_packv5_042523_0143.root, FC#3, port B3

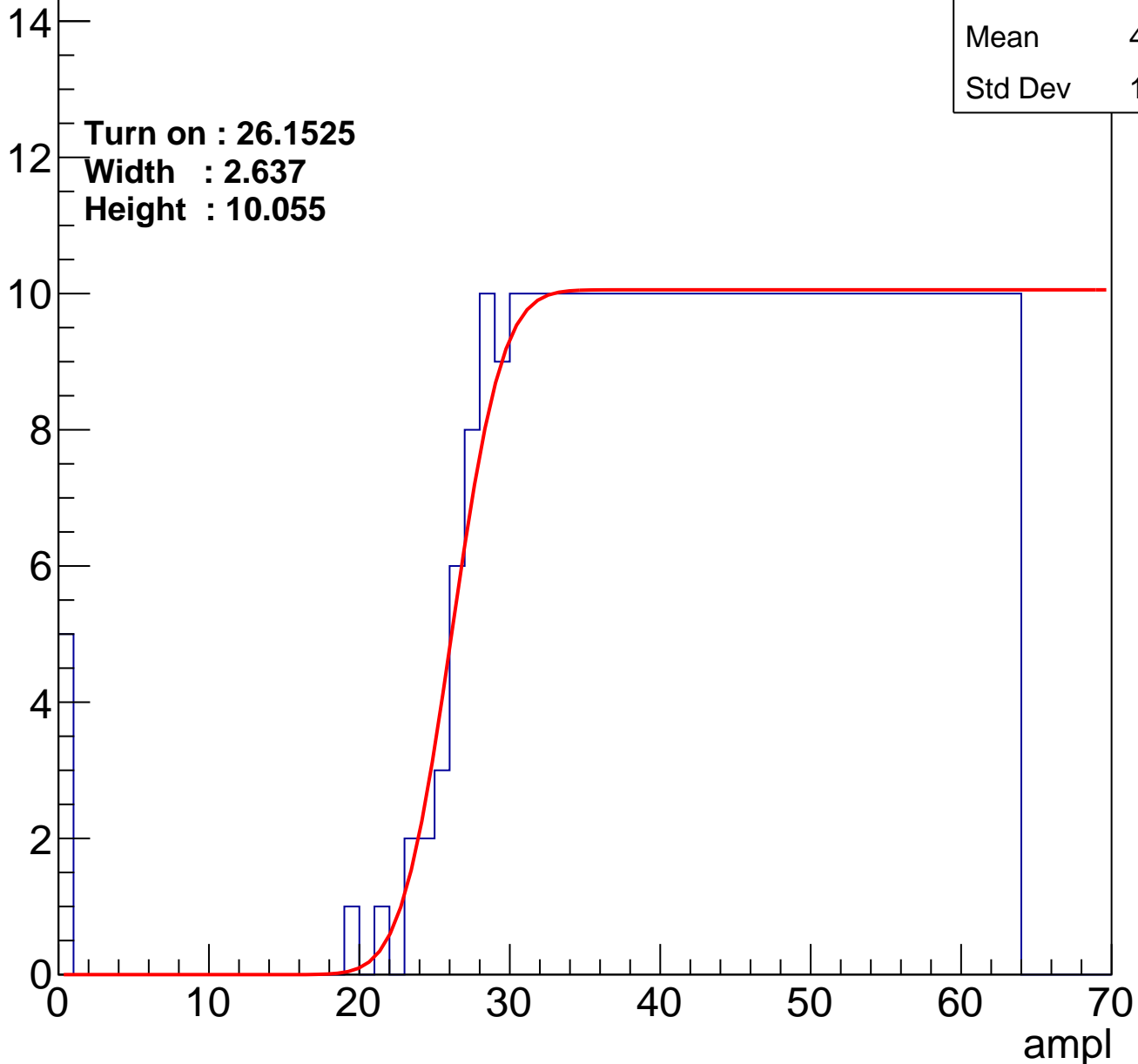
| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.75 |
| Std Dev | 12.16 |

Turn on : 26.1525

Width : 2.637

Height : 10.055

Entry



B1L000S, U1-ch115

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.41 |
| Std Dev | 12.21 |

Turn on : 24.6392

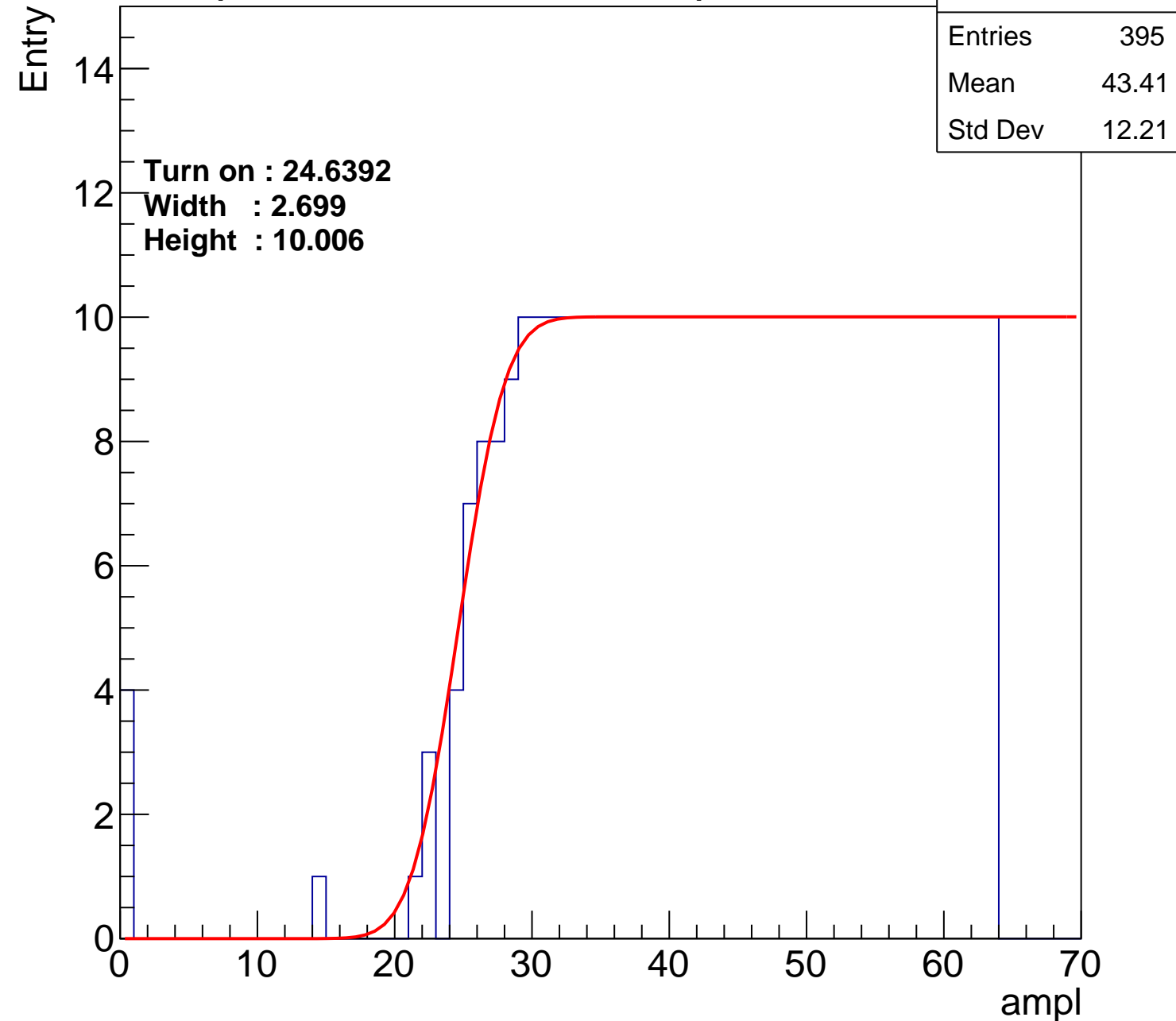
Width : 2.699

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch116

calib_packv5_042523_0143.root, FC#3, port B3

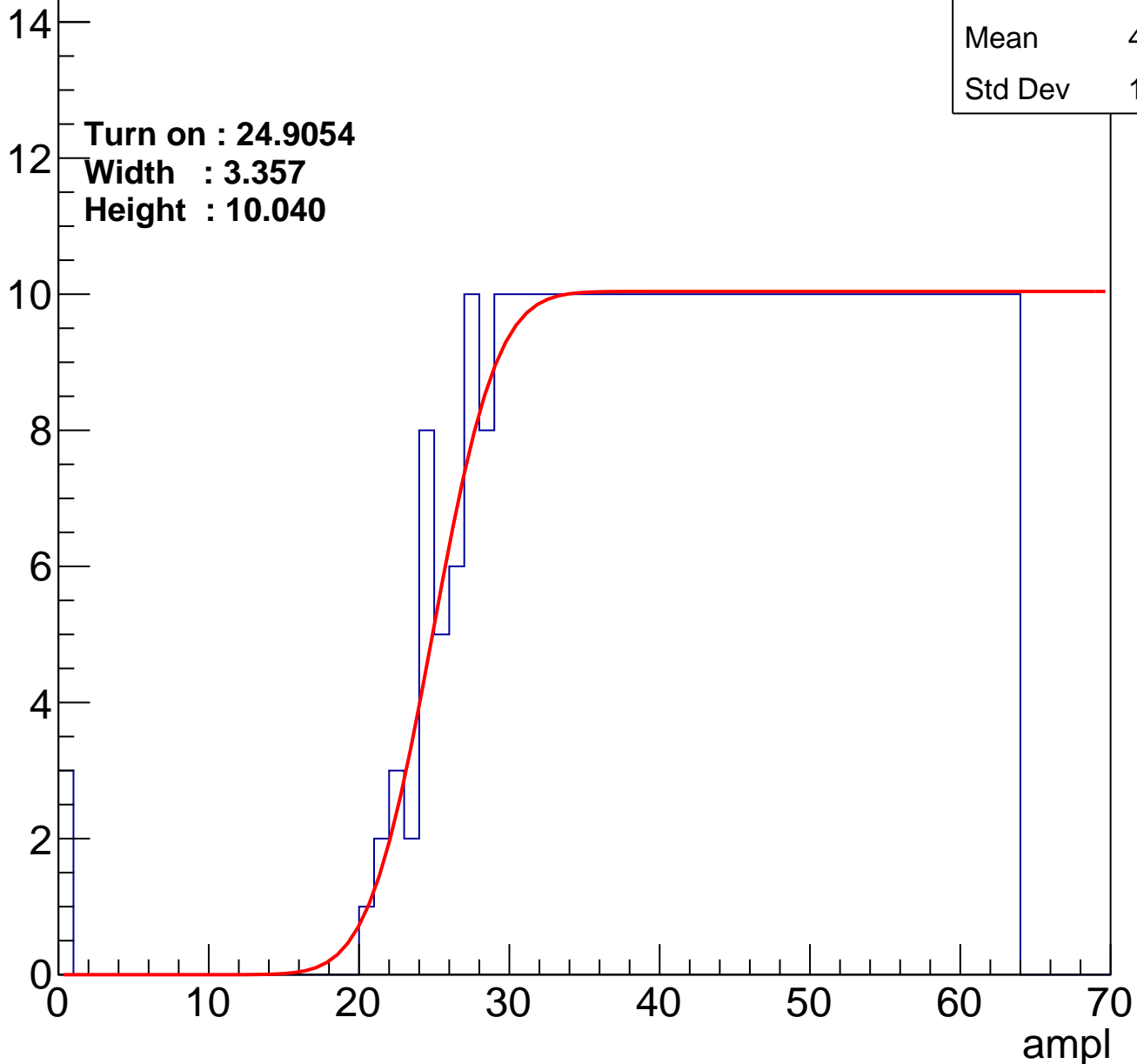
| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.32 |
| Std Dev | 12.13 |

Turn on : 24.9054

Width : 3.357

Height : 10.040

Entry



B1L000S, U1-ch117

calib_packv5_042523_0143.root, FC#3, port B3

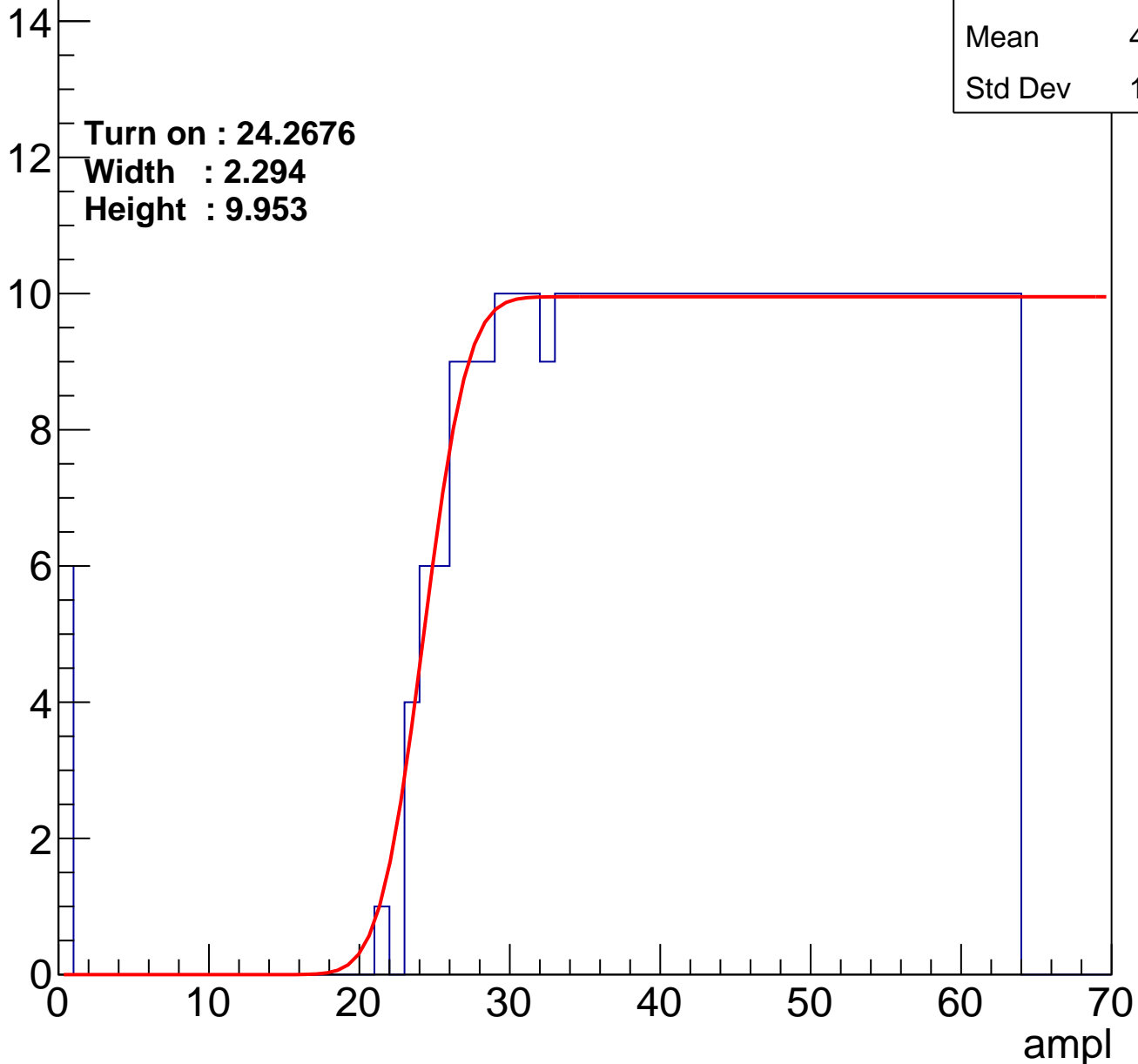
| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.12 |
| Std Dev | 12.56 |

Turn on : 24.2676

Width : 2.294

Height : 9.953

Entry



B1L000S, U1-ch118

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.47 |
| Std Dev | 12.05 |

Turn on : 25.0628

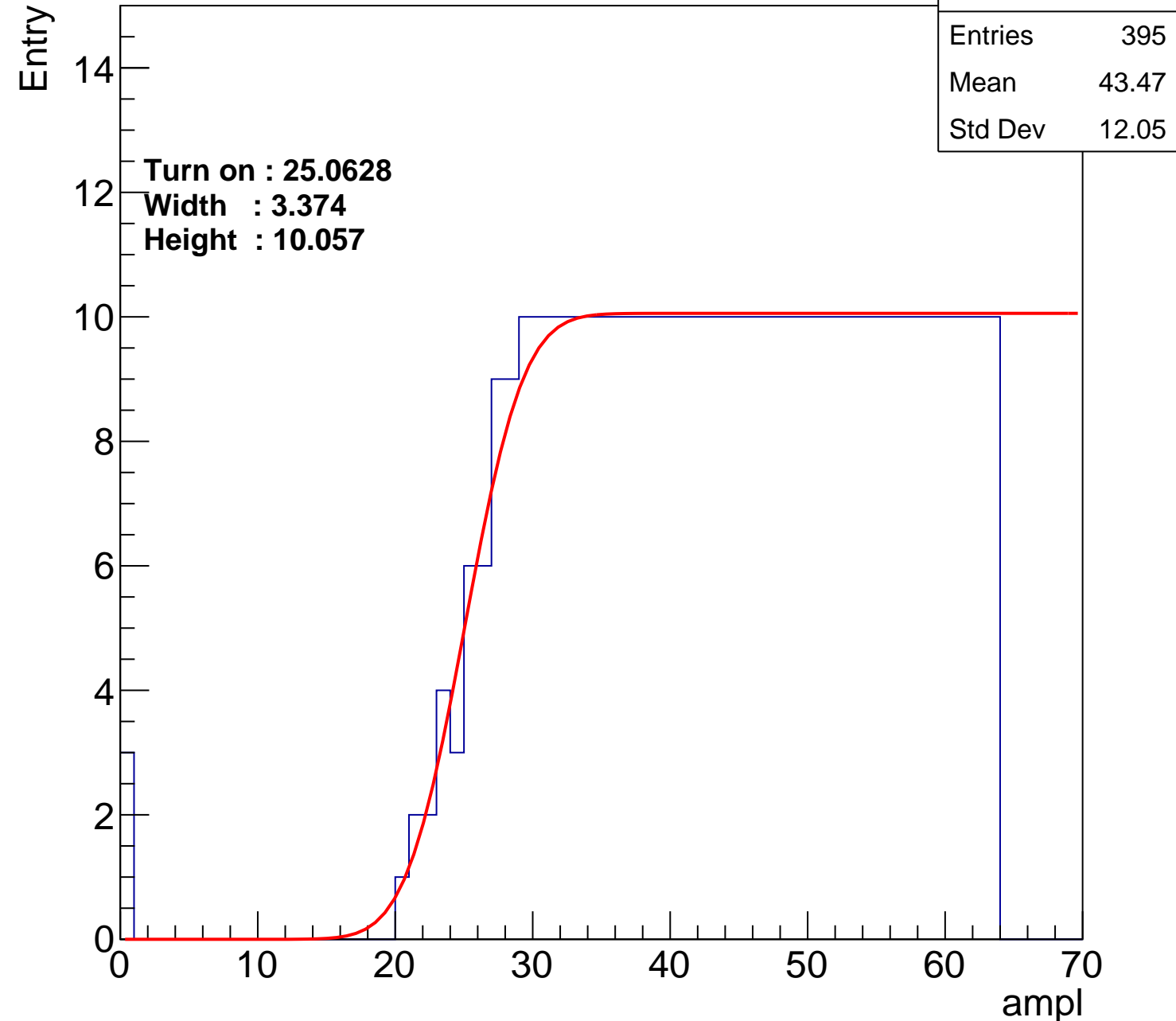
Width : 3.374

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch119

calib_packv5_042523_0143.root, FC#3, port B3

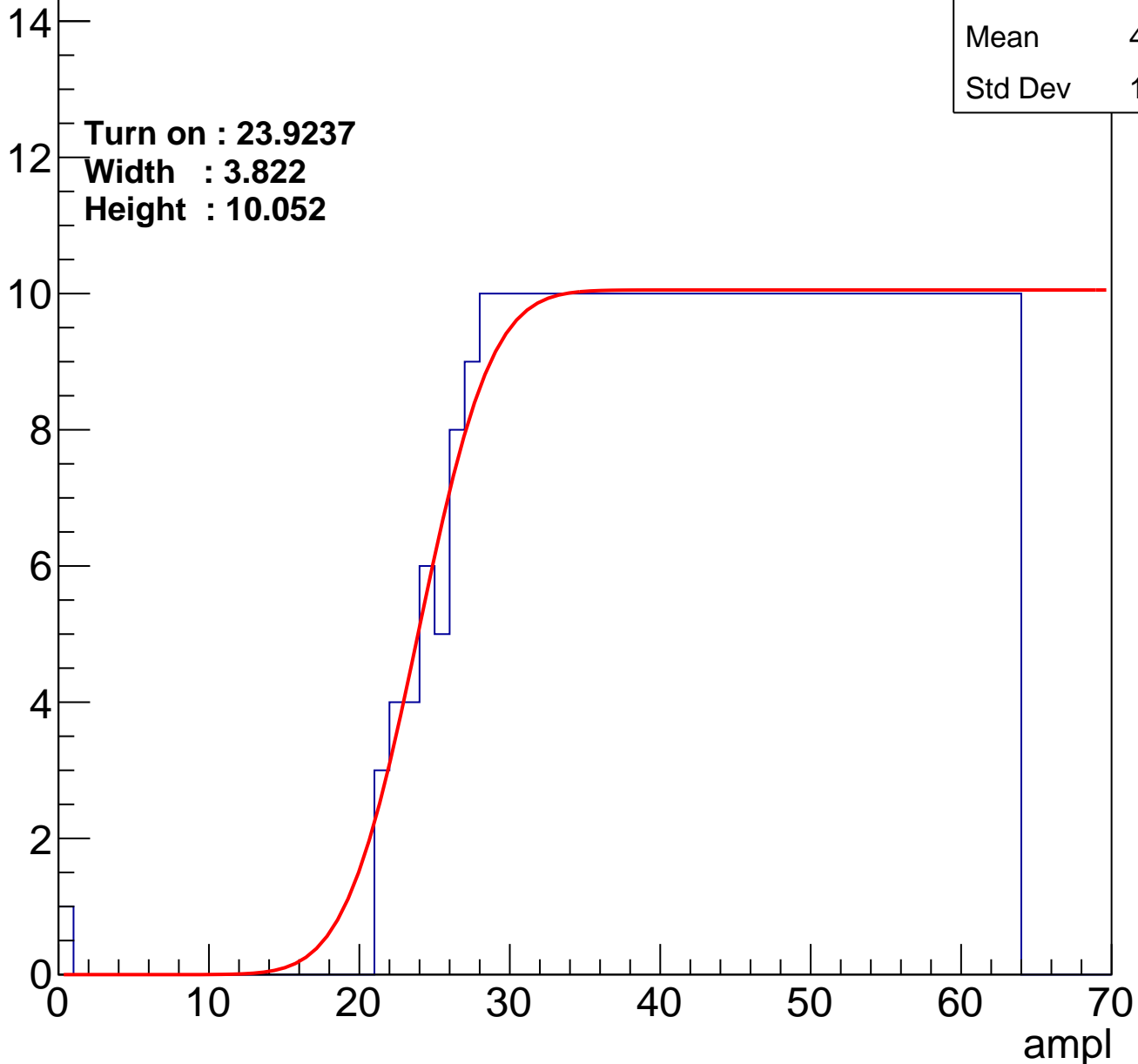
| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.36 |
| Std Dev | 11.84 |

Turn on : 23.9237

Width : 3.822

Height : 10.052

Entry



B1L000S, U1-ch120

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.08 |
| Std Dev | 11.73 |

Turn on : 26.3451

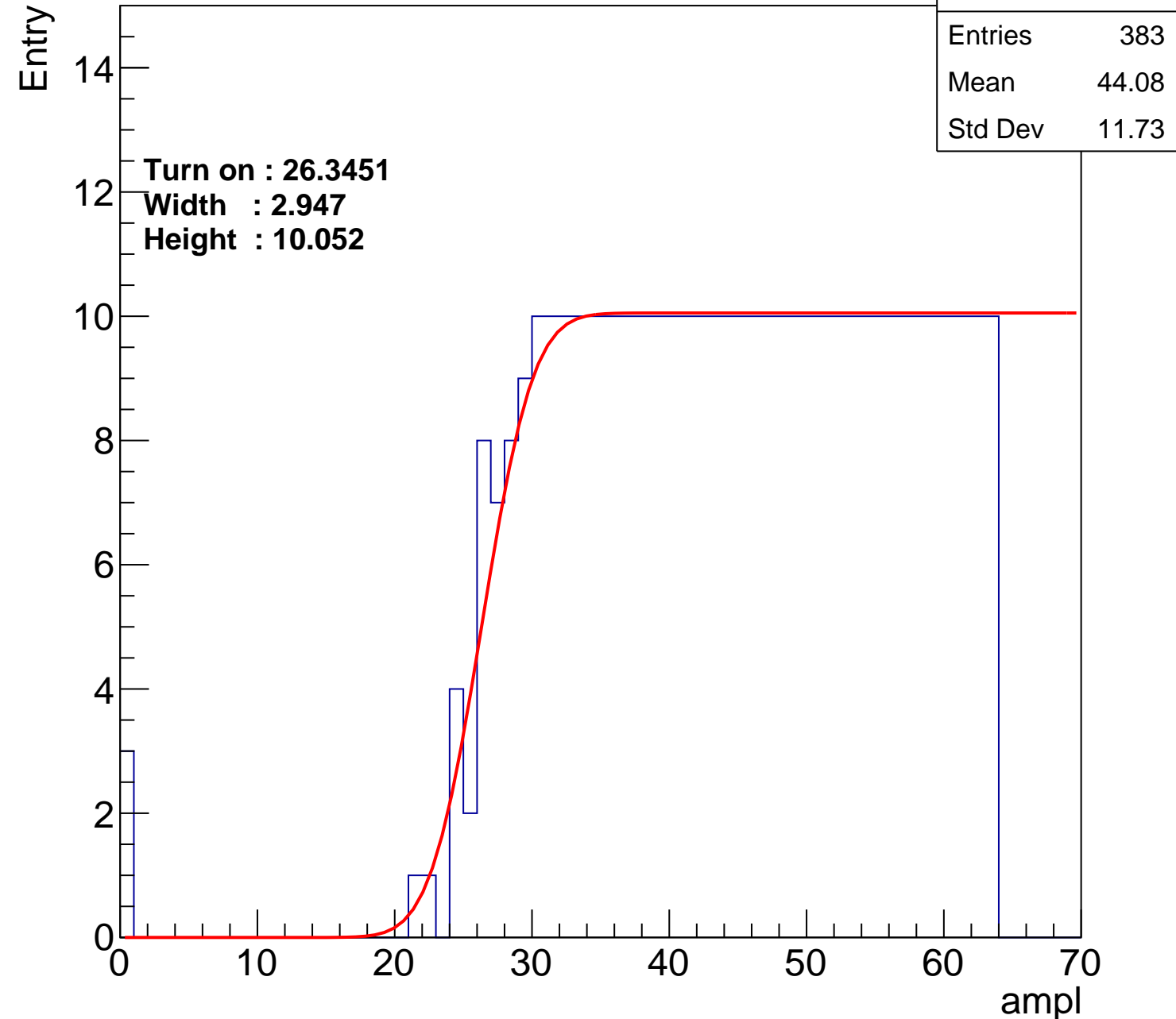
Width : 2.947

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch121

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.59 |
| Std Dev | 12.46 |

Turn on : 25.4980

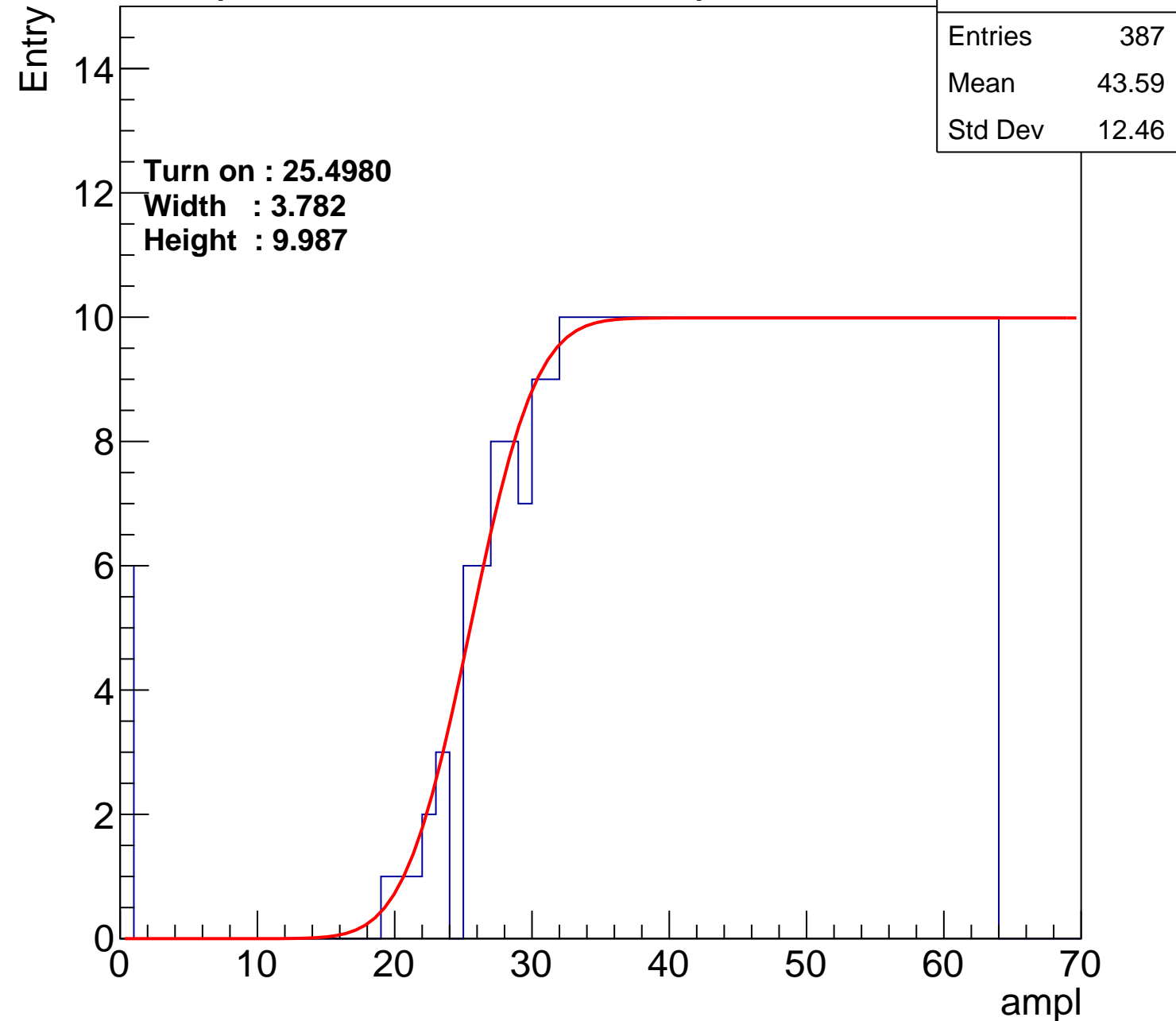
Width : 3.782

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch122

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.16 |
| Std Dev | 12.12 |

Turn on : 24.2977

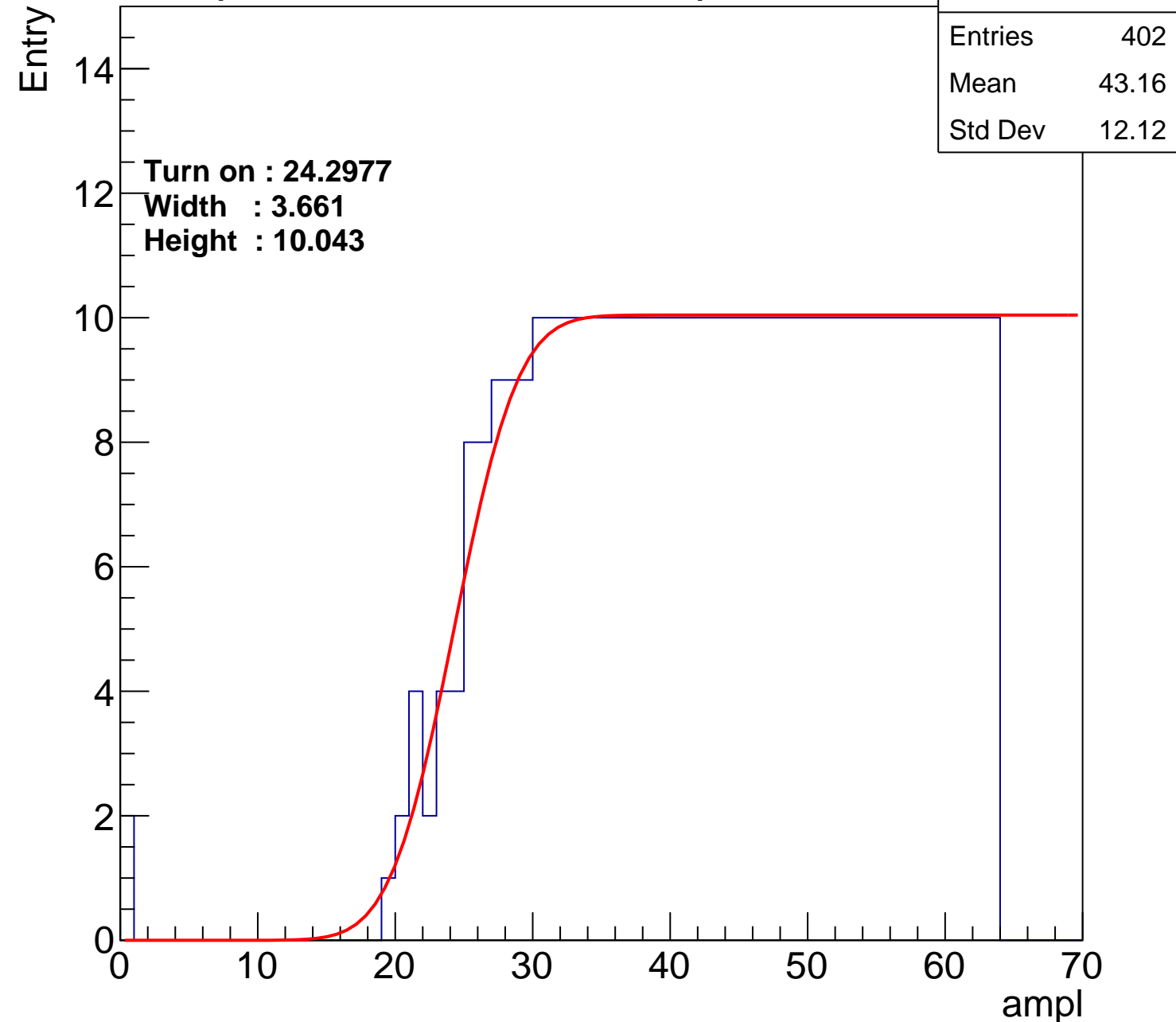
Width : 3.661

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch123

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.8 |
| Std Dev | 12.01 |

Turn on : 25.7401

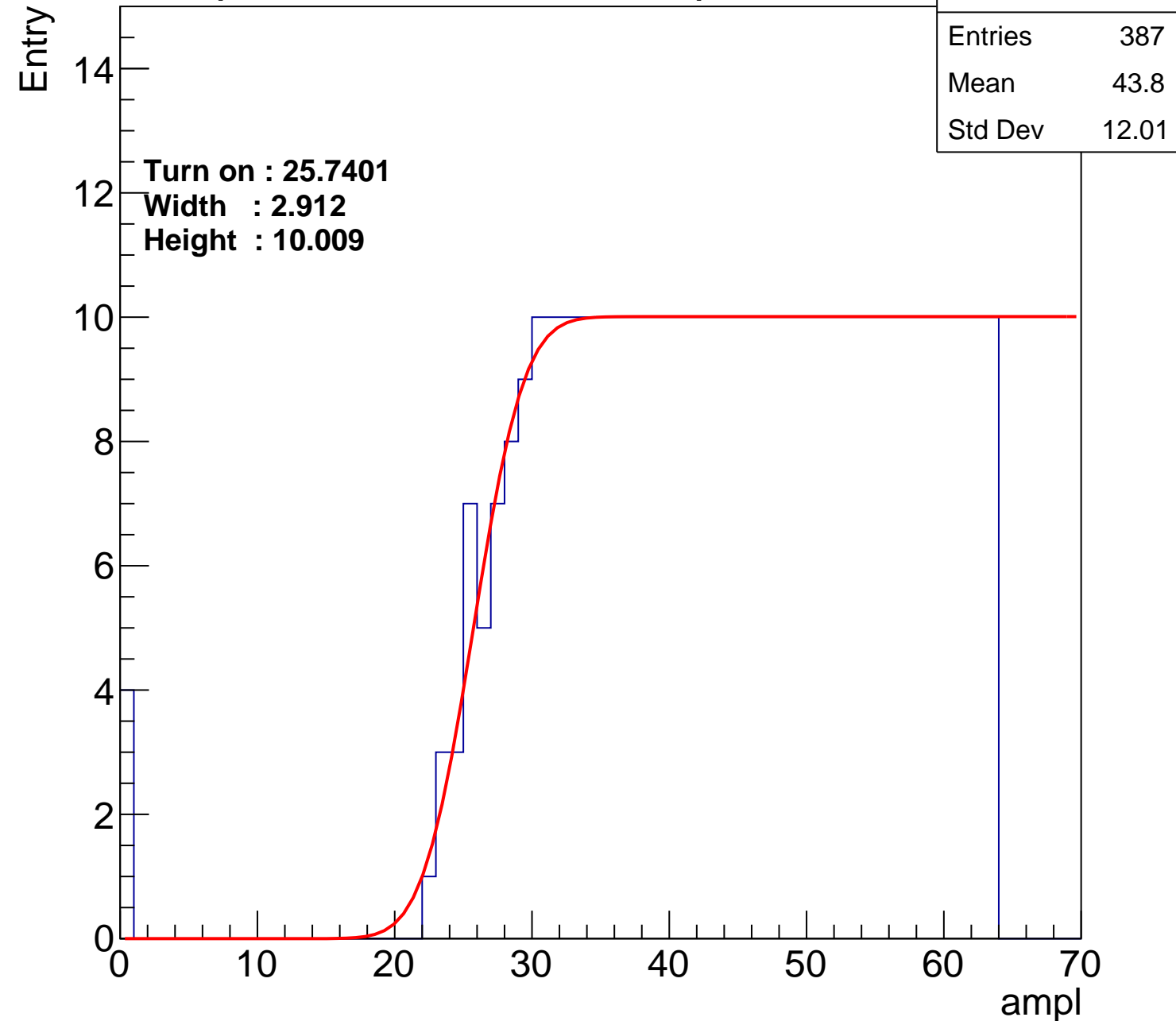
Width : 2.912

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch124

calib_packv5_042523_0143.root, FC#3, port B3

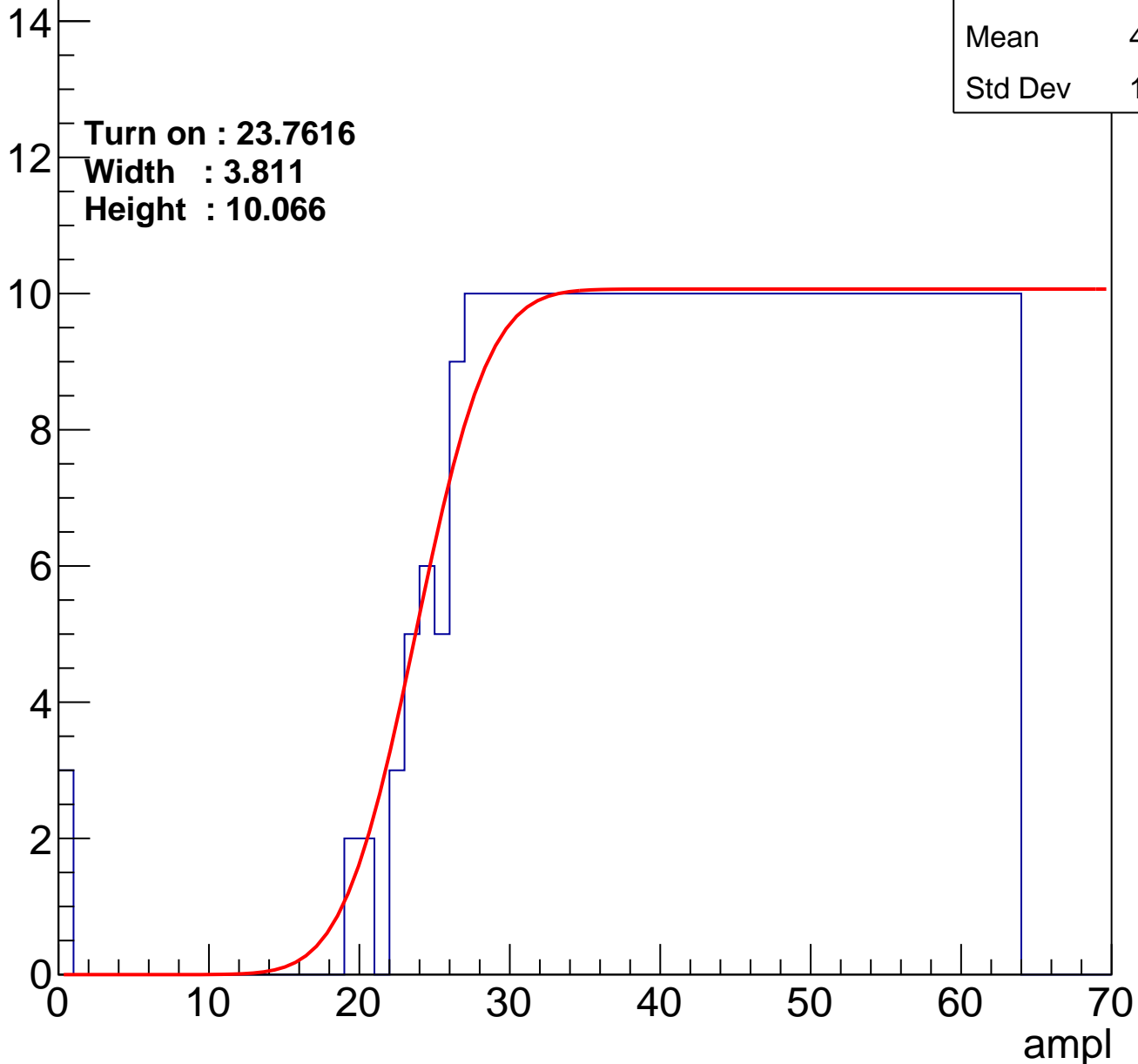
| | |
|---------|-------|
| Entries | 405 |
| Mean | 42.99 |
| Std Dev | 12.29 |

Turn on : 23.7616

Width : 3.811

Height : 10.066

Entry



B1L000S, U1-ch125

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.54 |
| Std Dev | 12.06 |

Turn on : 25.2930

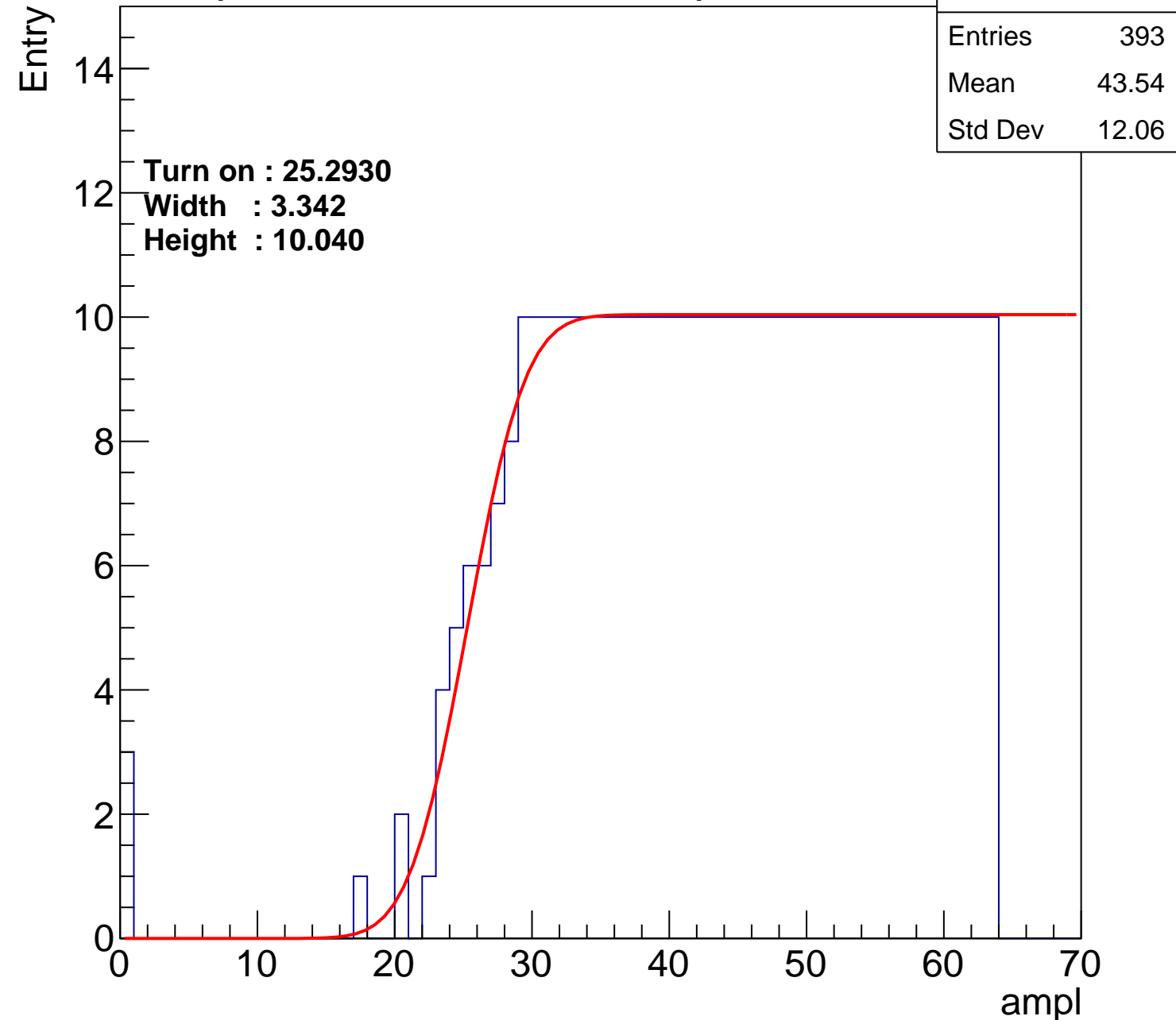
Width : 3.342

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch126

calib_packv5_042523_0143.root, FC#3, port B3

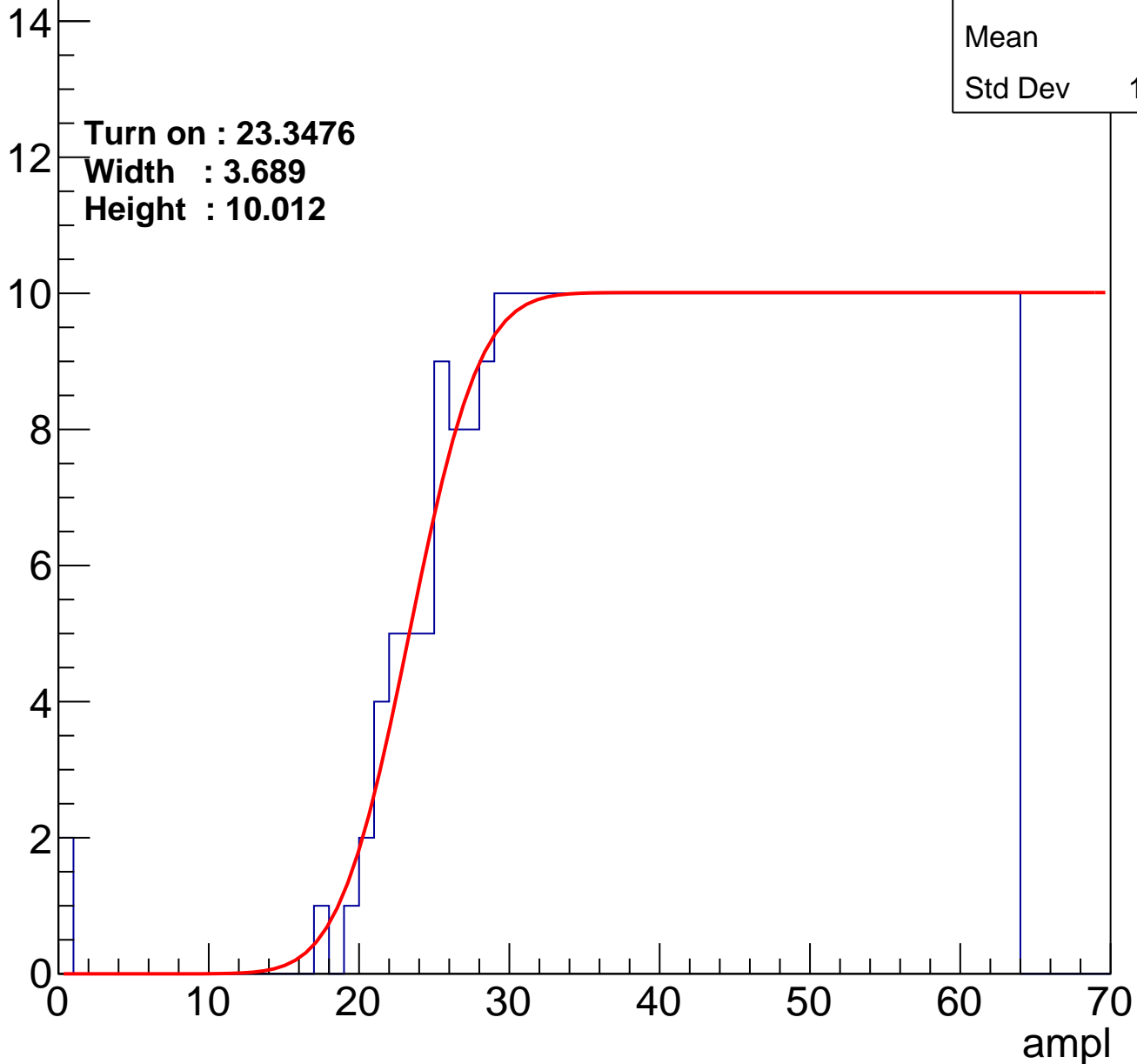
| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.8 |
| Std Dev | 12.32 |

Turn on : 23.3476

Width : 3.689

Height : 10.012

Entry



B1L000S, U1-ch127

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.27 |
| Std Dev | 12.54 |

Turn on : 25.5592

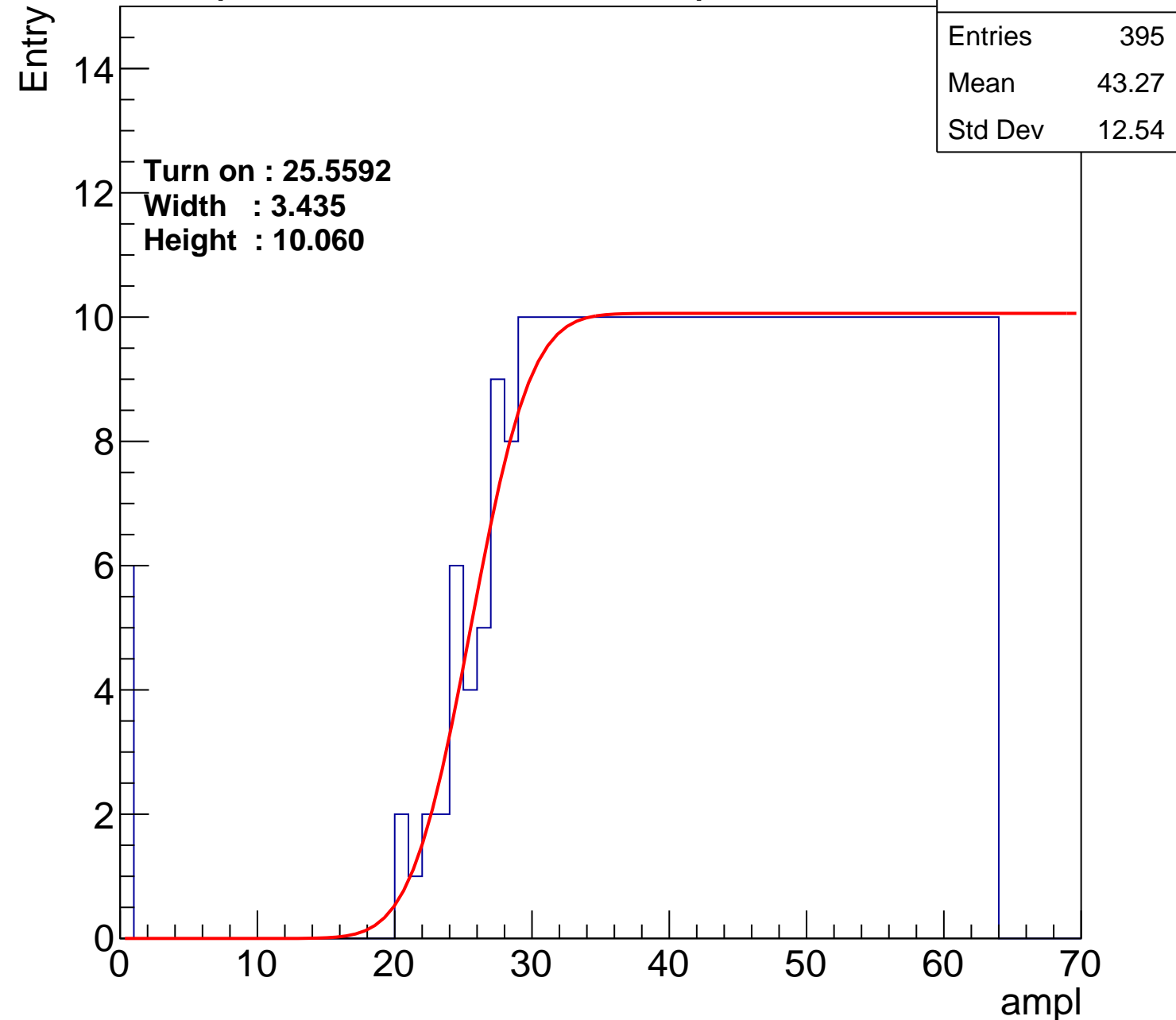
Width : 3.435

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U1-ch127

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.27 |
| Std Dev | 12.54 |

Turn on : 25.5592

Width : 3.435

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl

