



# B0L101S, U16-ch0, adc0

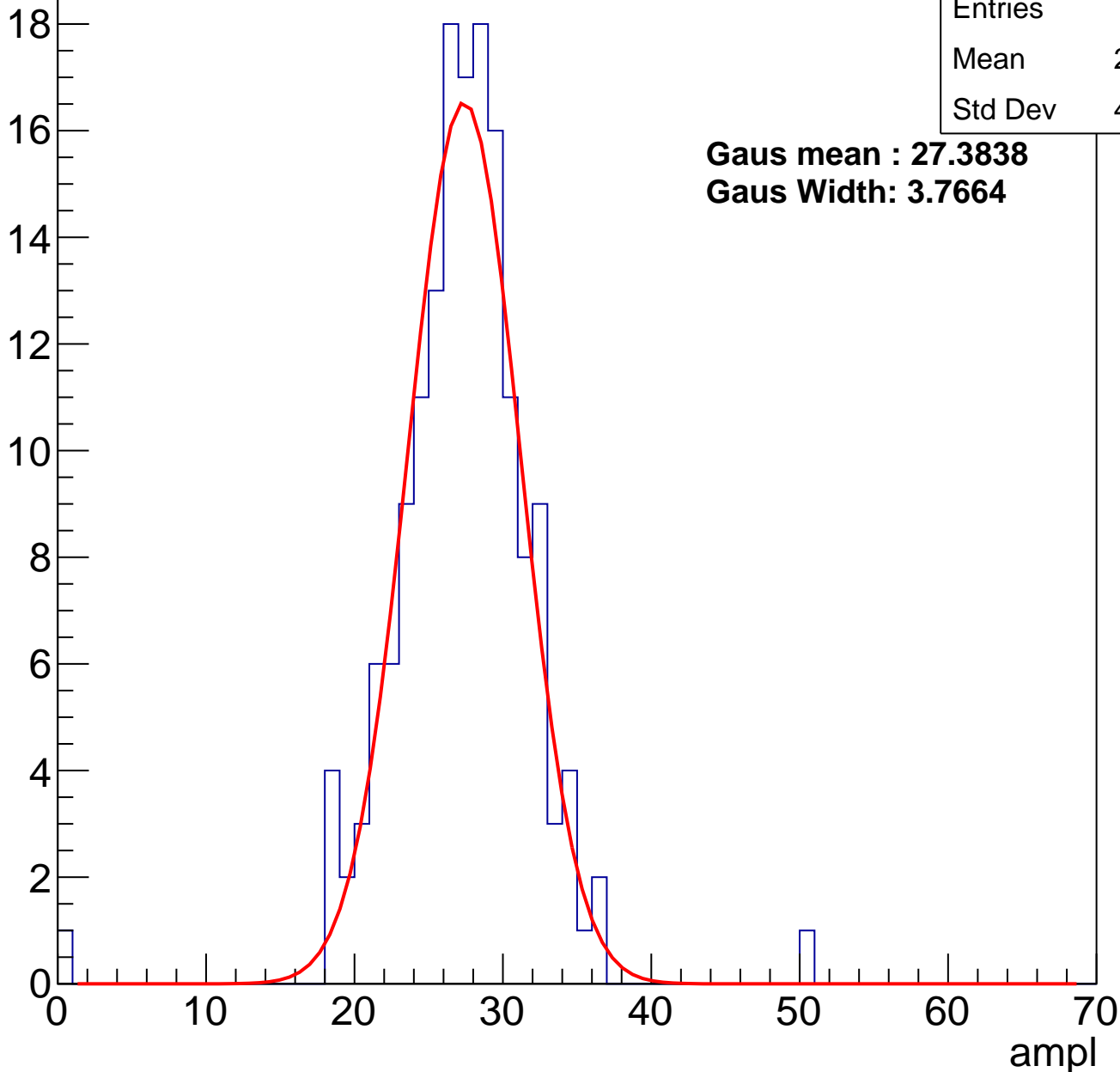
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 163   |
| Mean    | 26.85 |
| Std Dev | 4.697 |

**Gaus mean : 27.3838**

**Gaus Width: 3.7664**

Entry



# B0L101S, U16-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 34.8  |
| Std Dev | 4.931 |

**Gaus mean : 35.6942**

**Gaus Width: 4.0985**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

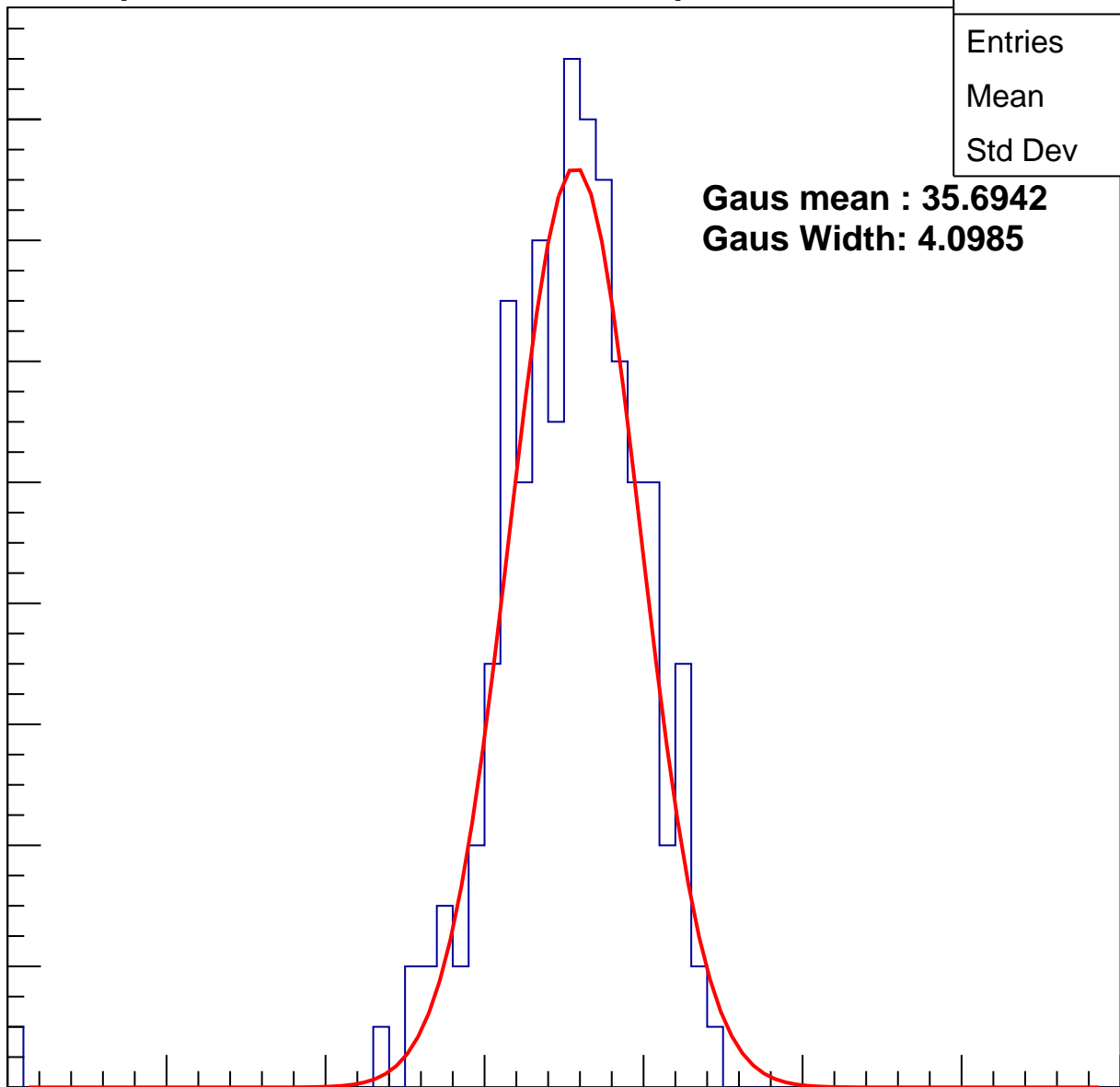
40

50

60

70

ampl



# B0L101S, U16-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 42.06 |
| Std Dev | 4.219 |

**Gaus mean : 42.8846**

**Gaus Width: 3.4395**

Entry

12

10

8

6

4

2

0

0

10

20

30

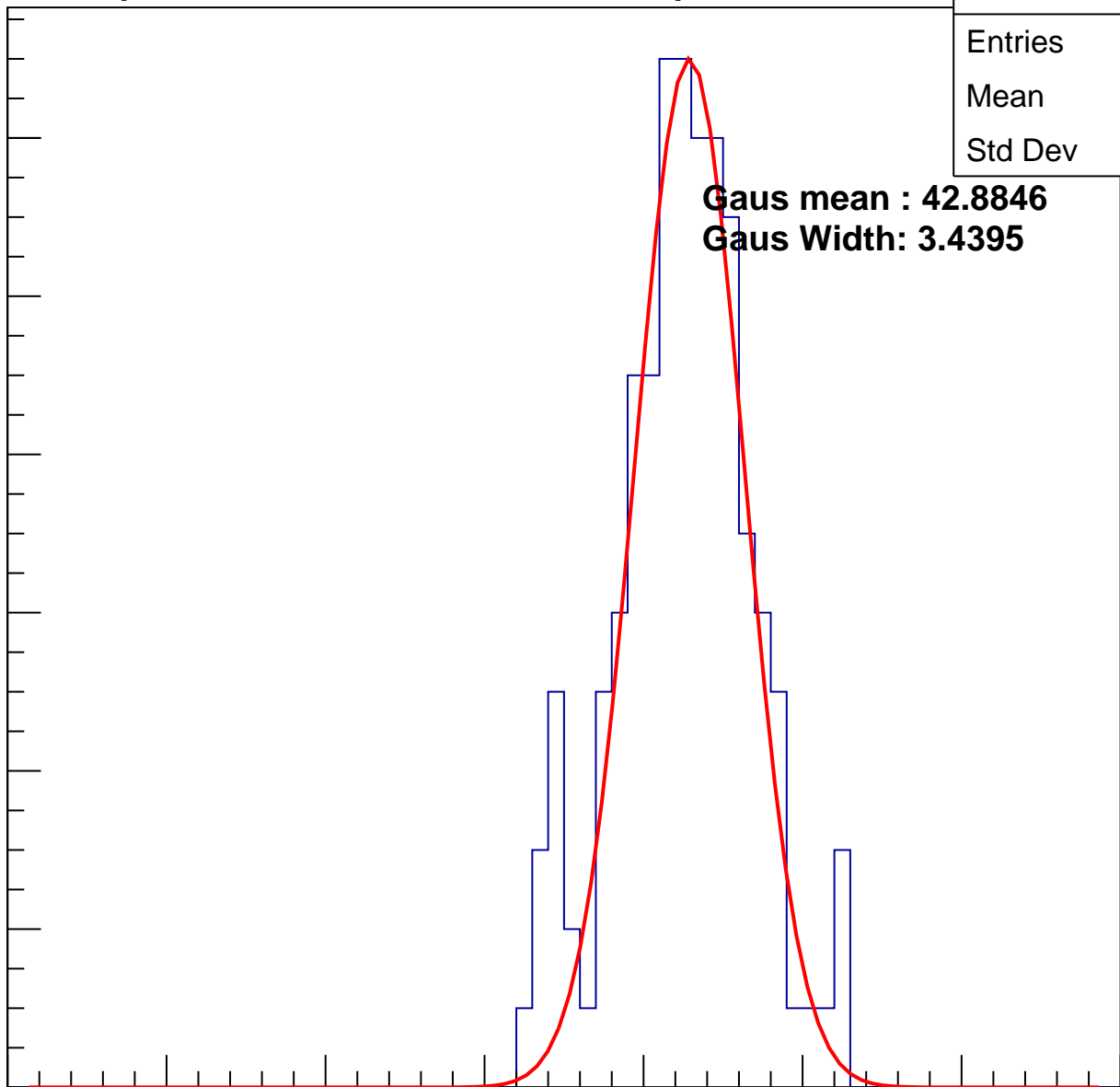
40

50

60

70

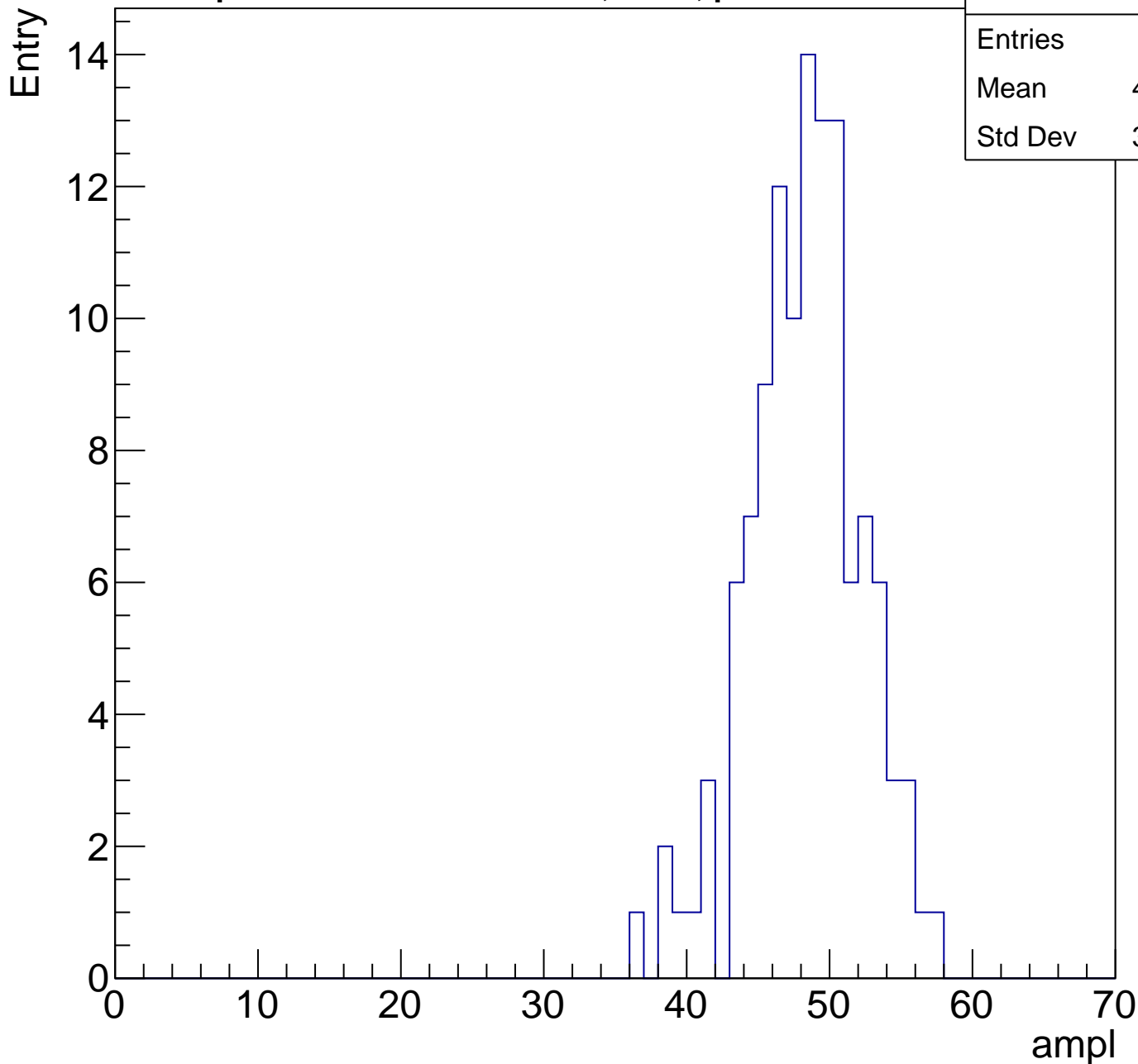
ampl



# B0L101S, U16-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

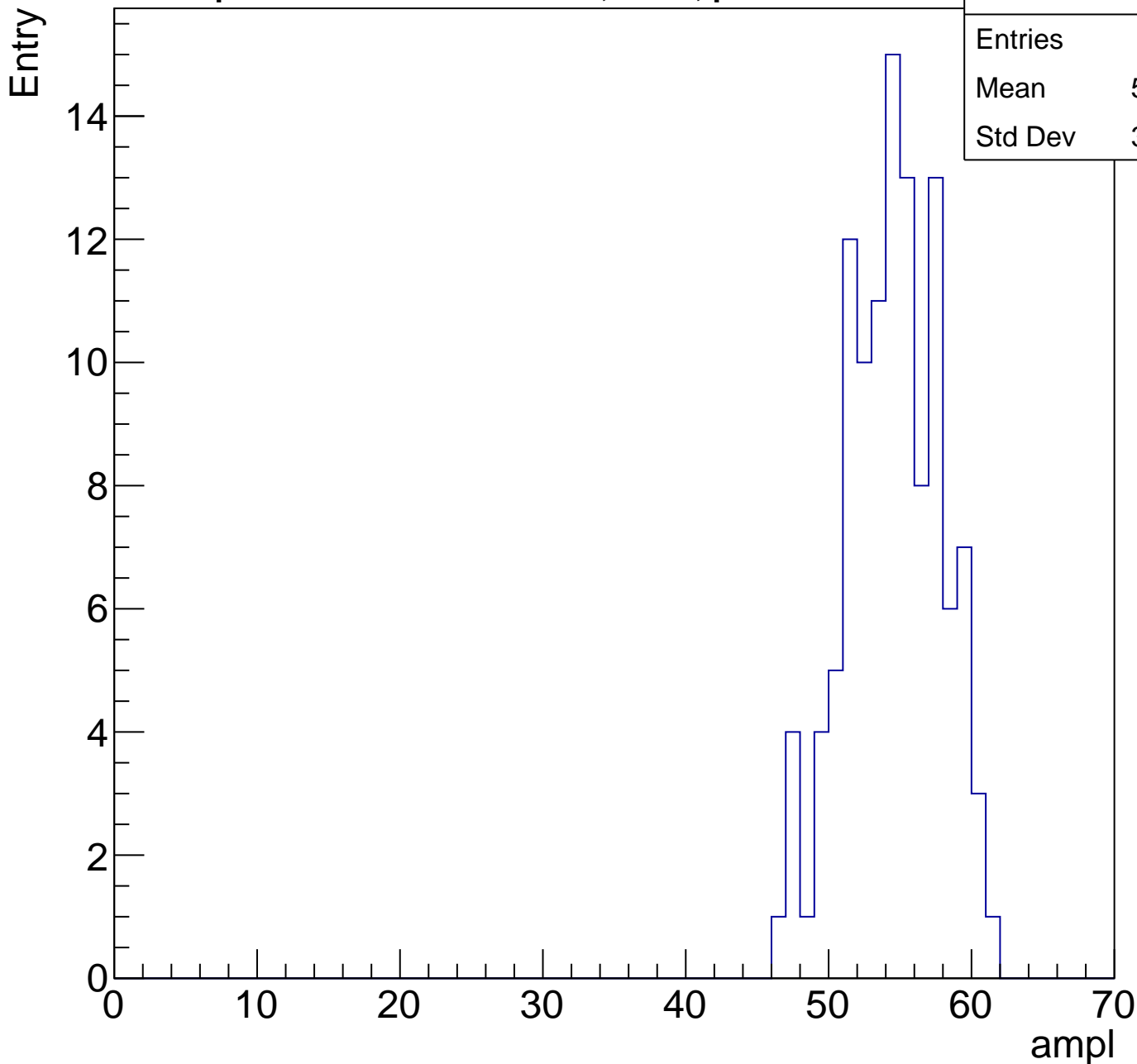
|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 47.85 |
| Std Dev | 3.906 |



# B0L101S, U16-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 54.03 |
| Std Dev | 3.278 |



# B0L101S, U16-ch0, adc5

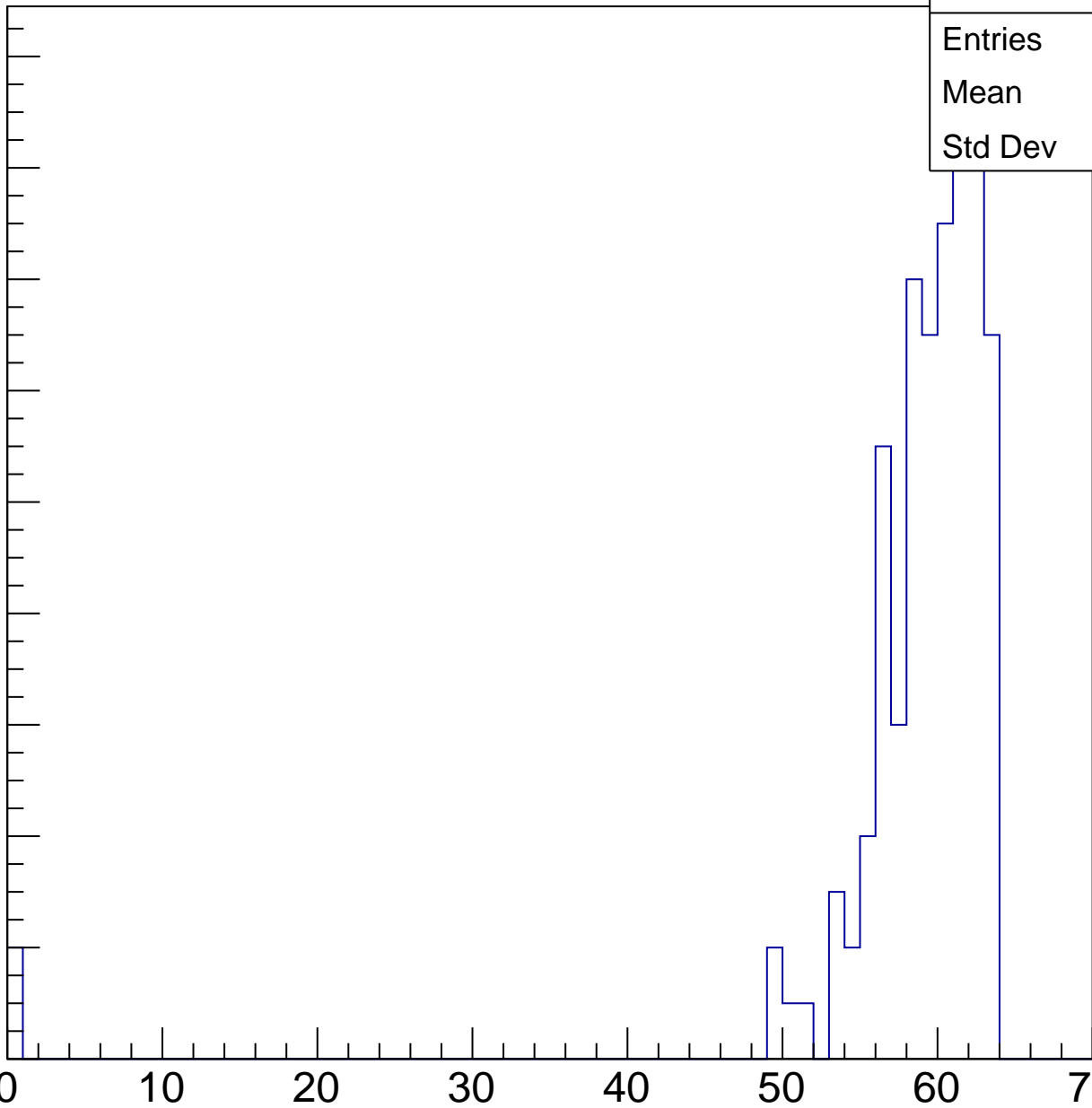
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 58.12 |
| Std Dev | 8.136 |

ampl

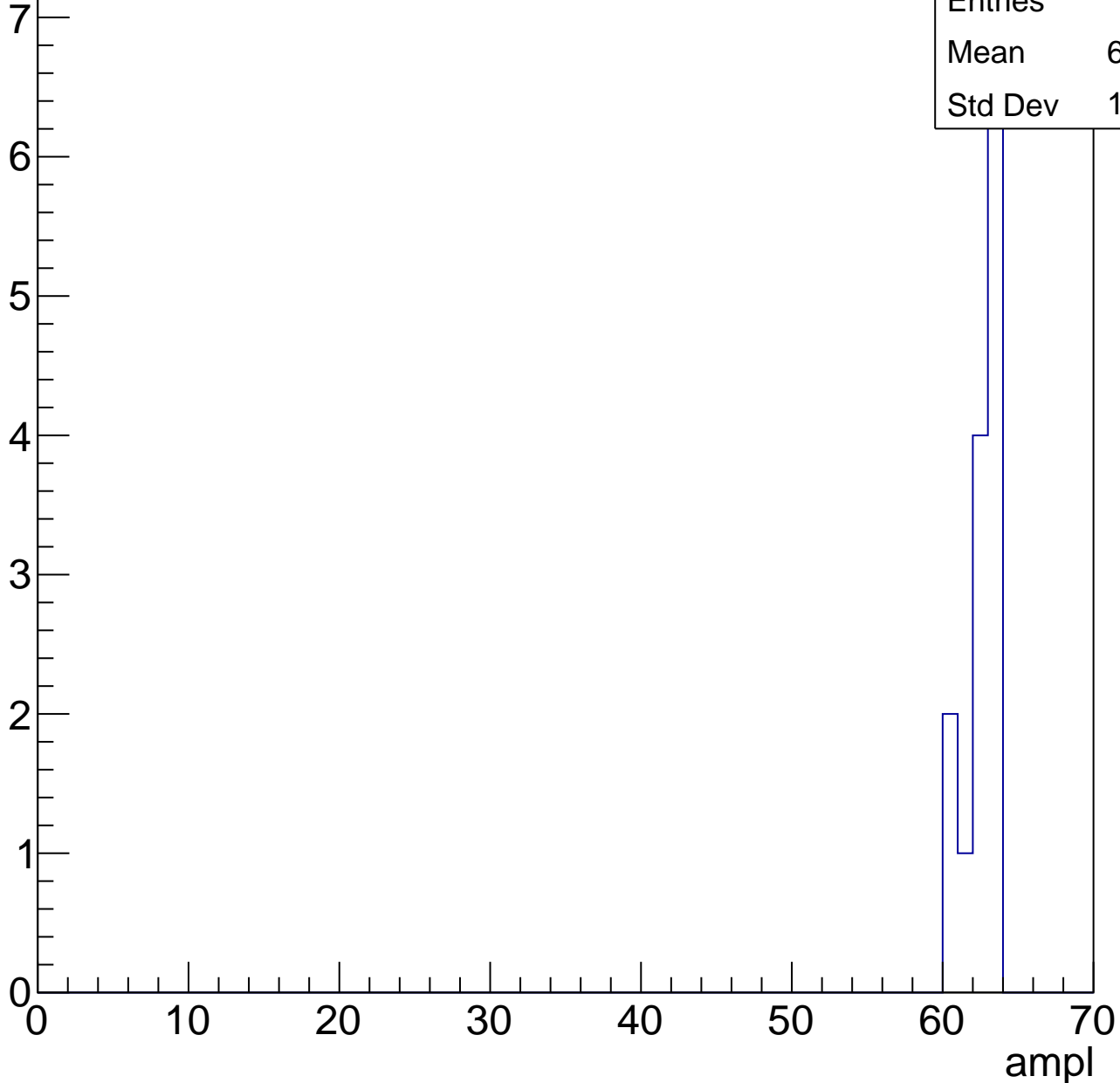


# B0L101S, U16-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 62.14 |
| Std Dev | 1.059 |





# B0L101S, U16-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch1, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

158

Mean

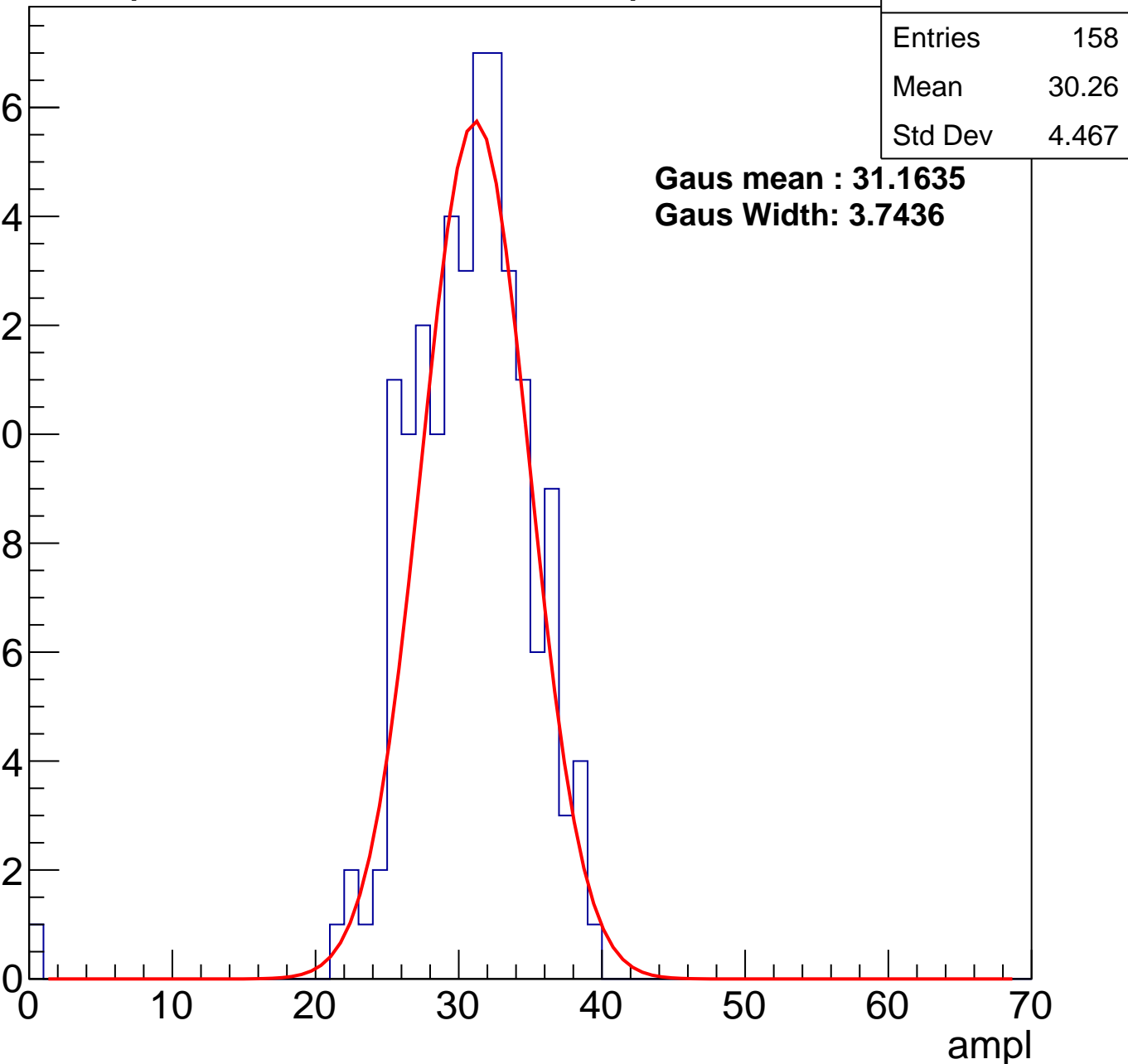
30.26

Std Dev

4.467

**Gaus mean : 31.1635**

**Gaus Width: 3.7436**



# B0L101S, U16-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 37.13 |
| Std Dev | 3.434 |

**Gaus mean : 37.6692**

**Gaus Width: 3.2493**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

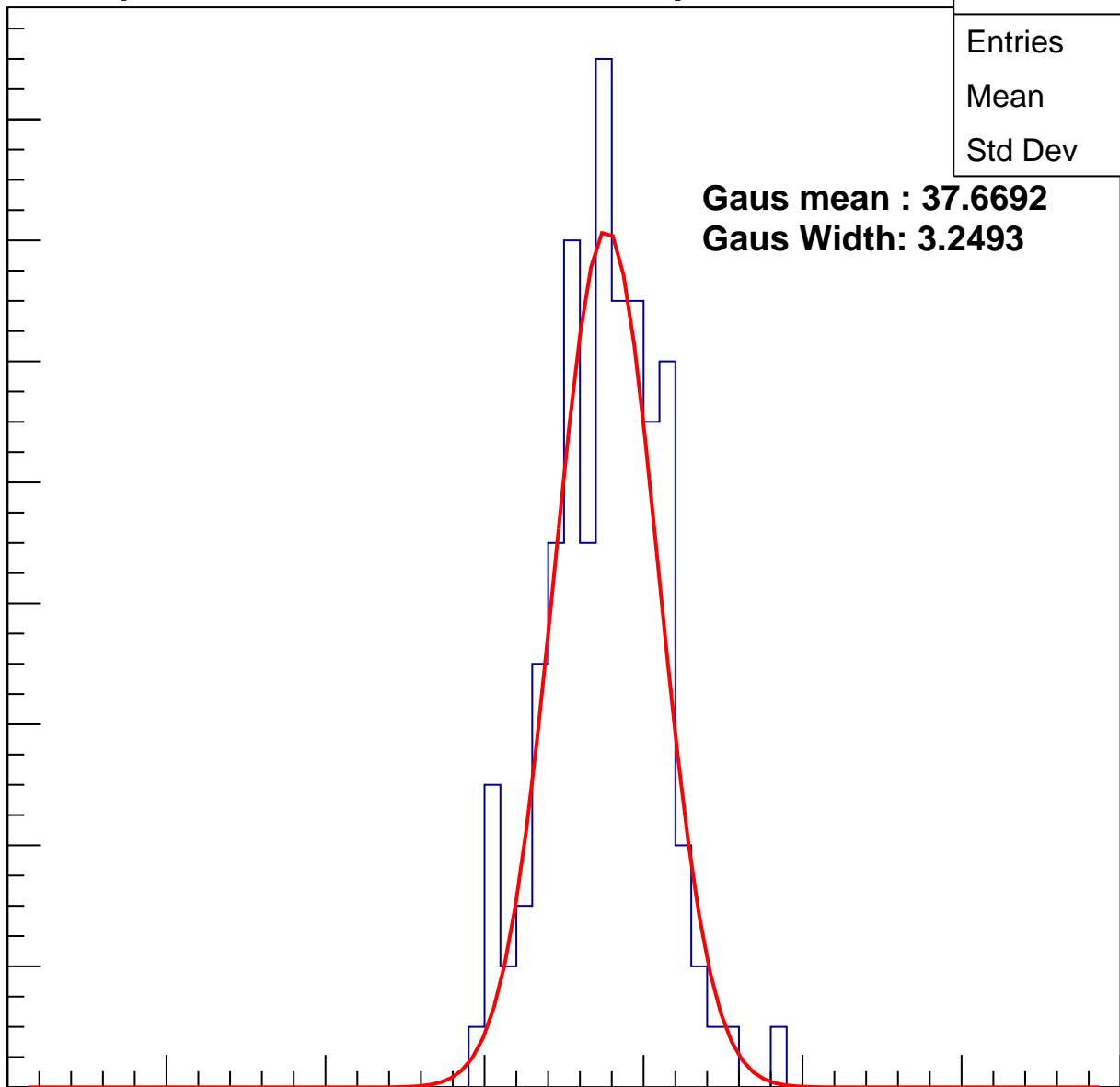
40

50

60

70

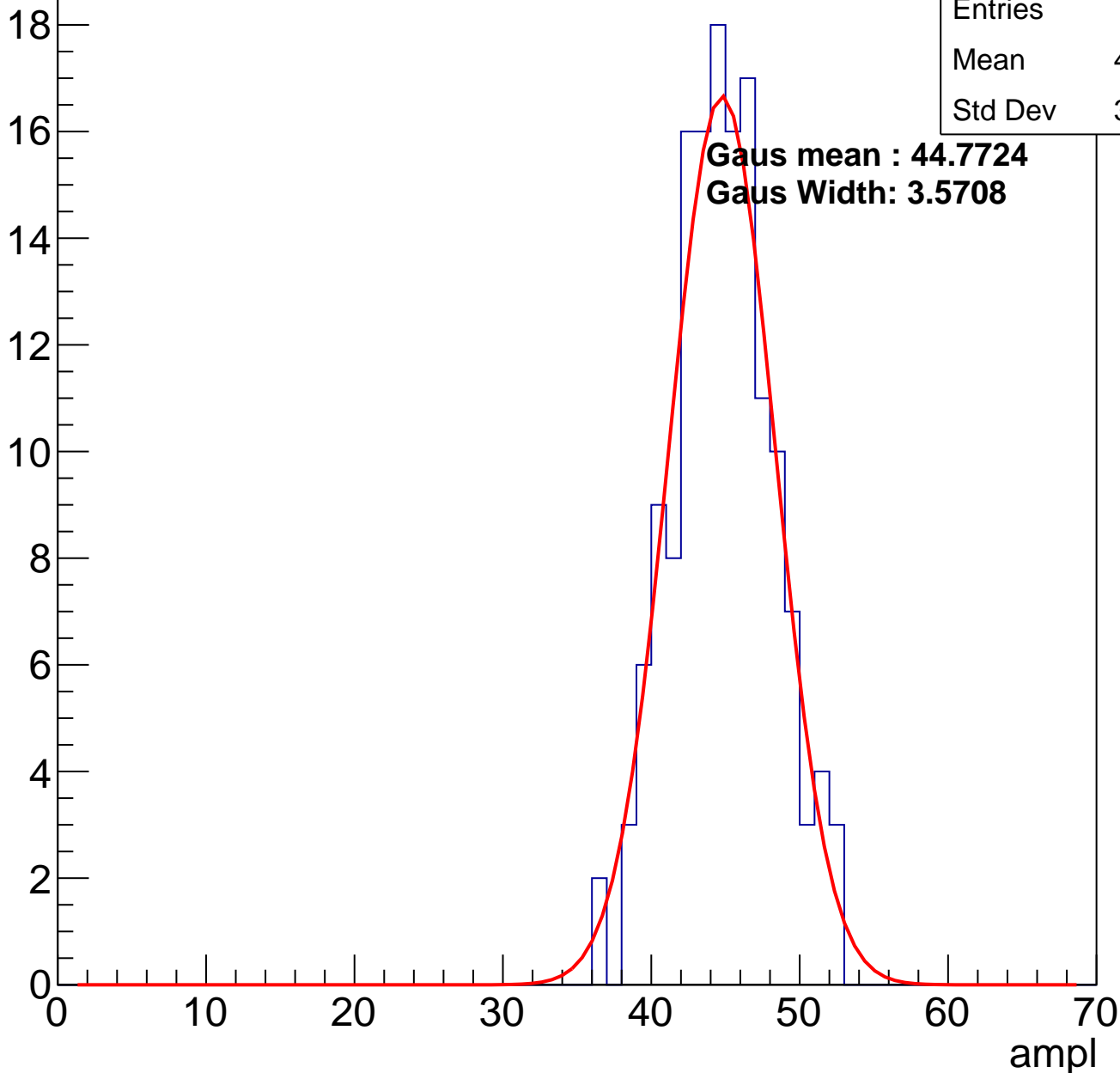
ampl



# B0L101S, U16-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch1, adc3

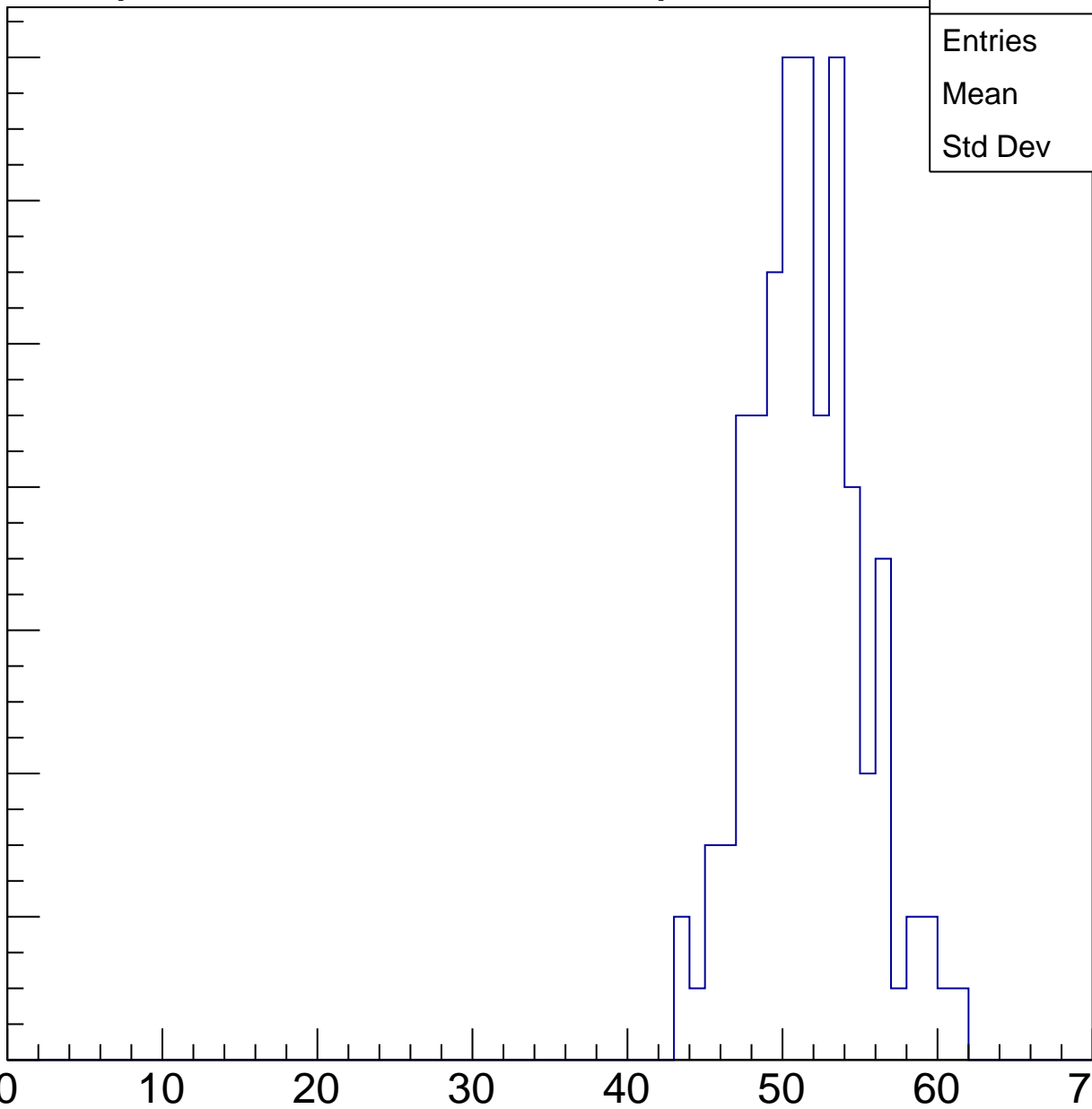
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 51.1  |
| Std Dev | 3.564 |

ampl



# B0L101S, U16-ch1, adc4

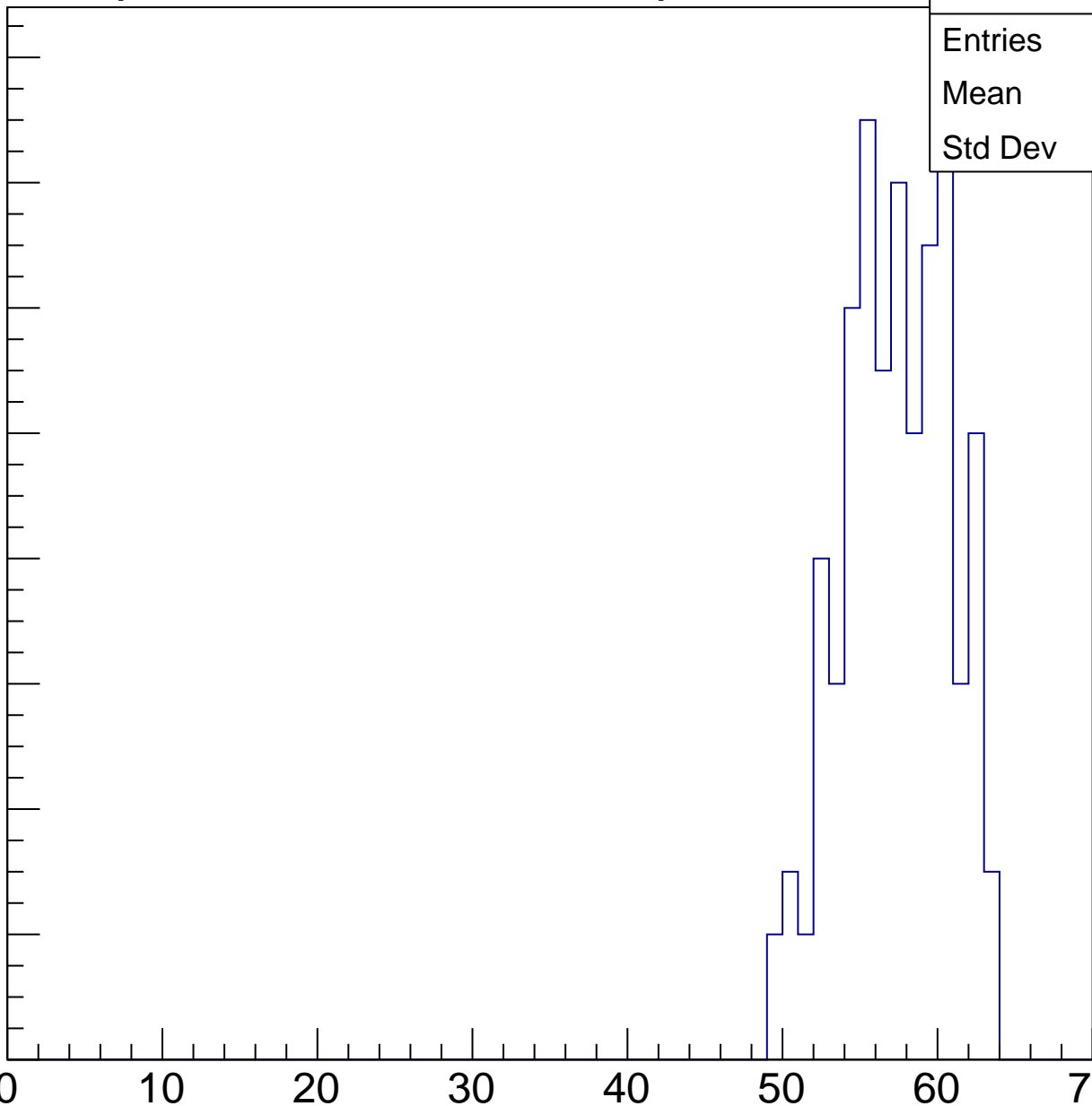
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 56.89 |
| Std Dev | 3.362 |

ampl



# B0L101S, U16-ch1, adc5

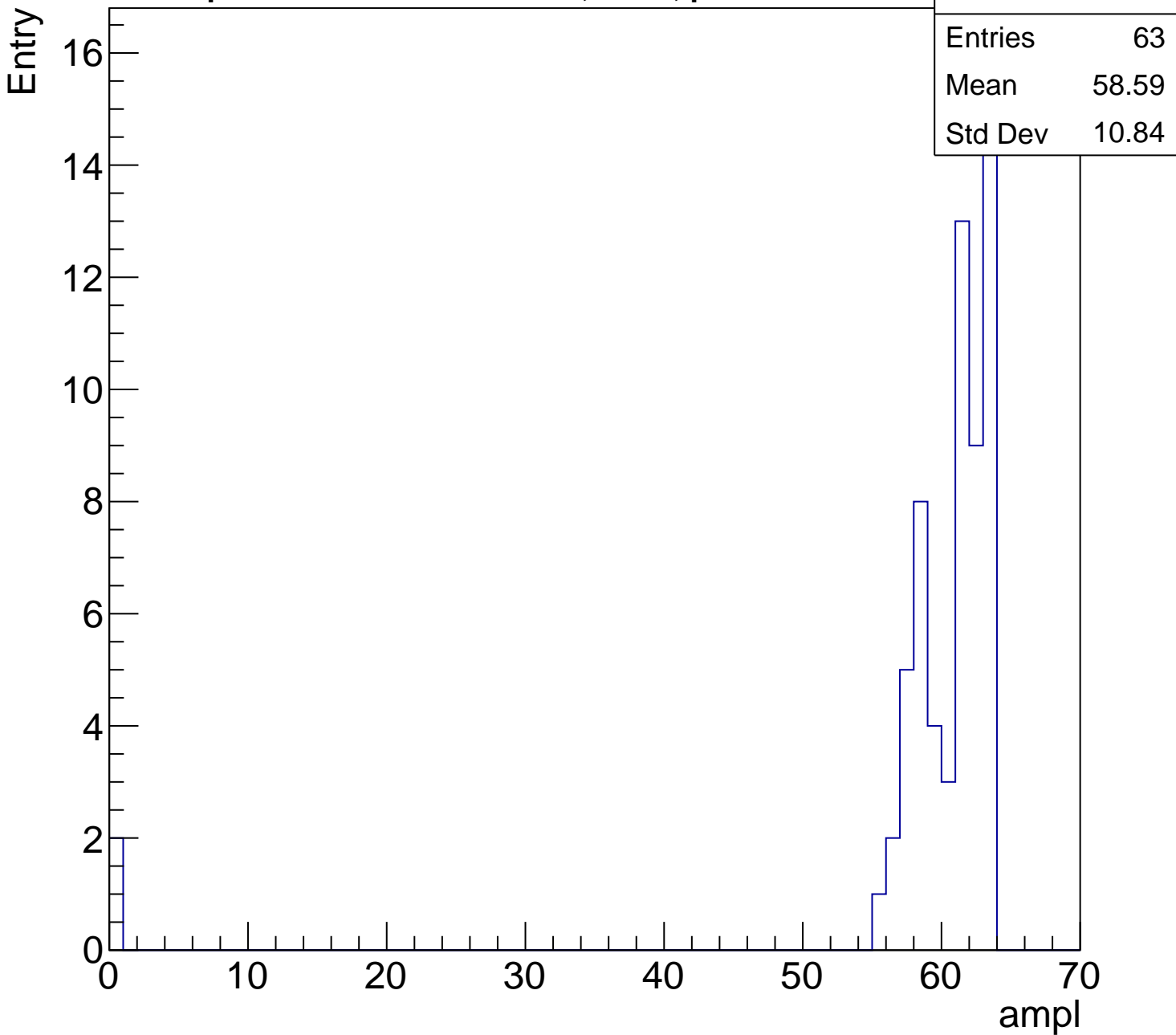
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 58.59 |
| Std Dev | 10.84 |

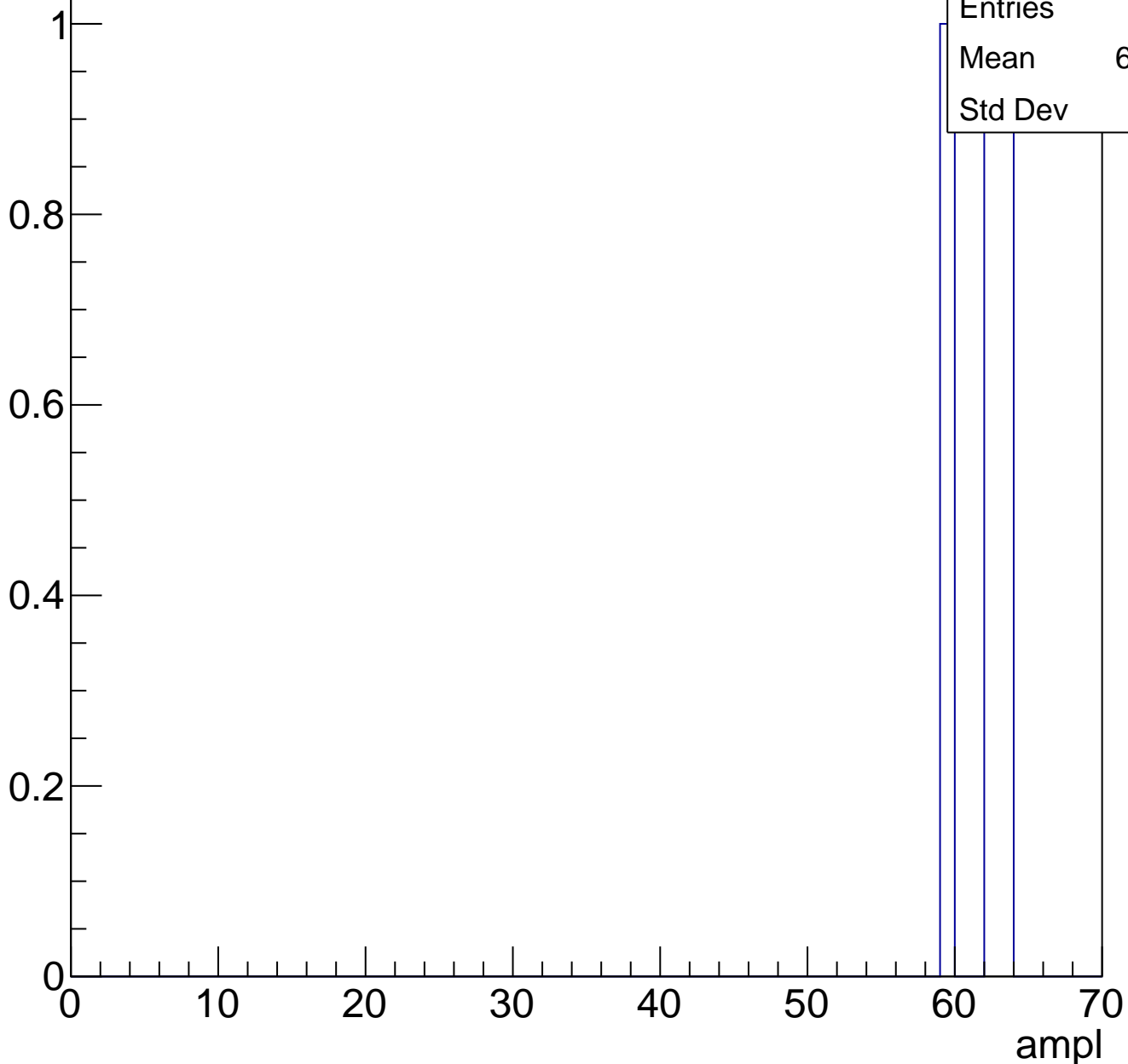
ampl



# B0L101S, U16-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch2, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 27.31 |
| Std Dev | 4.824 |

**Gaus mean : 28.5305**

**Gaus Width: 3.9874**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

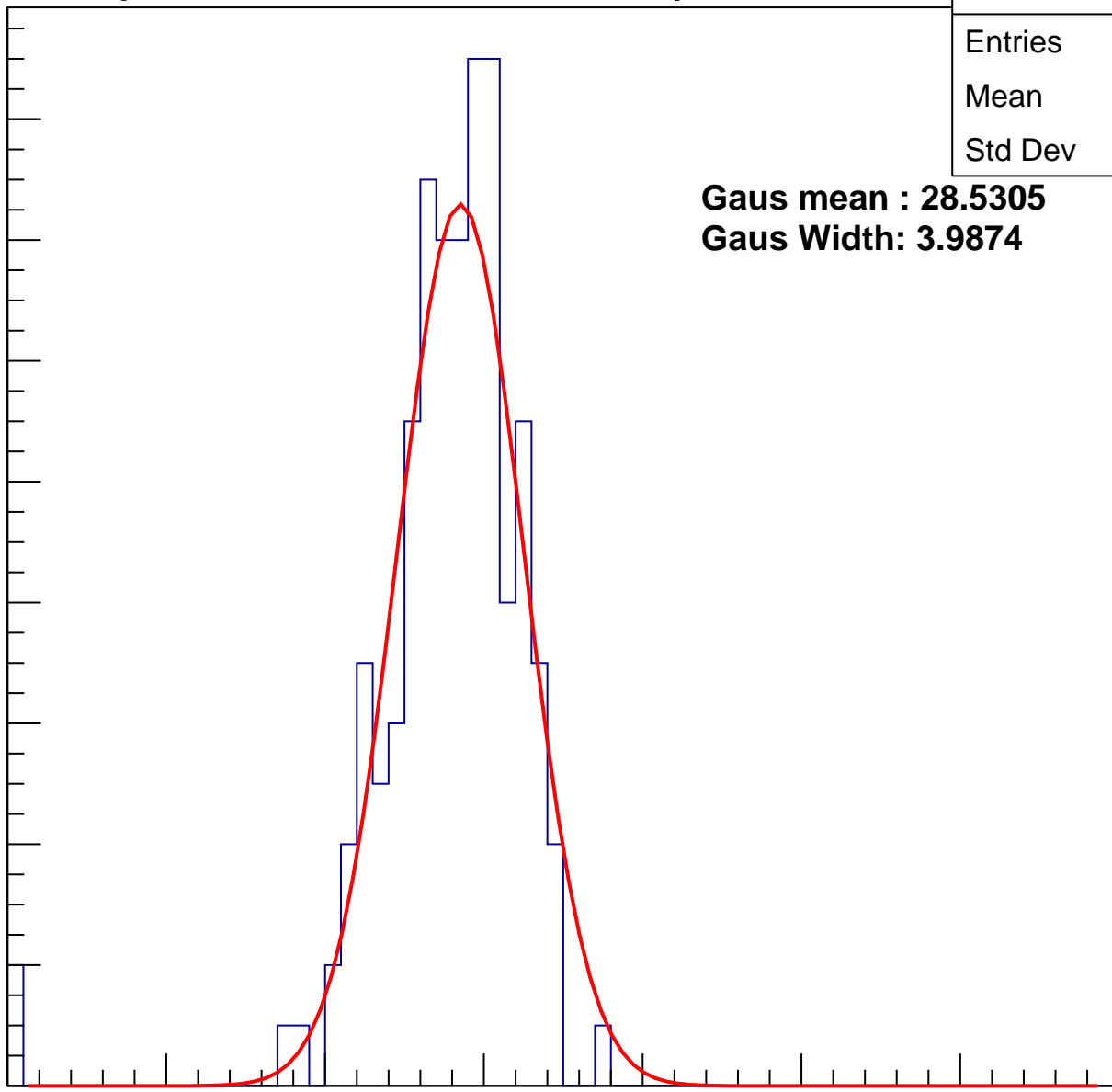
40

50

60

70

ampl



# B0L101S, U16-ch2, adc1

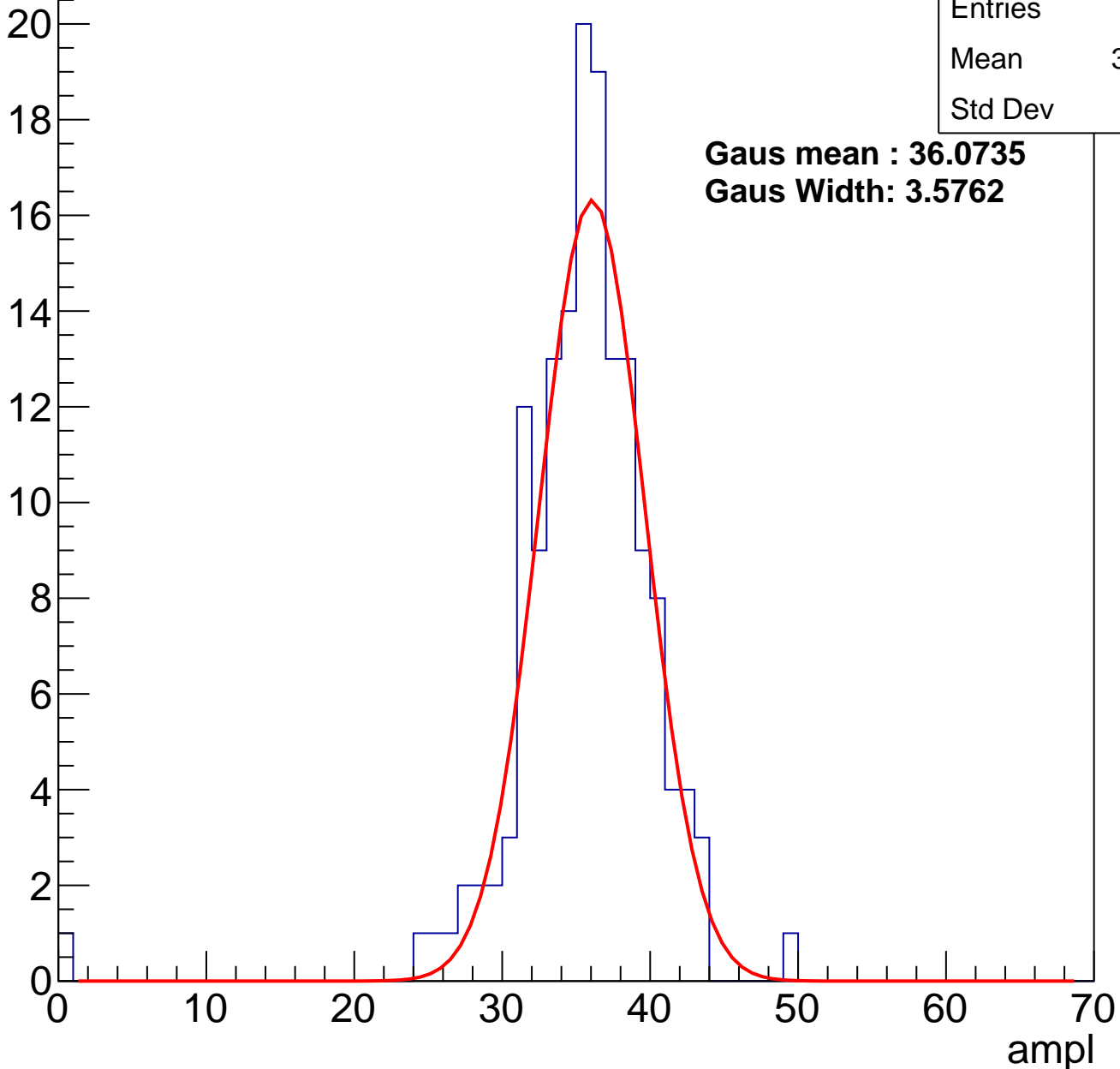
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 35.08 |
| Std Dev | 4.75  |

**Gaus mean : 36.0735**

**Gaus Width: 3.5762**

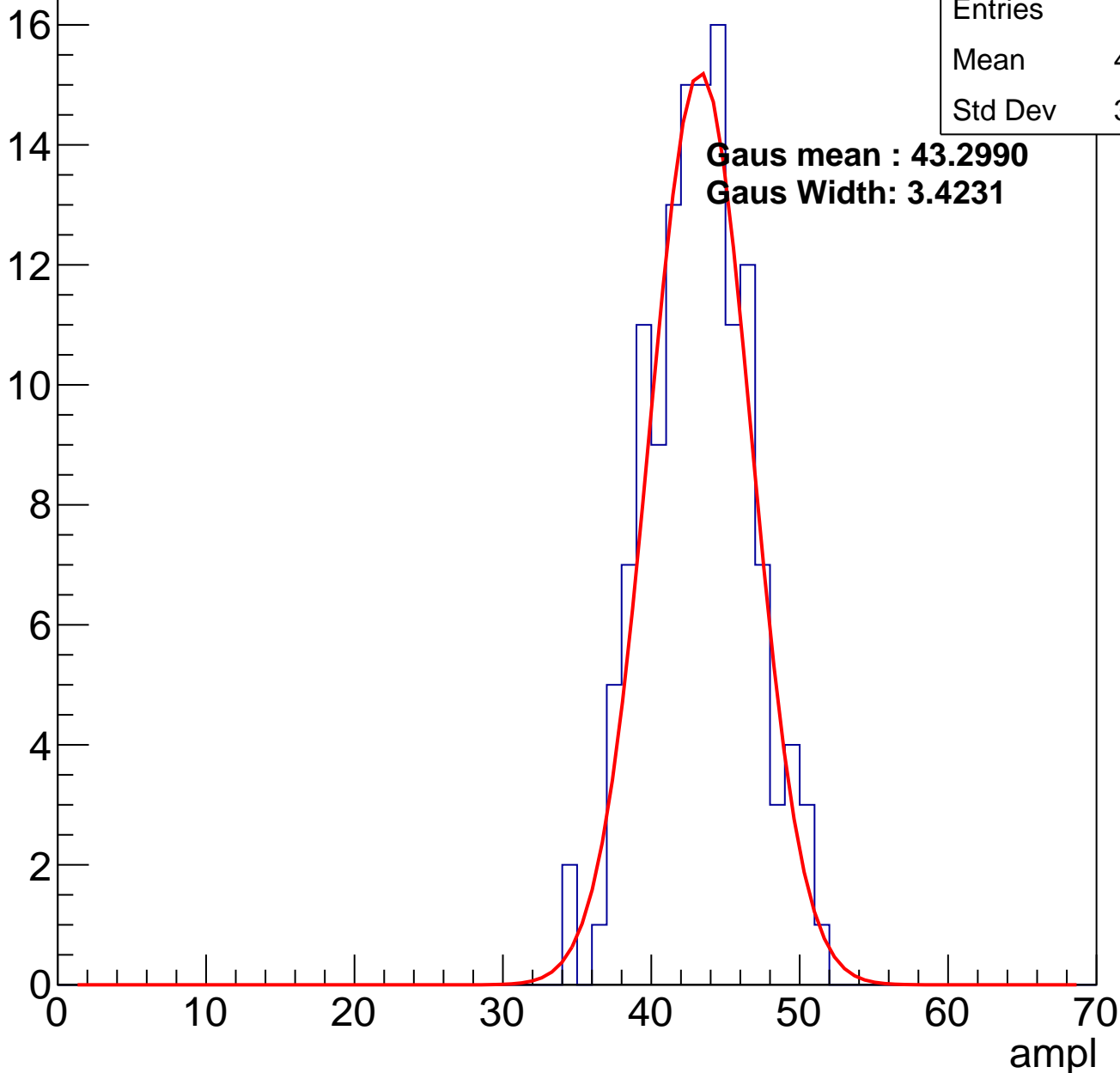
Entry



# B0L101S, U16-ch2, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

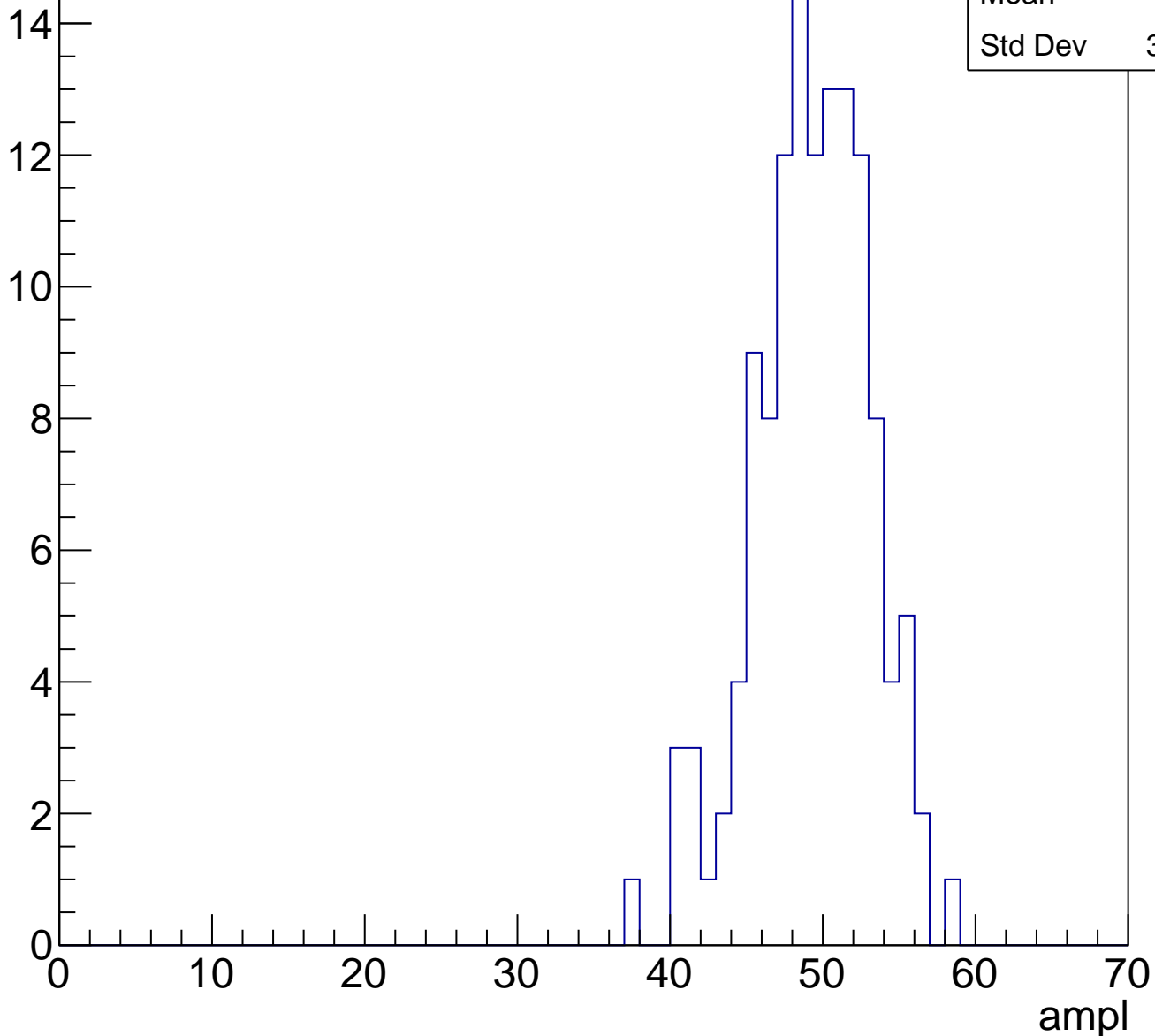


# B0L101S, U16-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 48.84 |
| Std Dev | 3.787 |

Entry

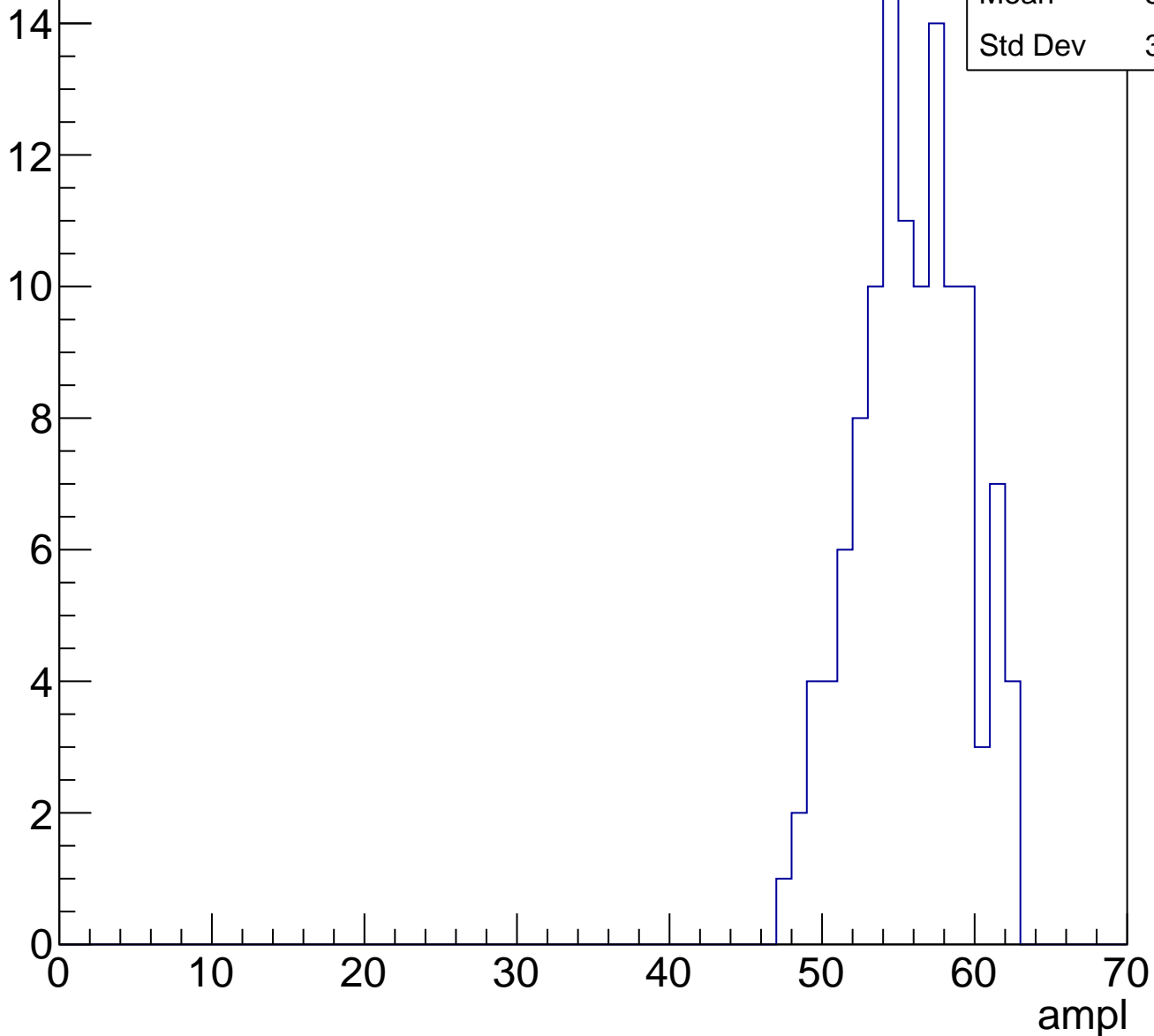


# B0L101S, U16-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 55.37 |
| Std Dev | 3.493 |

Entry



# B0L101S, U16-ch2, adc5

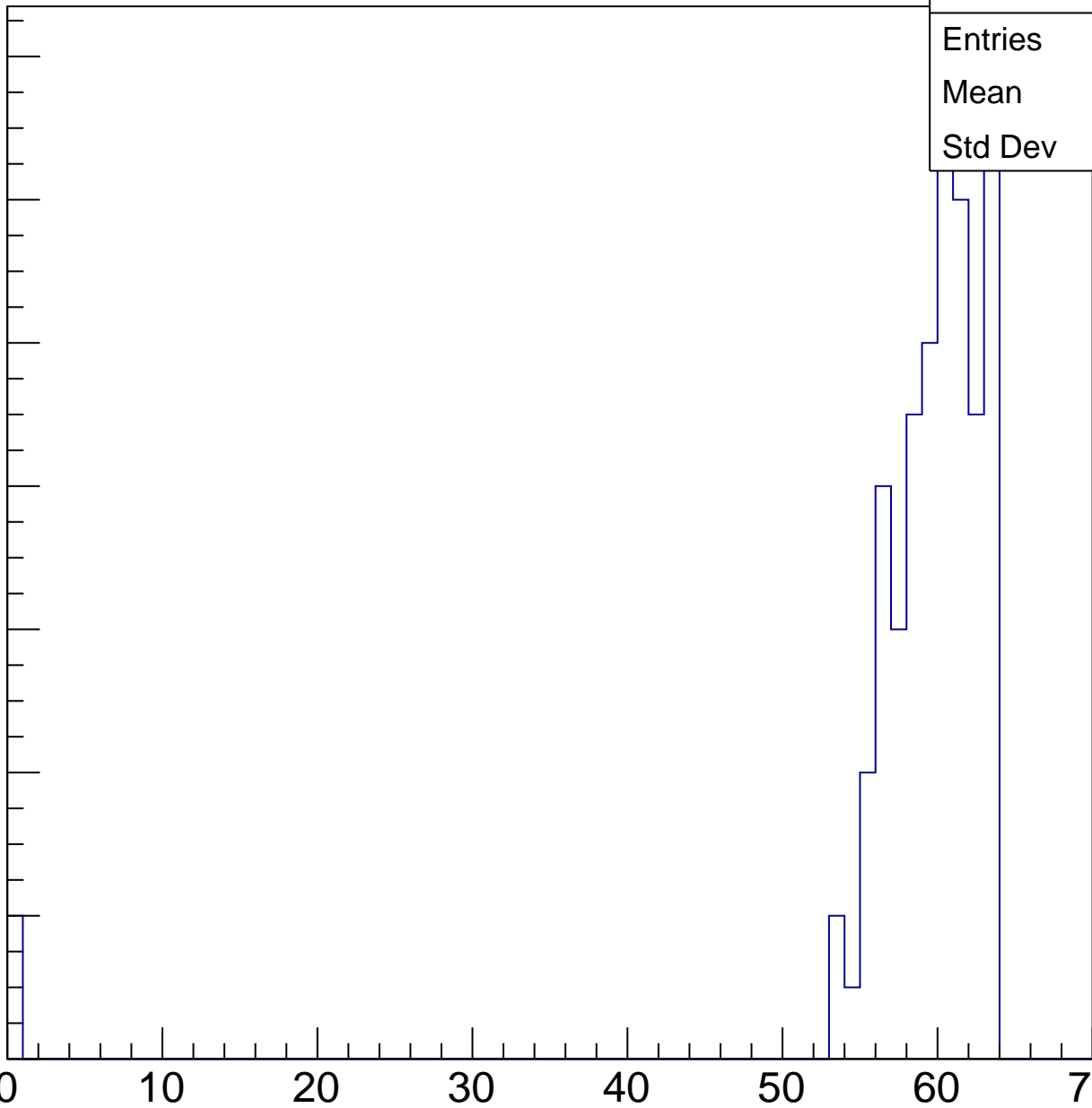
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 58.16 |
| Std Dev | 9.147 |

ampl

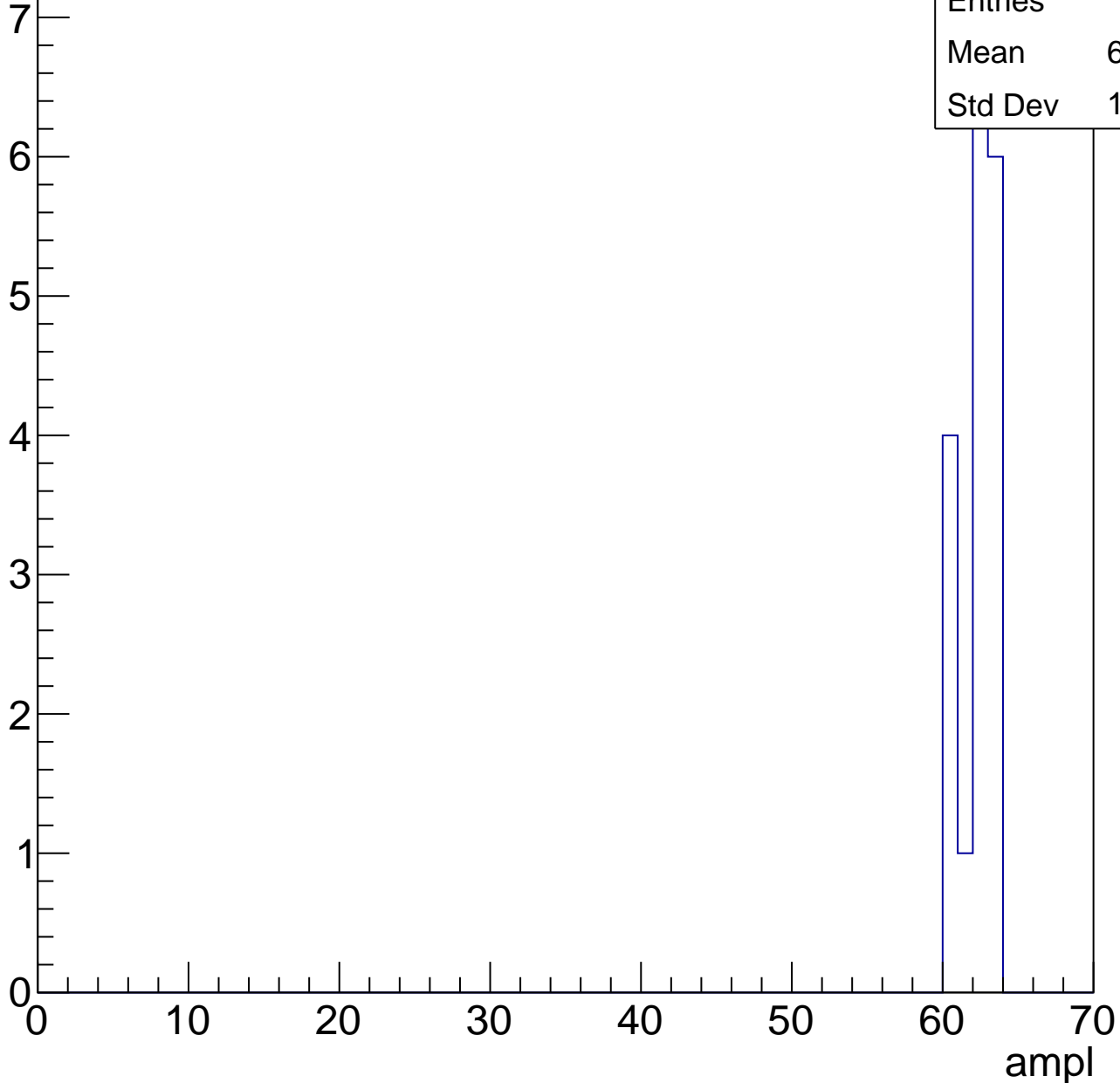


# B0L101S, U16-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 18    |
| Mean    | 61.83 |
| Std Dev | 1.118 |





# B0L101S, U16-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch3, adc0

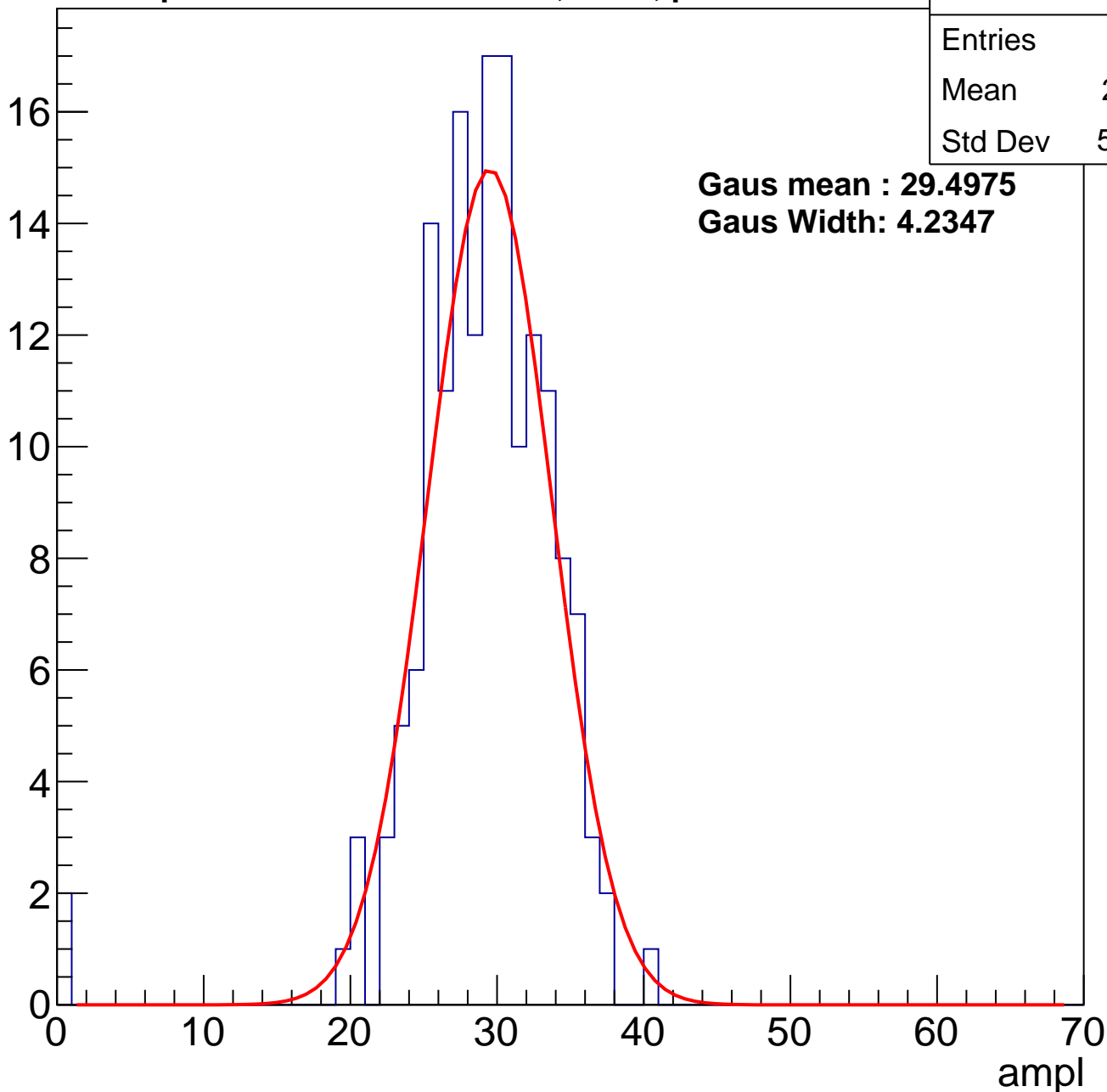
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 161   |
| Mean    | 28.61 |
| Std Dev | 5.025 |

**Gaus mean : 29.4975**

**Gaus Width: 4.2347**

Entry



# B0L101S, U16-ch3, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

164

Mean

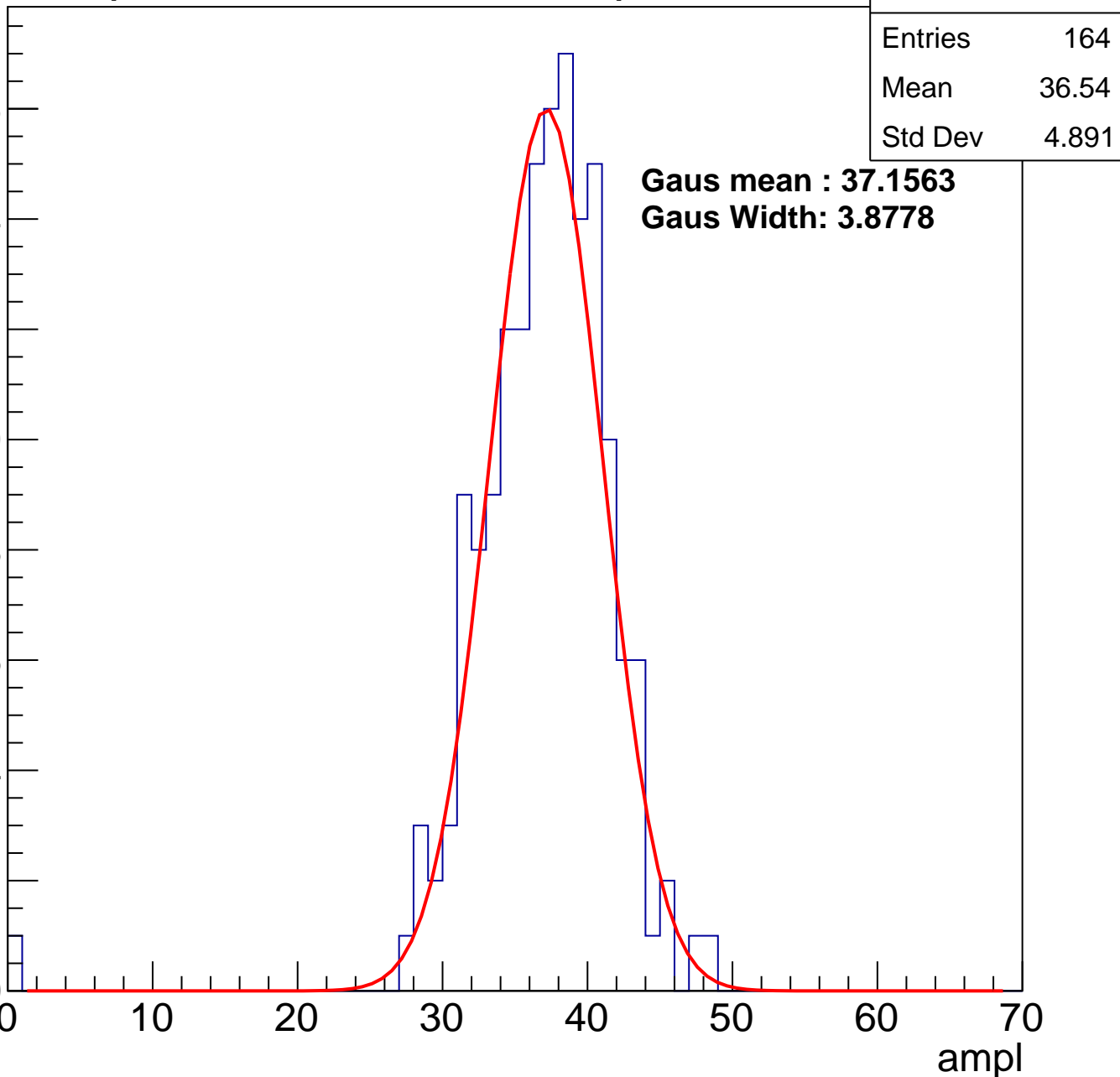
36.54

Std Dev

4.891

**Gaus mean : 37.1563**

**Gaus Width: 3.8778**



# B0L101S, U16-ch3, adc2

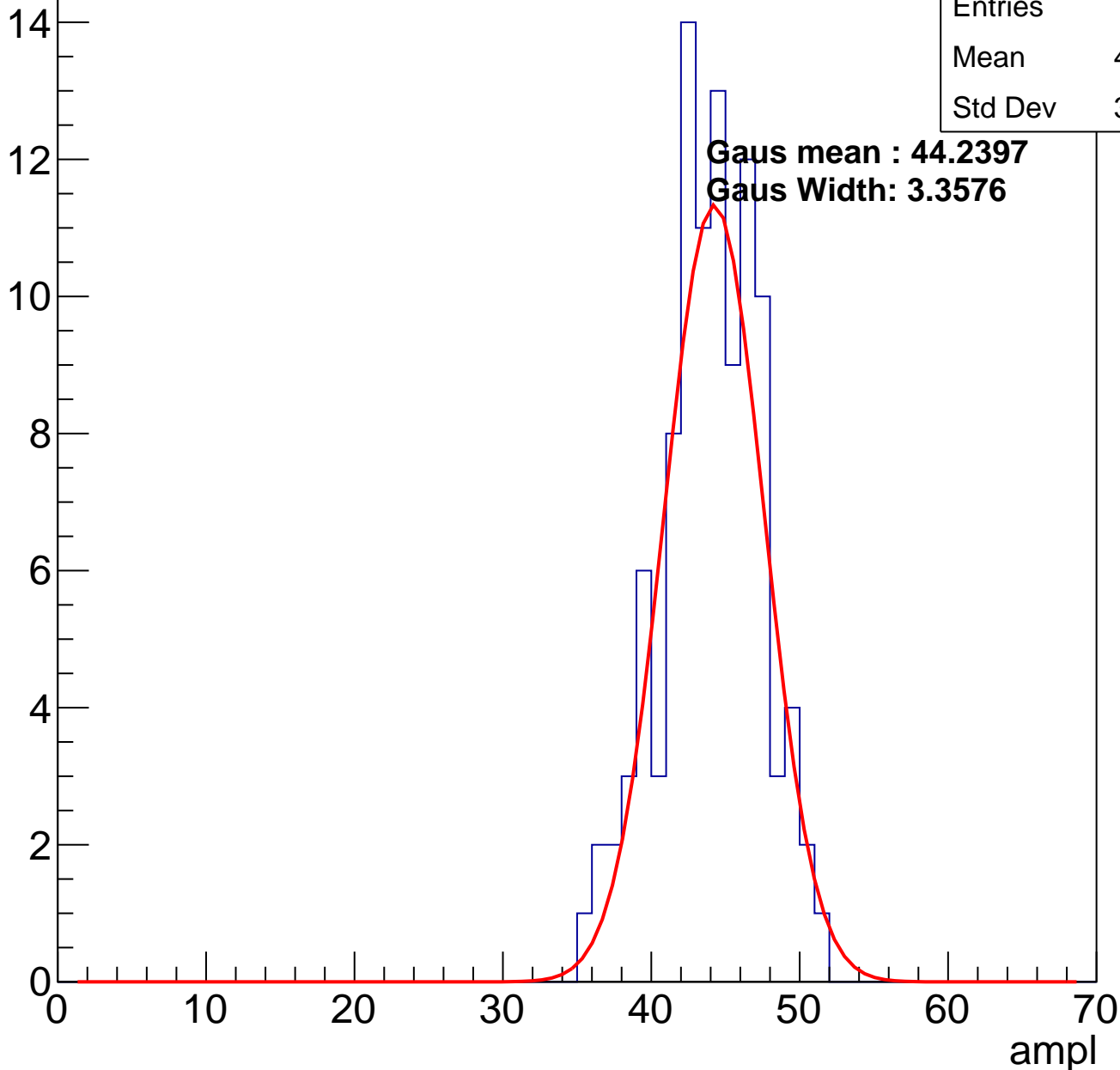
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 43.54 |
| Std Dev | 3.319 |

**Gaus mean : 44.2397**

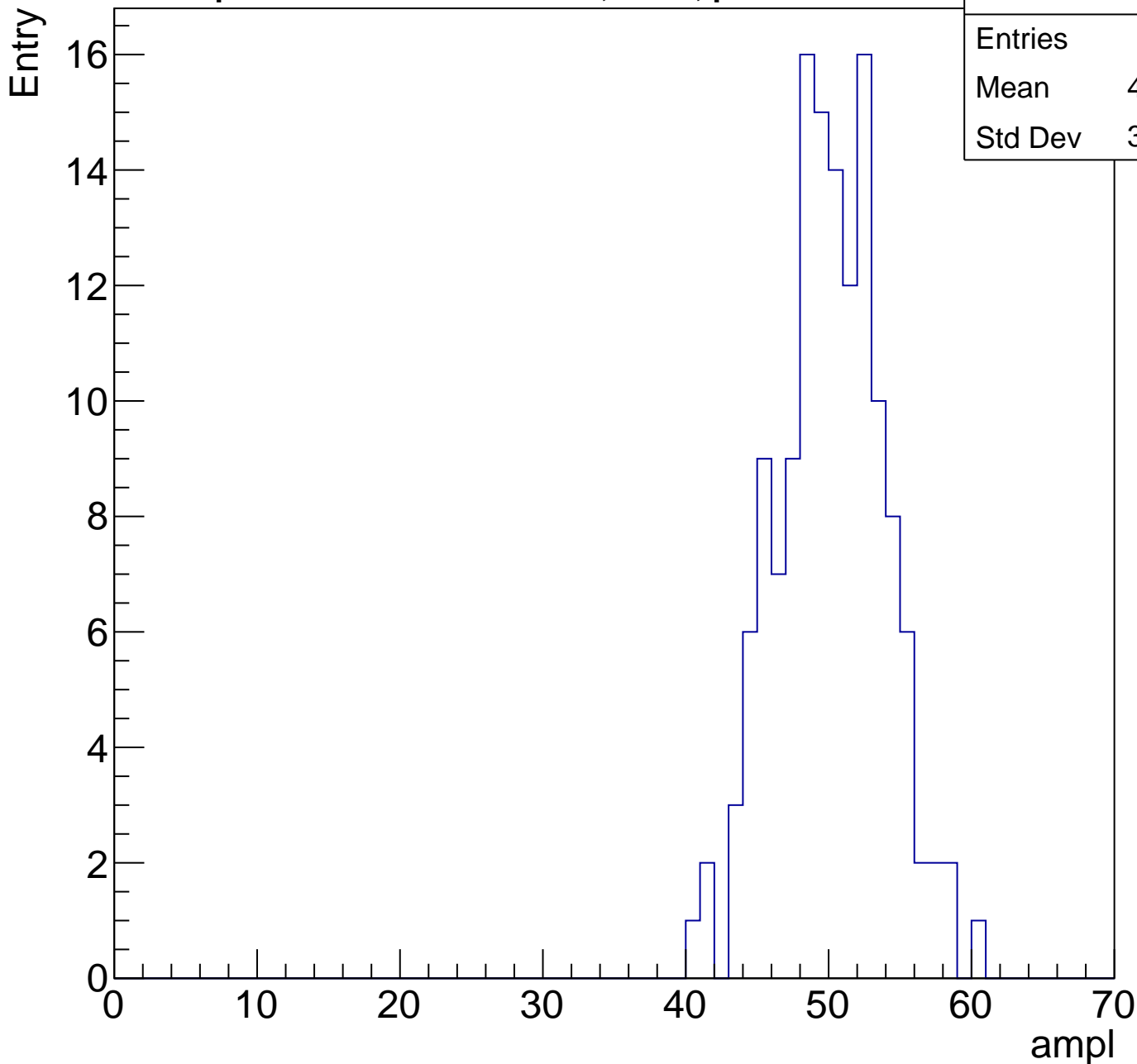
**Gaus Width: 3.3576**



# B0L101S, U16-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 141   |
| Mean    | 49.69 |
| Std Dev | 3.719 |



# B0L101S, U16-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

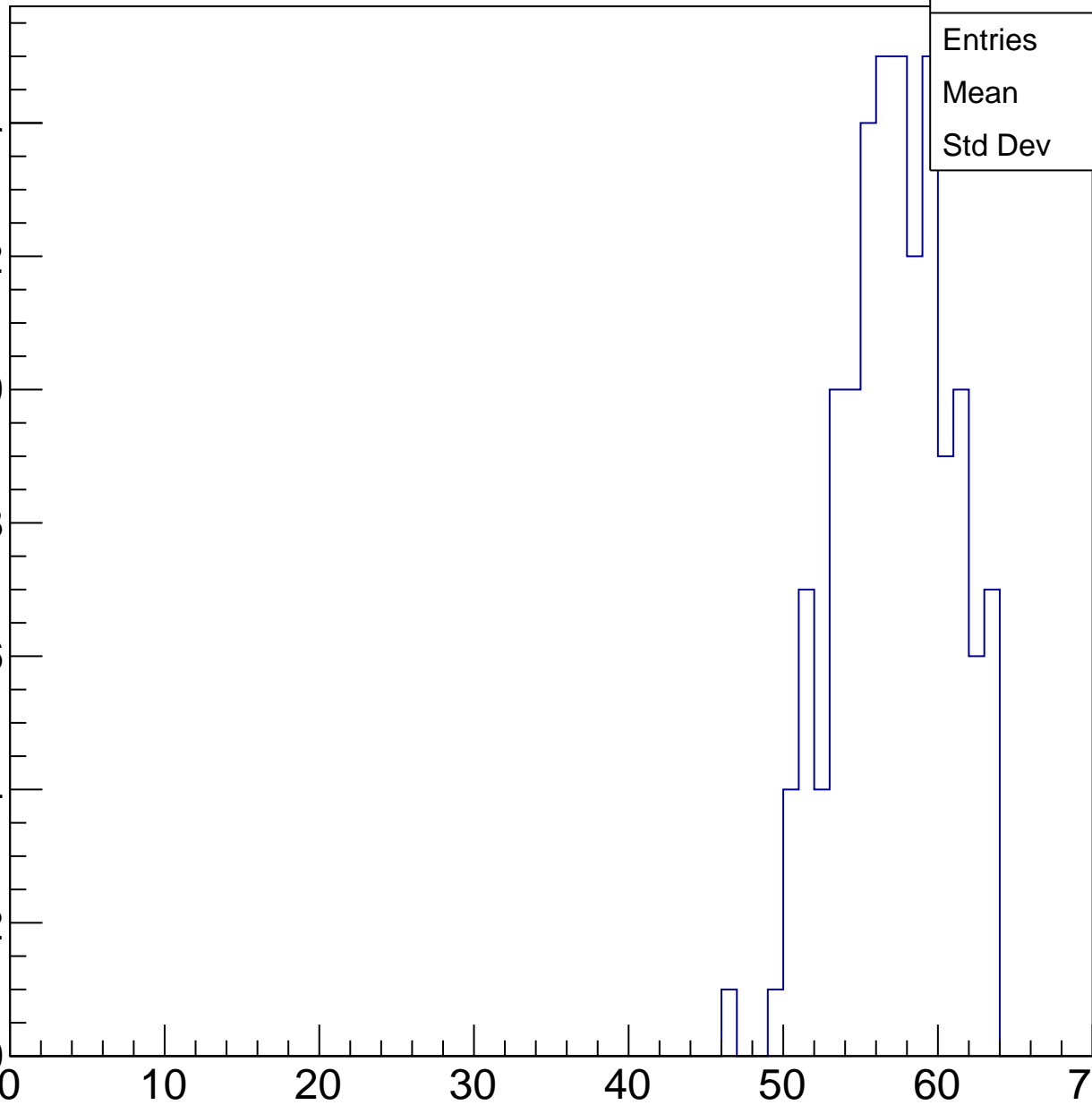
4

2

0

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 56.71 |
| Std Dev | 3.543 |

ampl



# B0L101S, U16-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

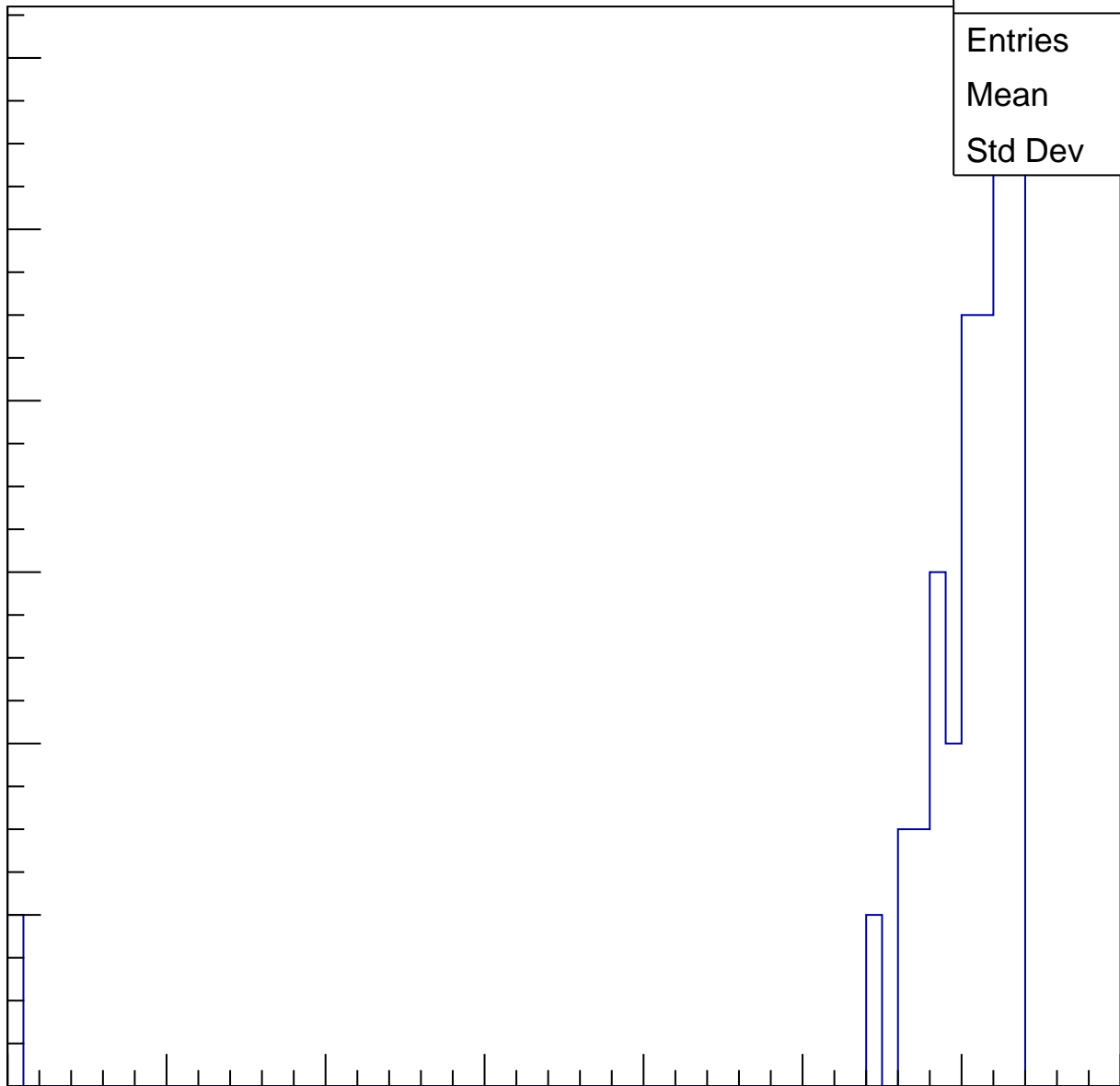
Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 58.31 |
| Std Dev | 10.98 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

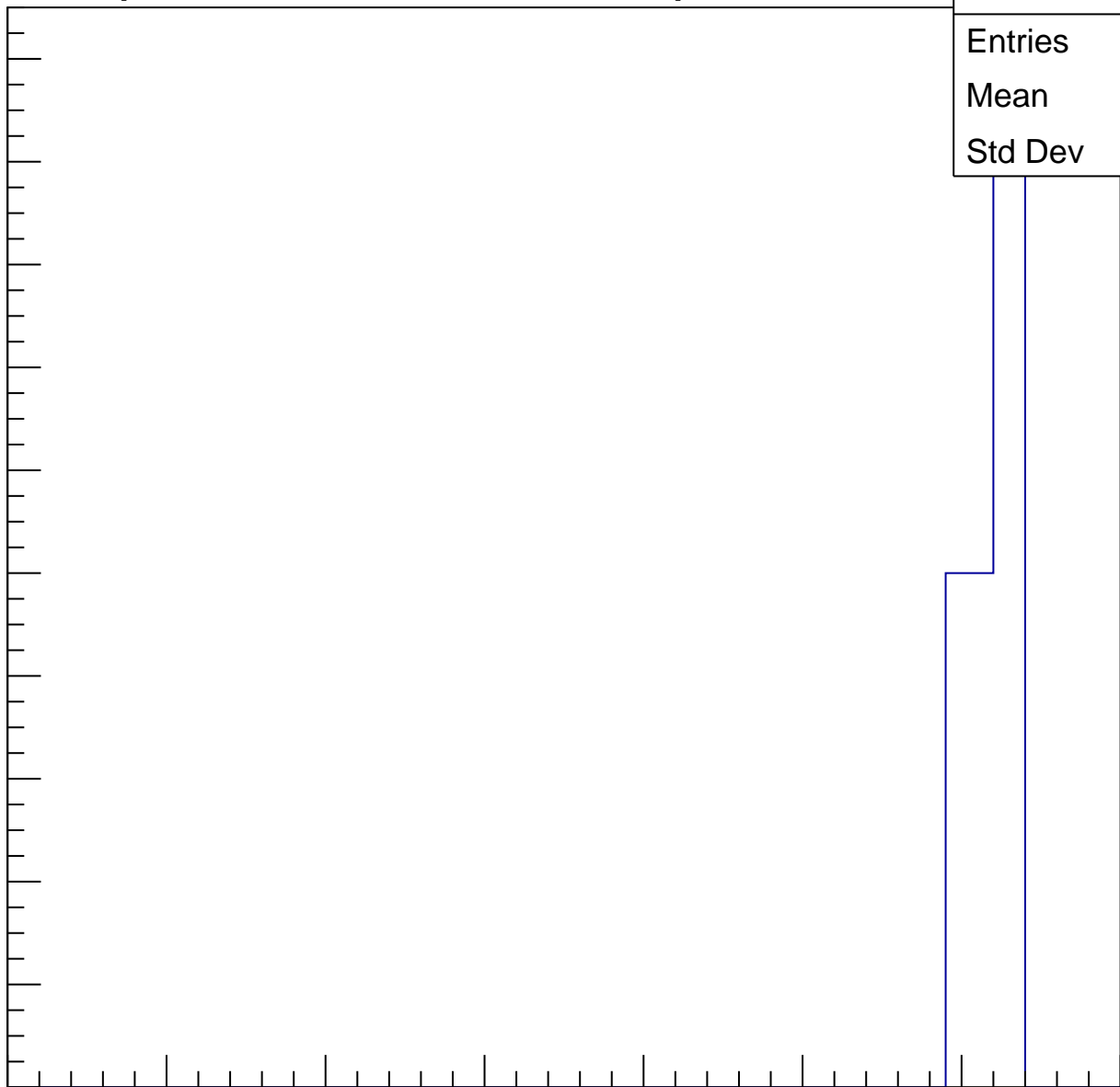
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 7     |
| Mean    | 61.43 |
| Std Dev | 1.4   |

0 10 20 30 40 50 60 70

ampl





# B0L101S, U16-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch4, adc0

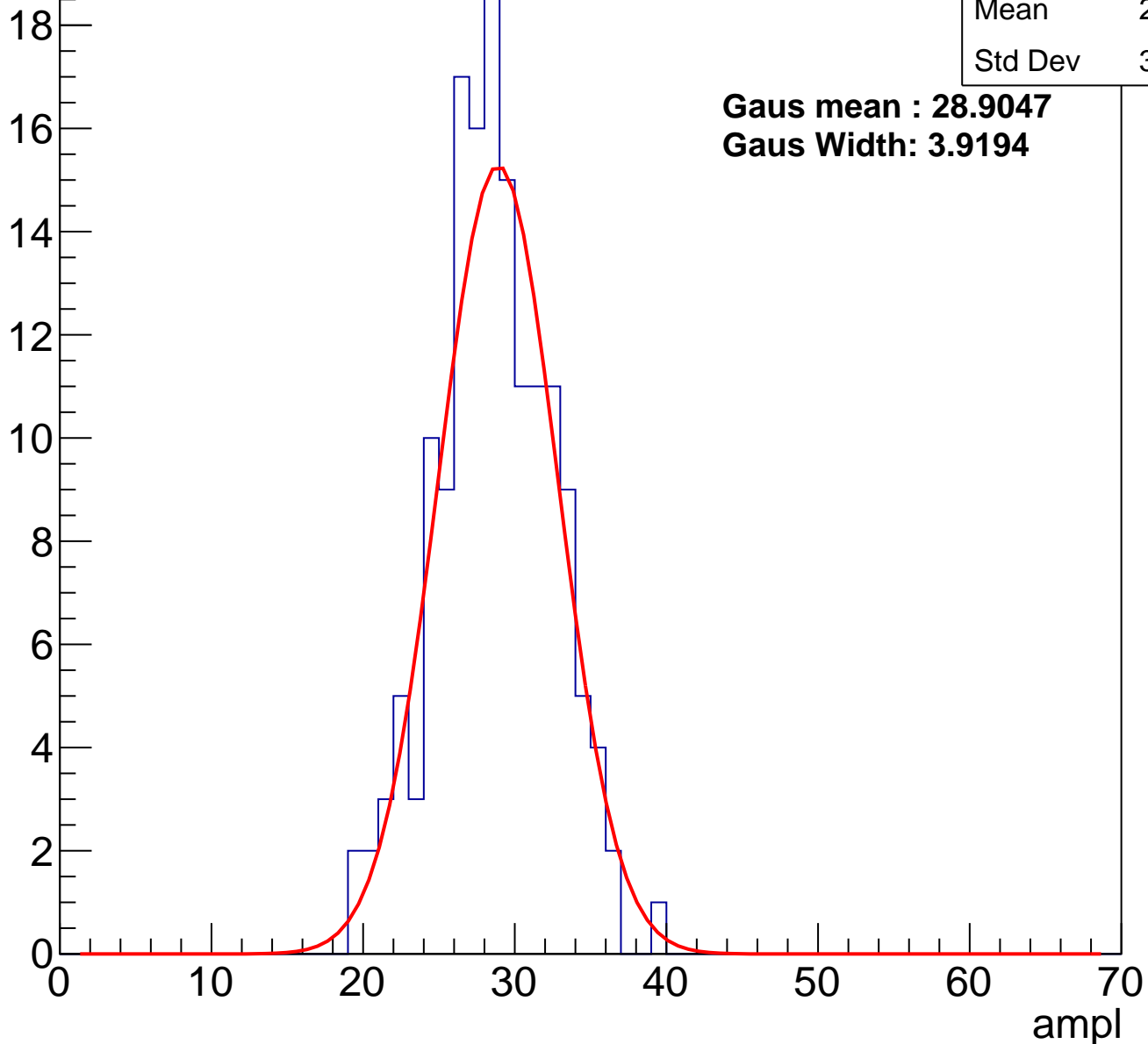
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 28.17 |
| Std Dev | 3.764 |

**Gaus mean : 28.9047**

**Gaus Width: 3.9194**

Entry



# B0L101S, U16-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

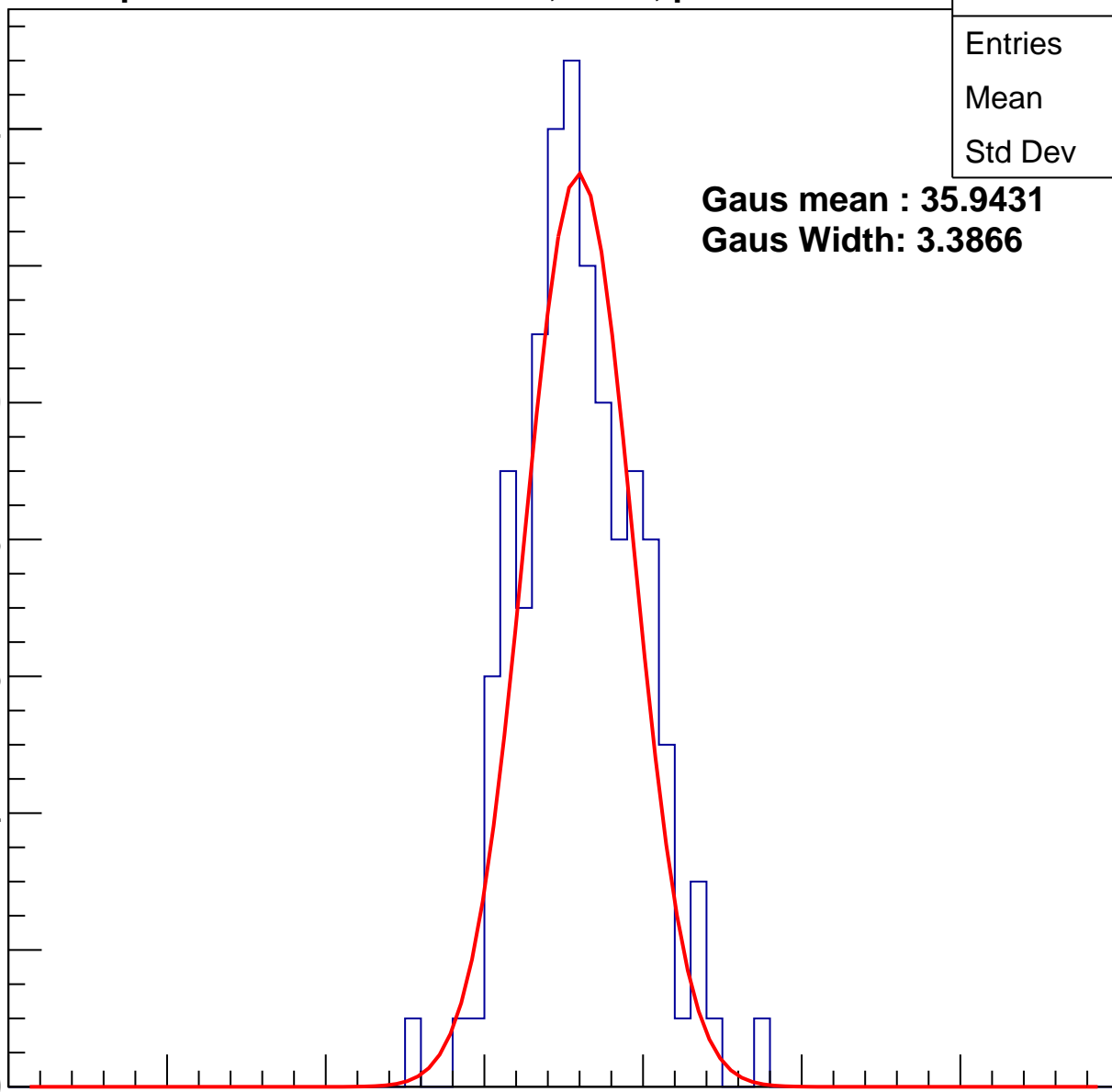
70

ampl

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 35.55 |
| Std Dev | 3.692 |

**Gaus mean : 35.9431**

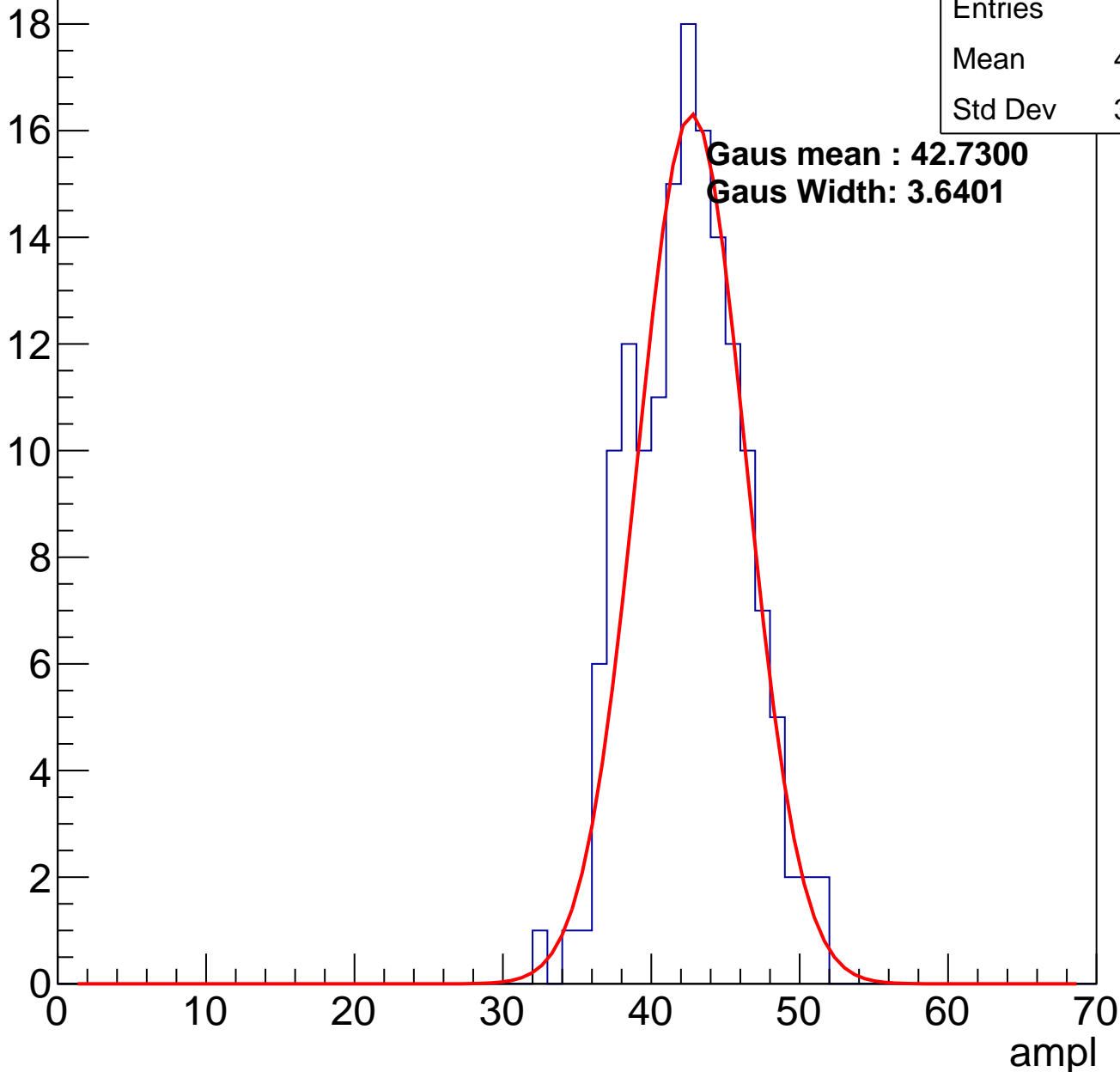
**Gaus Width: 3.3866**



# B0L101S, U16-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch4, adc3

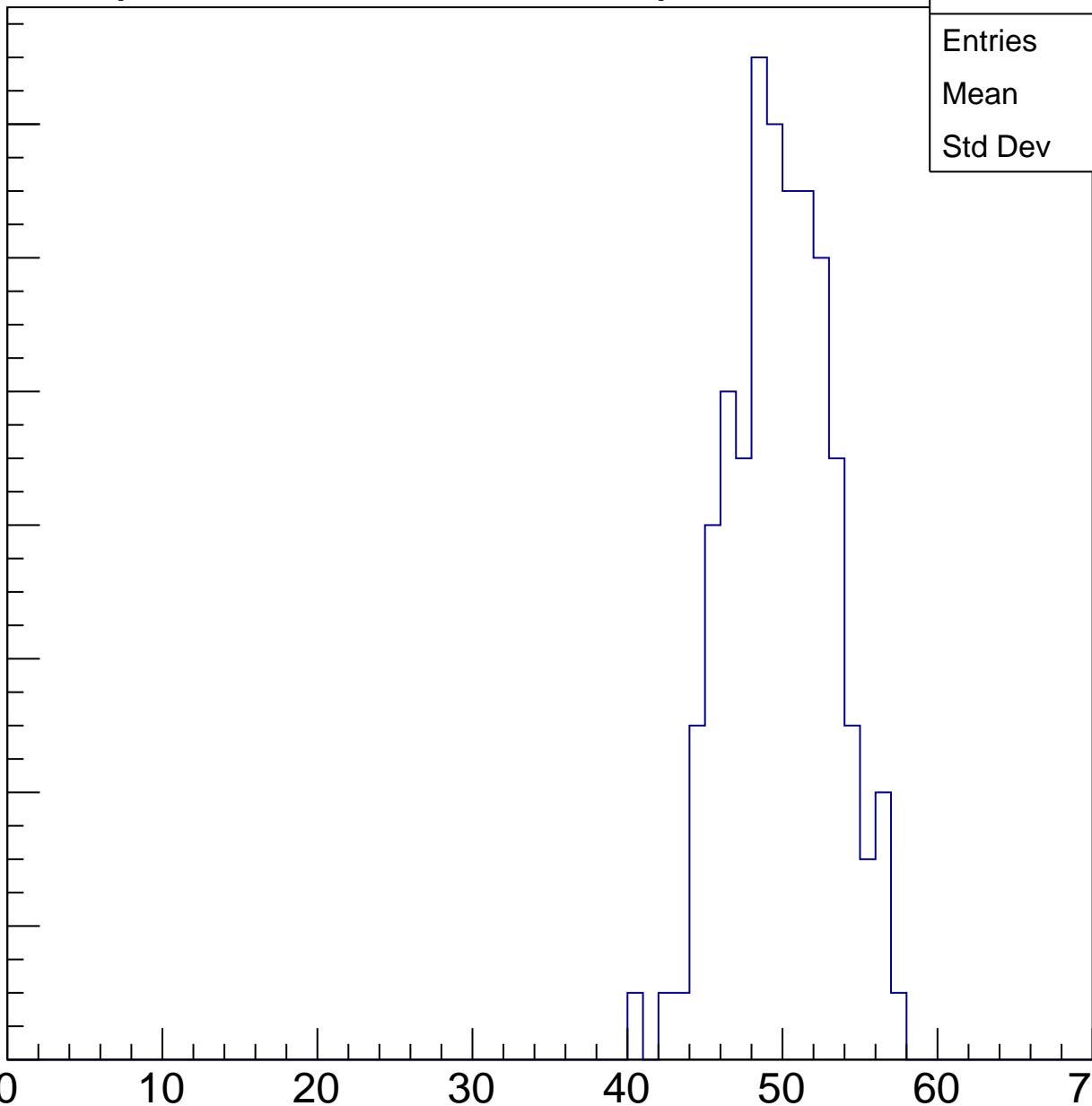
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 49.39 |
| Std Dev | 3.301 |

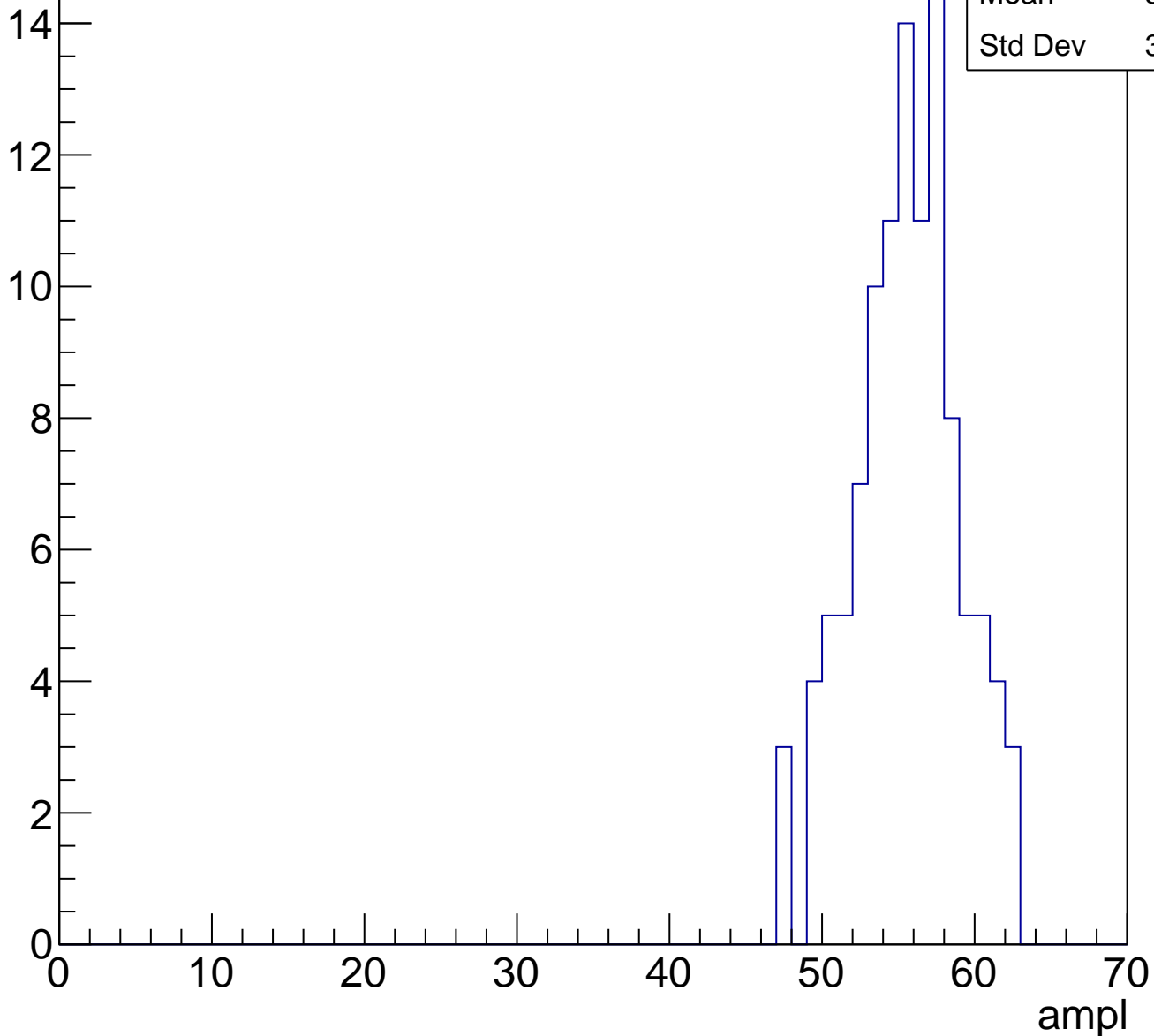
ampl



# B0L101S, U16-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

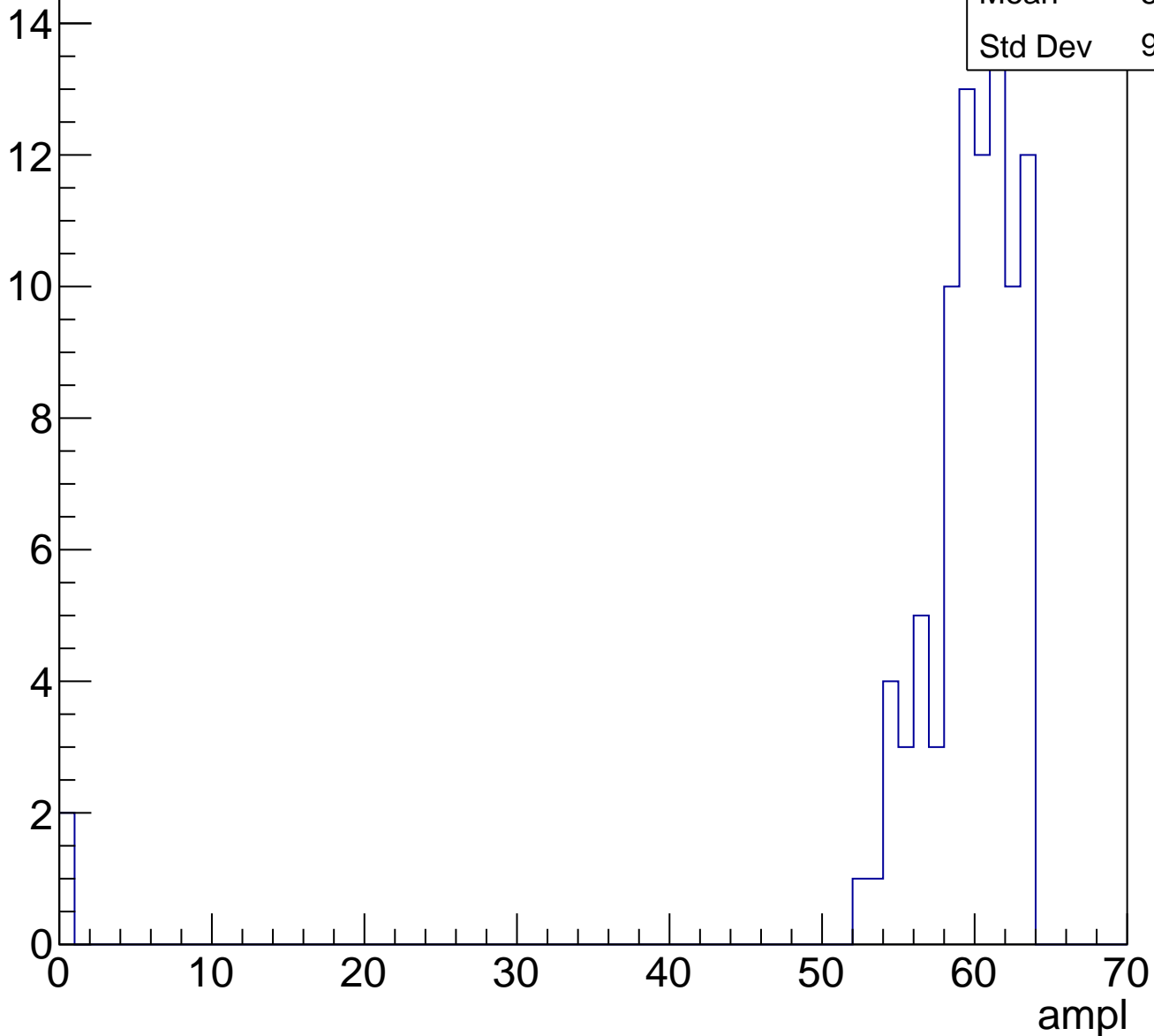
Entry



# B0L101S, U16-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

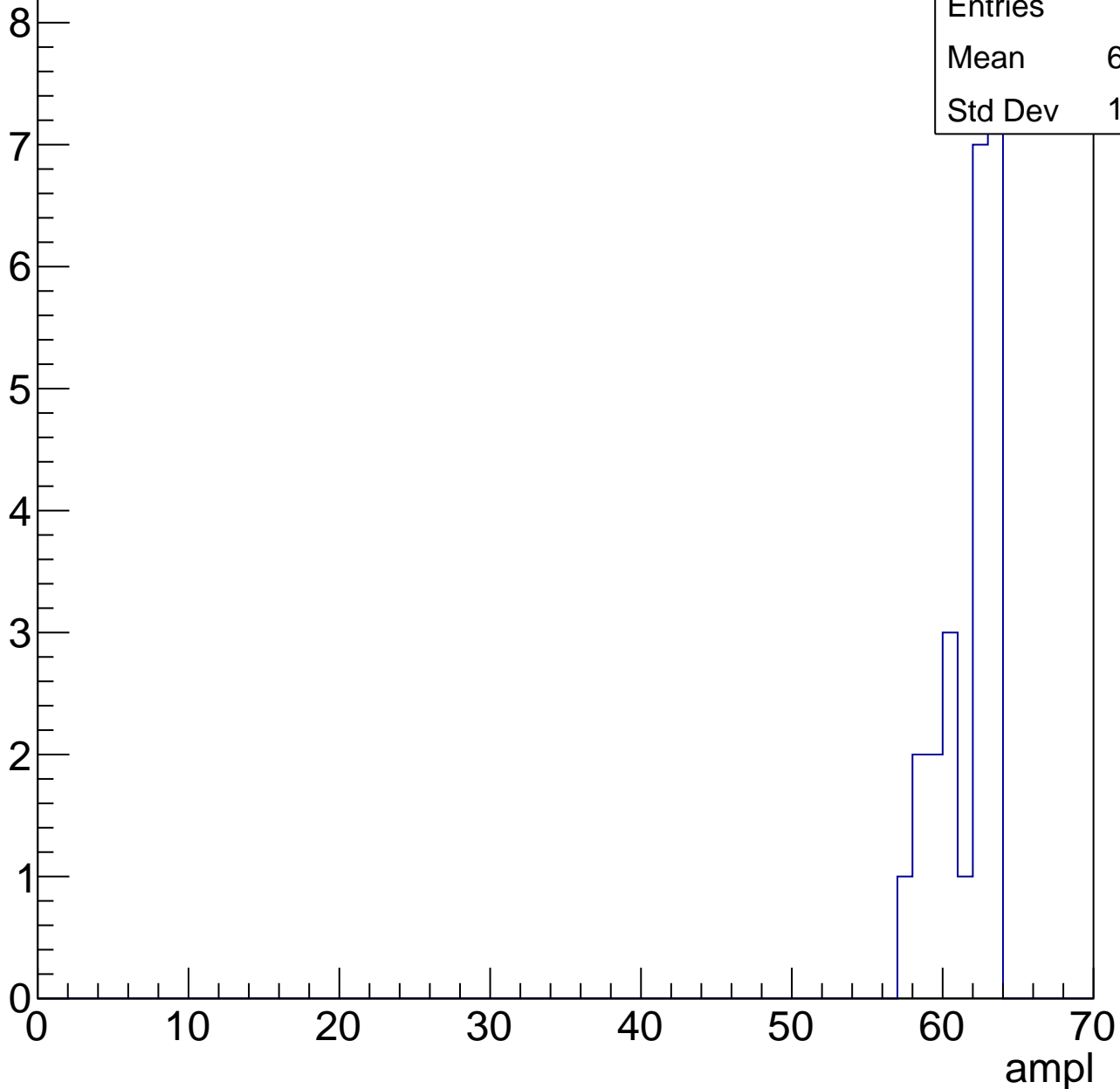


# B0L101S, U16-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 24    |
| Mean    | 61.25 |
| Std Dev | 1.854 |





# B0L101S, U16-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 3 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch5, adc0

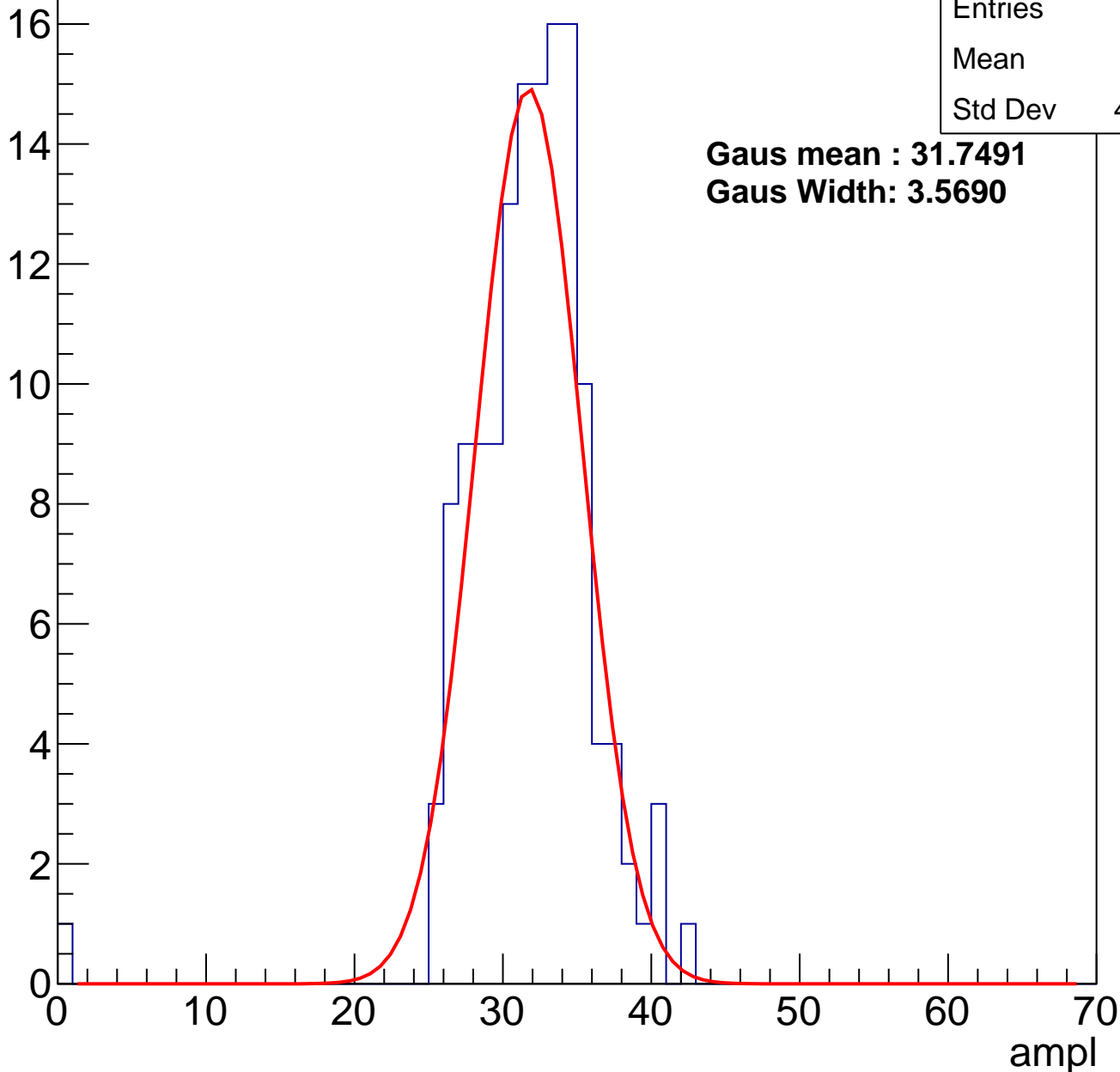
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 31.4  |
| Std Dev | 4.386 |

**Gaus mean : 31.7491**

**Gaus Width: 3.5690**

Entry



# B0L101S, U16-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

163

Mean

38.73

Std Dev

5.714

**Gaus mean : 39.4114**

**Gaus Width: 3.5739**

0

10

20

30

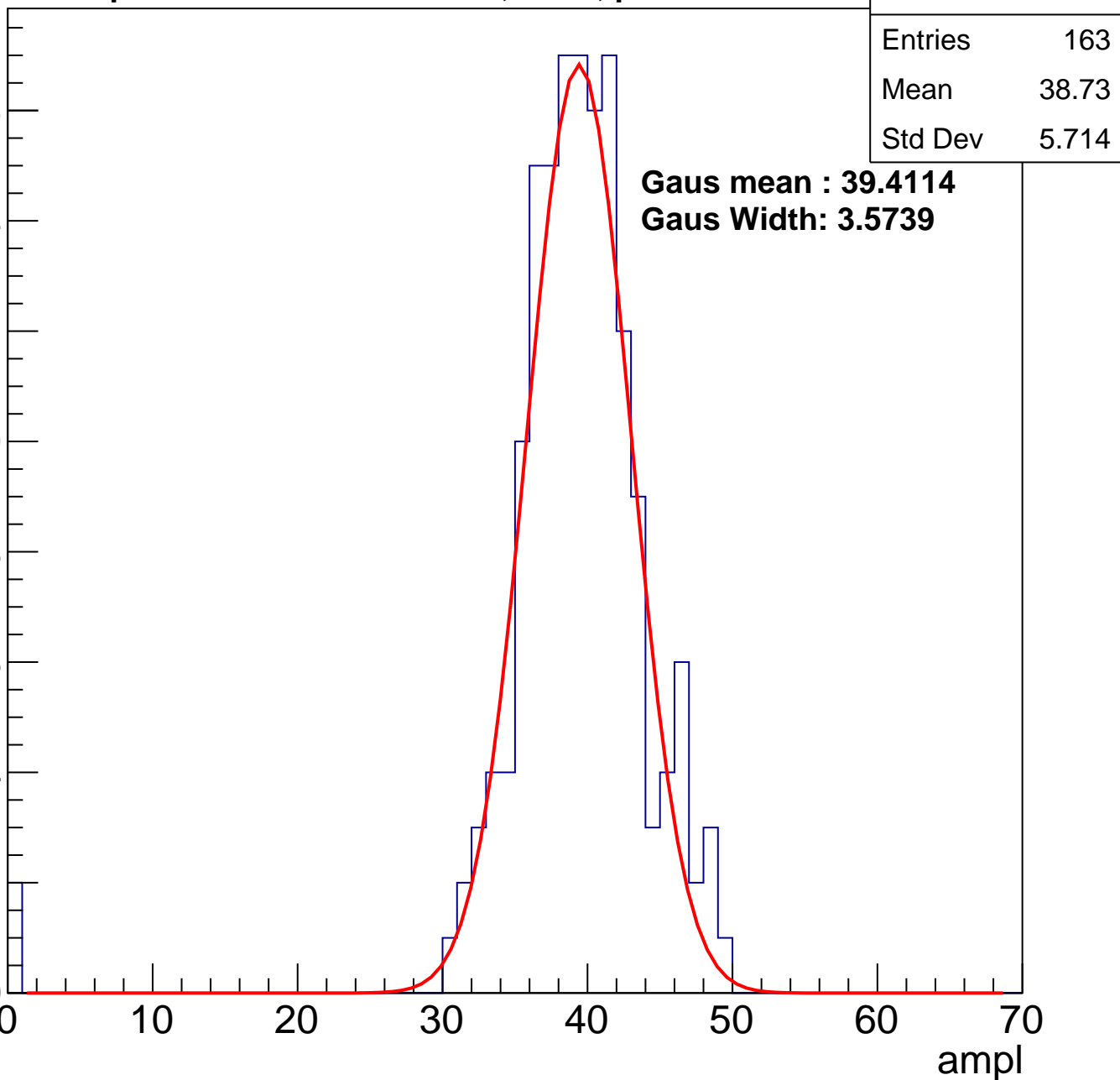
40

50

60

70

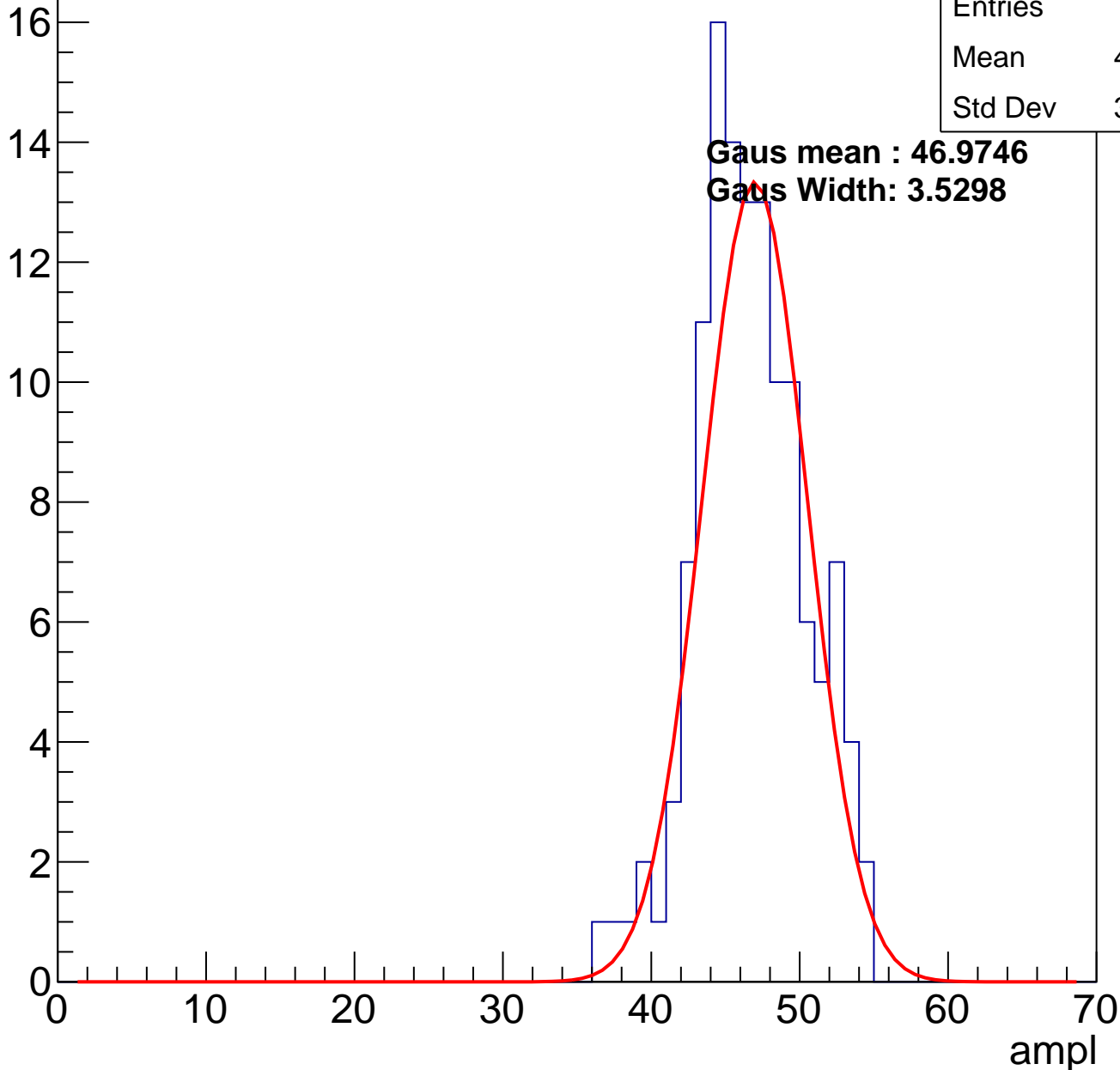
ampl



# B0L101S, U16-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

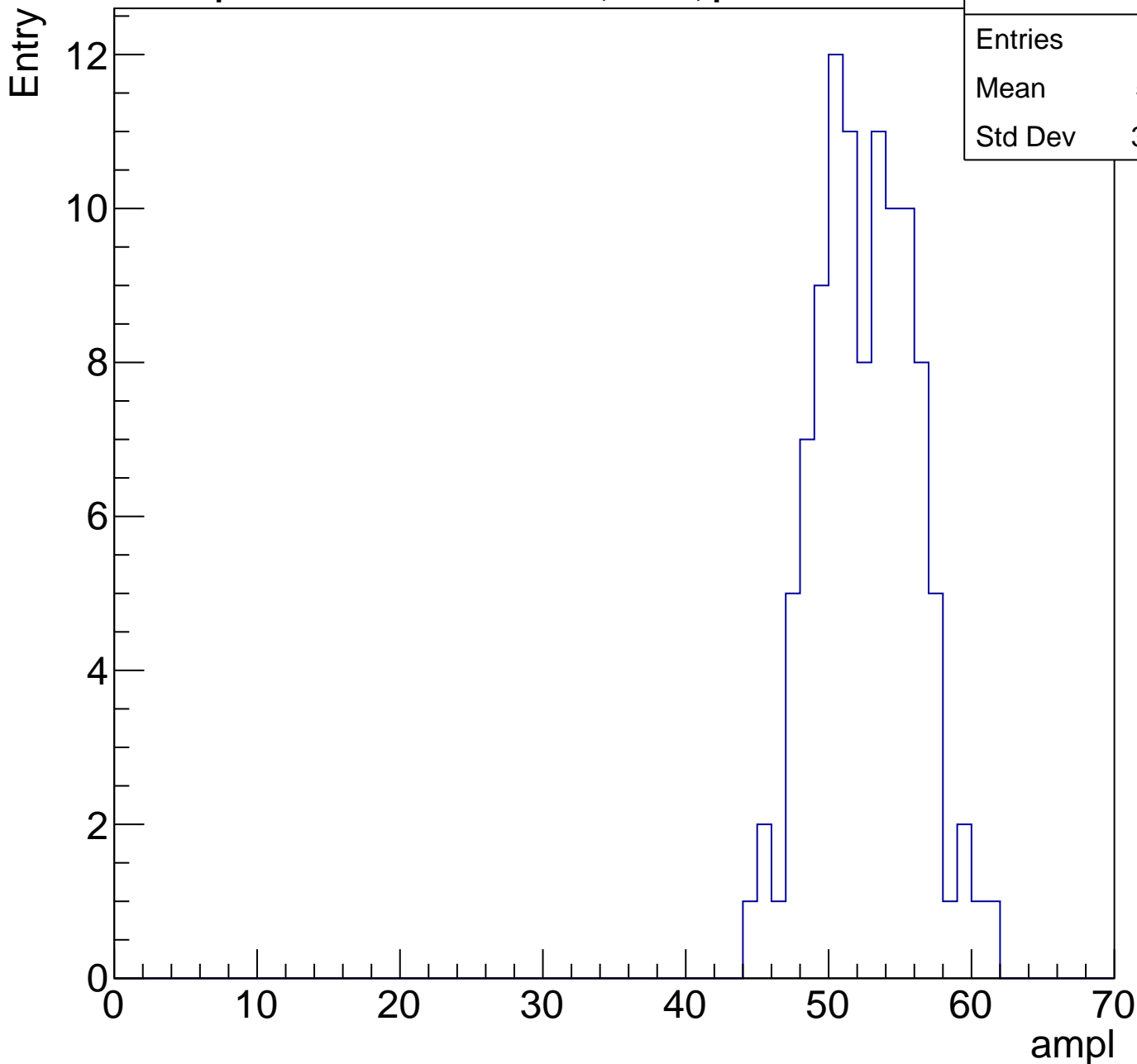
Entry



# B0L101S, U16-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

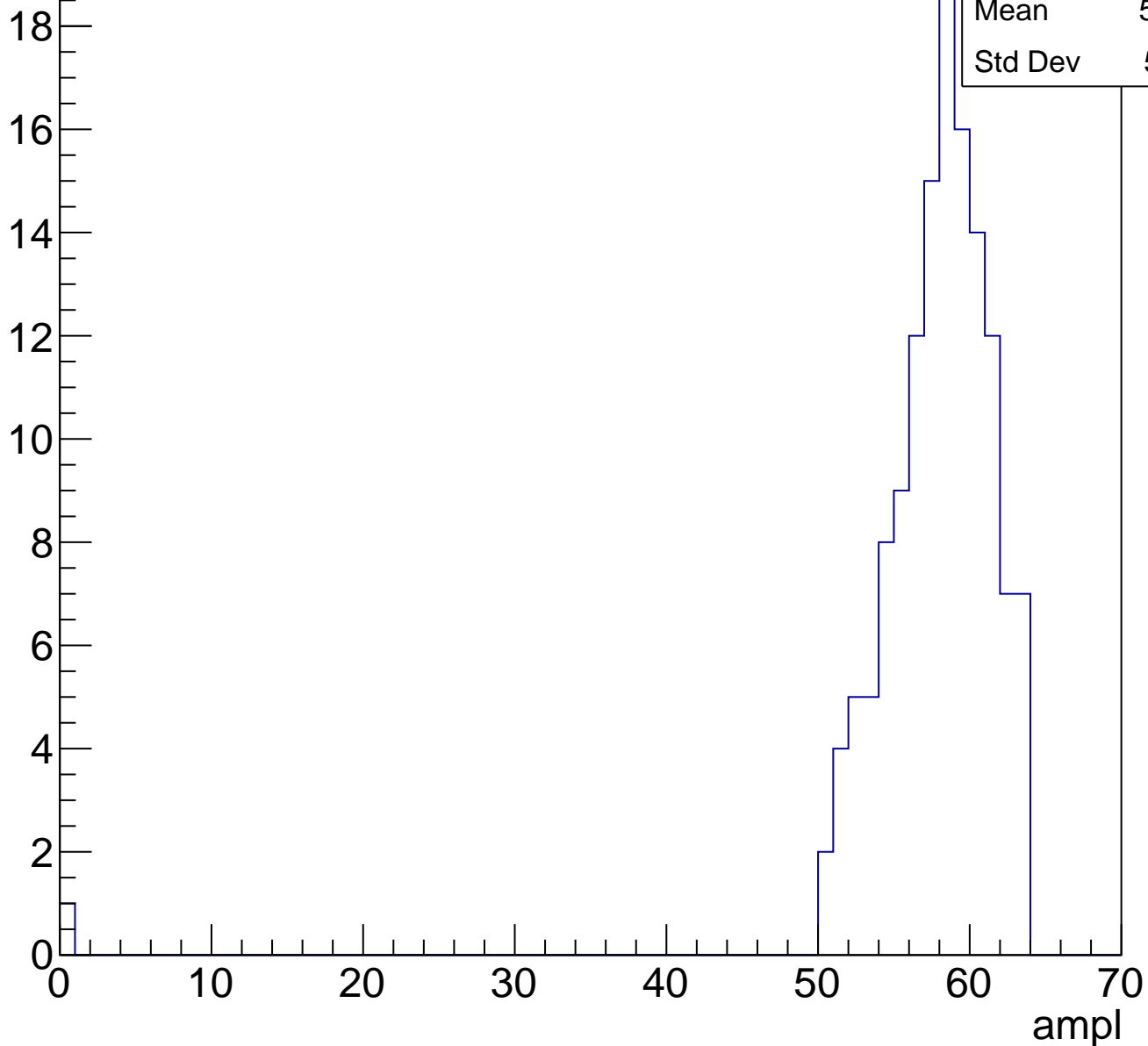
|         |       |
|---------|-------|
| Entries | 105   |
| Mean    | 52.11 |
| Std Dev | 3.446 |



# B0L101S, U16-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 60.1  |
| Std Dev | 9.629 |

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 61 |
| Std Dev | 0  |



# B0L101S, U16-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch6, adc0

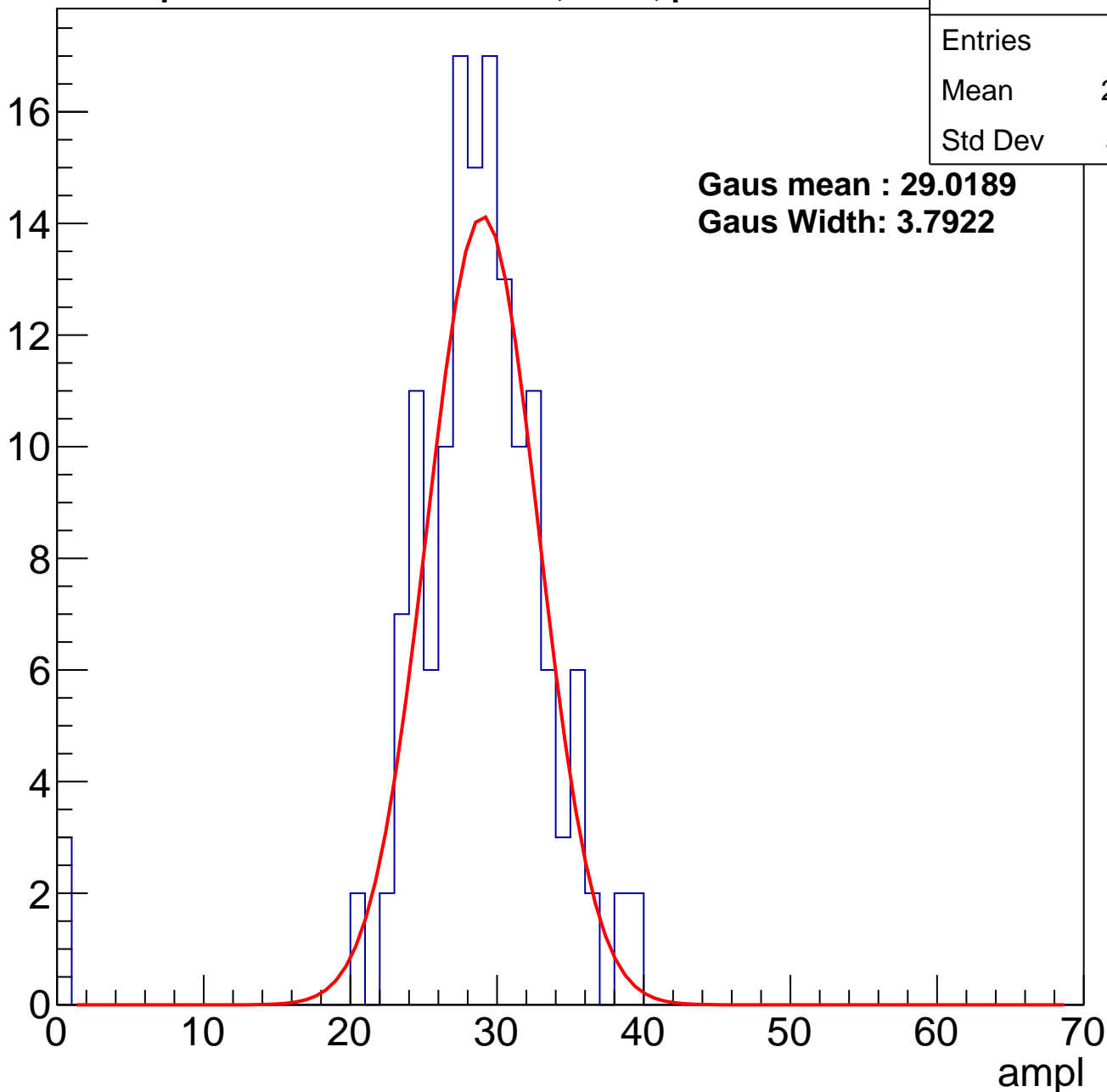
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 145   |
| Mean    | 28.13 |
| Std Dev | 5.551 |

**Gaus mean : 29.0189**

**Gaus Width: 3.7922**



# B0L101S, U16-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 35.38 |
| Std Dev | 3.164 |

**Gaus mean : 35.6261**

**Gaus Width: 3.3880**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

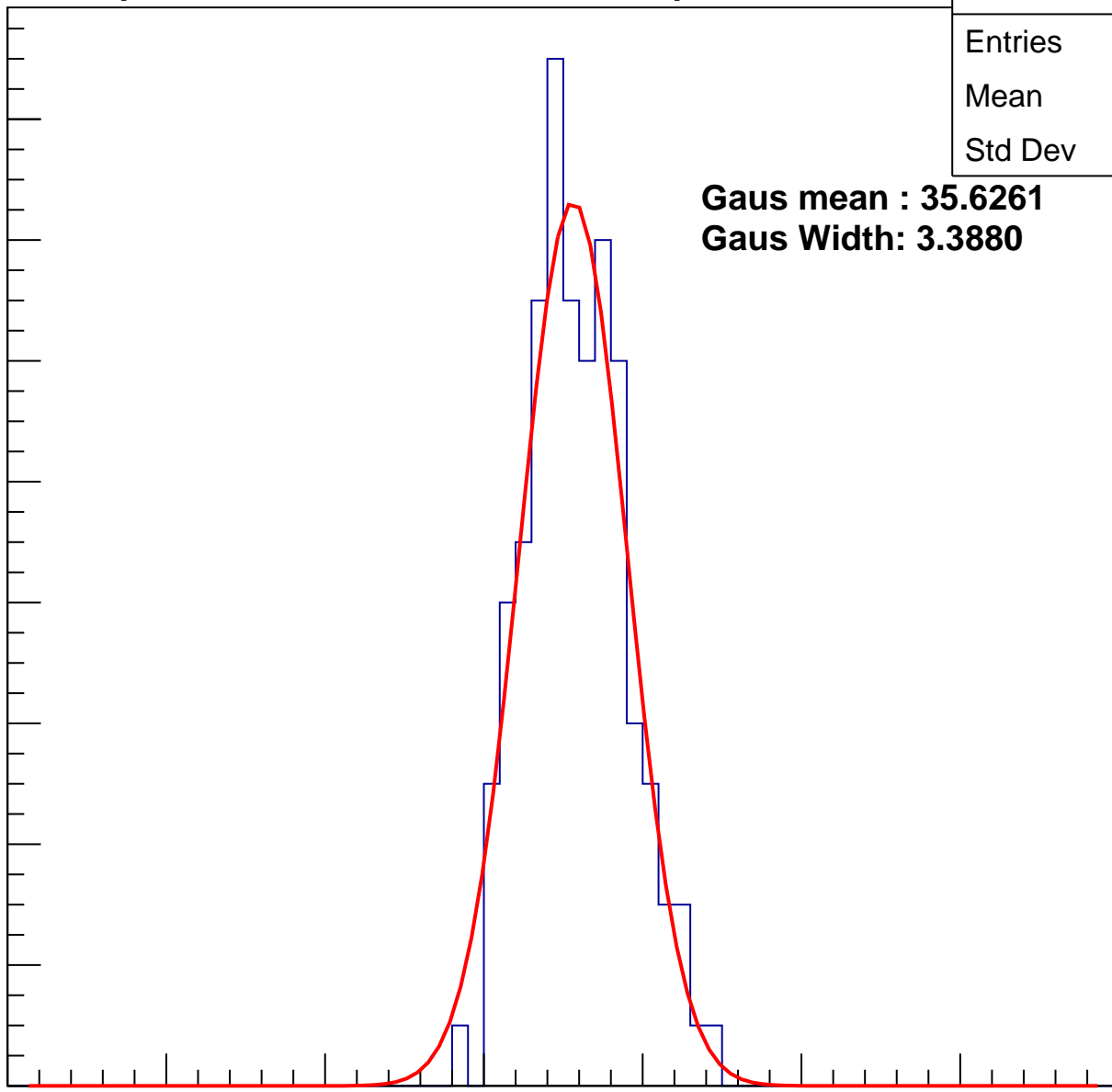
40

50

60

70

ampl



# B0L101S, U16-ch6, adc2

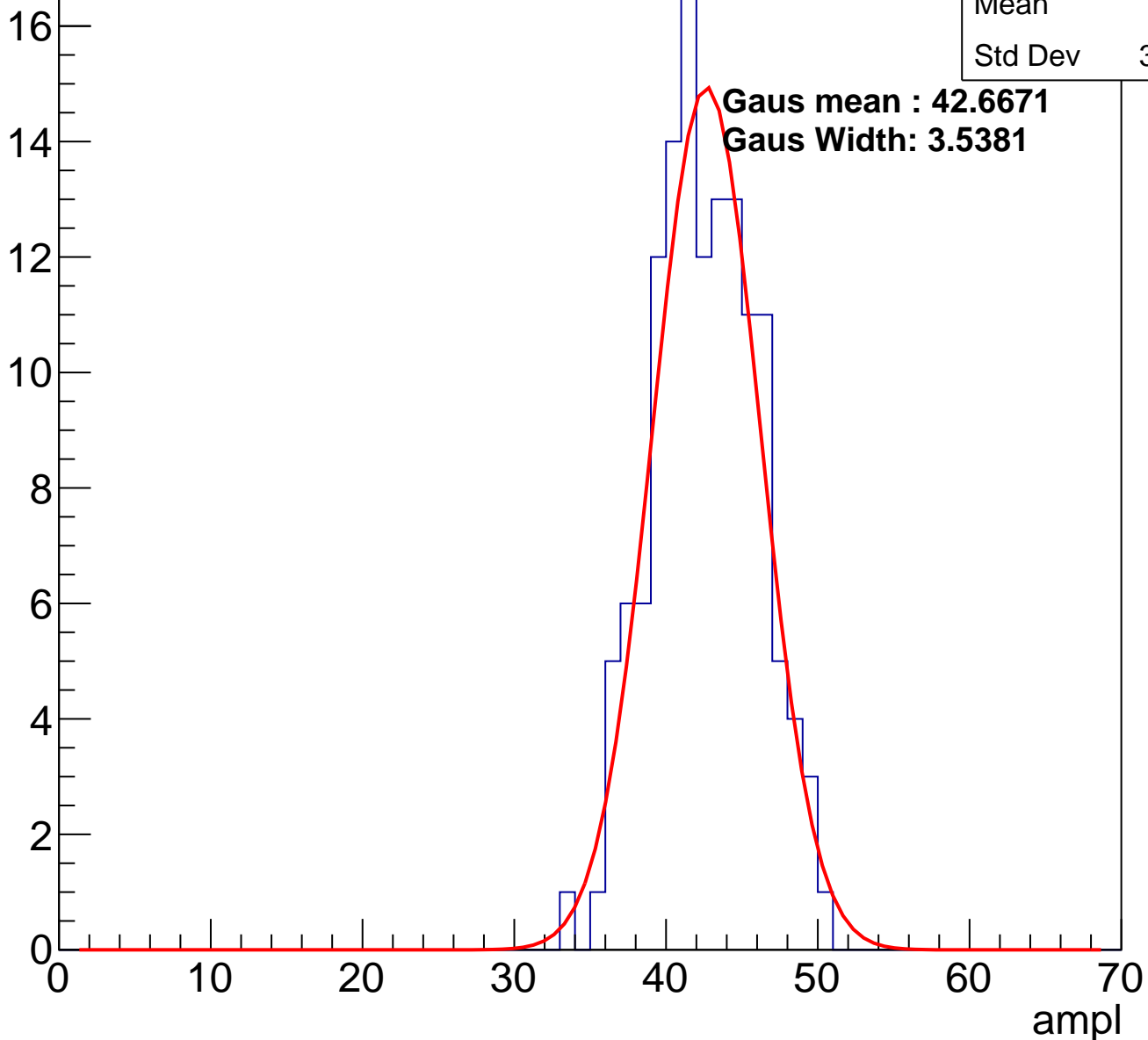
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 42.1  |
| Std Dev | 3.395 |

**Gaus mean : 42.6671**

**Gaus Width: 3.5381**



# B0L101S, U16-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

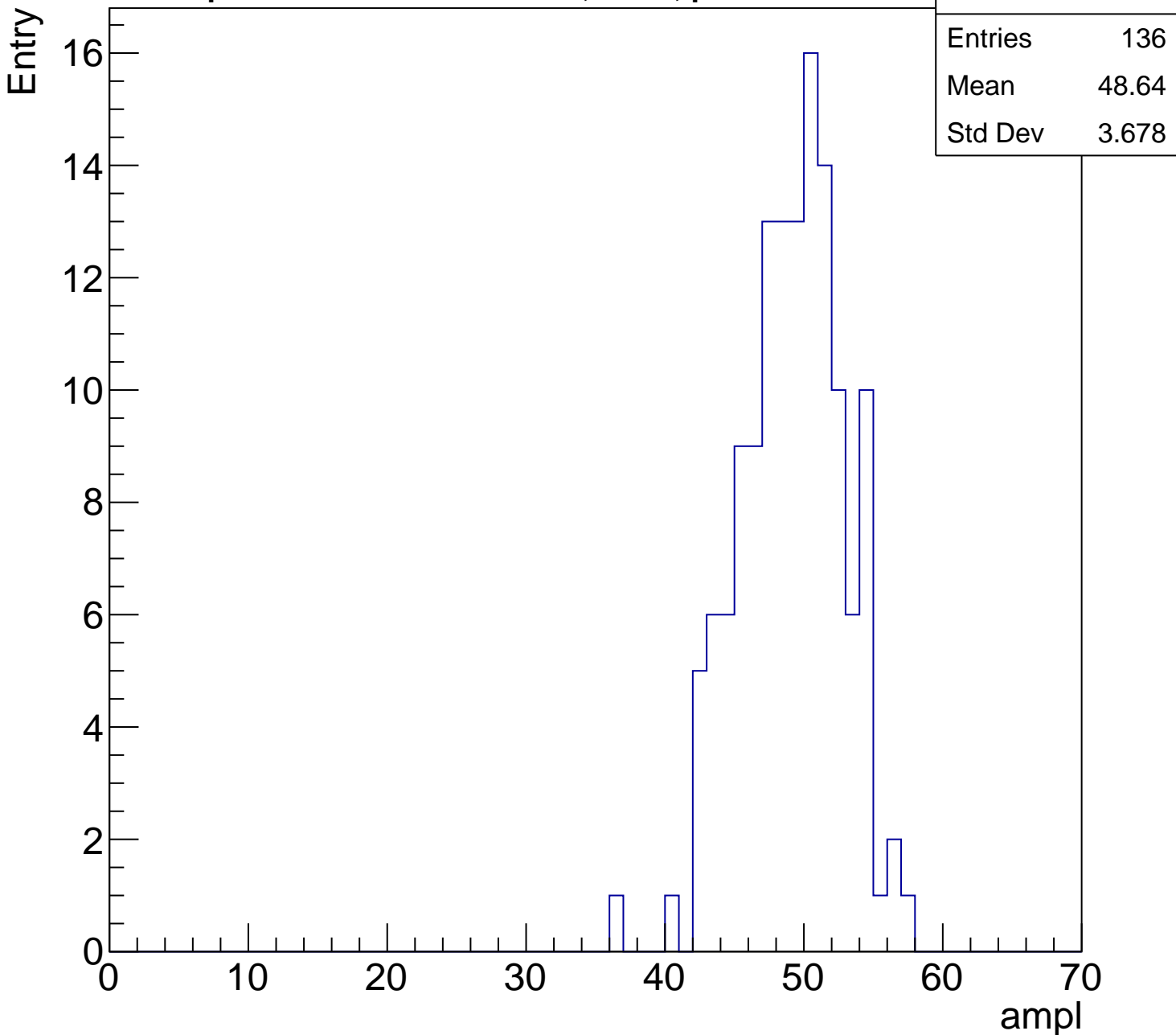
|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 48.64 |
| Std Dev | 3.678 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

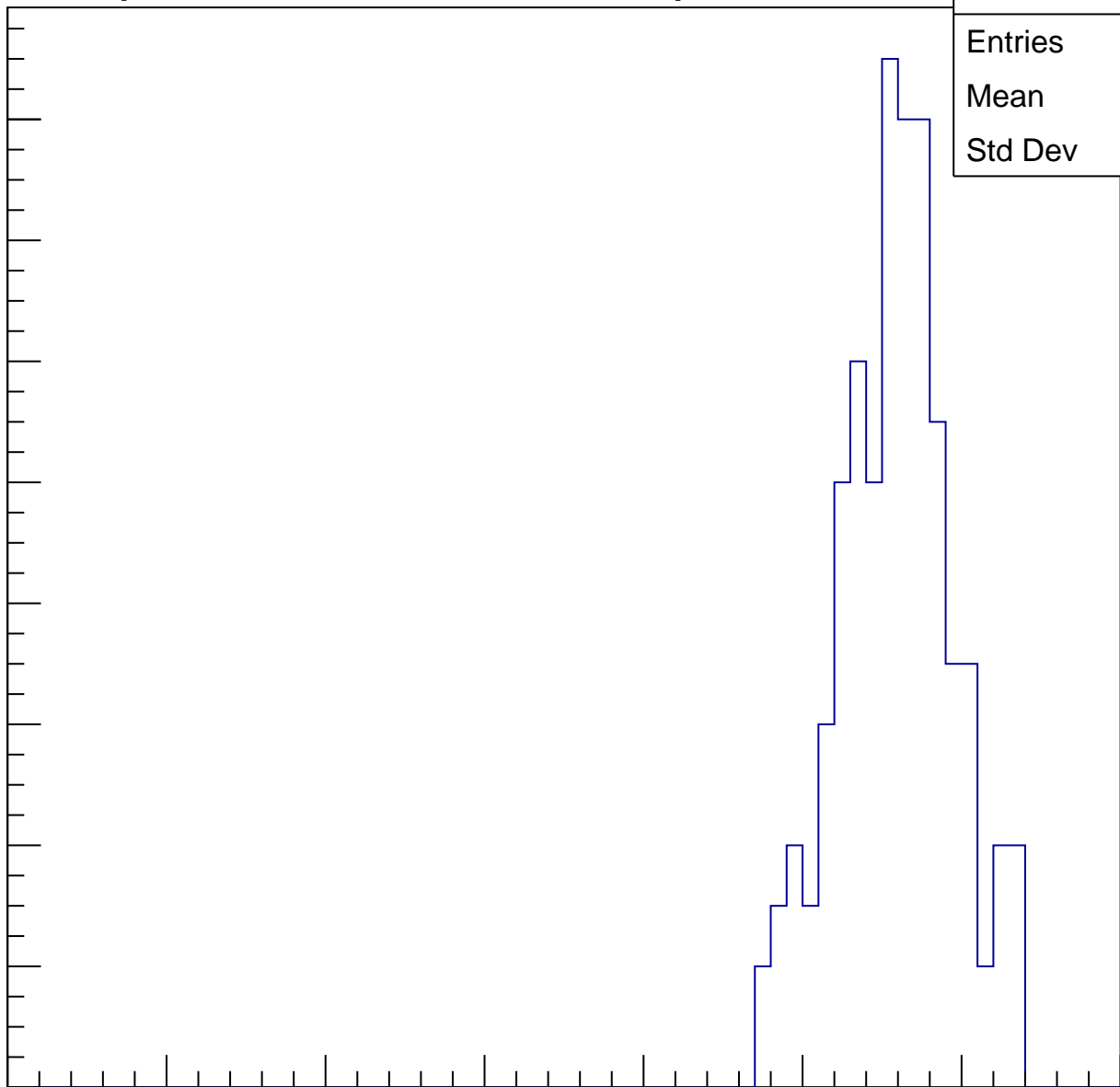
|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 55.39 |
| Std Dev | 3.57  |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch6, adc5

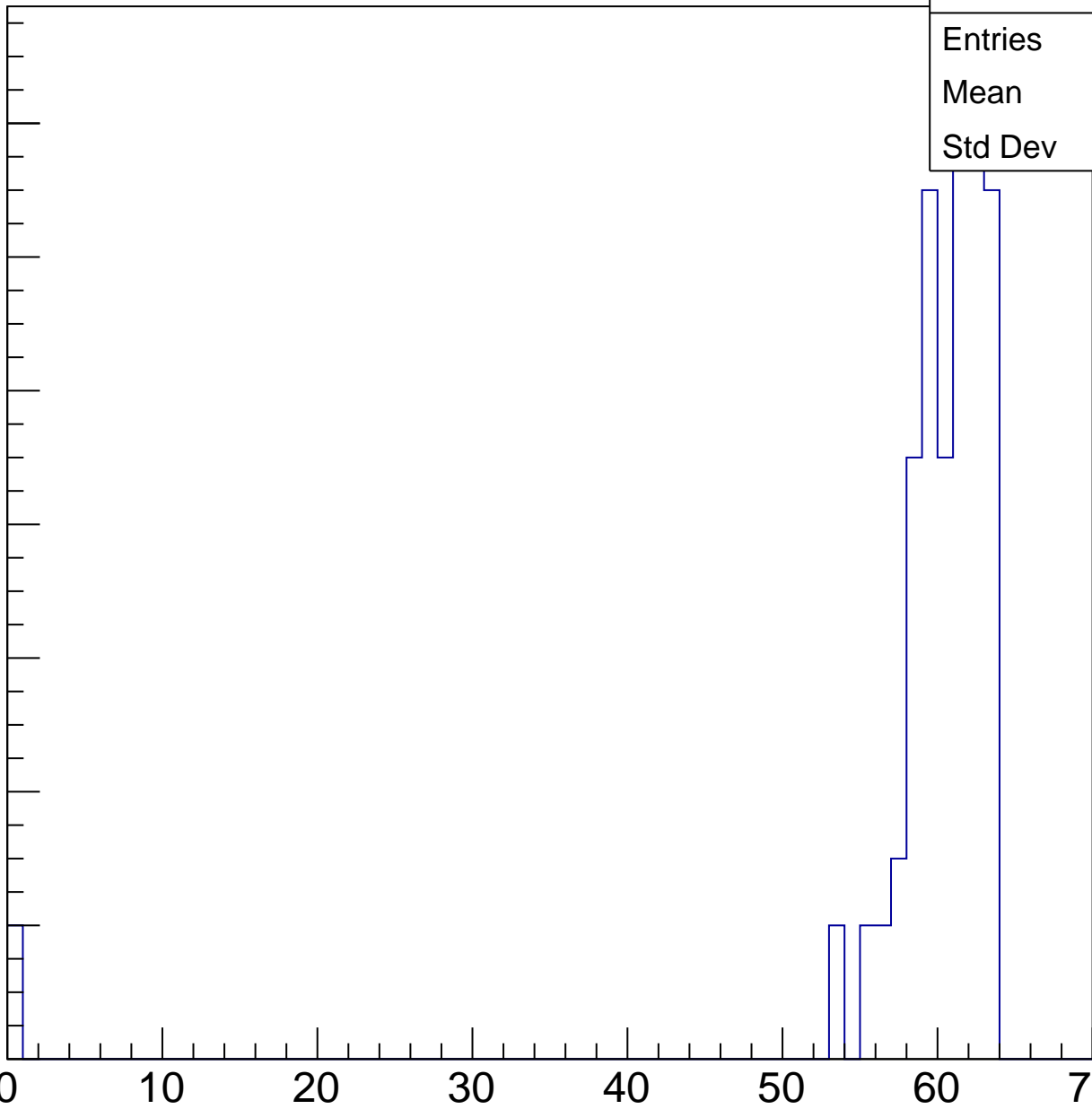
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 85    |
| Mean    | 58.73 |
| Std Dev | 9.4   |

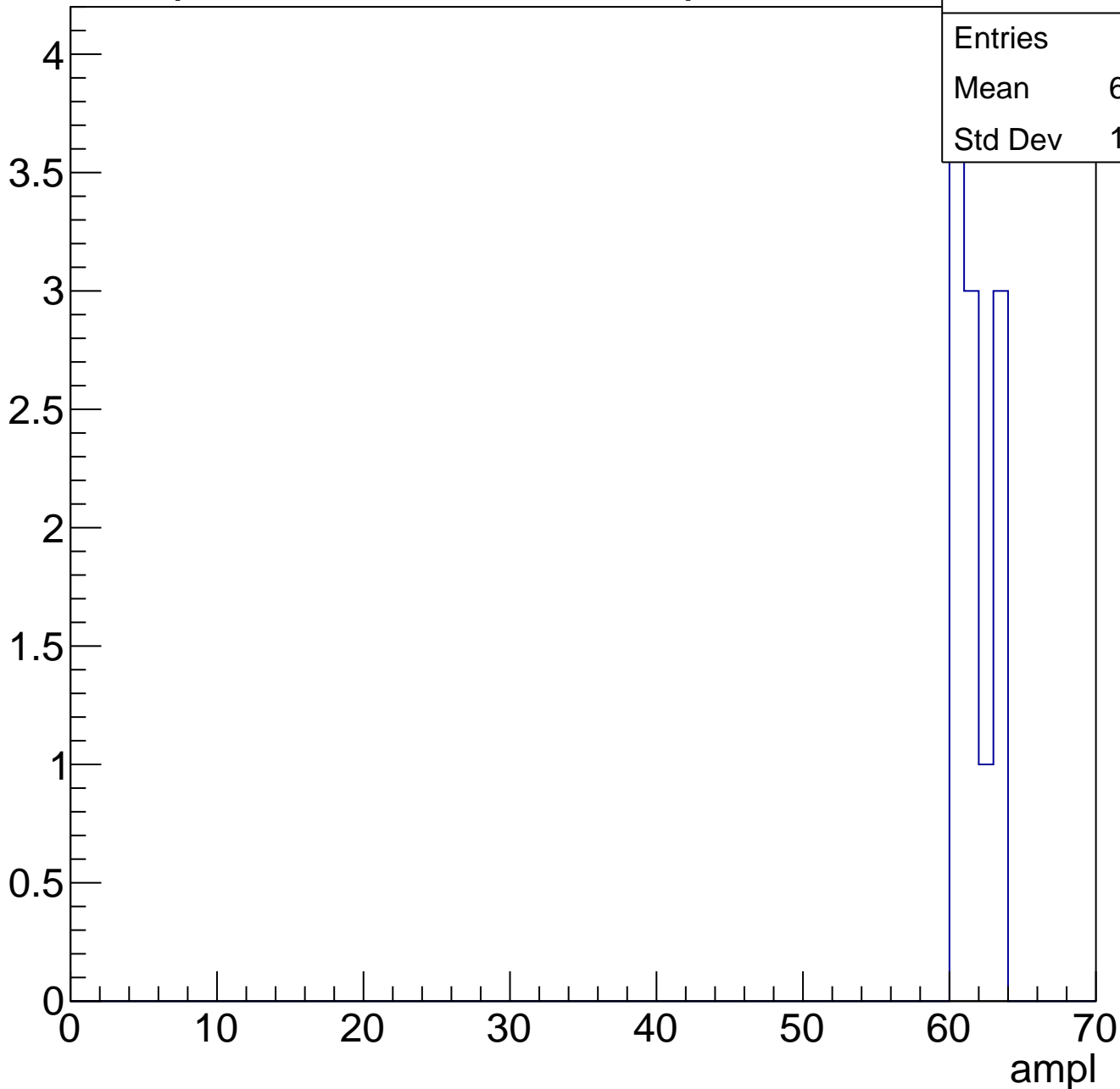
ampl



# B0L101S, U16-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch7, adc0

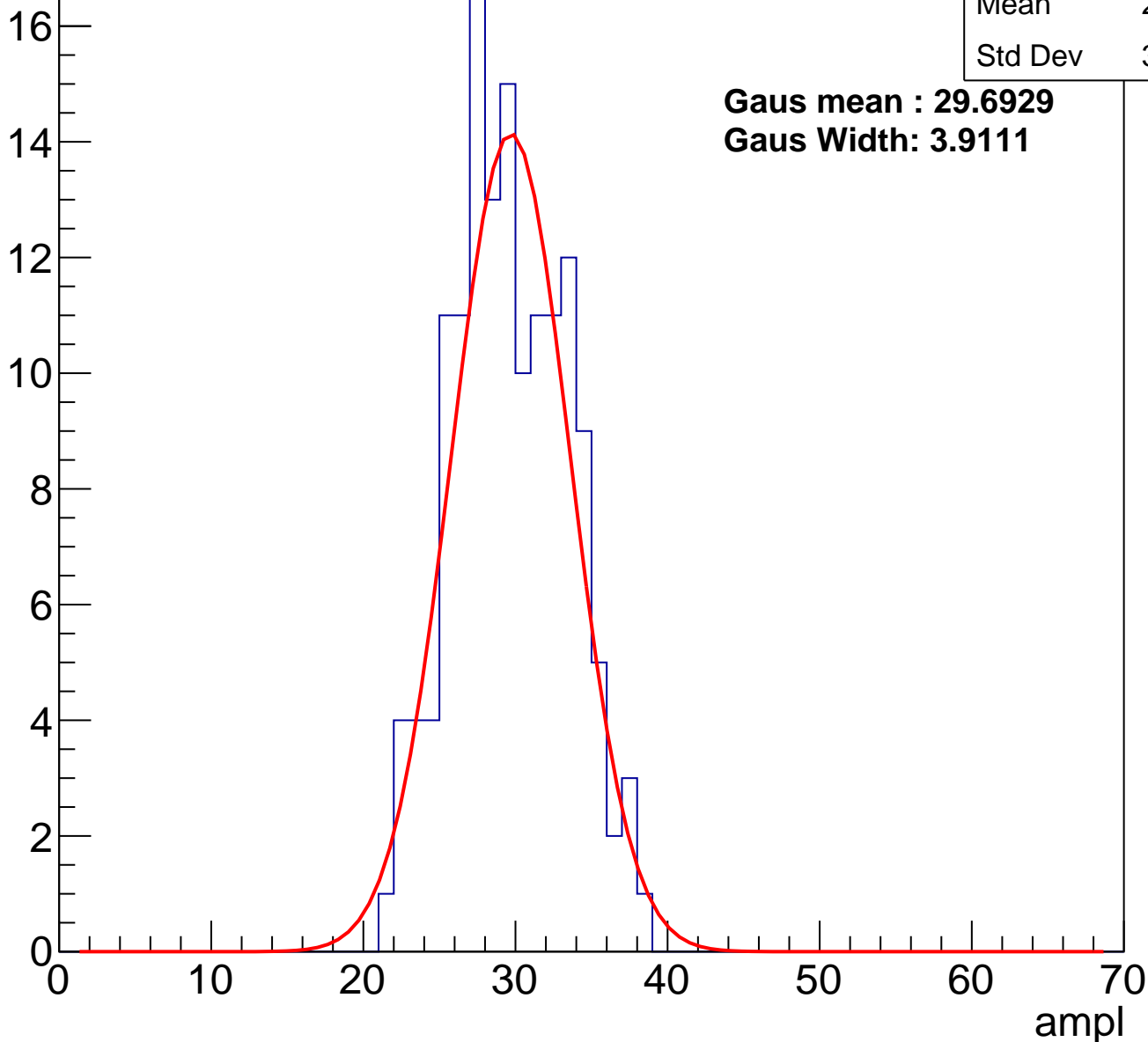
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 29.22 |
| Std Dev | 3.703 |

**Gaus mean : 29.6929**

**Gaus Width: 3.9111**



# B0L101S, U16-ch7, adc1

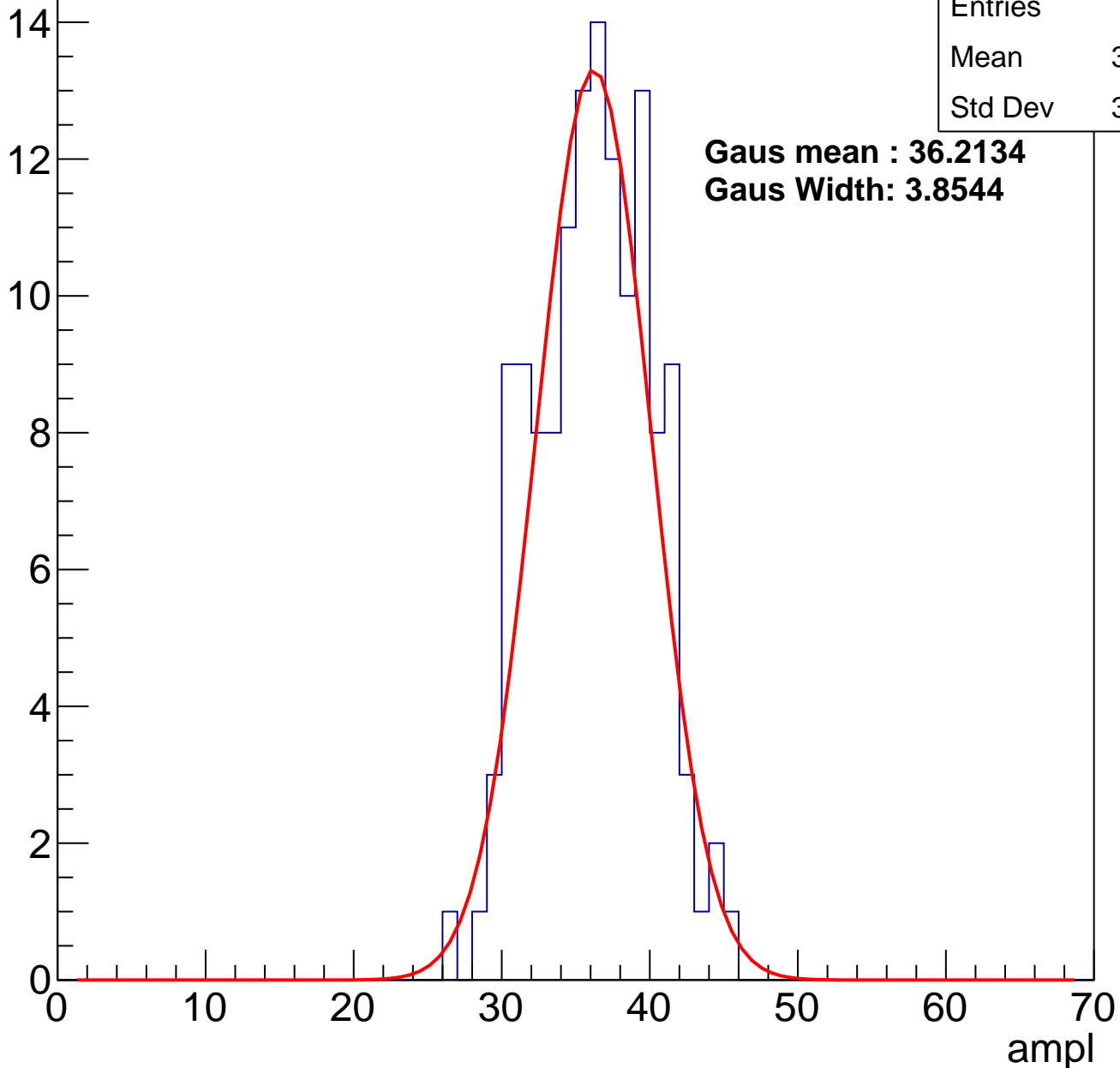
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 35.77 |
| Std Dev | 3.833 |

**Gaus mean : 36.2134**

**Gaus Width: 3.8544**

Entry



# B0L101S, U16-ch7, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

154

Mean

42.97

Std Dev

3.632

**Gaus mean : 43.3318**

**Gaus Width: 3.6858**

0

10

20

30

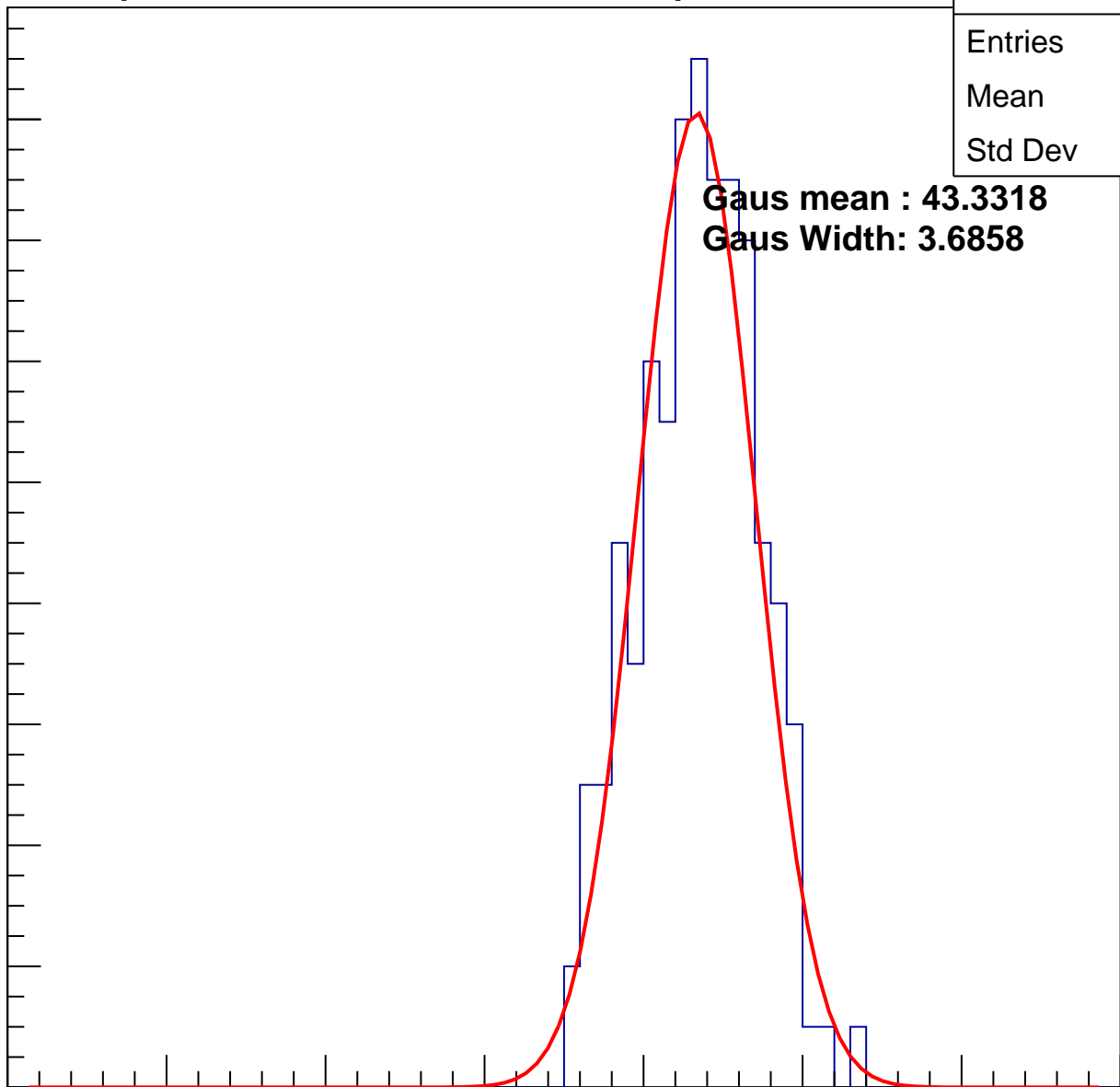
40

50

60

70

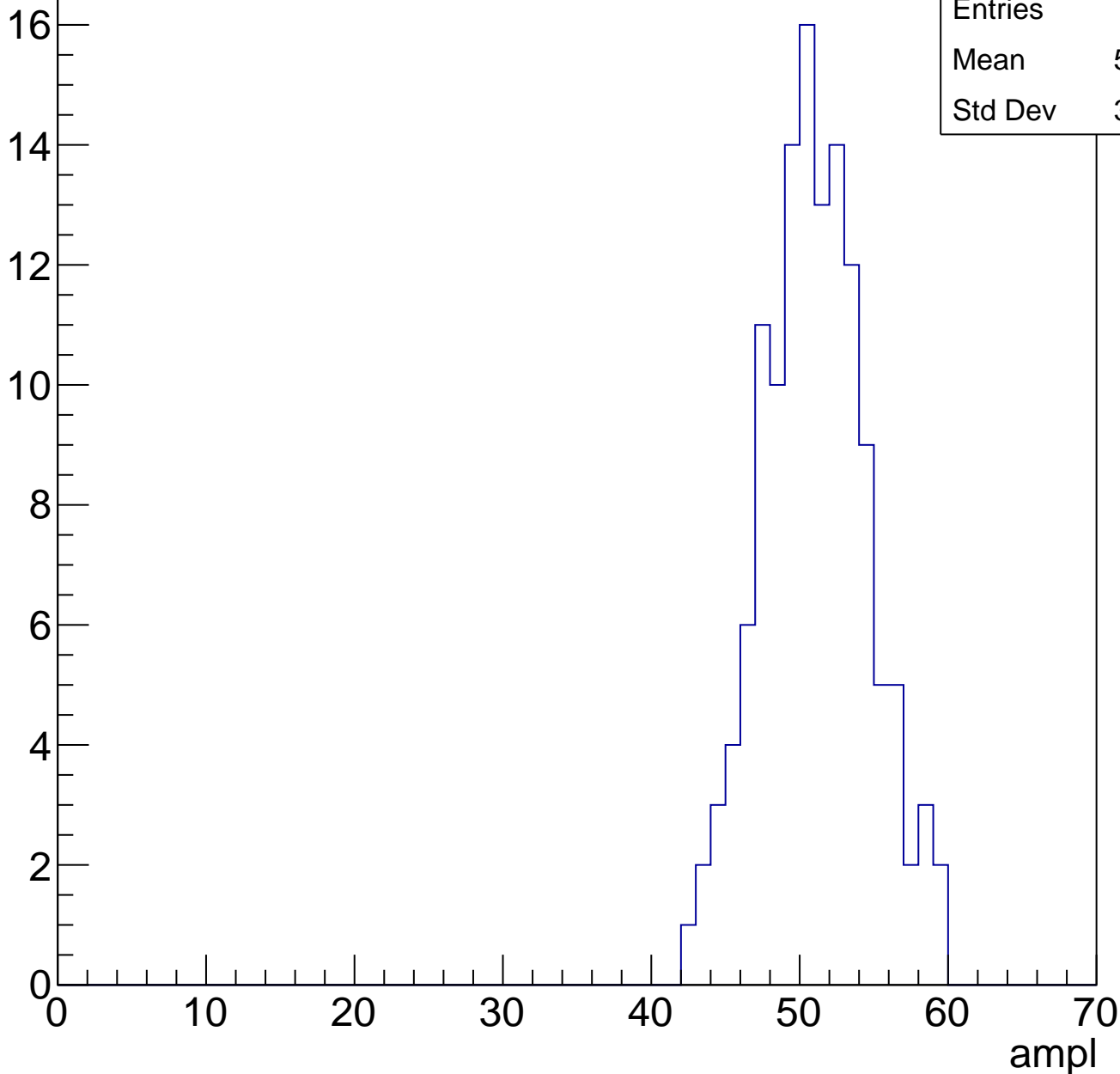
ampl



# B0L101S, U16-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 50.55 |
| Std Dev | 3.543 |

# B0L101S, U16-ch7, adc4

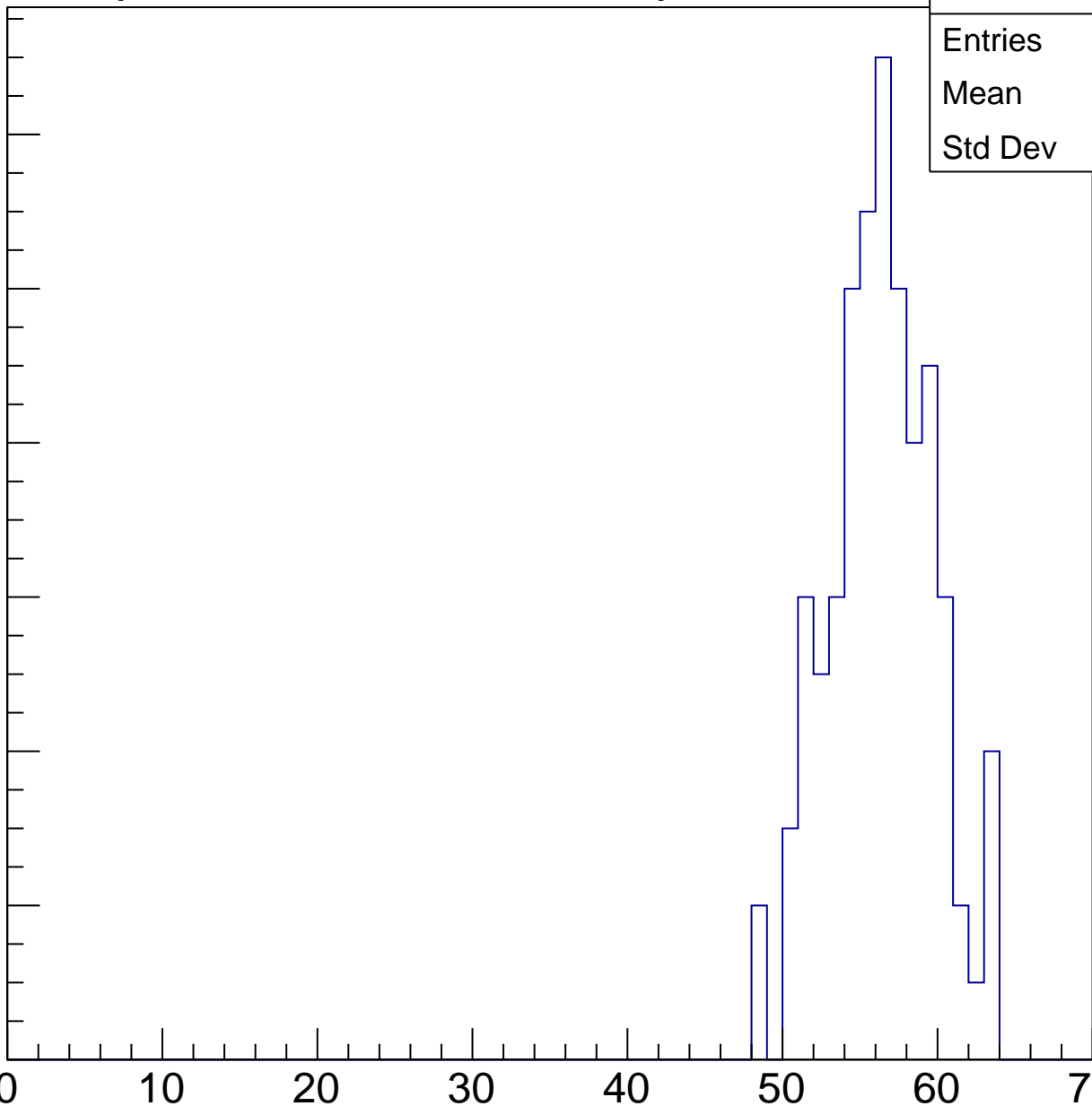
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 96    |
| Mean    | 55.88 |
| Std Dev | 3.339 |

ampl



# B0L101S, U16-ch7, adc5

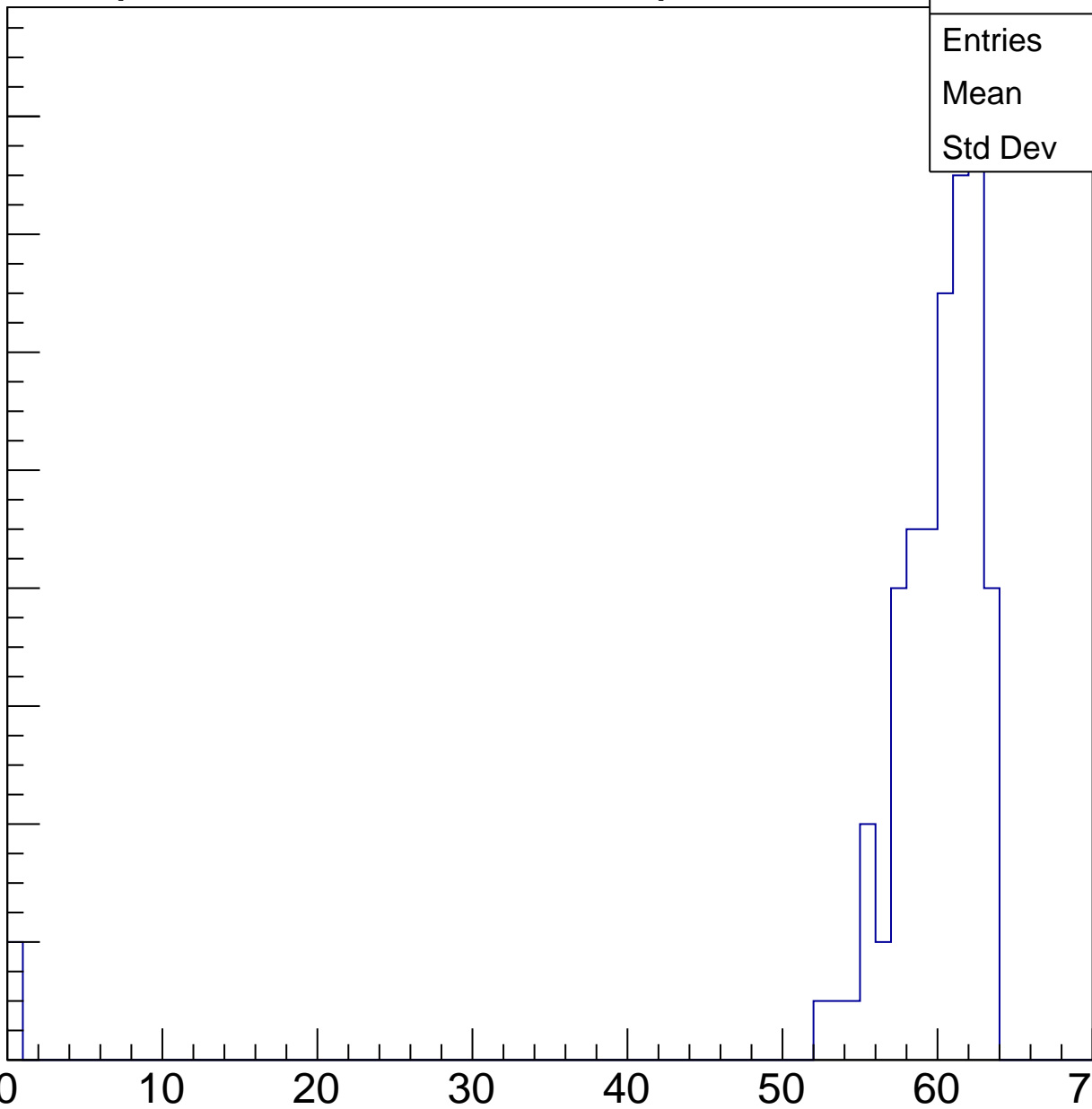
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 58.37 |
| Std Dev | 9.137 |

ampl



# B0L101S, U16-ch7, adc6

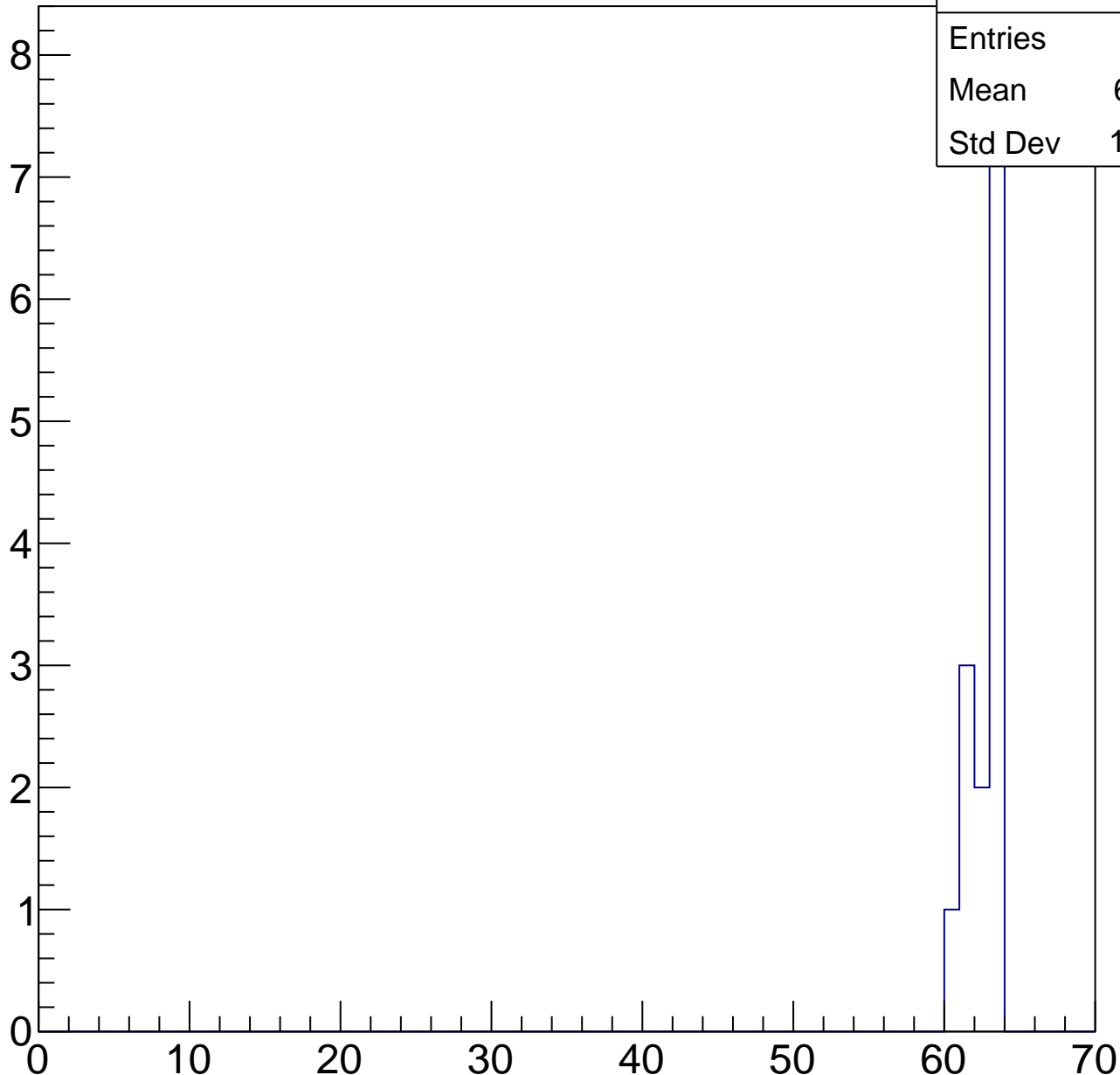
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 62.21 |
| Std Dev | 1.013 |

ampl





# B0L101S, U16-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 29.46 |
| Std Dev | 4.298 |

**Gaus mean : 30.1521**

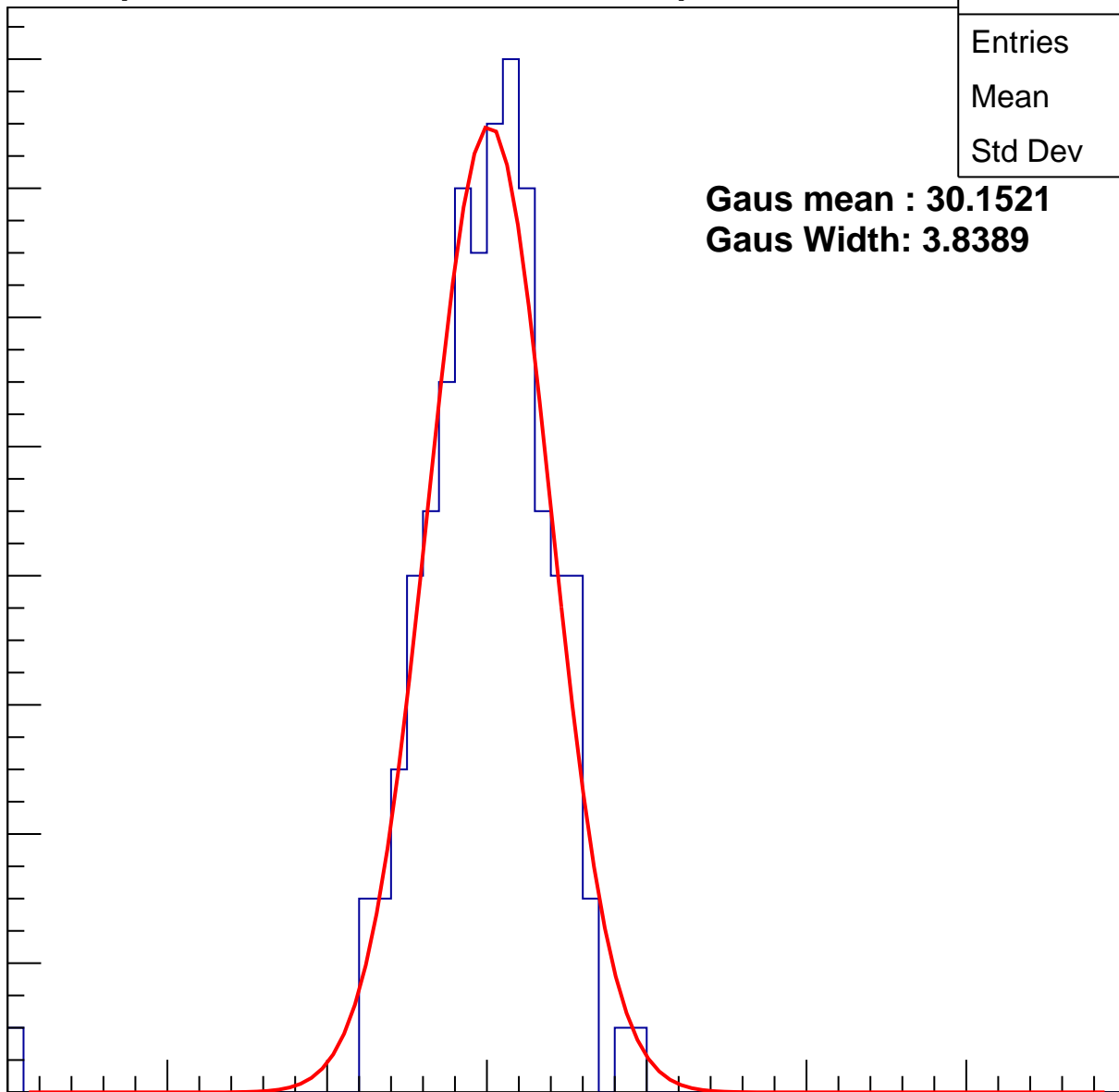
**Gaus Width: 3.8389**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch8, adc1

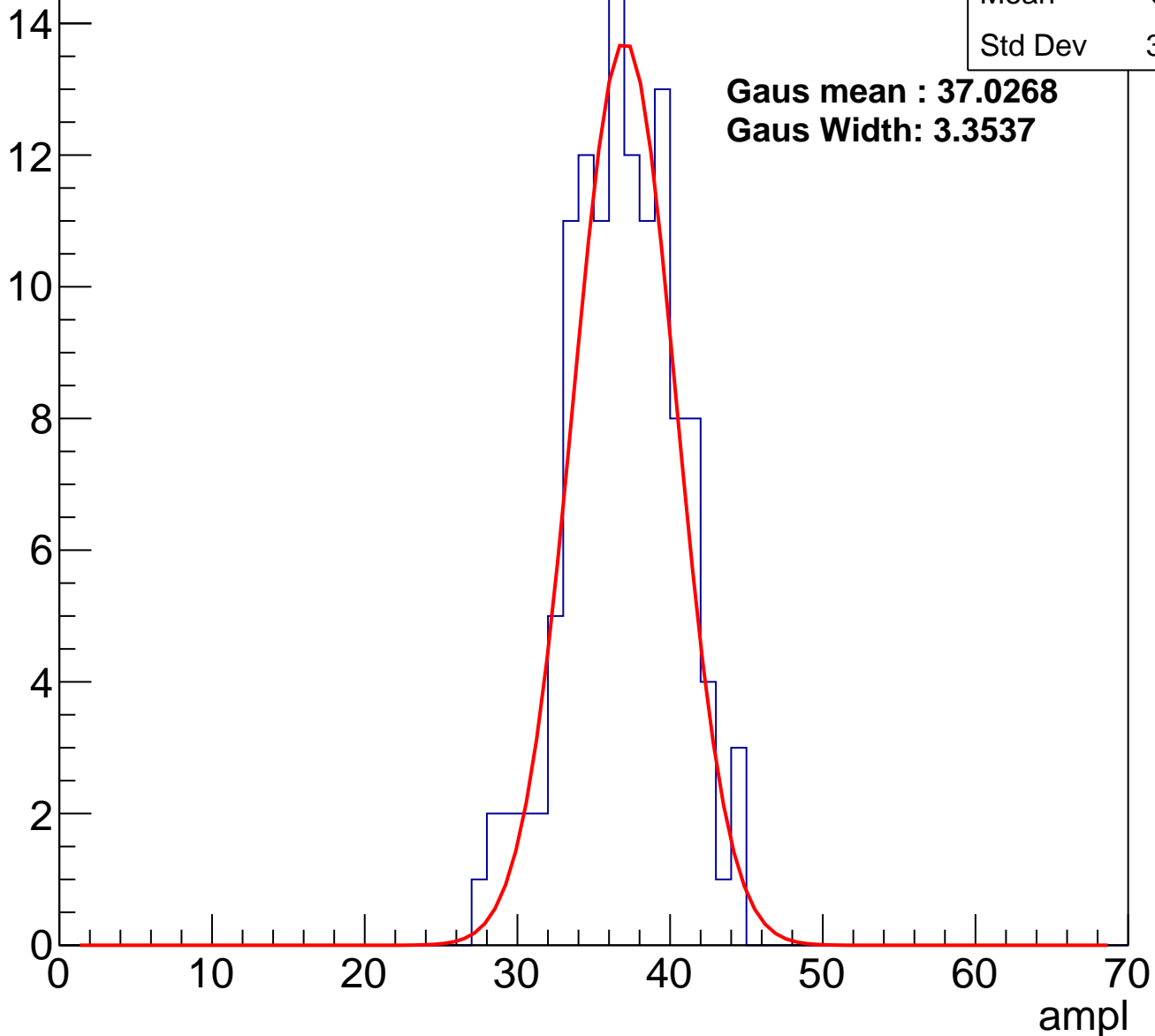
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 36.41 |
| Std Dev | 3.529 |

**Gaus mean : 37.0268**

**Gaus Width: 3.3537**

Entry



# B0L101S, U16-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

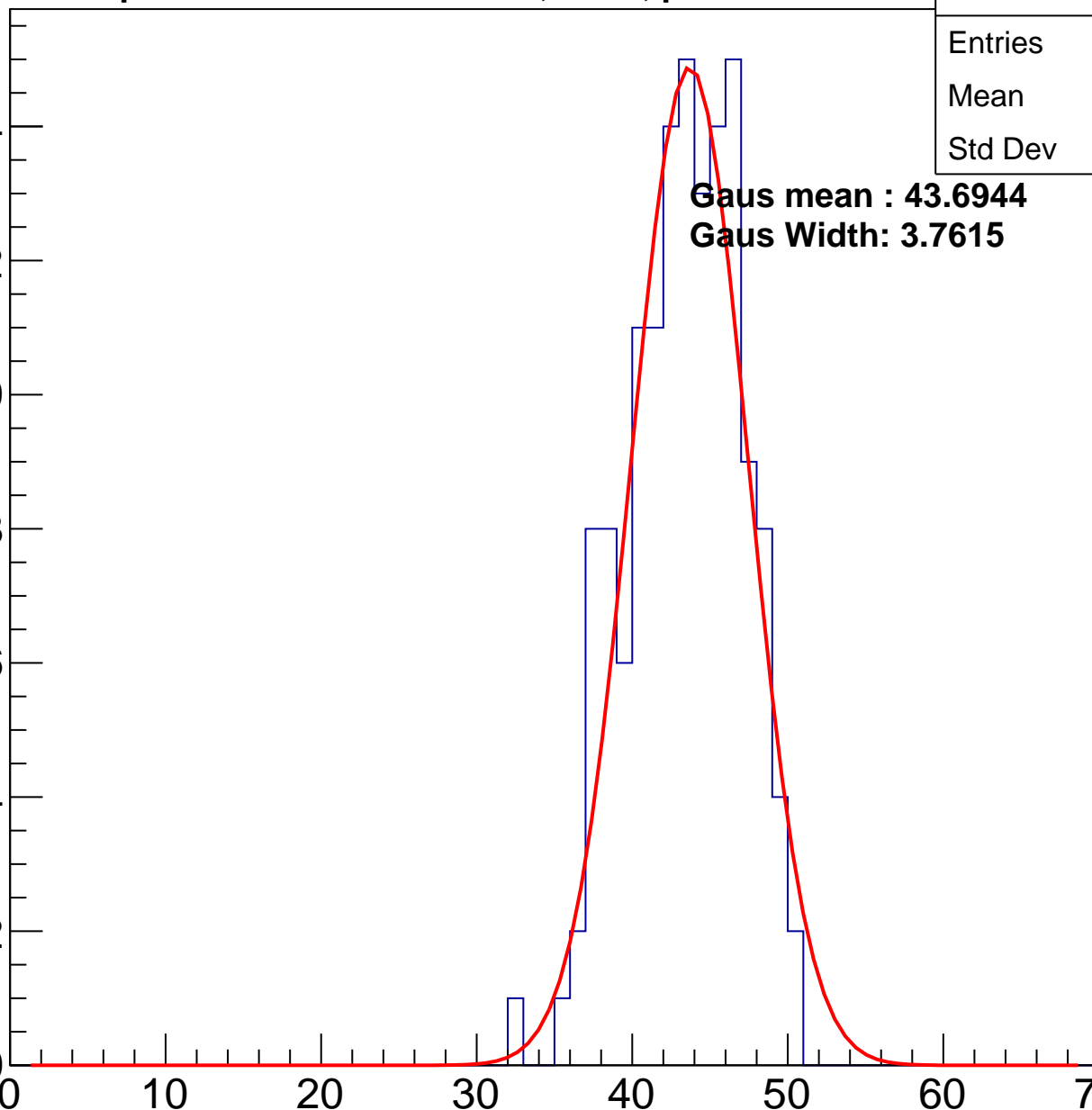
70

ampl

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 42.9  |
| Std Dev | 3.573 |

**Gaus mean : 43.6944**

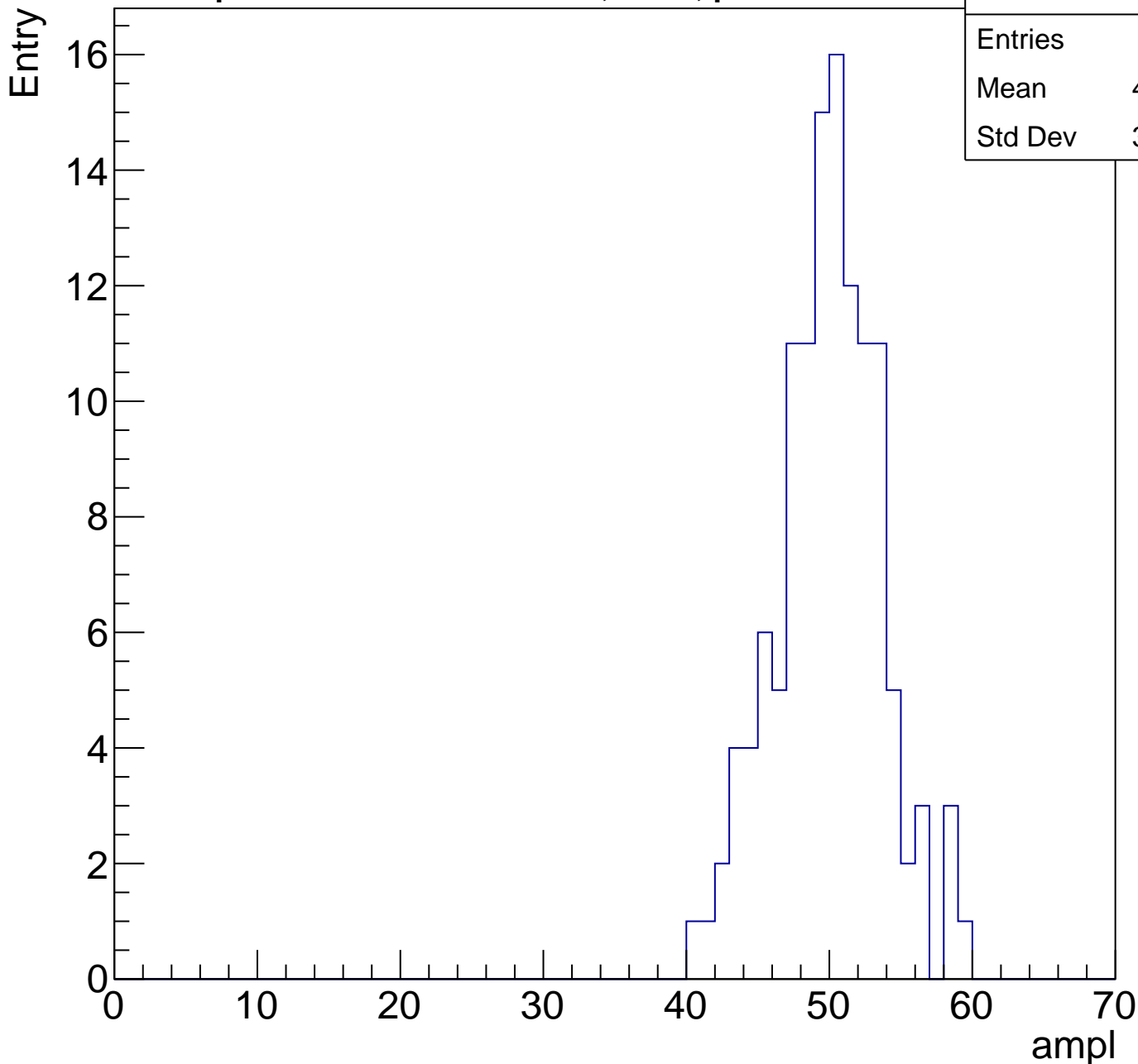
**Gaus Width: 3.7615**



# B0L101S, U16-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 49.52 |
| Std Dev | 3.675 |



# B0L101S, U16-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

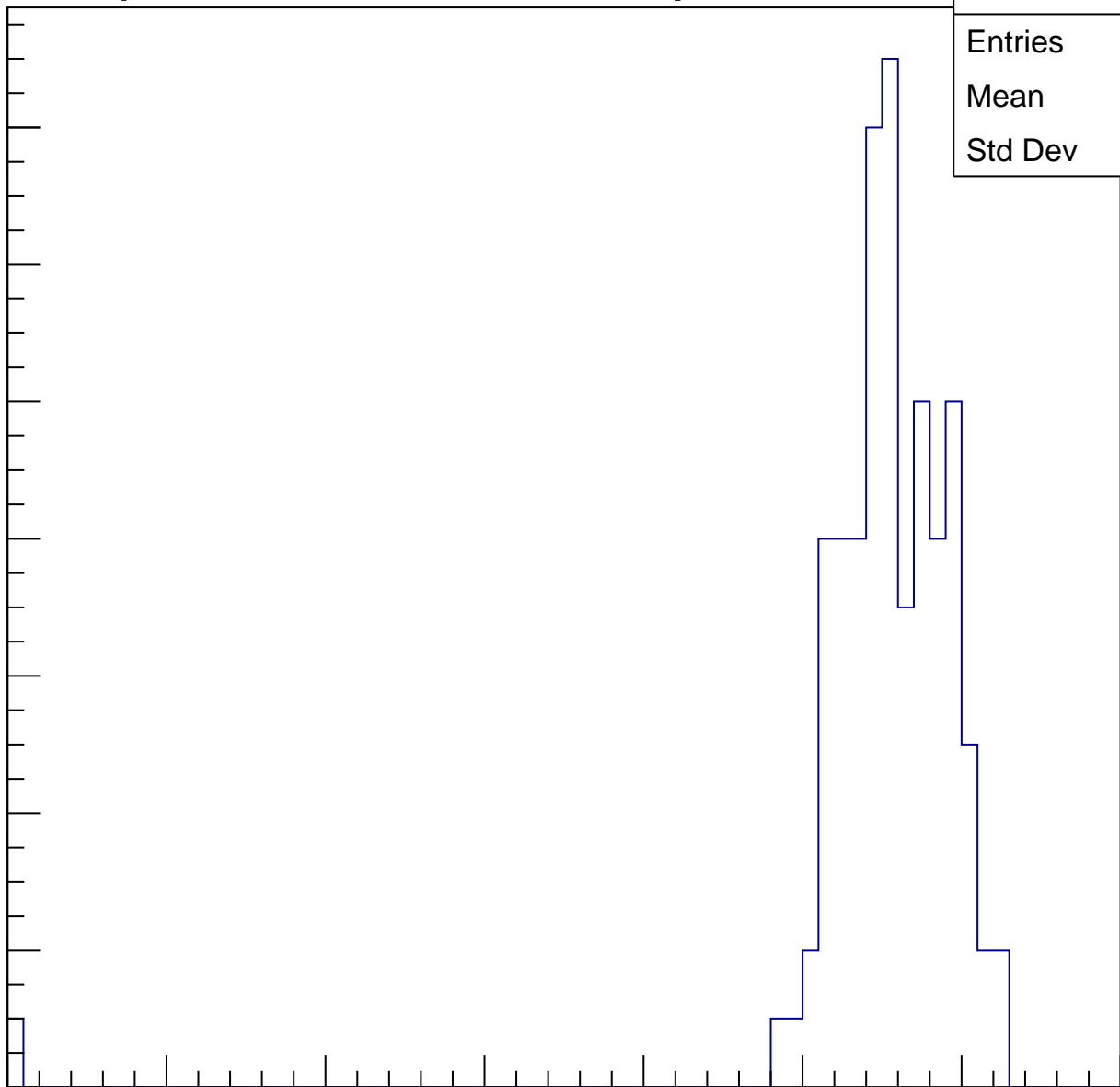
|         |       |
|---------|-------|
| Entries | 102   |
| Mean    | 54.78 |
| Std Dev | 6.243 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch8, adc5

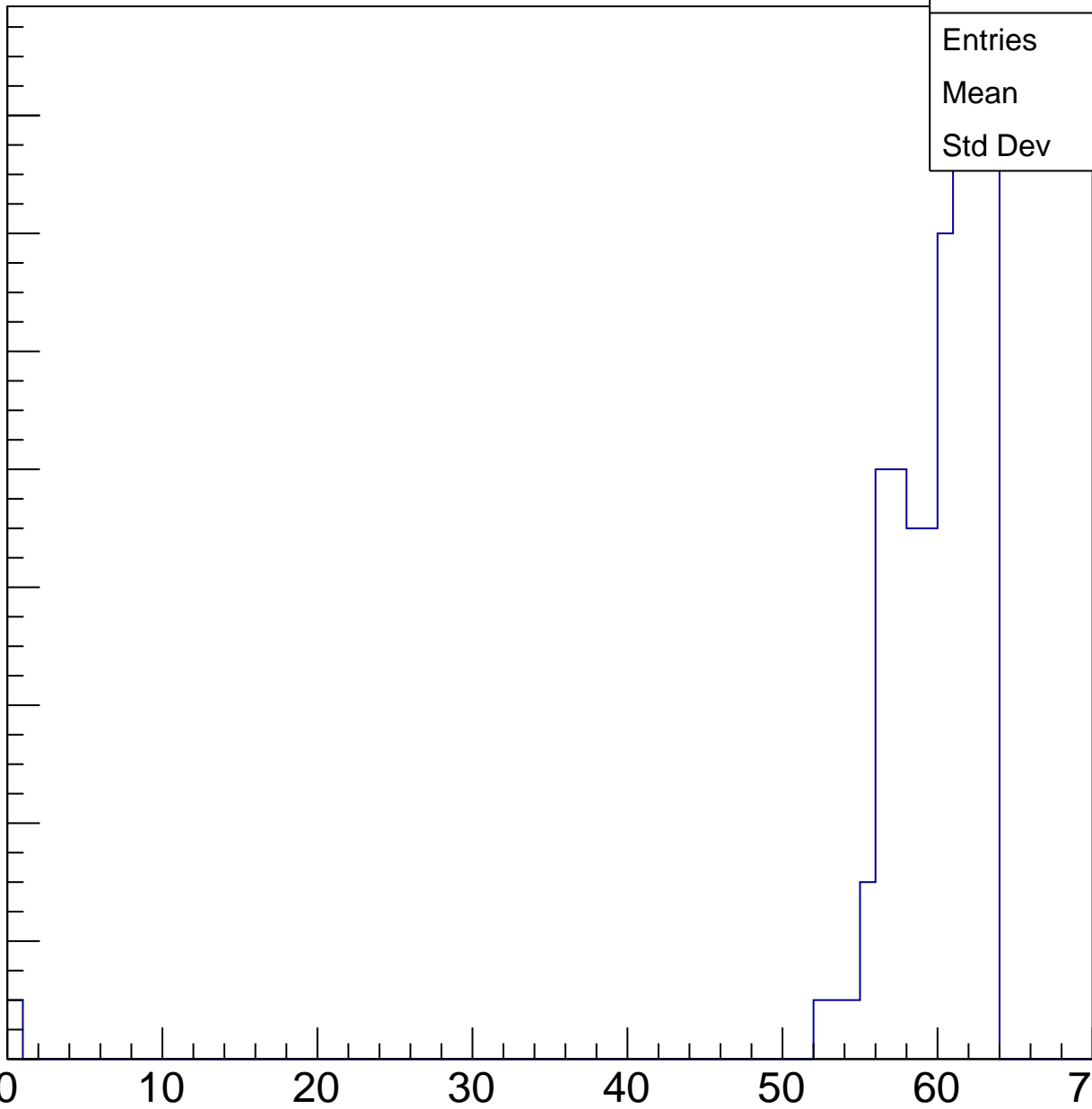
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 59.16 |
| Std Dev | 6.265 |

ampl



# B0L101S, U16-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 30.3  |
| Std Dev | 5.769 |

**Gaus mean : 31.0945**

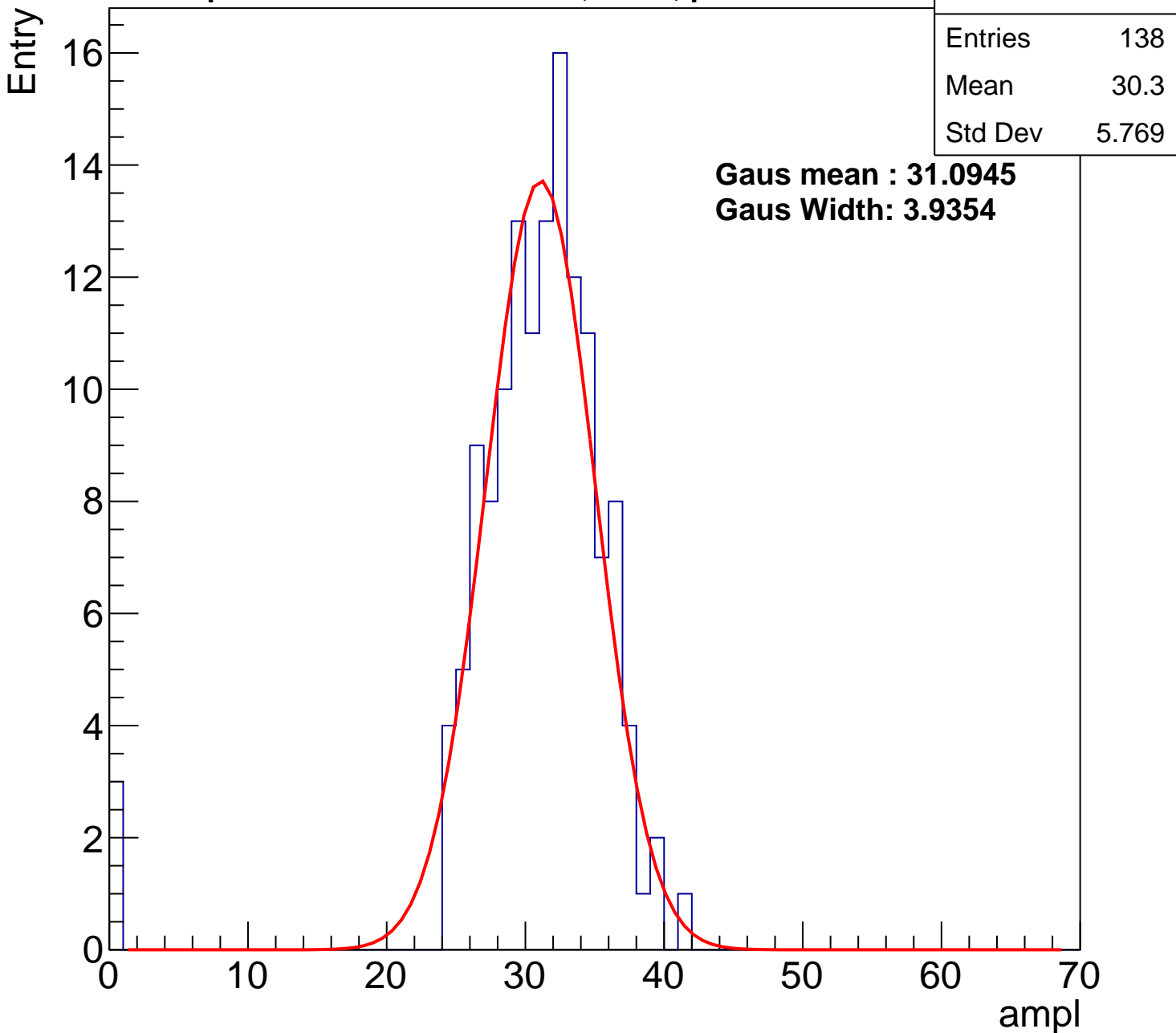
**Gaus Width: 3.9354**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 37.54 |
| Std Dev | 3.628 |

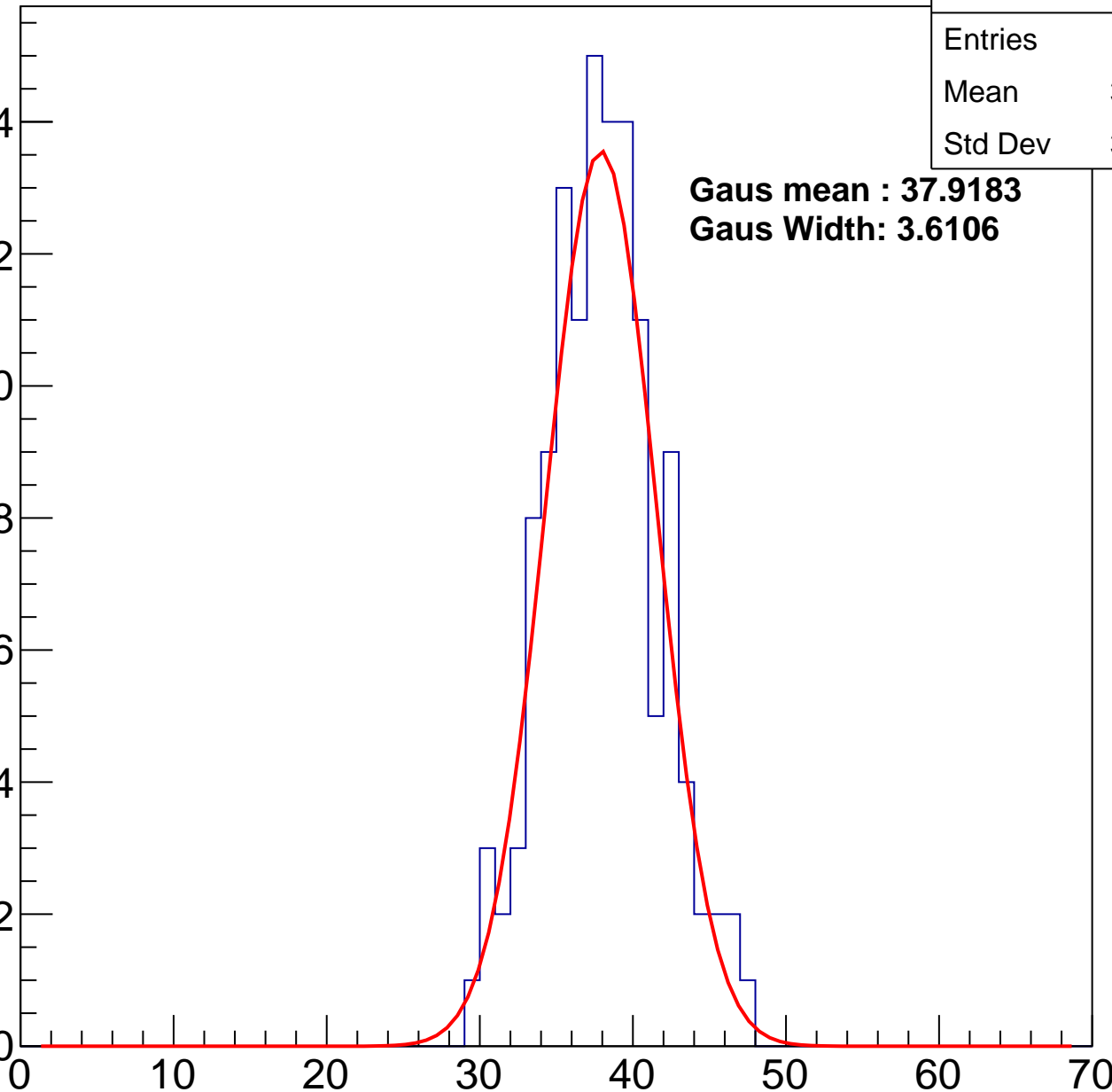
**Gaus mean : 37.9183**

**Gaus Width: 3.6106**

Entry

14  
12  
10  
8  
6  
4  
2  
0

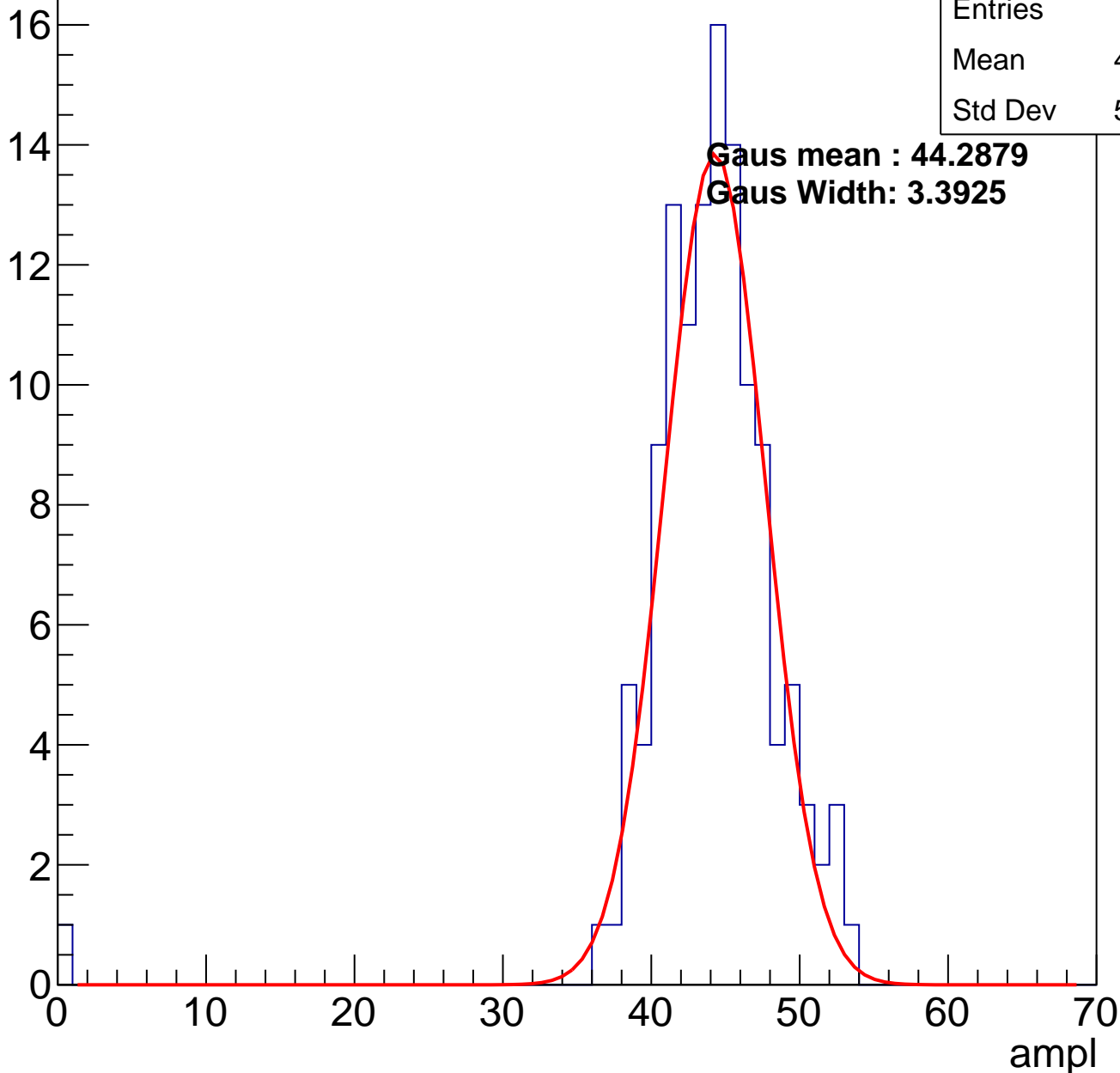
ampl



# B0L101S, U16-ch9, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

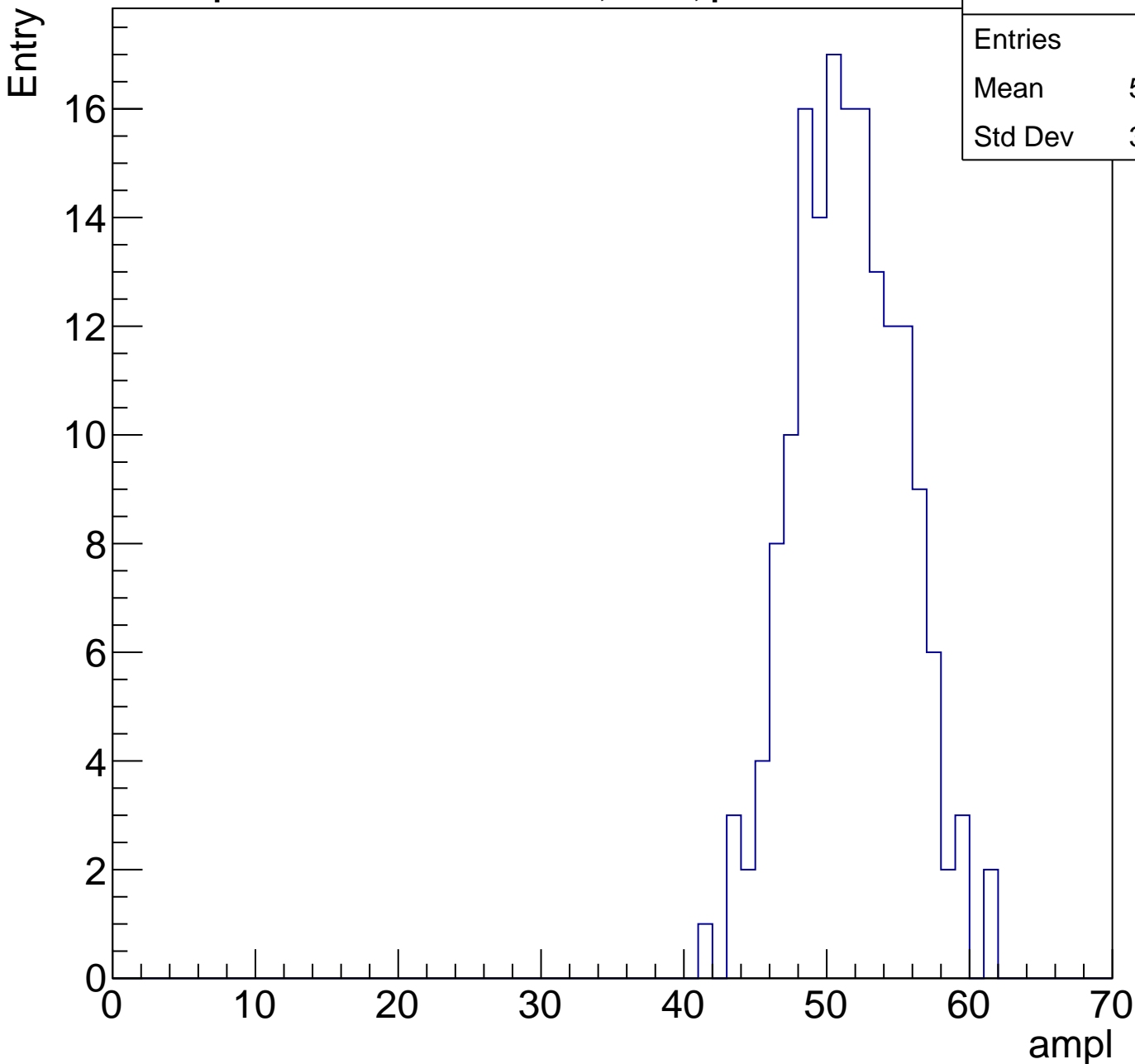
Entry



# B0L101S, U16-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

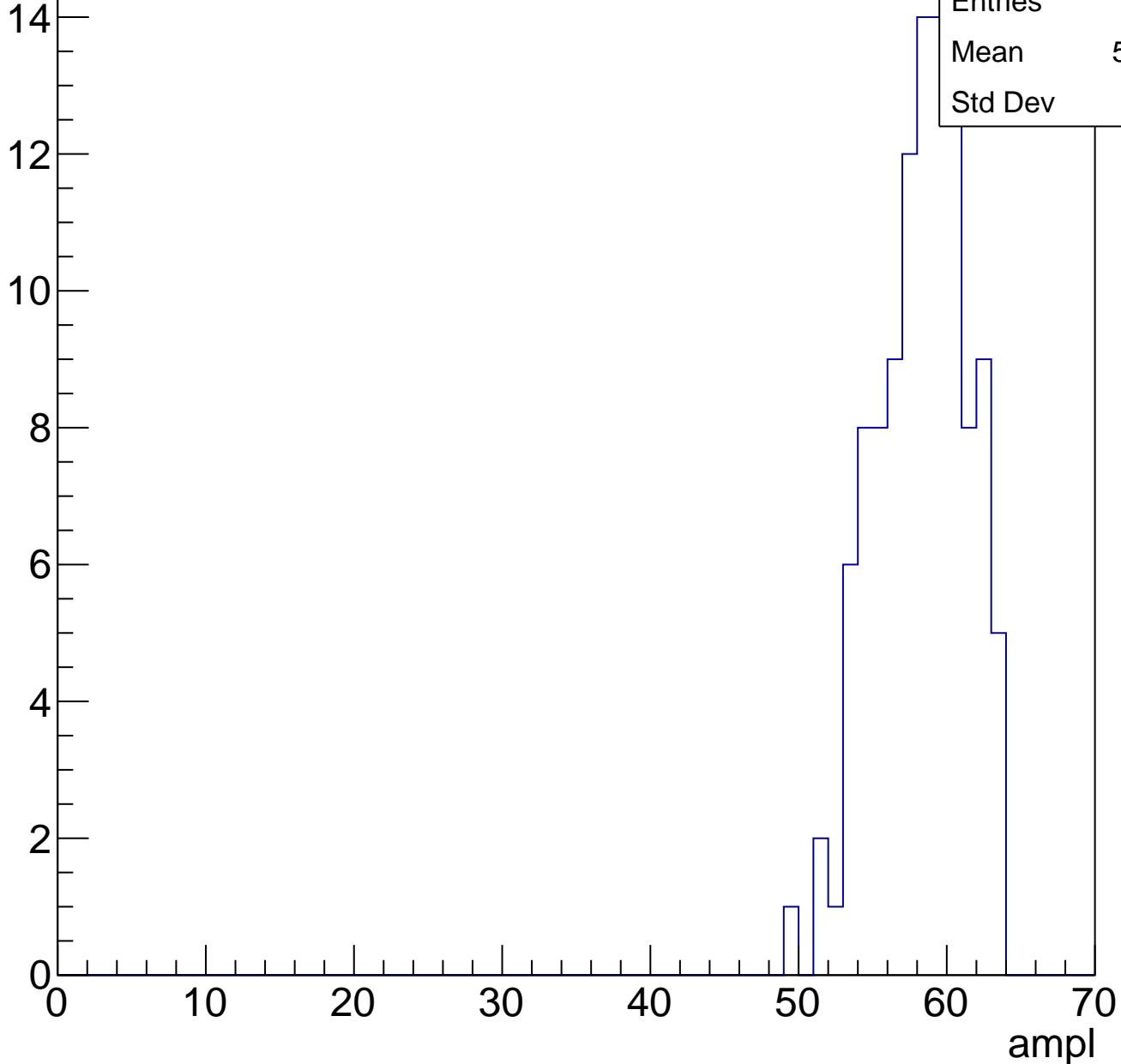
|         |       |
|---------|-------|
| Entries | 166   |
| Mean    | 51.12 |
| Std Dev | 3.808 |



# B0L101S, U16-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

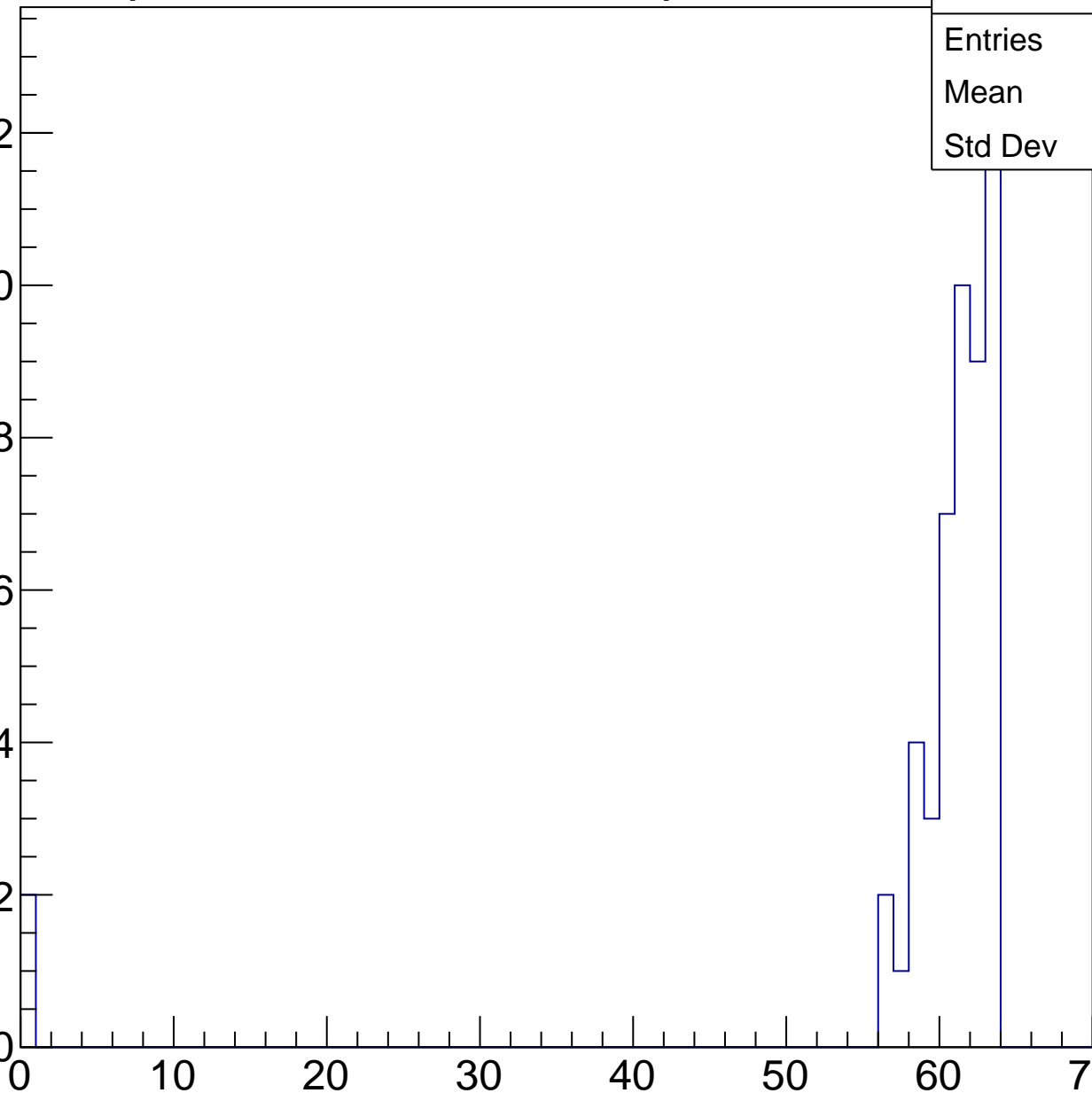
4

2

0

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 58.53 |
| Std Dev | 11.97 |

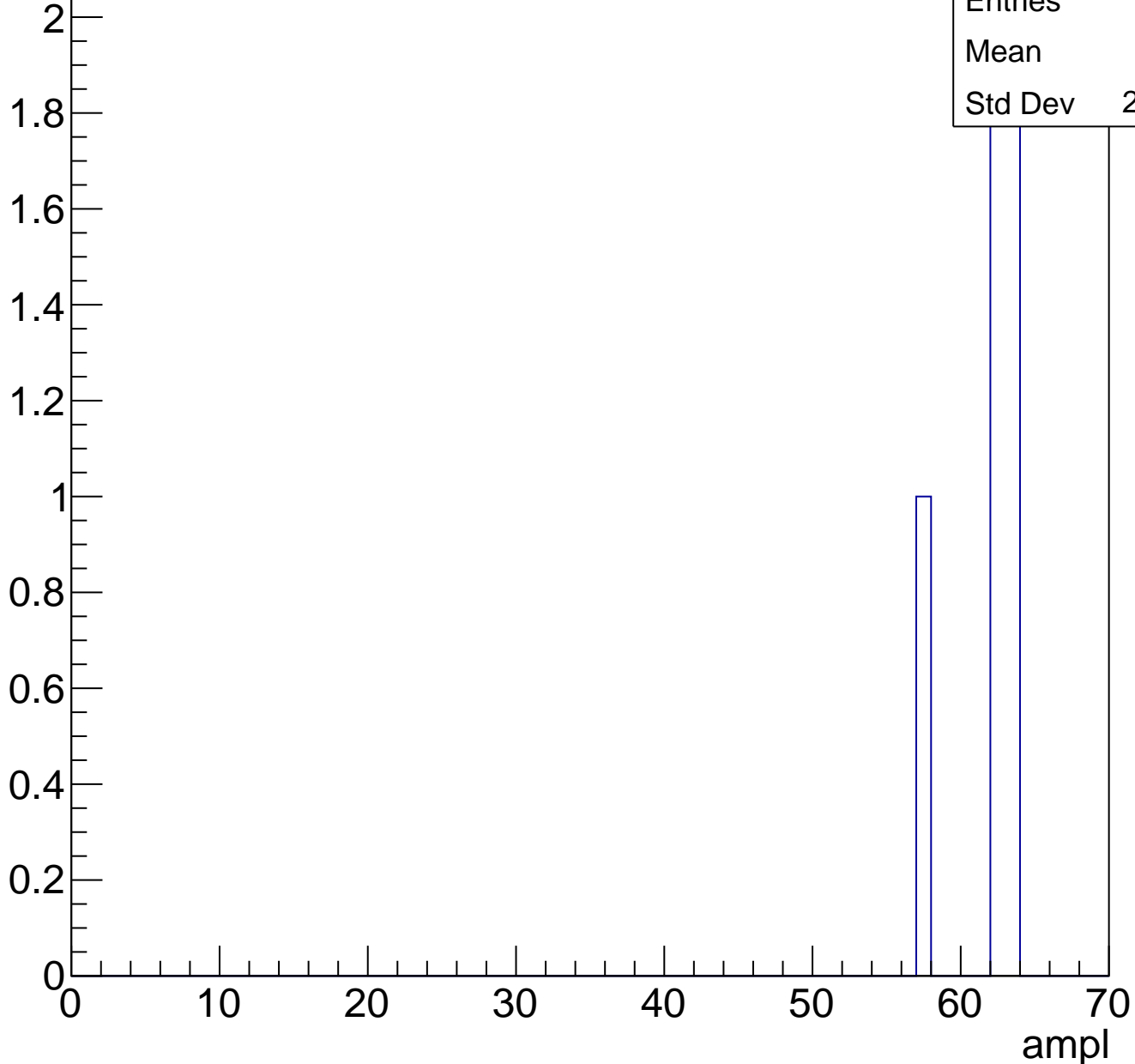
ampl



# B0L101S, U16-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch9, adc7

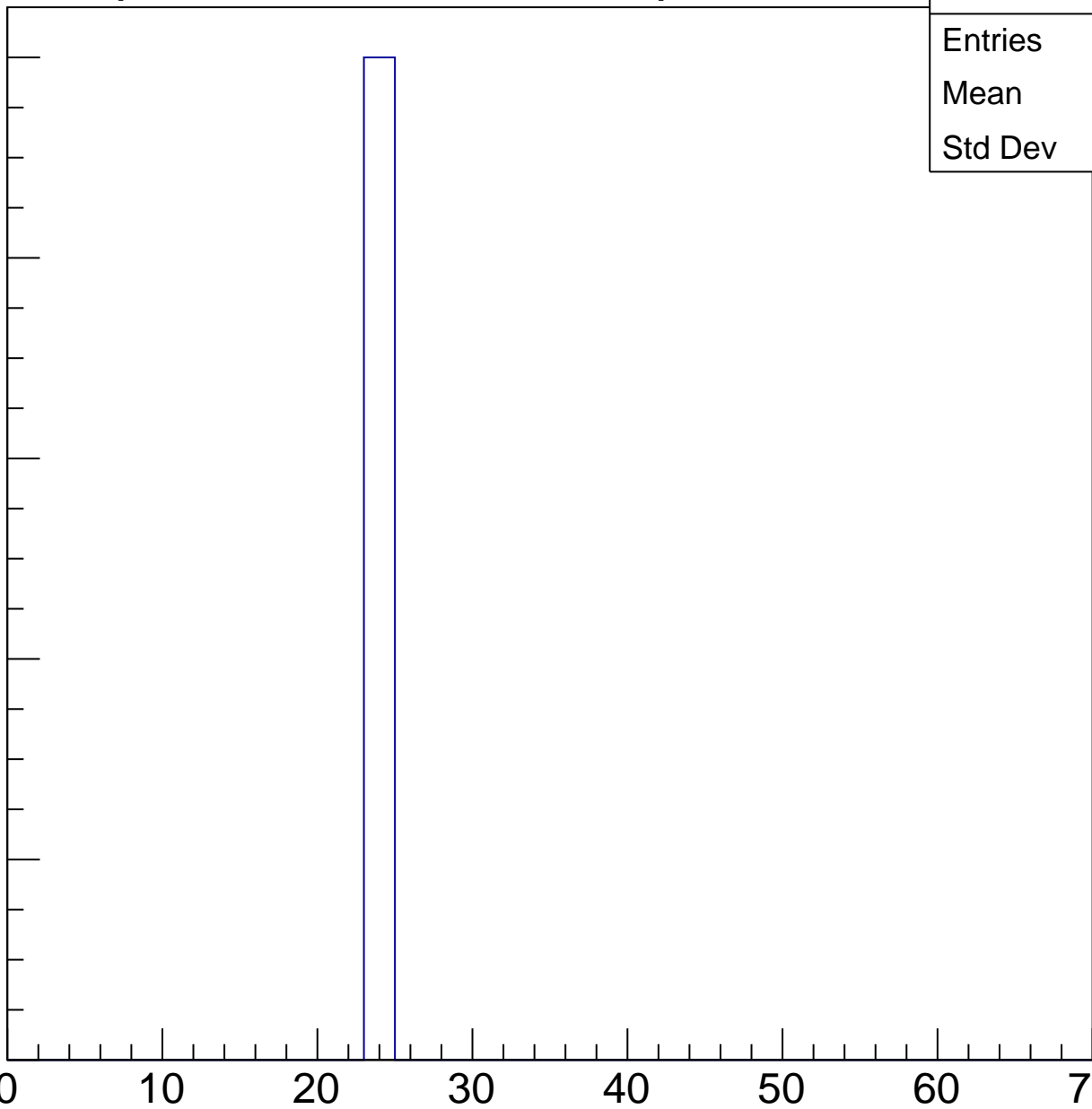
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 23.5 |
| Std Dev | 0.5  |

ampl



# B0L101S, U16-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 28.47 |
| Std Dev | 5.007 |

**Gaus mean : 29.4227**

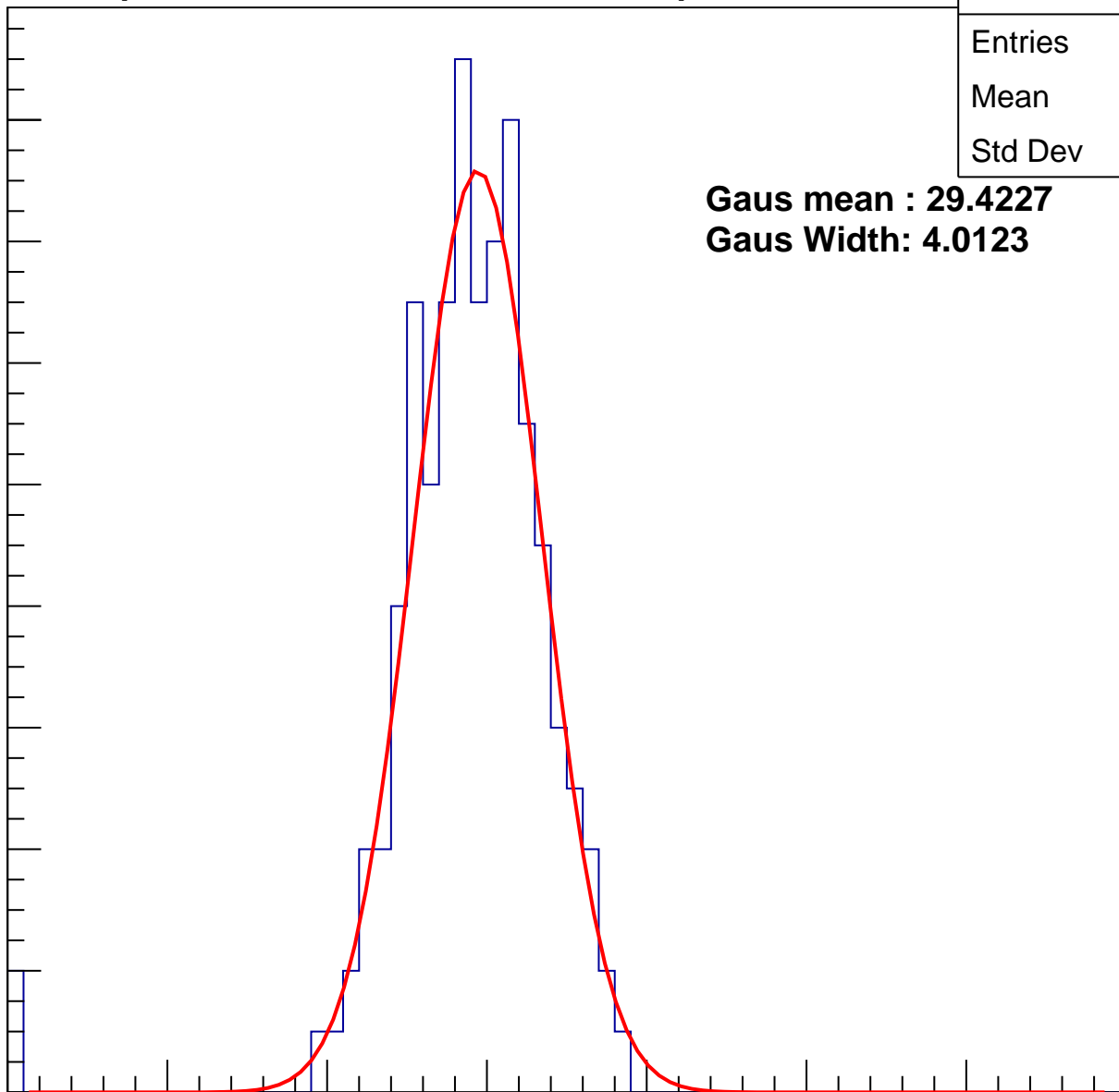
**Gaus Width: 4.0123**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 106   |
| Mean    | 34.75 |
| Std Dev | 3.672 |

**Gaus mean : 35.3196**

**Gaus Width: 3.3741**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch10, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 152   |
| Mean    | 41.66 |
| Std Dev | 5.044 |

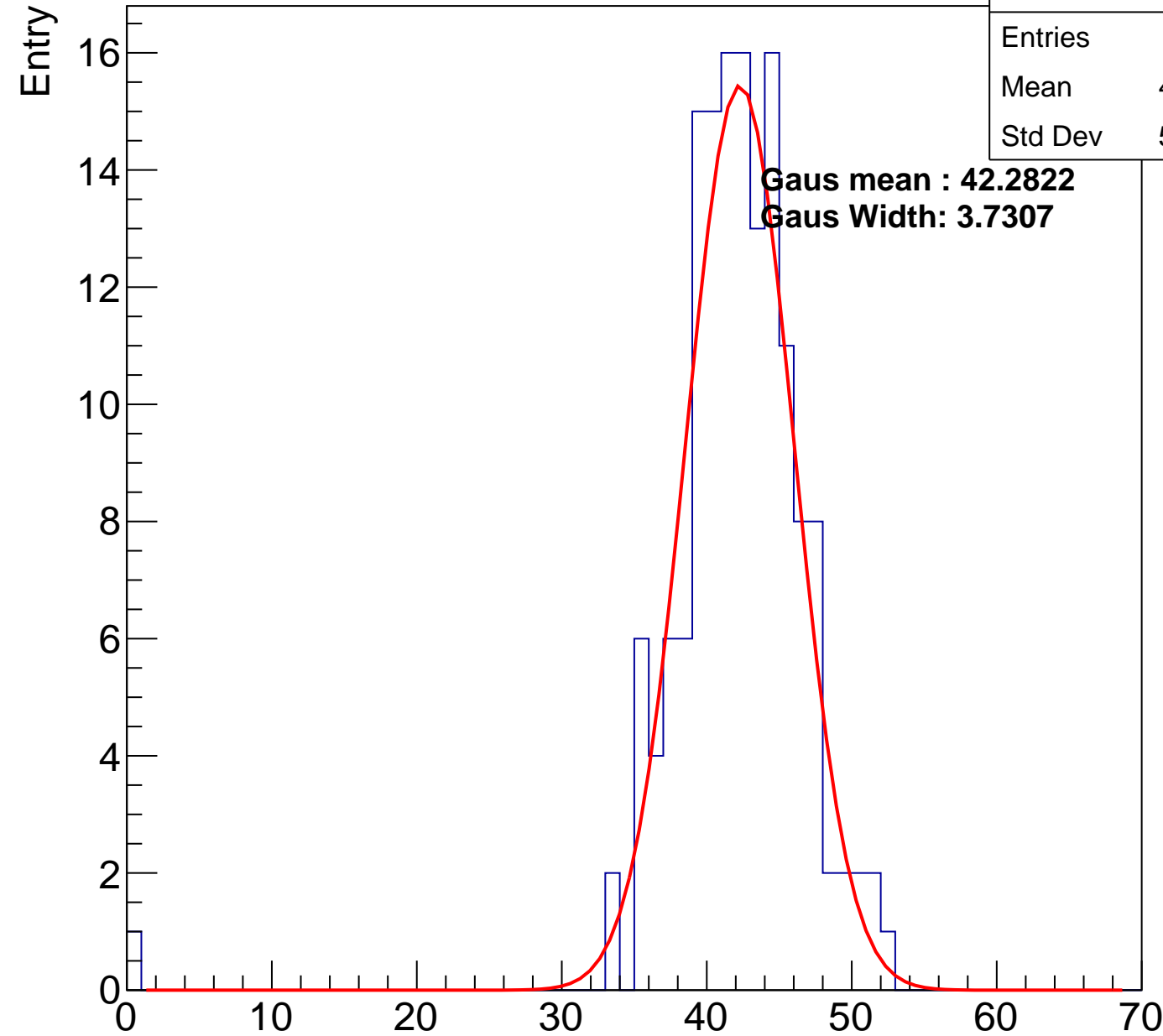
**Gaus mean : 42.2822**

**Gaus Width: 3.7307**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

125

Mean

48.26

Std Dev

3.988

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 54.75 |
| Std Dev | 3.17  |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

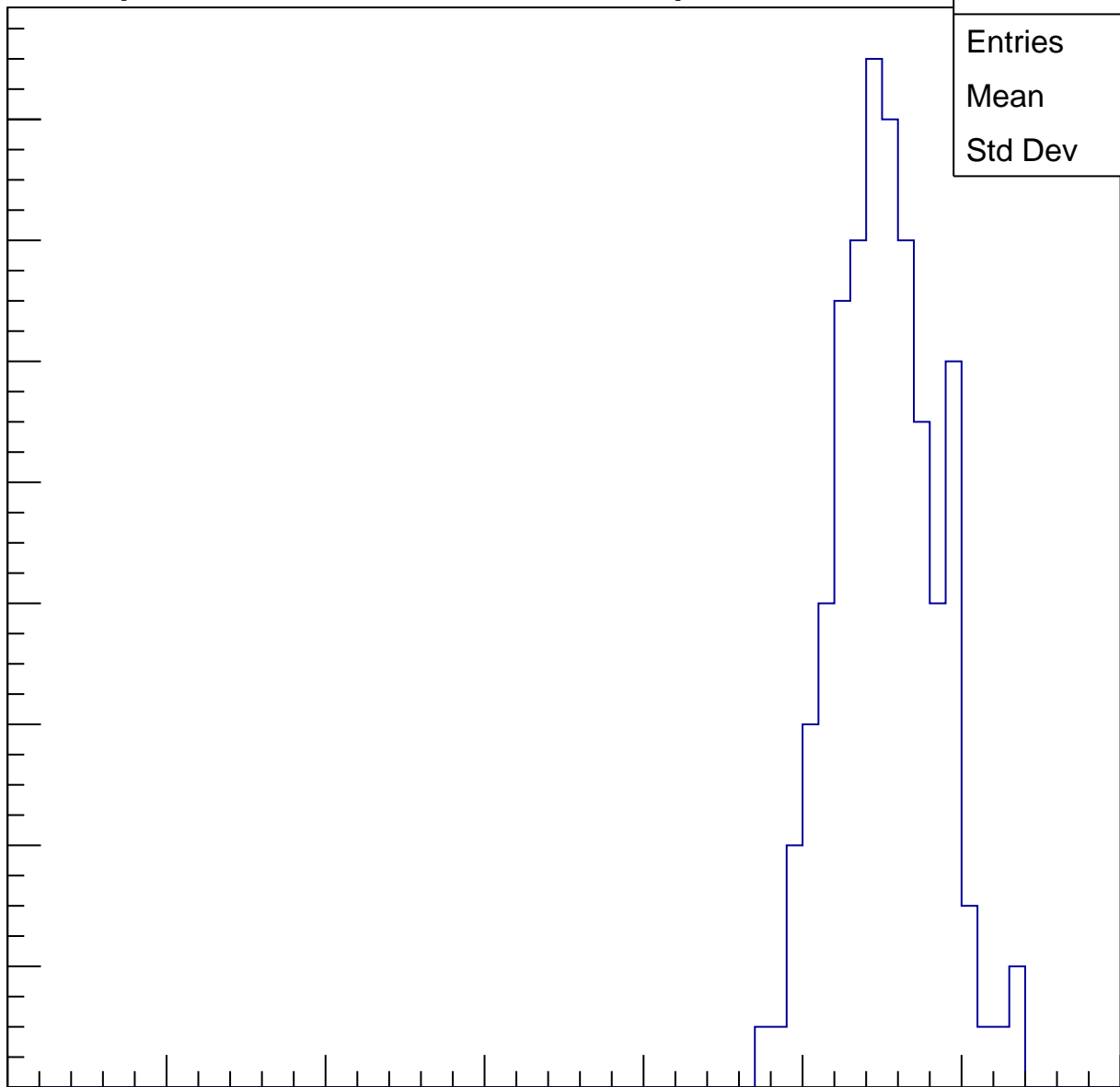
40

50

60

70

ampl



# B0L101S, U16-ch10, adc5

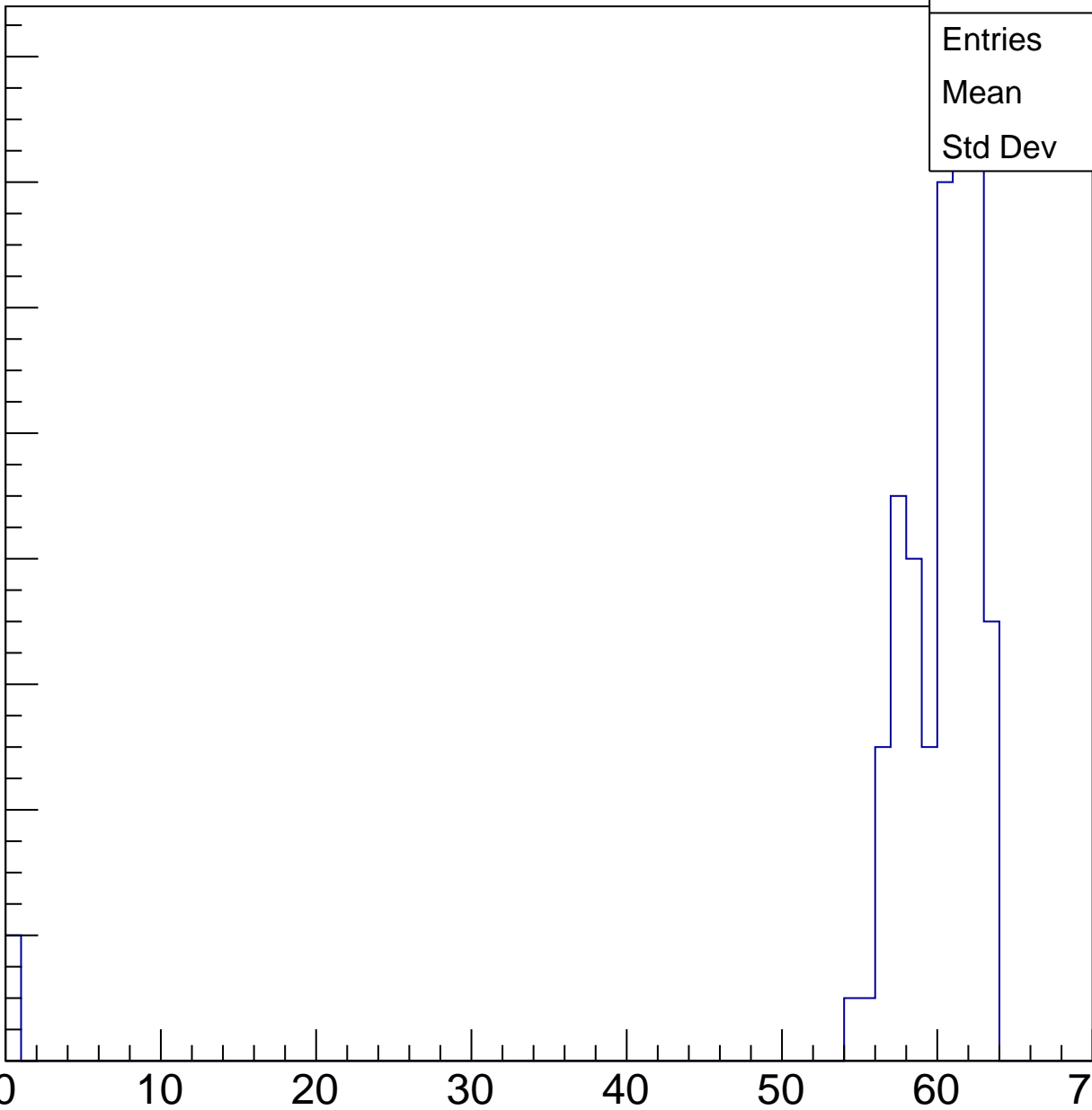
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 58.42 |
| Std Dev | 9.439 |

ampl



# B0L101S, U16-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

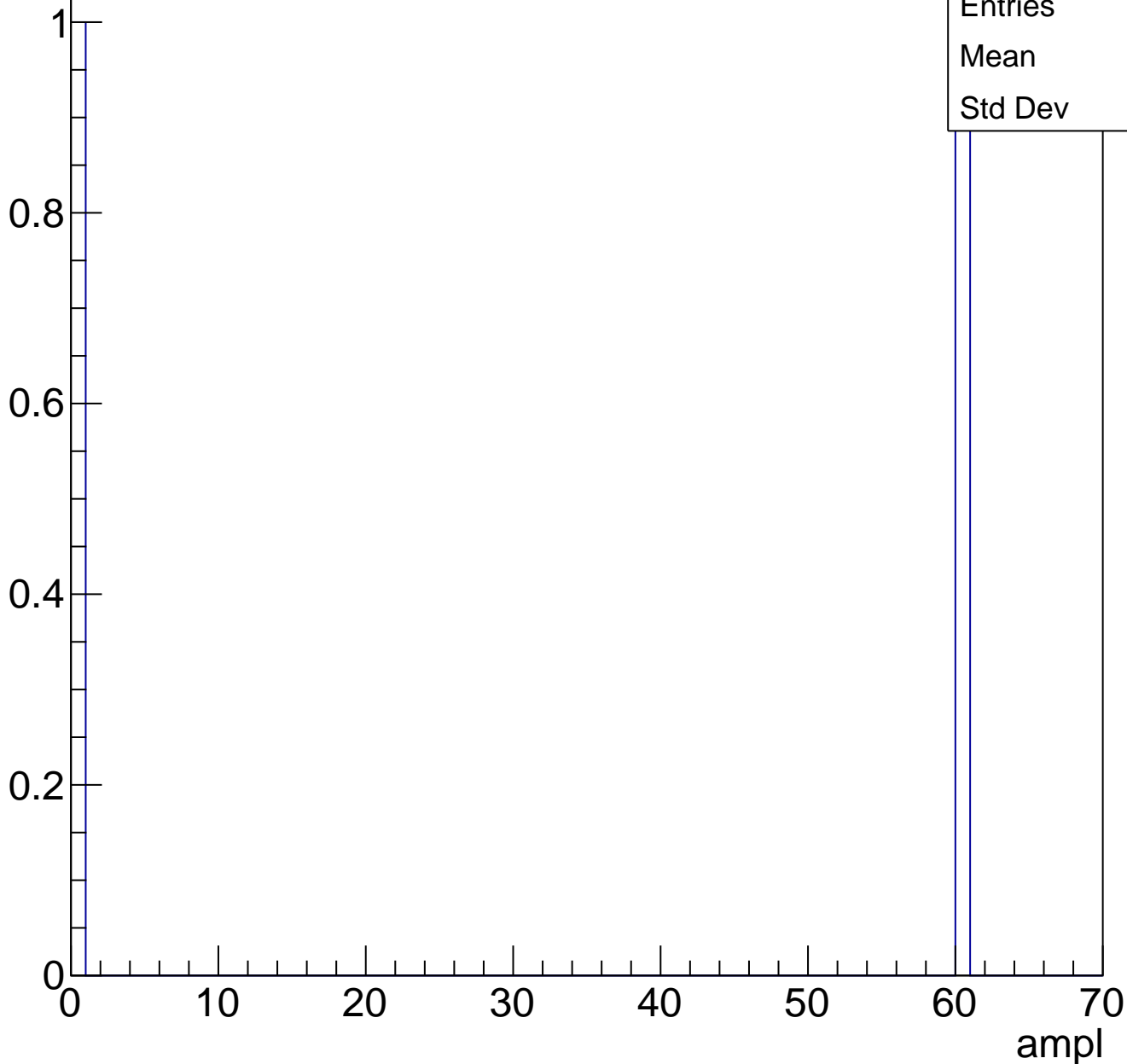
|         |       |
|---------|-------|
| Entries | 23    |
| Mean    | 61.65 |
| Std Dev | 1.631 |



# B0L101S, U16-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch11, adc0

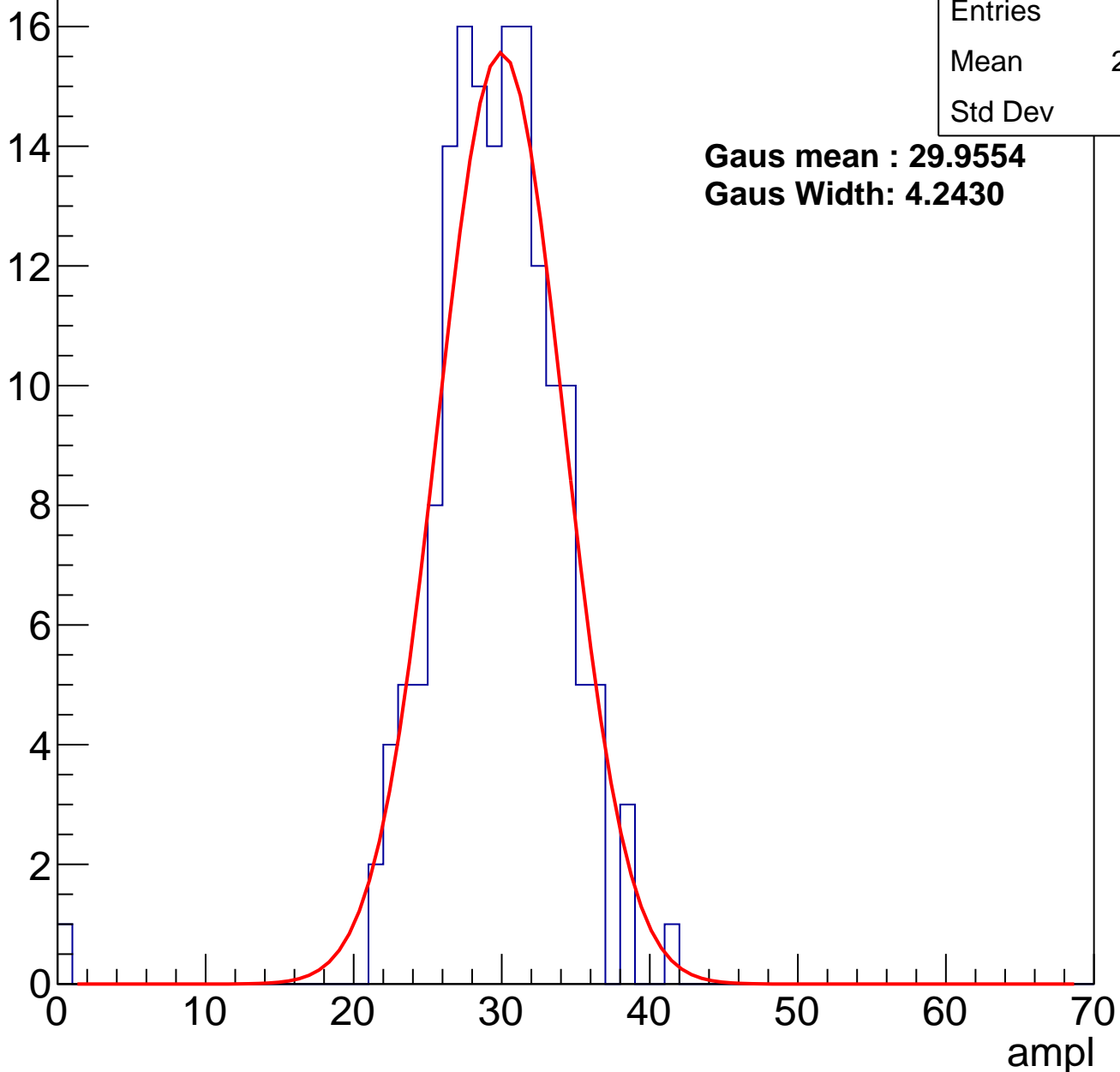
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 162   |
| Mean    | 29.18 |
| Std Dev | 4.45  |

**Gaus mean : 29.9554**

**Gaus Width: 4.2430**

Entry



# B0L101S, U16-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 35.6  |
| Std Dev | 4.799 |

**Gaus mean : 35.9923**

**Gaus Width: 3.2531**

Entry

14

12

10

8

6

4

2

0

0

10

20

30

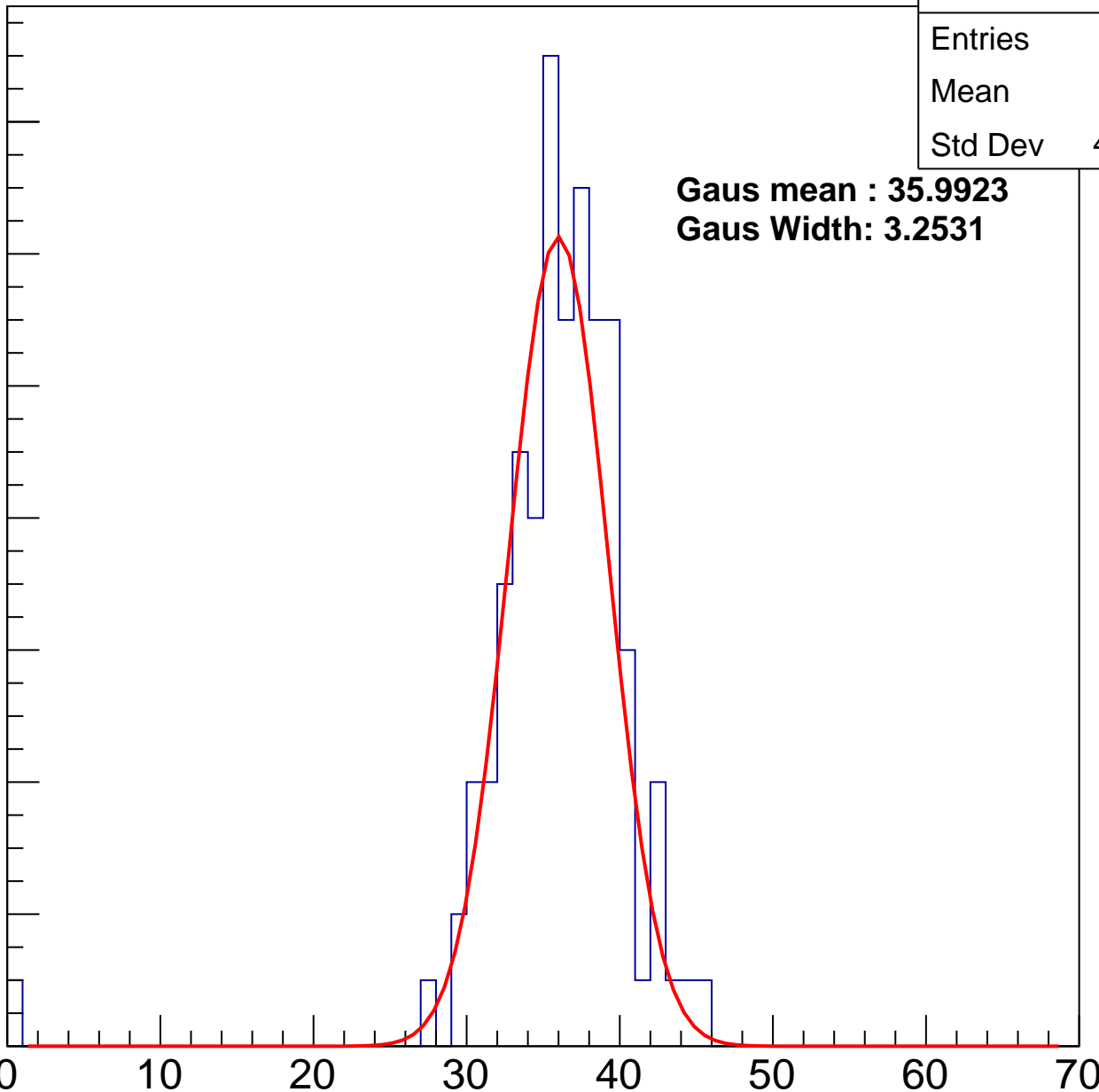
40

50

60

70

ampl



# B0L101S, U16-ch11, adc2

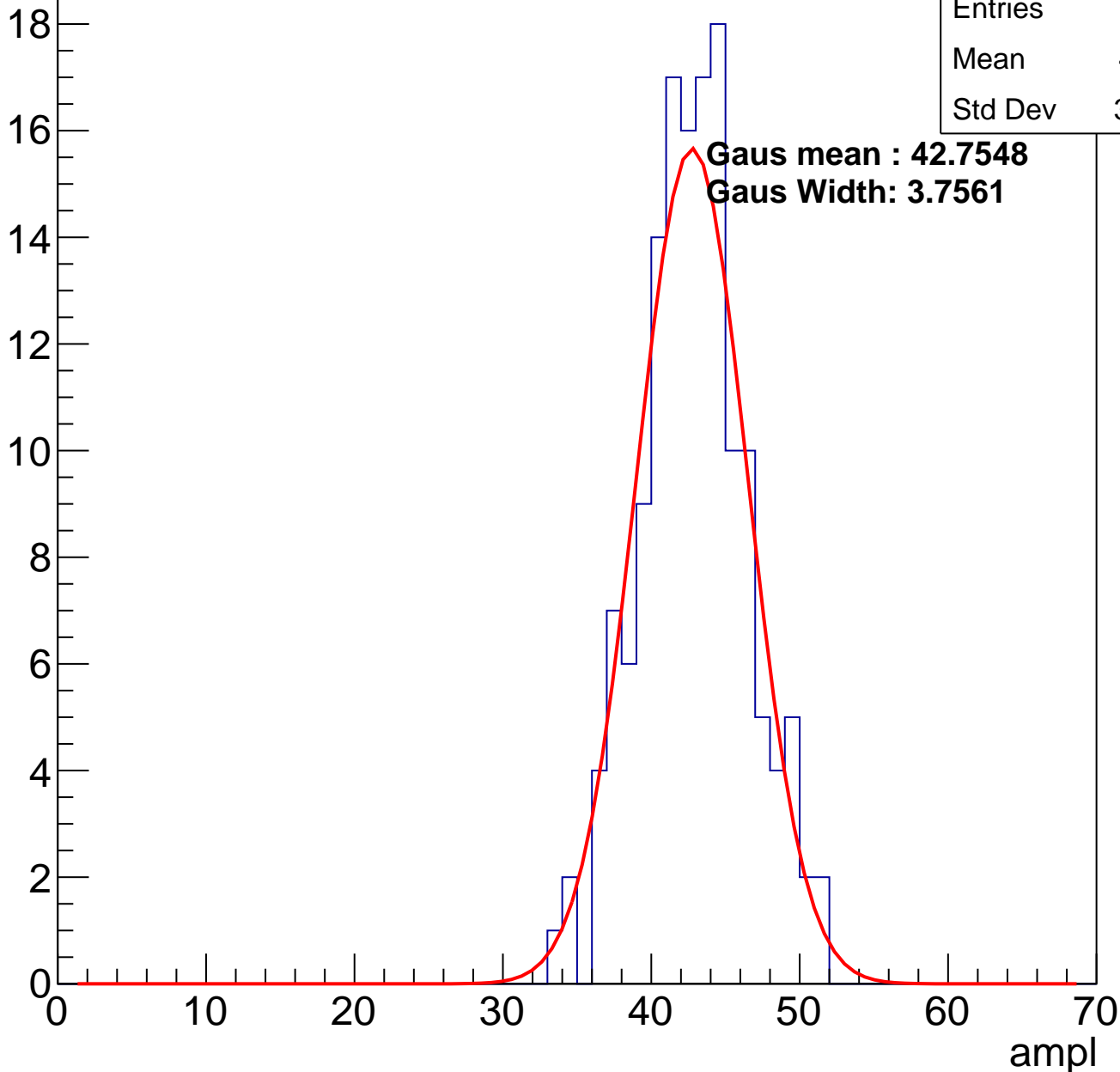
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 42.41 |
| Std Dev | 3.567 |

**Gaus mean : 42.7548**

**Gaus Width: 3.7561**

Entry

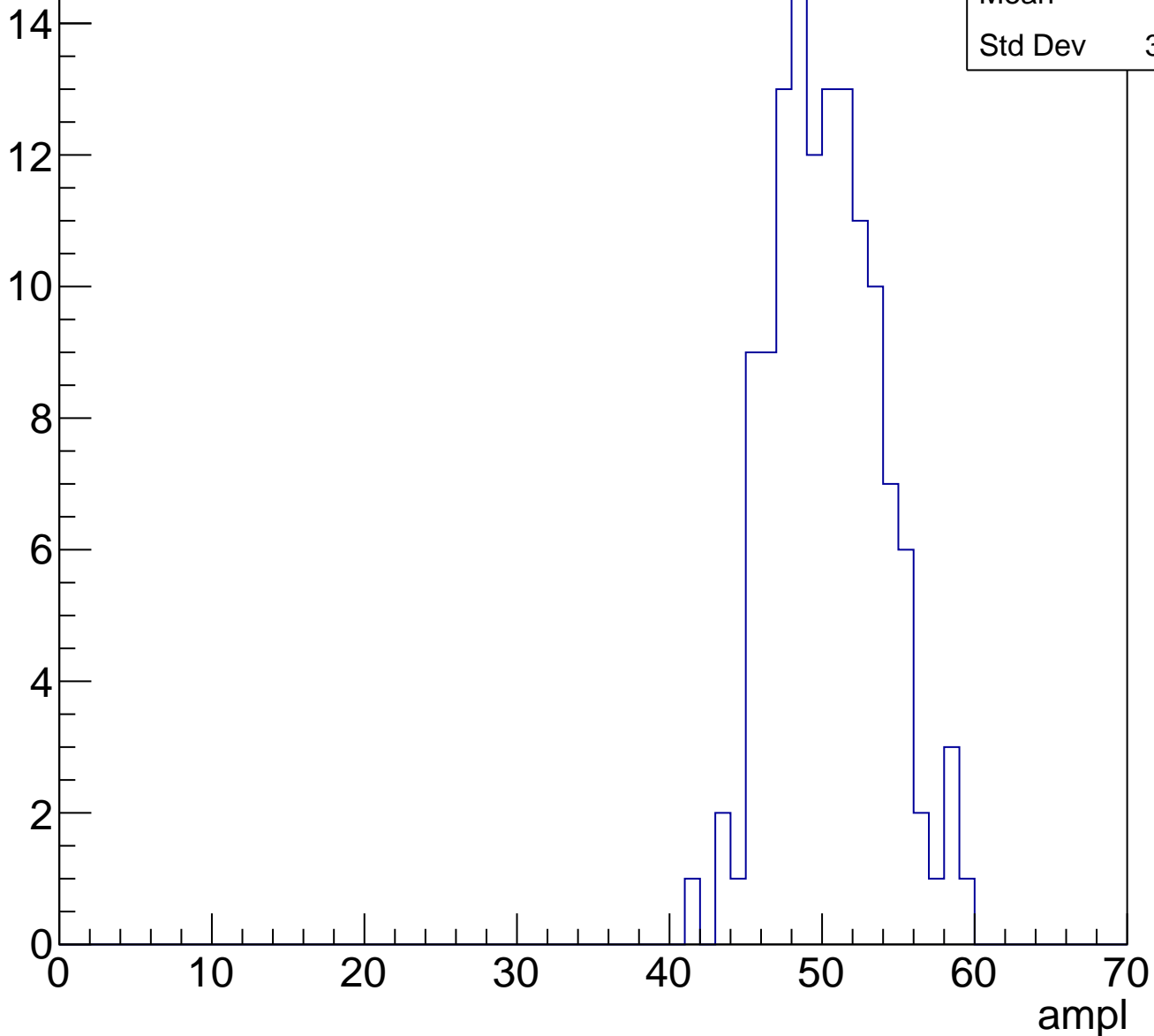


# B0L101S, U16-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 49.88 |
| Std Dev | 3.493 |

Entry



# B0L101S, U16-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

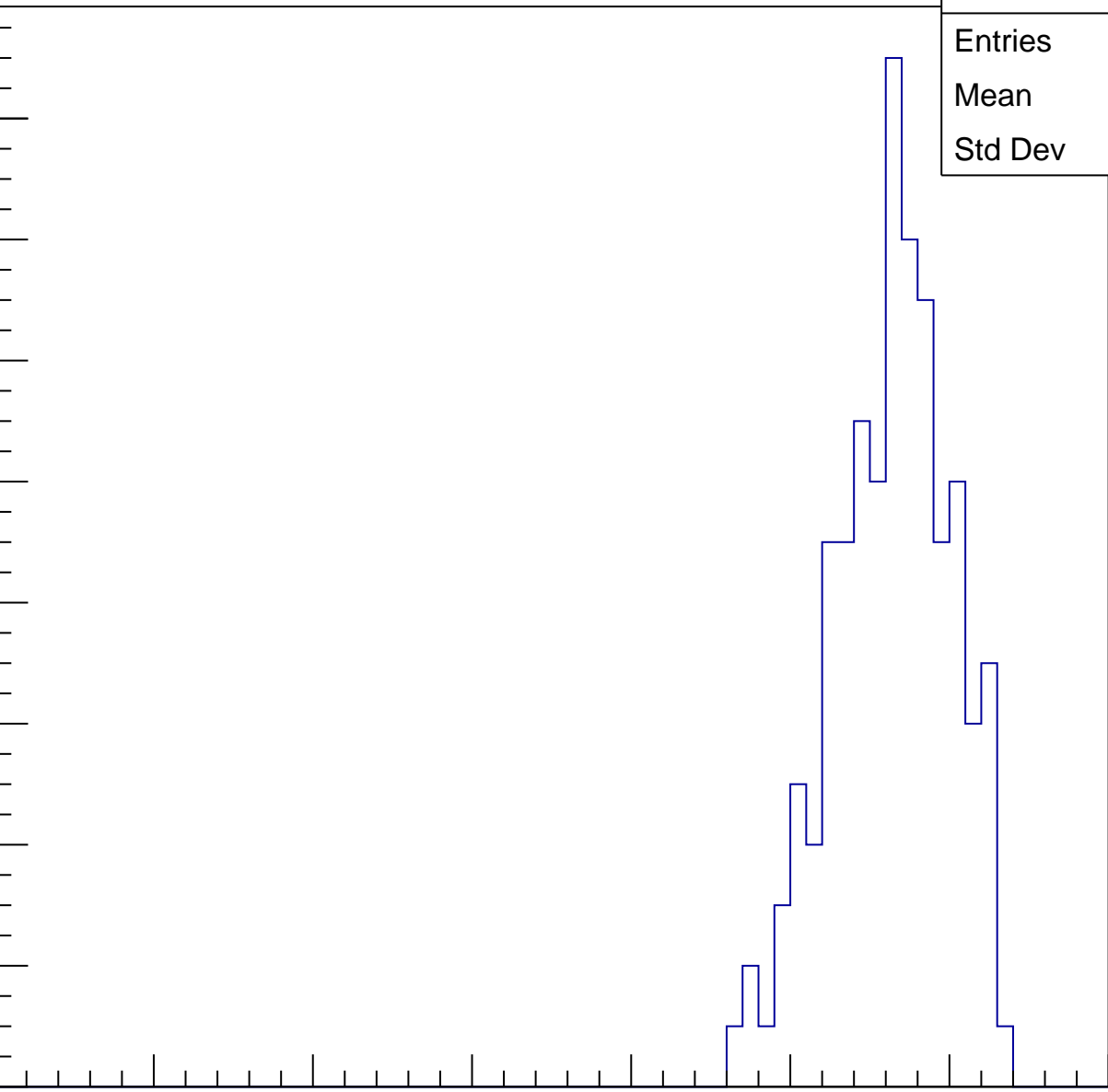
|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 55.88 |
| Std Dev | 3.68  |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch11, adc5

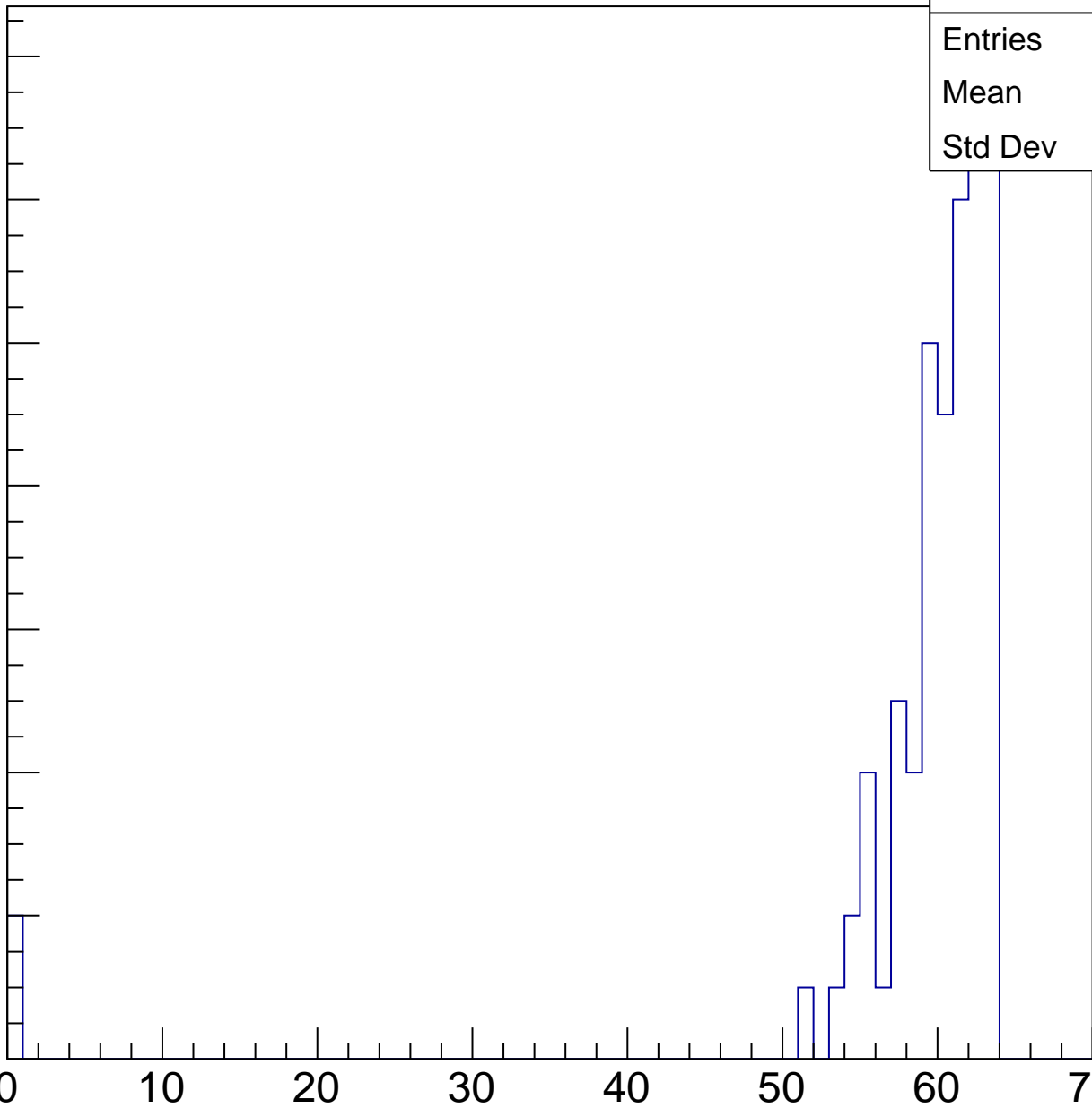
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 58.4  |
| Std Dev | 9.857 |

ampl

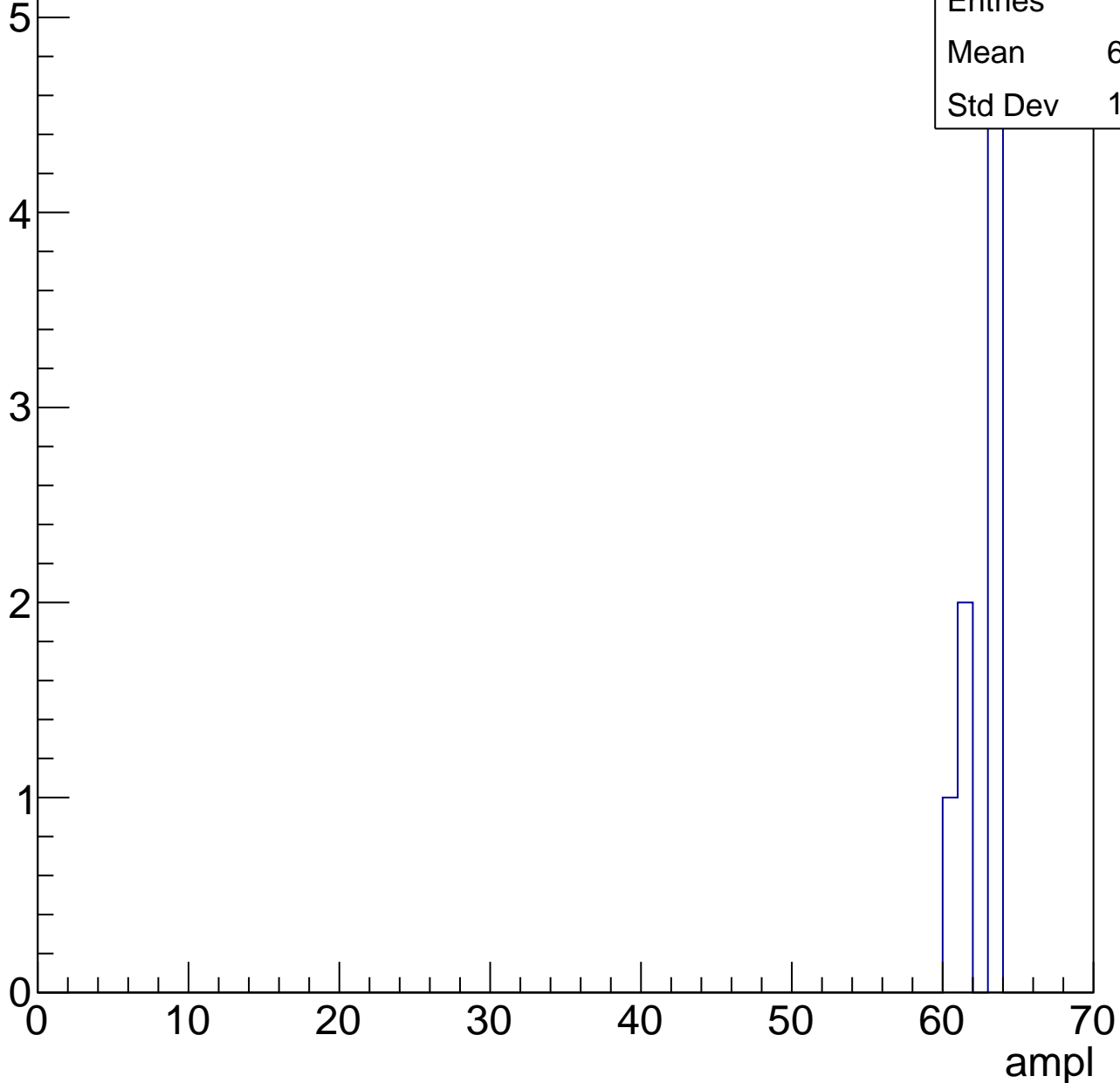


# B0L101S, U16-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 8     |
| Mean    | 62.12 |
| Std Dev | 1.166 |





# B0L101S, U16-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

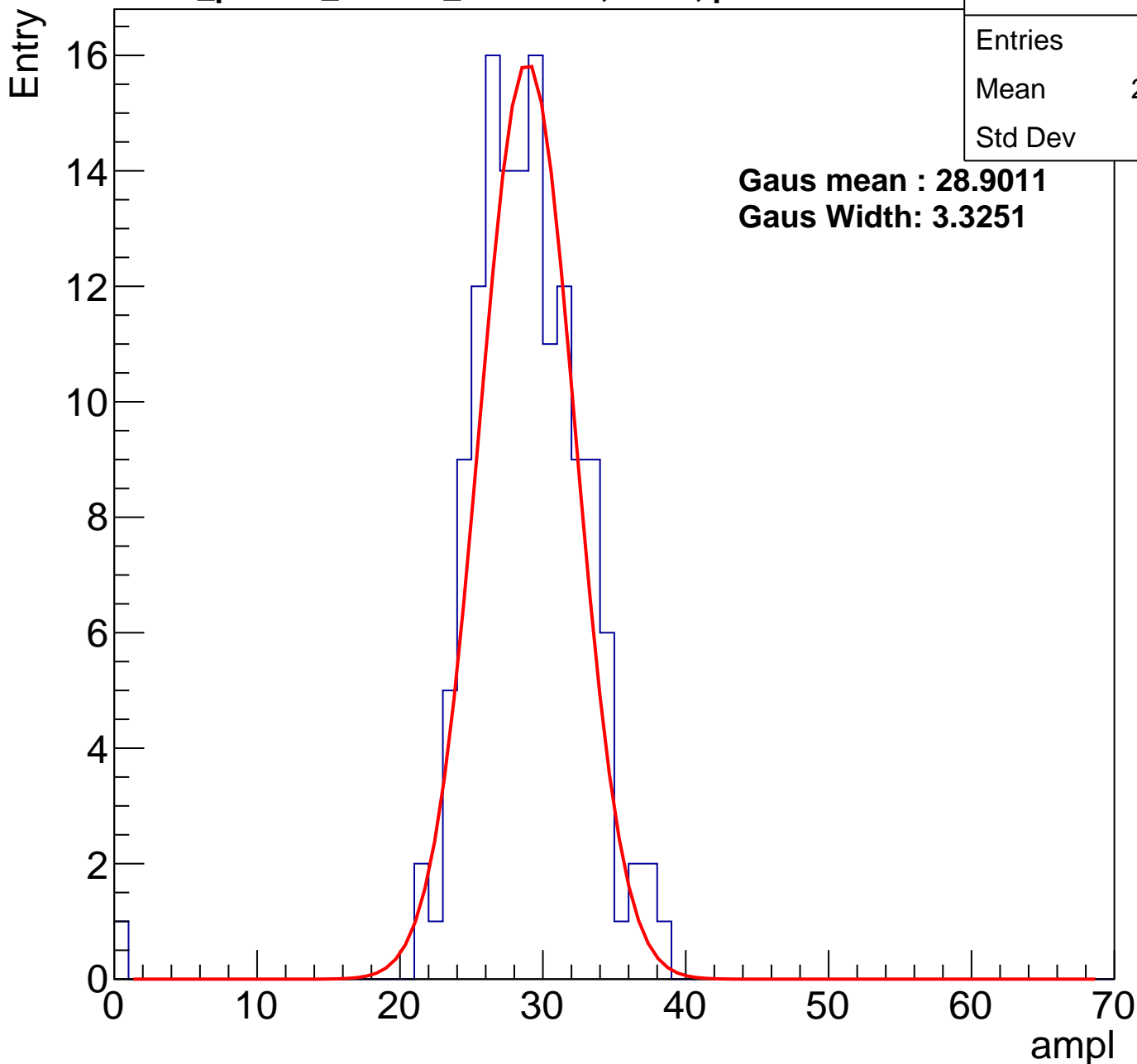
# B0L101S, U16-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 28.36 |
| Std Dev | 4.22  |

**Gaus mean : 28.9011**

**Gaus Width: 3.3251**



# B0L101S, U16-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 35.87 |
| Std Dev | 3.779 |

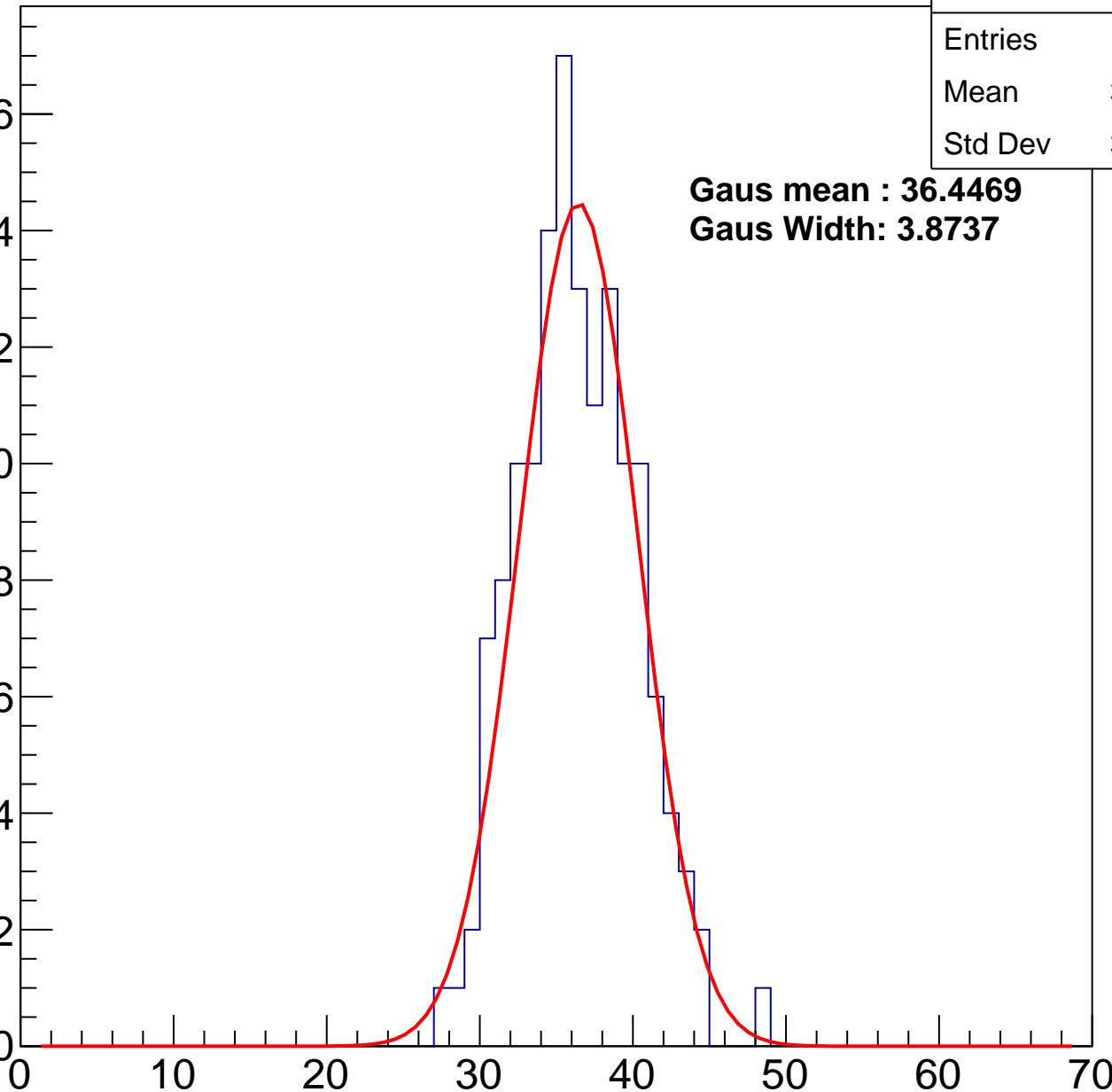
**Gaus mean : 36.4469**

**Gaus Width: 3.8737**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

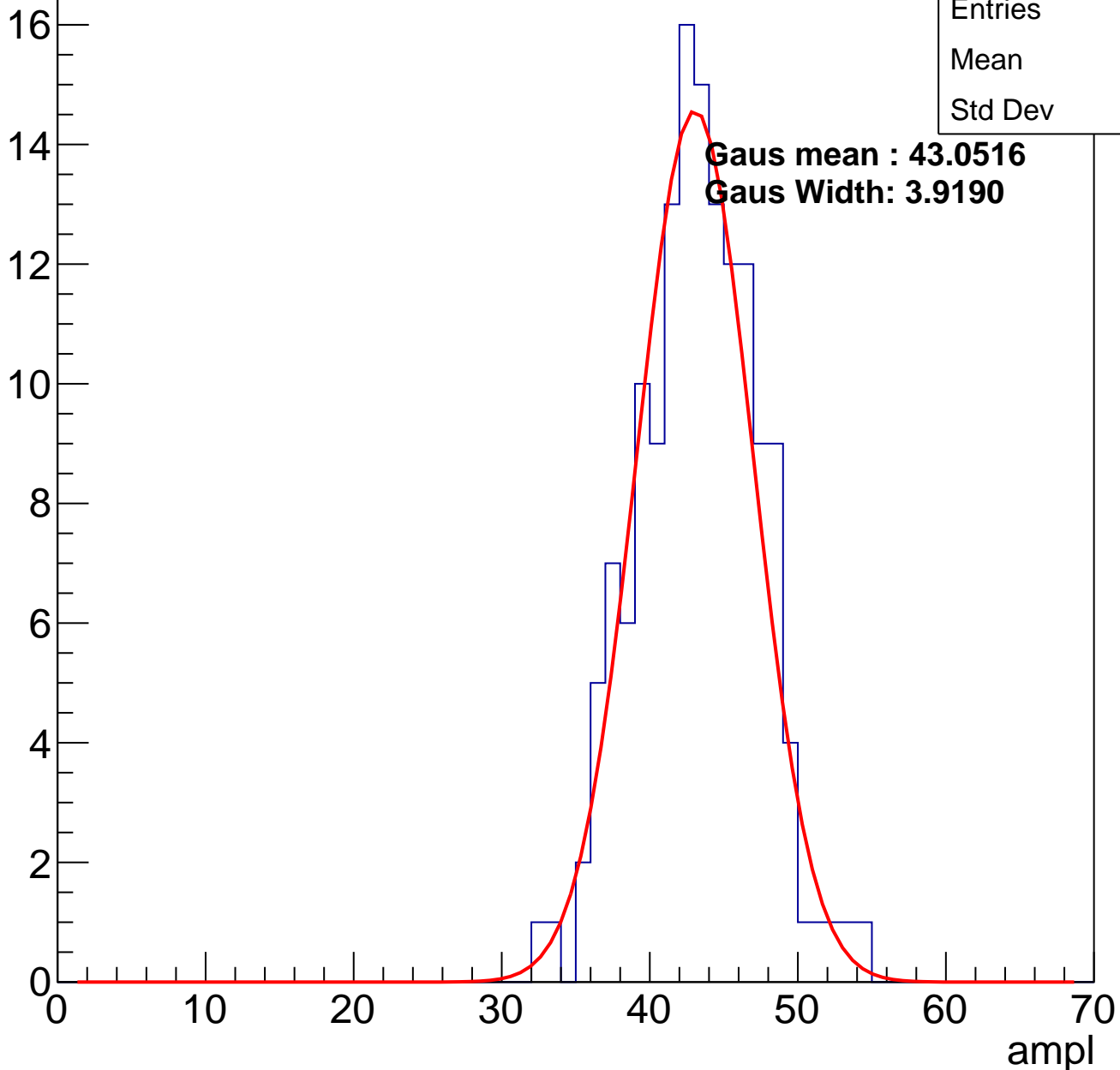
ampl



# B0L101S, U16-ch12, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

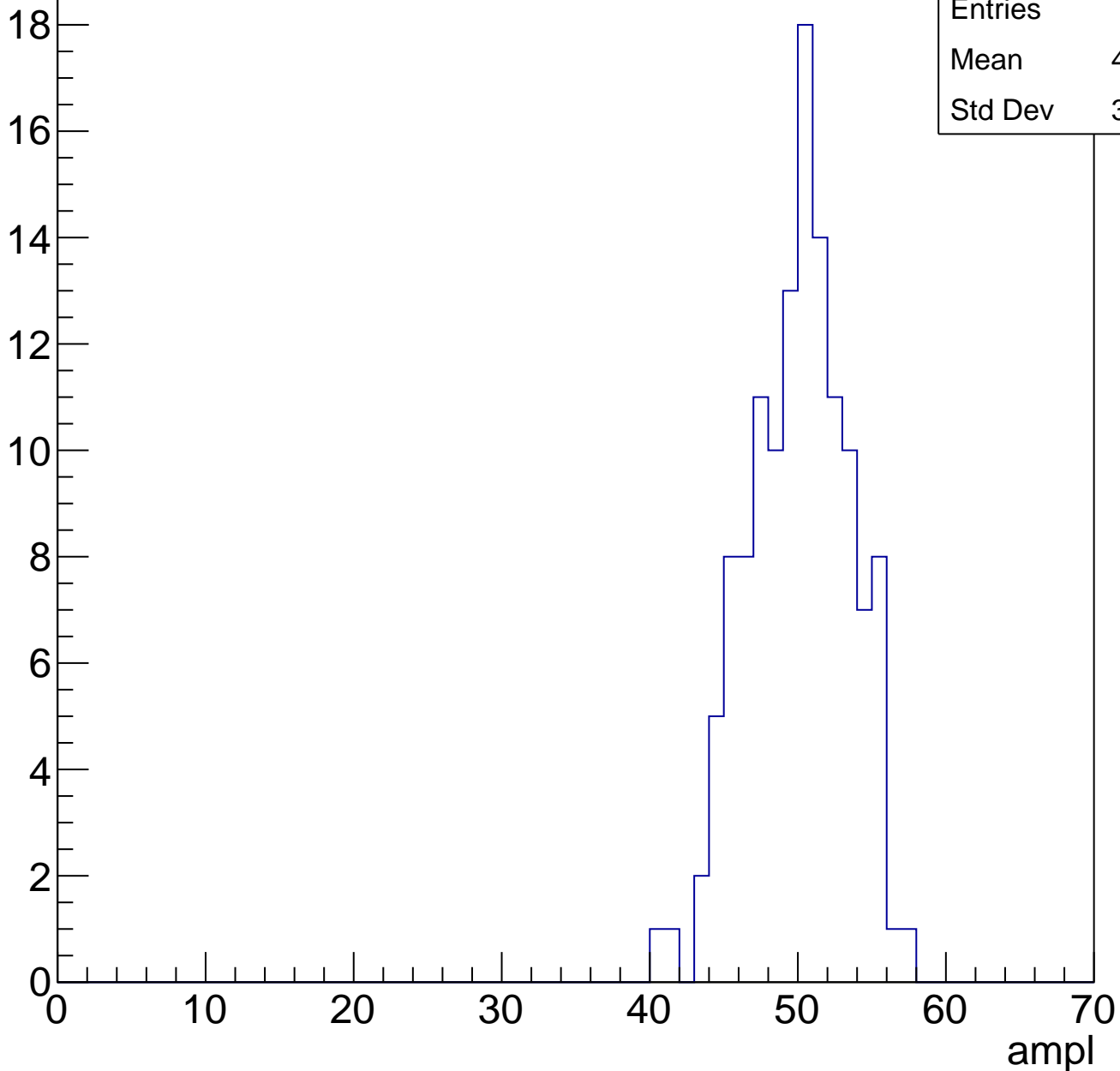


|         |      |
|---------|------|
| Entries | 149  |
| Mean    | 42.8 |
| Std Dev | 4.01 |

# B0L101S, U16-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 56.27 |
| Std Dev | 3.376 |

Entry

12

10

8

6

4

2

0

0

10

20

30

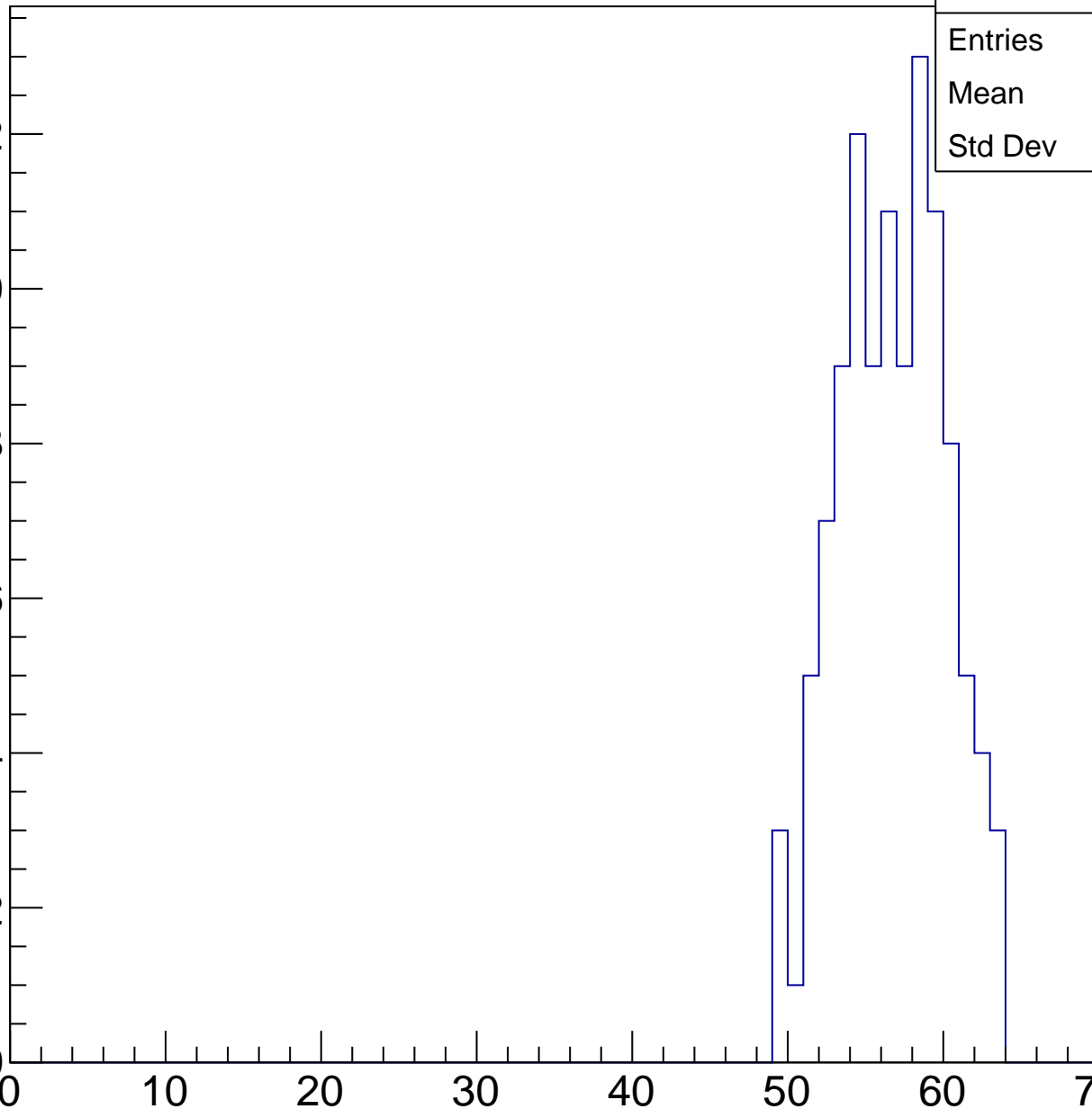
40

50

60

70

ampl



# B0L101S, U16-ch12, adc5

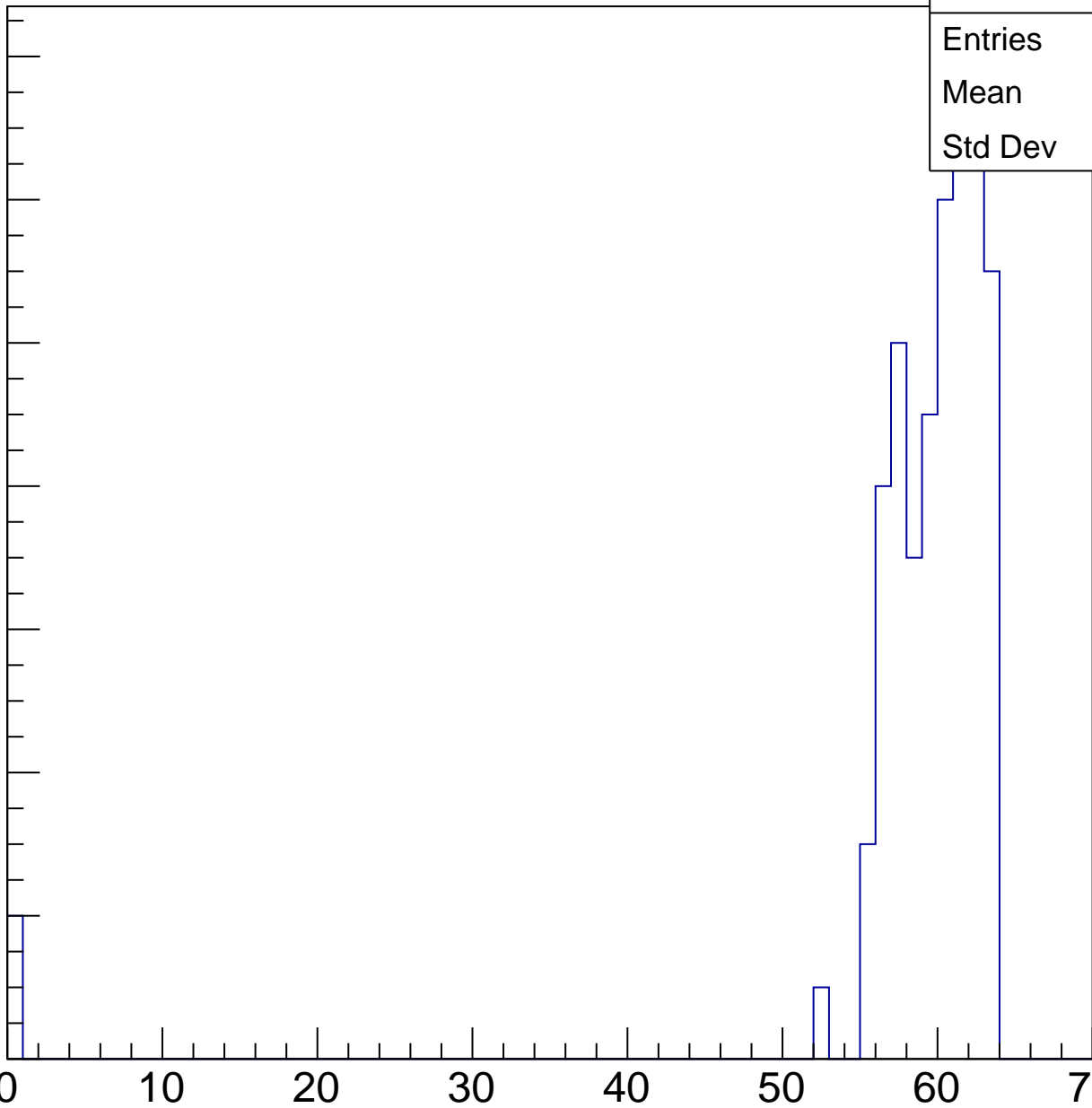
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 91    |
| Mean    | 58.32 |
| Std Dev | 9.082 |

ampl

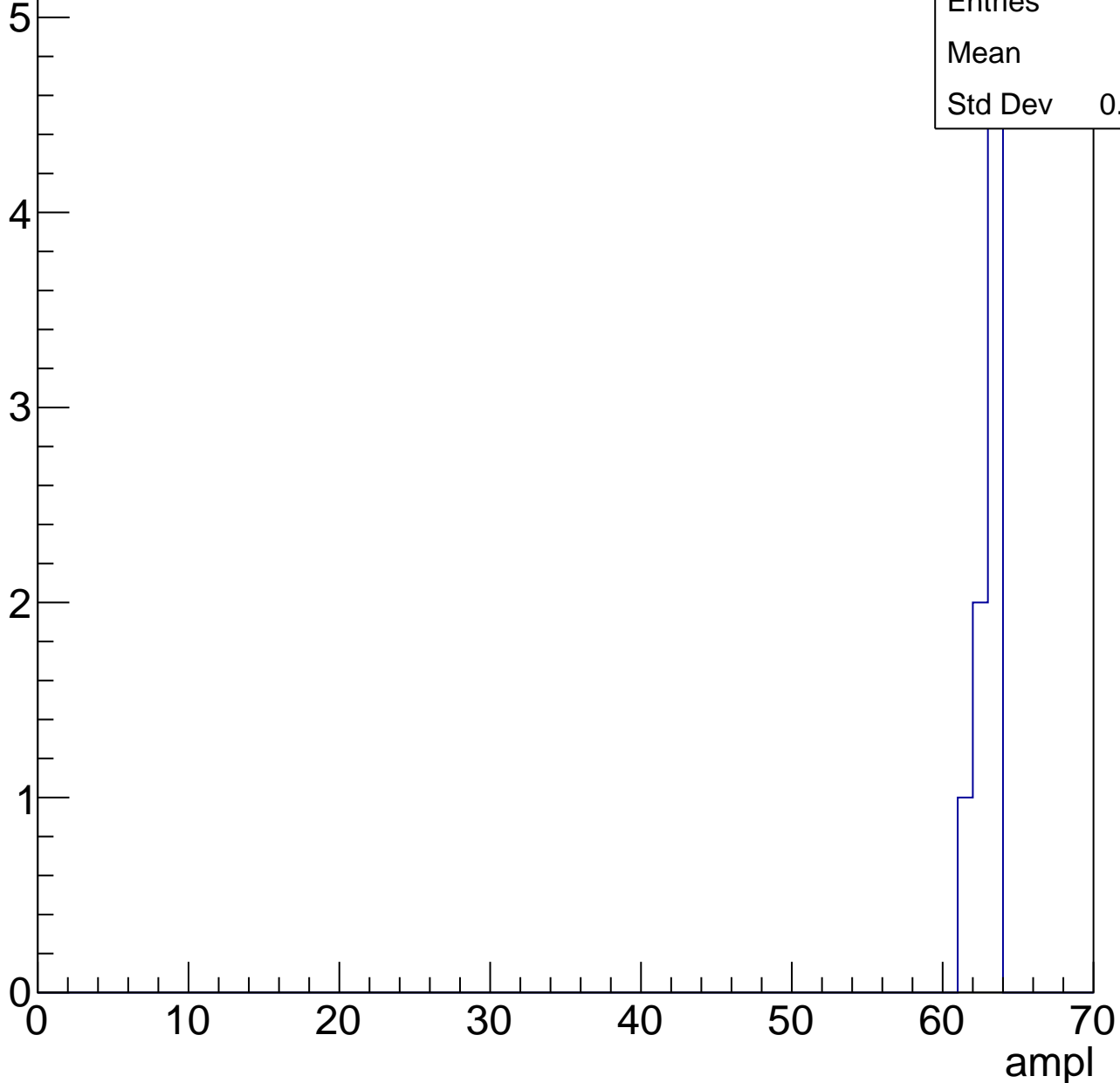


# B0L101S, U16-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 8      |
| Mean    | 62.5   |
| Std Dev | 0.7071 |

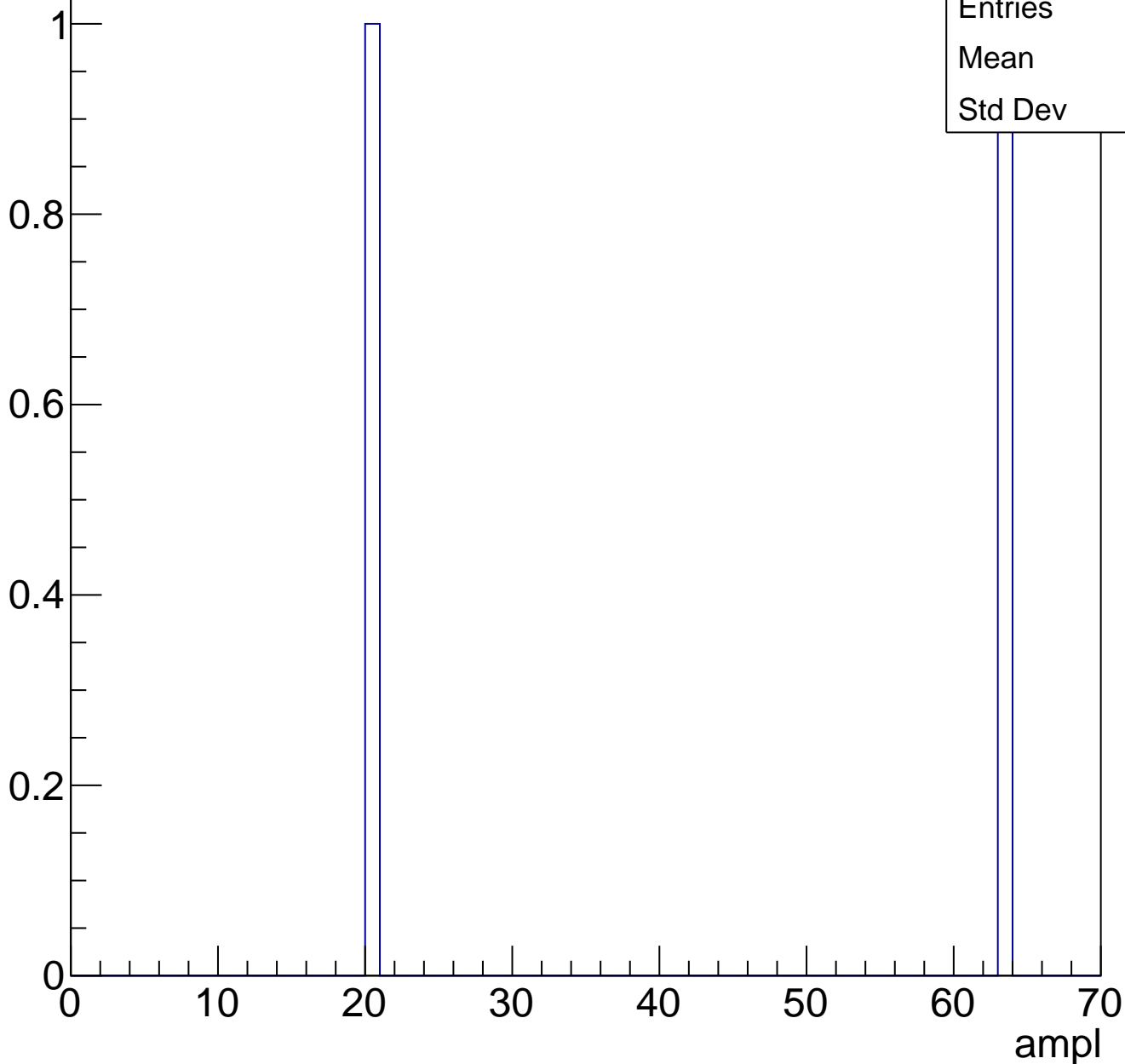




# B0L101S, U16-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 28.92 |
| Std Dev | 5.085 |

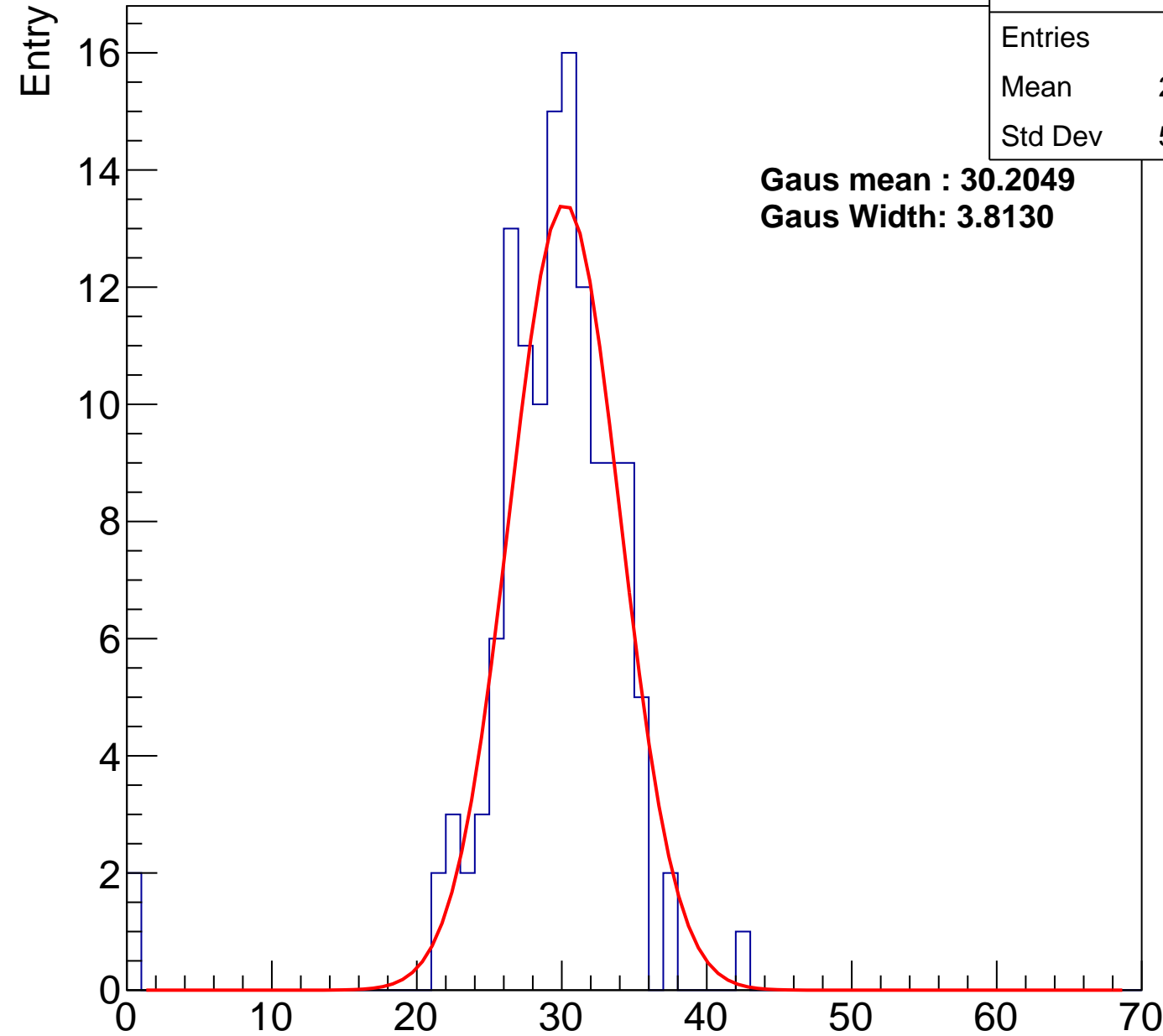
**Gaus mean : 30.2049**

**Gaus Width: 3.8130**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

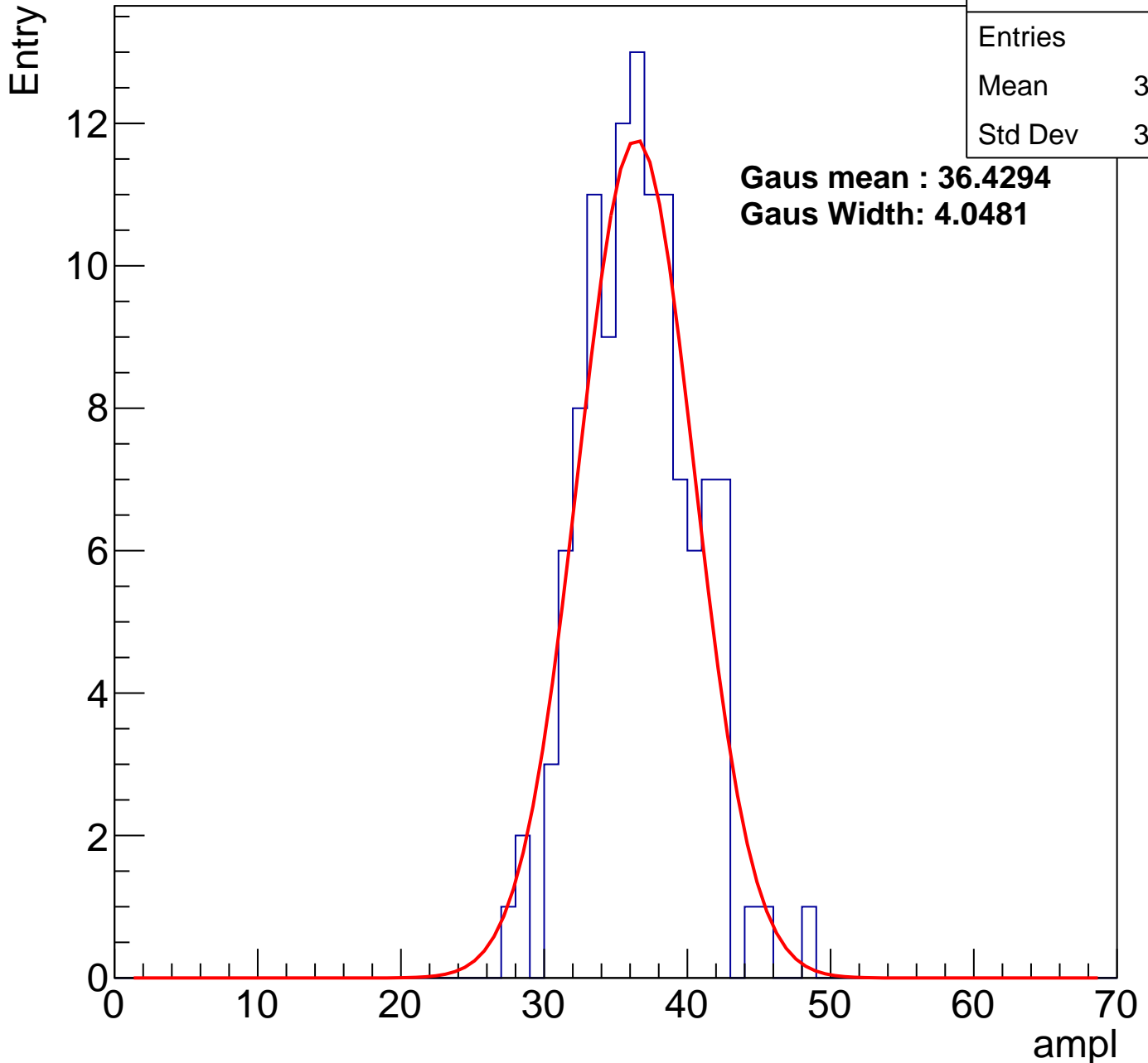


# B0L101S, U16-ch13, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 36.14 |
| Std Dev | 3.778 |

**Gaus mean : 36.4294**  
**Gaus Width: 4.0481**



# B0L101S, U16-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 41.39 |
| Std Dev | 3.839 |

**Gaus mean : 41.7178**

**Gaus Width: 4.1390**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

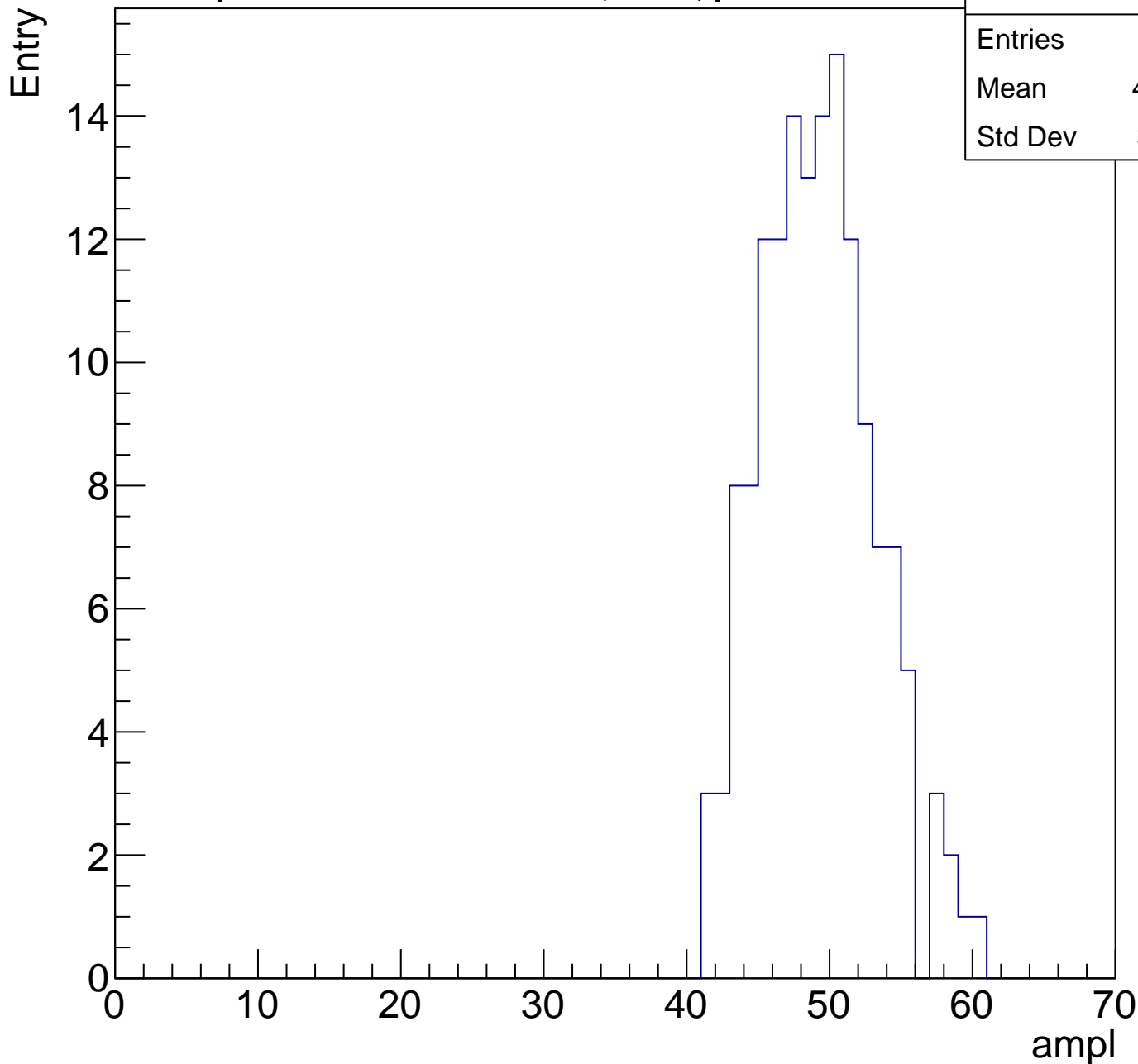
60

70

# B0L101S, U16-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 48.76 |
| Std Dev | 3.991 |



# B0L101S, U16-ch13, adc4

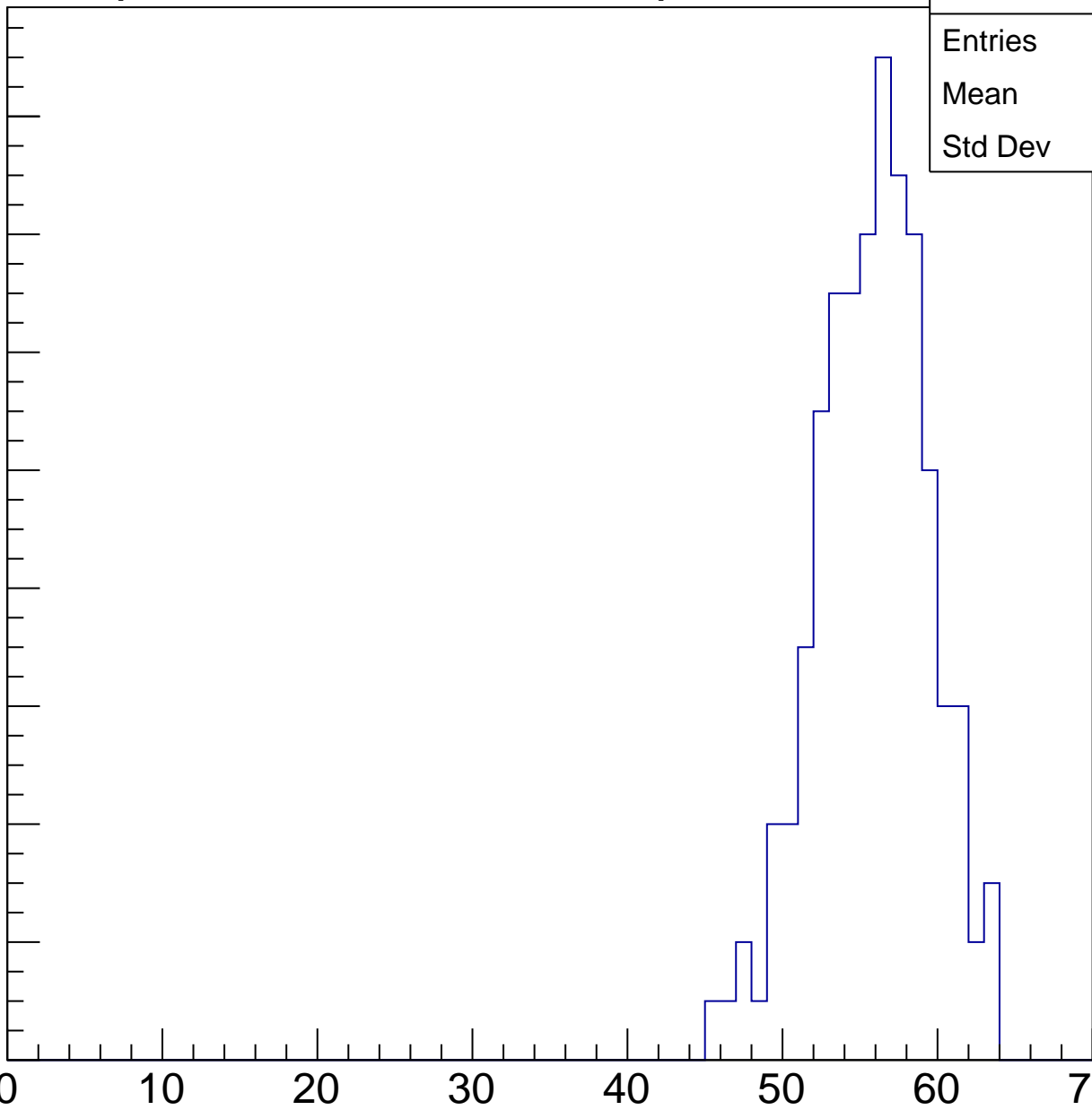
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 55.33 |
| Std Dev | 3.615 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch13, adc5

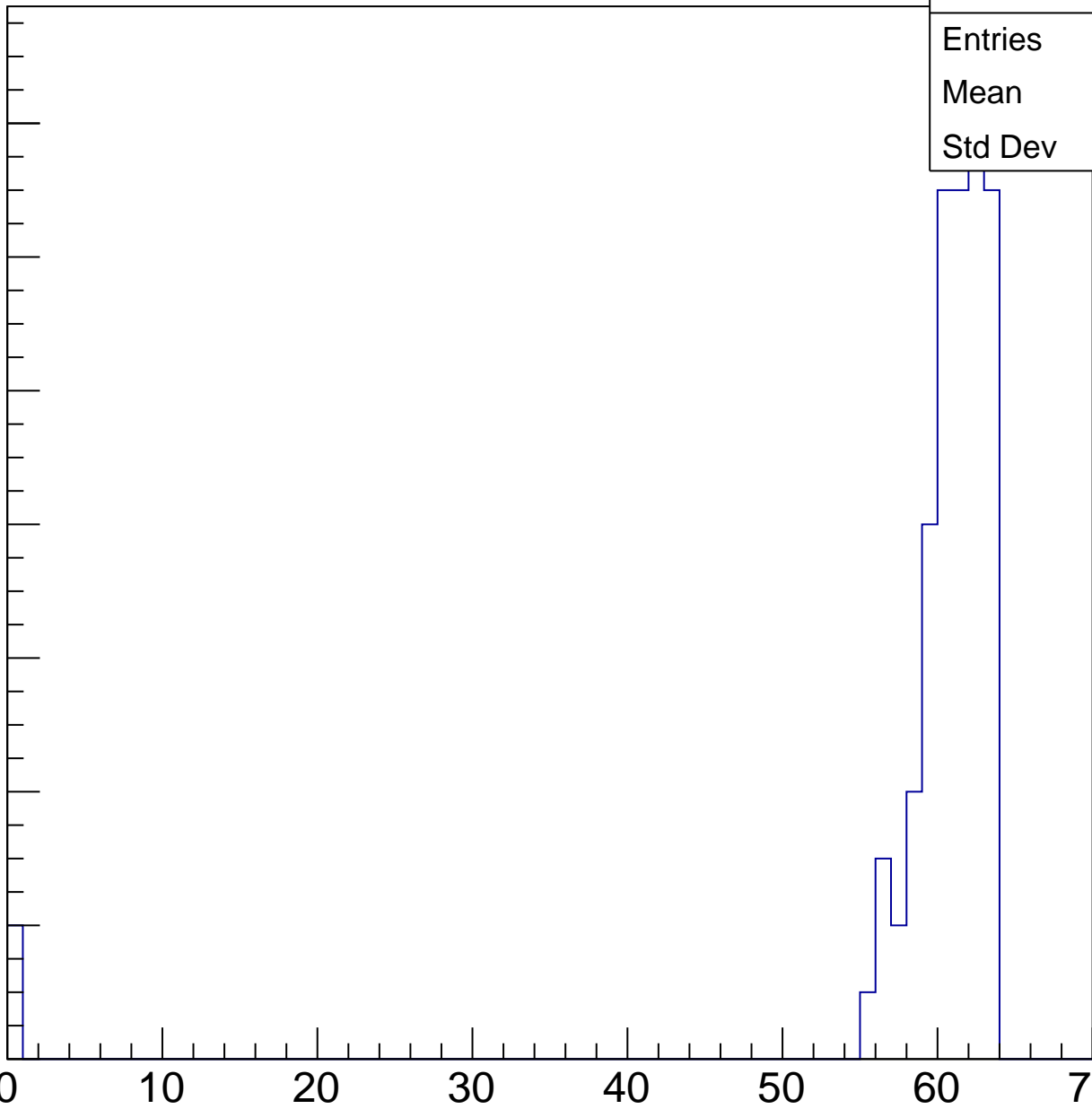
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 58.96 |
| Std Dev | 10.02 |

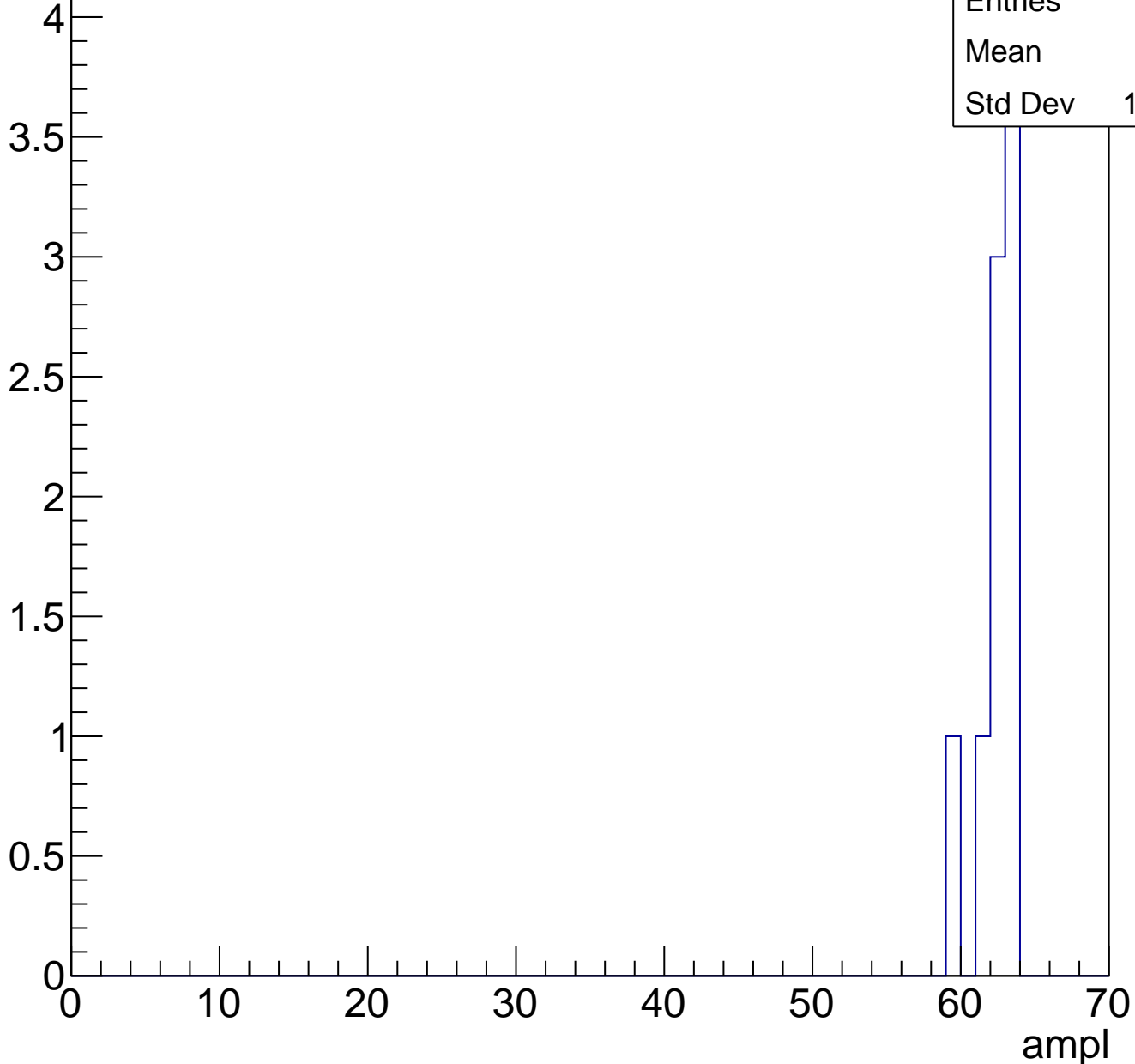
ampl



# B0L101S, U16-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



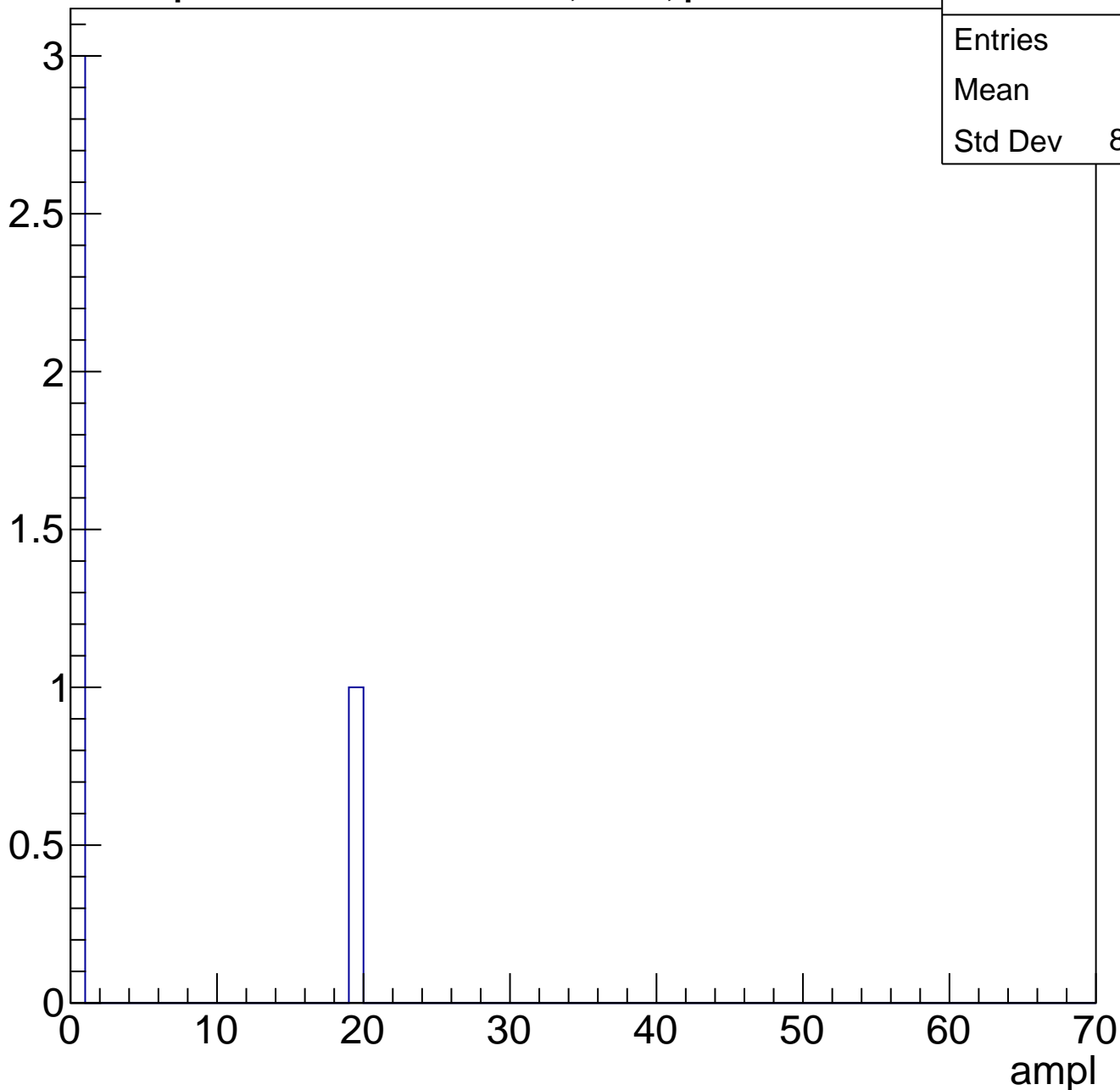
|         |       |
|---------|-------|
| Entries | 9     |
| Mean    | 62    |
| Std Dev | 1.247 |



# B0L101S, U16-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 4.75  |
| Std Dev | 8.227 |

# B0L101S, U16-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 28.01 |
| Std Dev | 5.07  |

**Gaus mean : 28.9930**

**Gaus Width: 3.7782**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

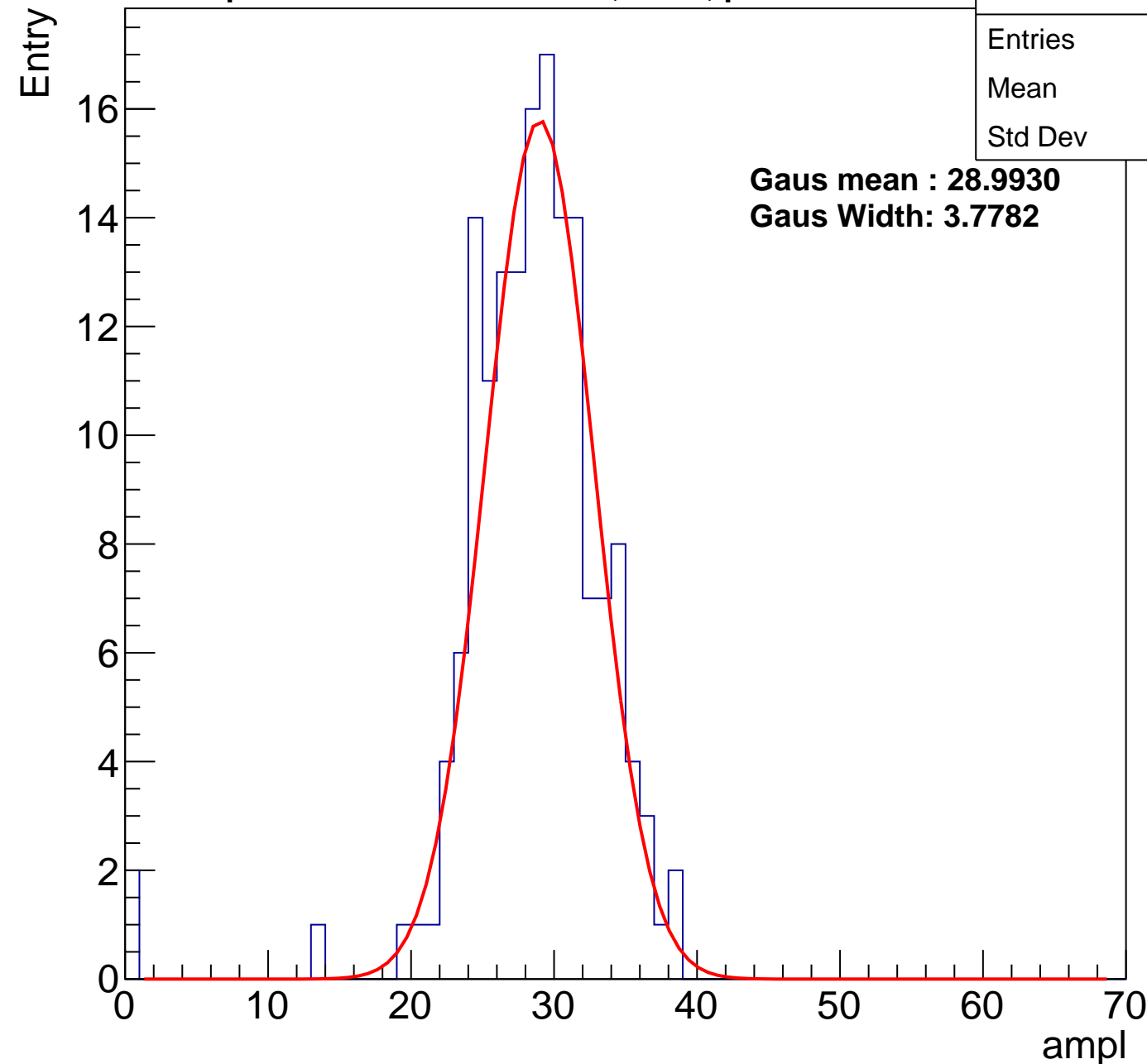
40

50

60

70

ampl



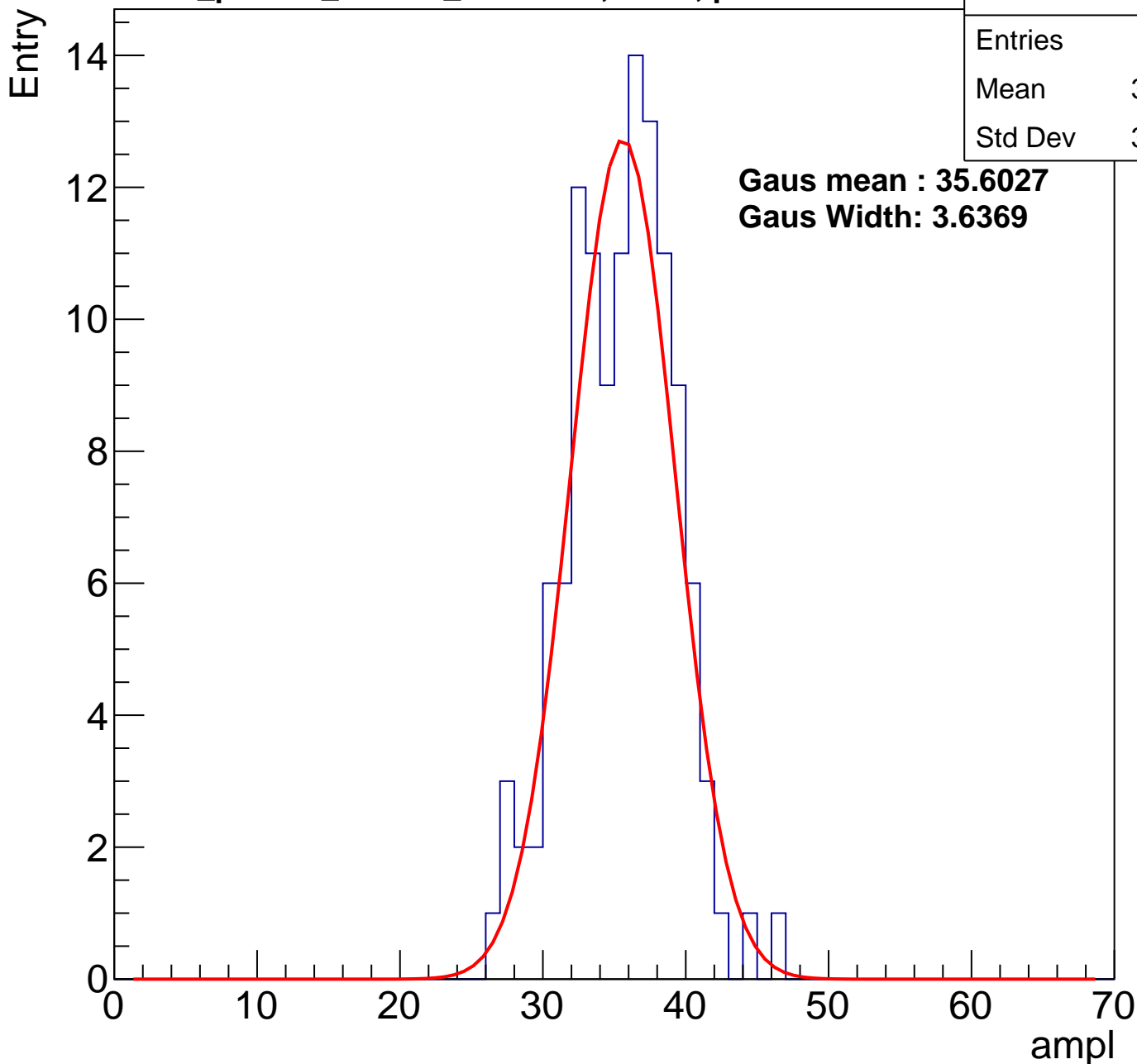
# B0L101S, U16-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 35.03 |
| Std Dev | 3.697 |

**Gaus mean : 35.6027**

**Gaus Width: 3.6369**



# B0L101S, U16-ch14, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 40.94 |
| Std Dev | 3.503 |

**Gaus mean : 41.6502**

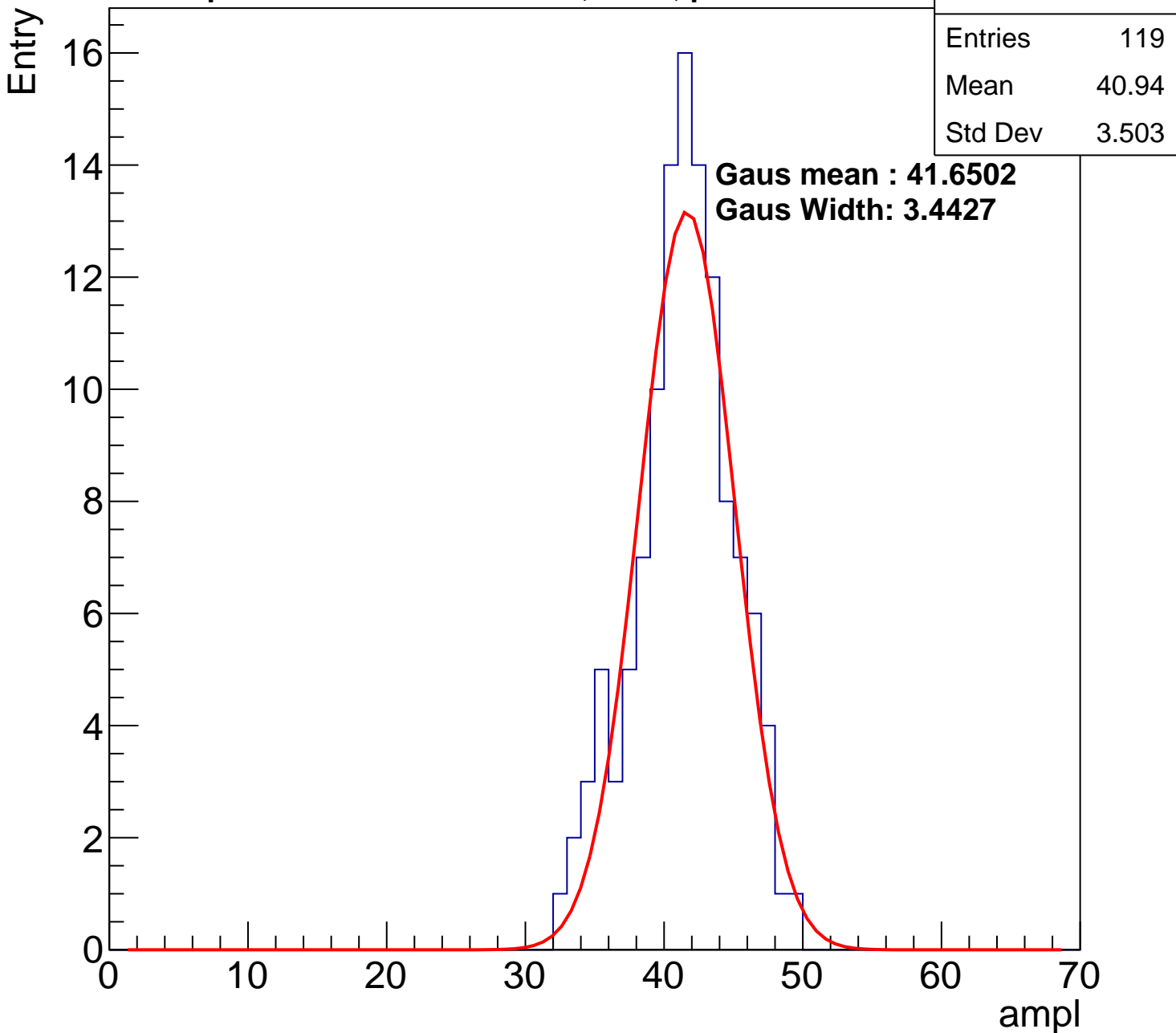
**Gaus Width: 3.4427**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

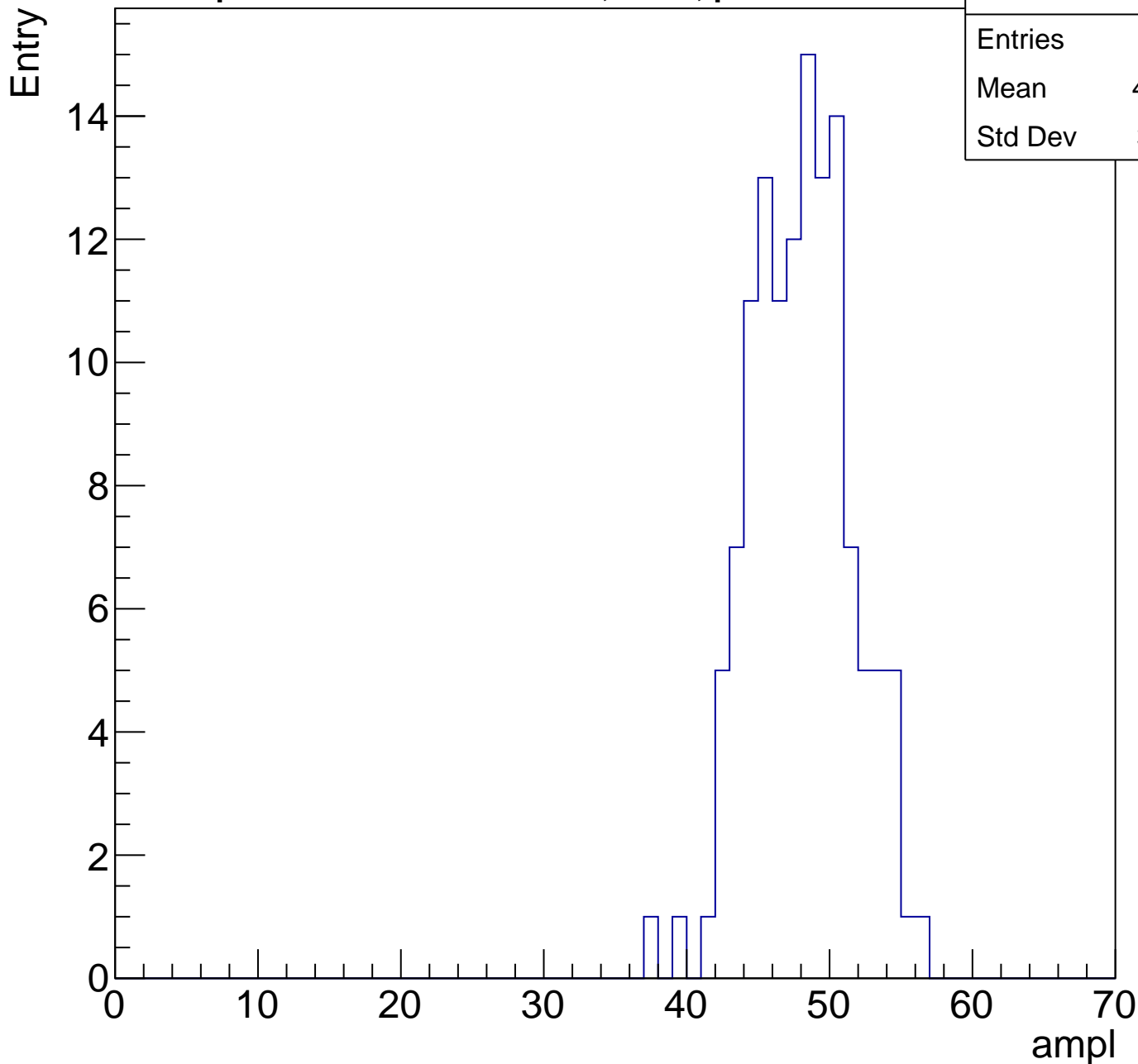
0 10 20 30 40 50 60 70



# B0L101S, U16-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

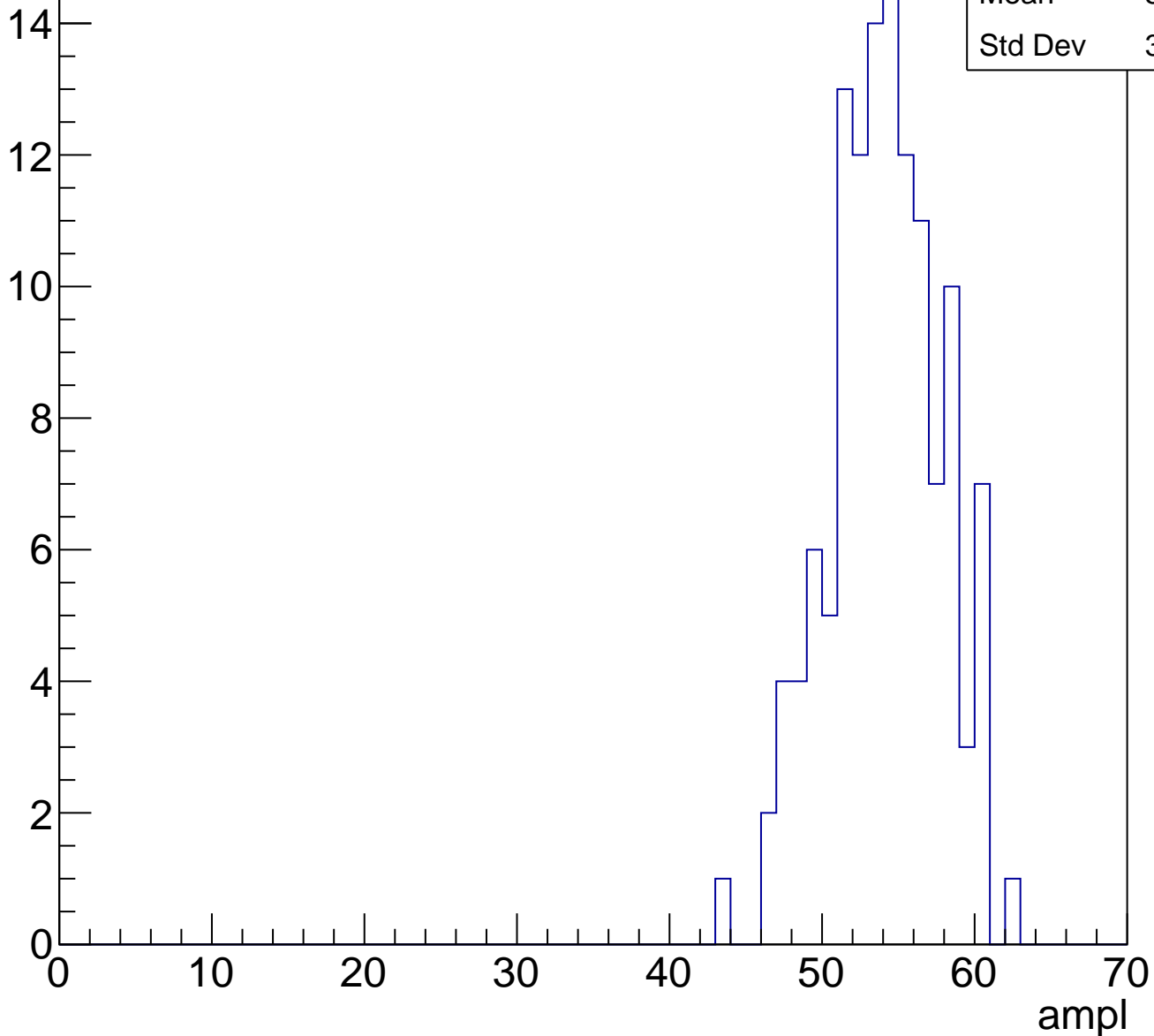
|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 47.55 |
| Std Dev | 3.491 |



# B0L101S, U16-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch14, adc5

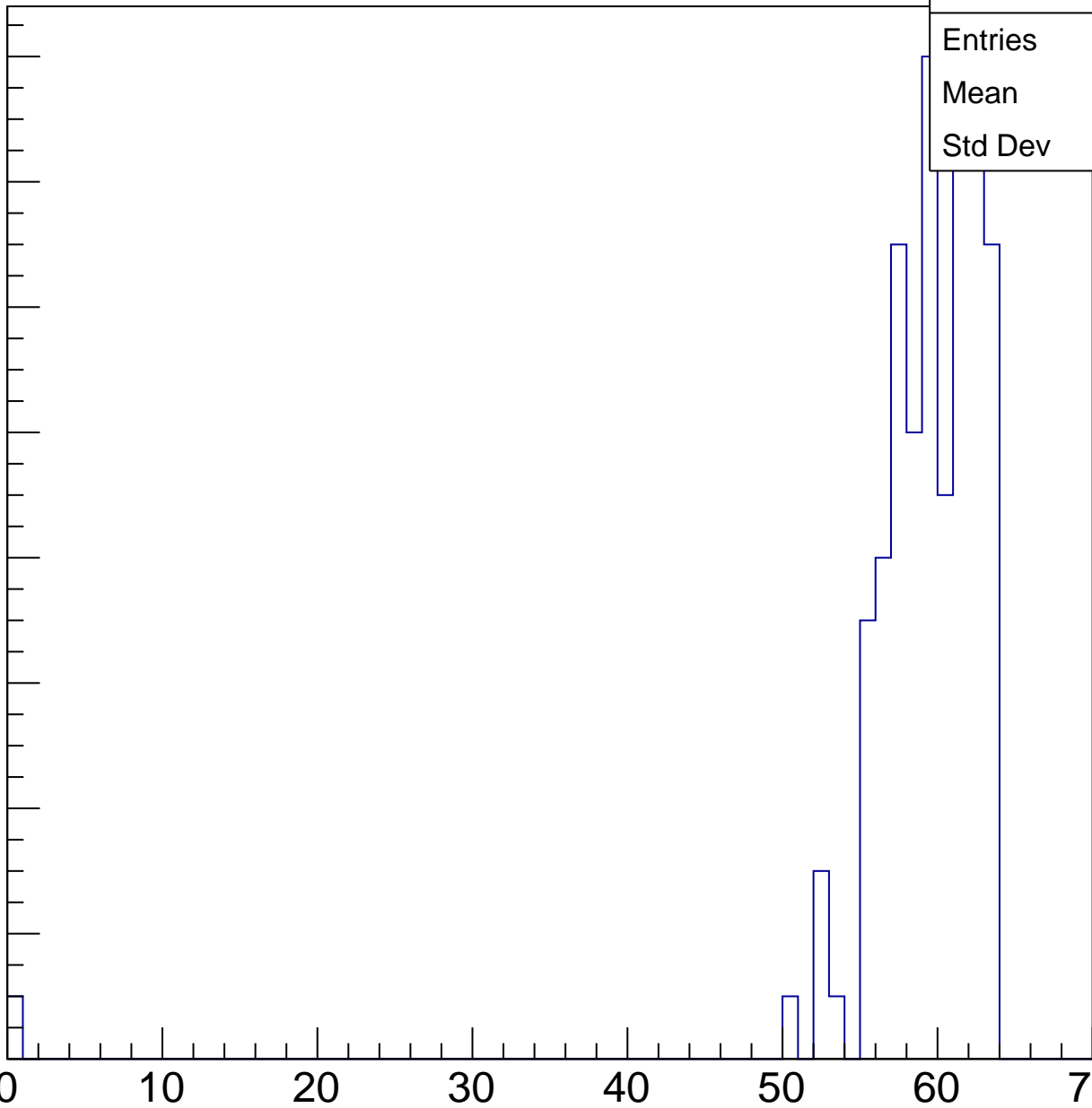
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 58.61 |
| Std Dev | 6.236 |

ampl

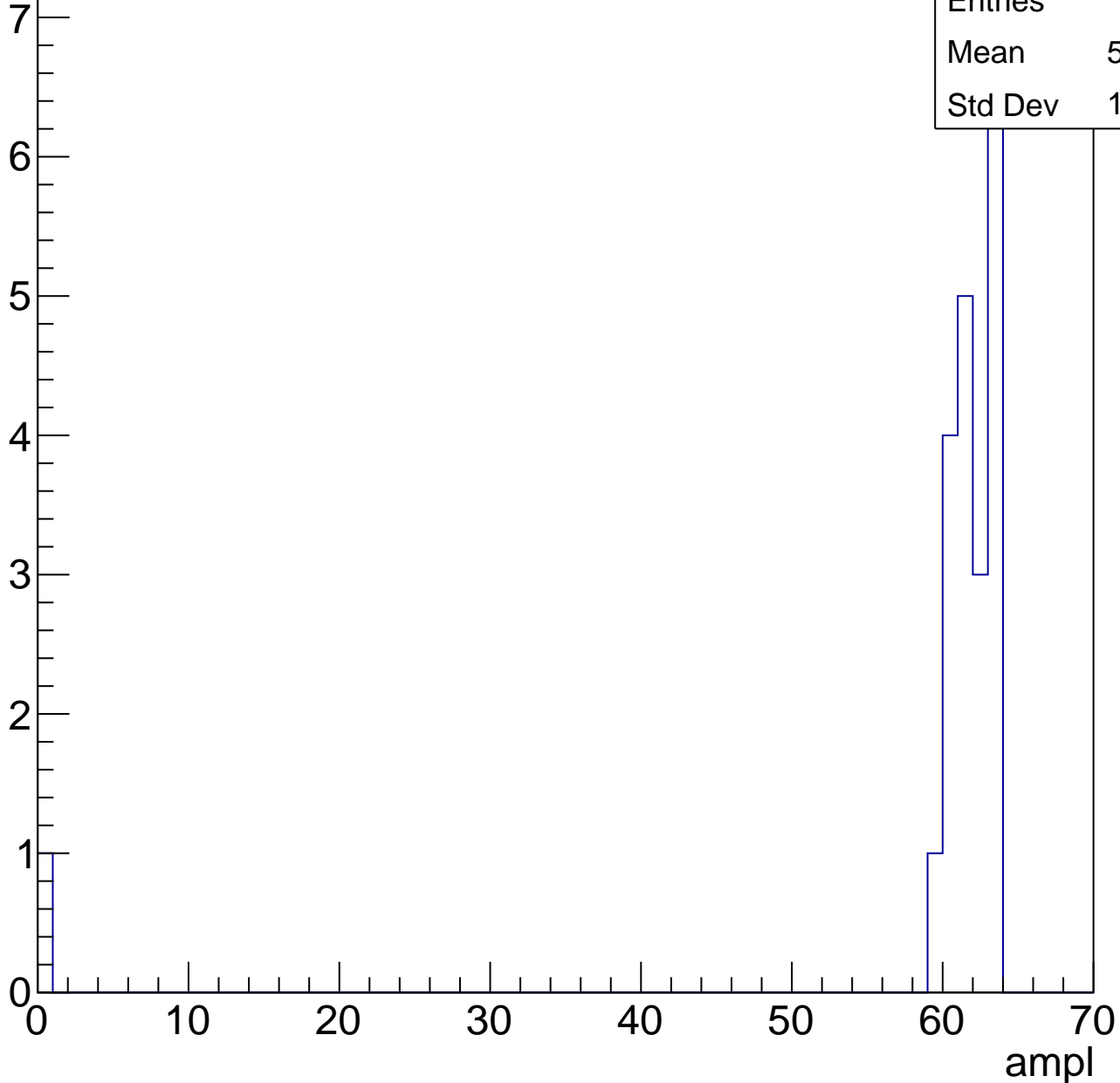


# B0L101S, U16-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 58.62 |
| Std Dev | 13.17 |





# B0L101S, U16-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 29.05 |
| Std Dev | 3.471 |

**Gaus mean : 29.7573**

**Gaus Width: 3.7290**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

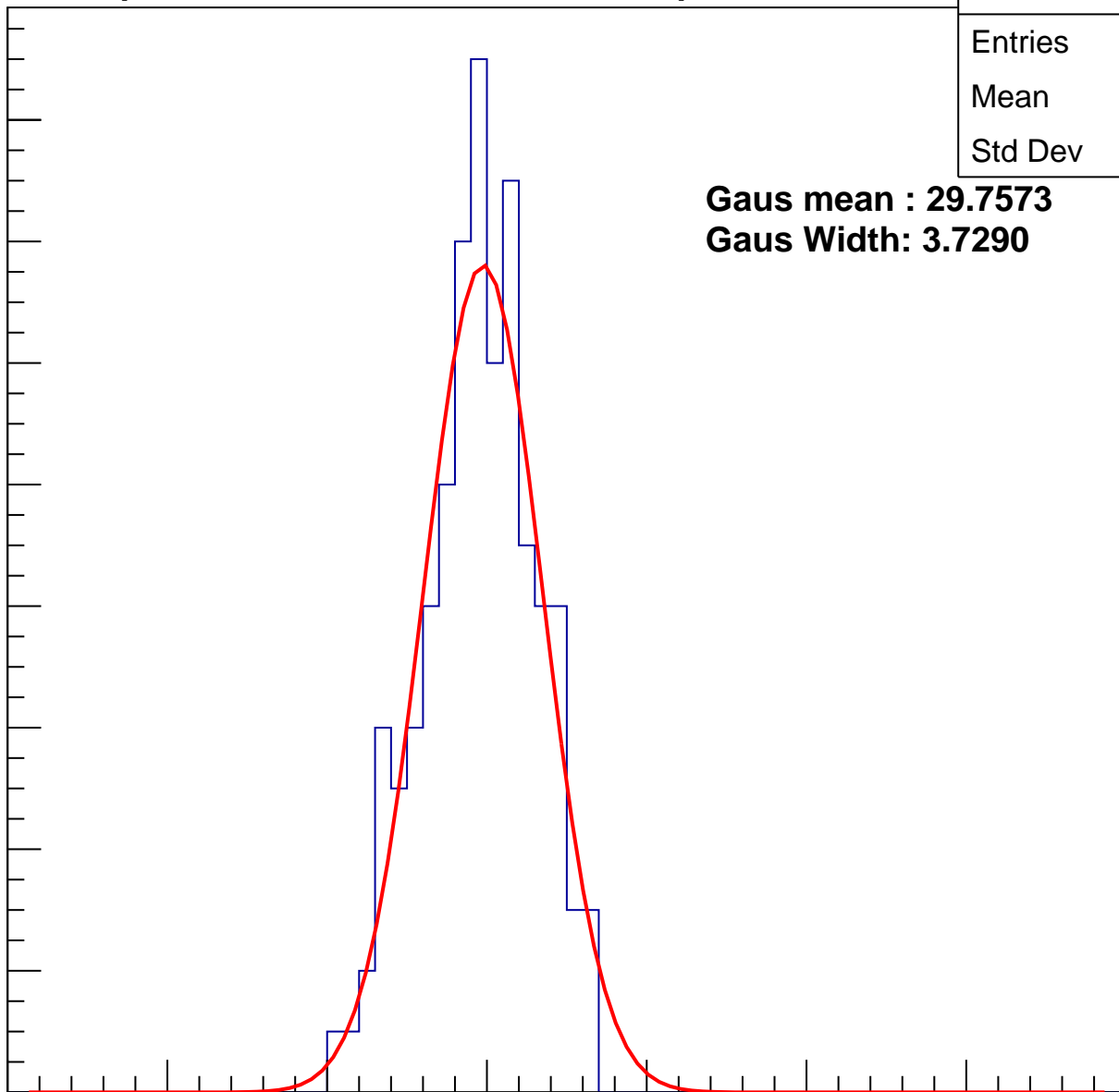
40

50

60

70

ampl



# B0L101S, U16-ch15, adc1

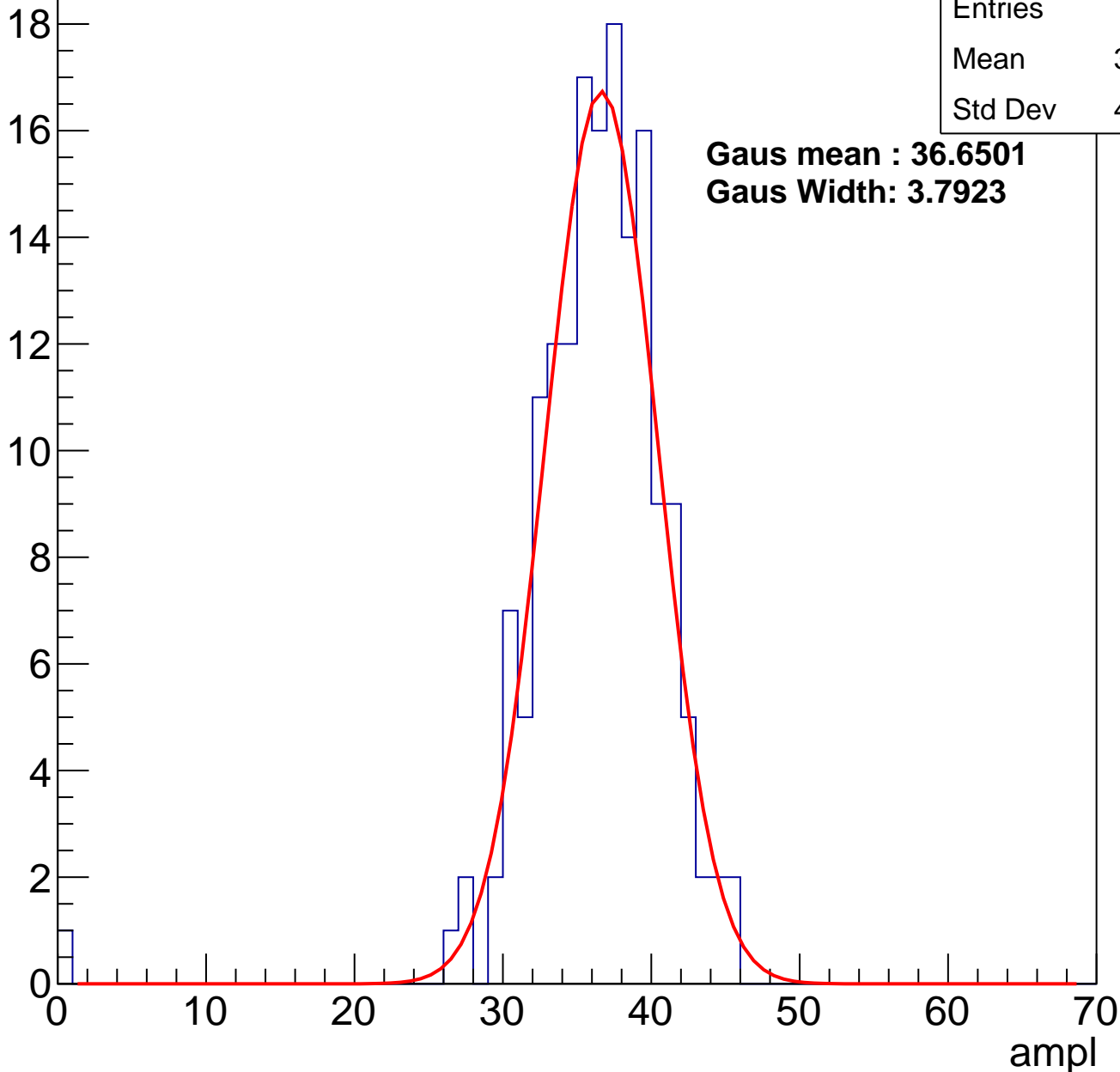
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 163   |
| Mean    | 35.92 |
| Std Dev | 4.658 |

**Gaus mean : 36.6501**

**Gaus Width: 3.7923**

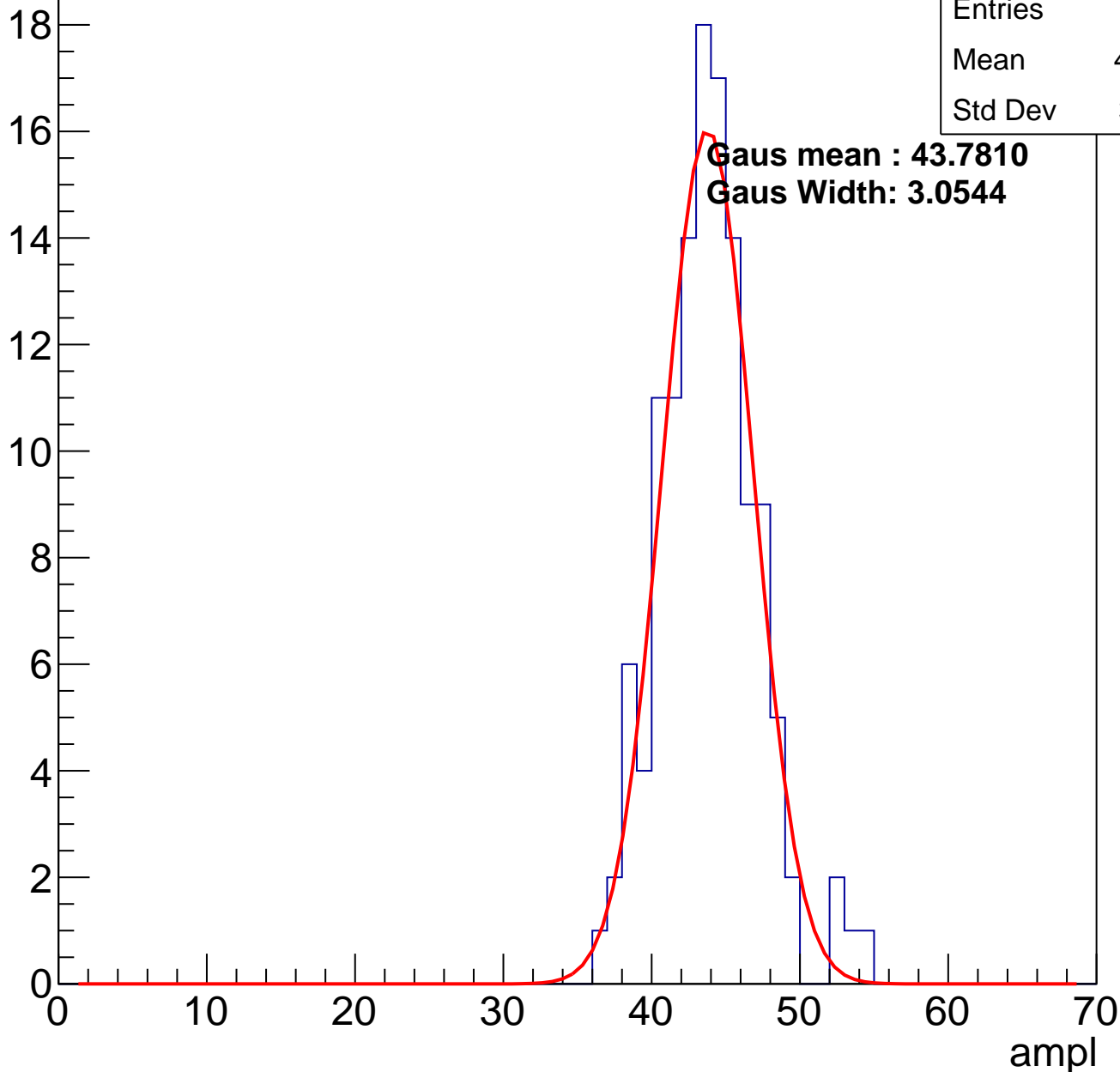
Entry



# B0L101S, U16-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

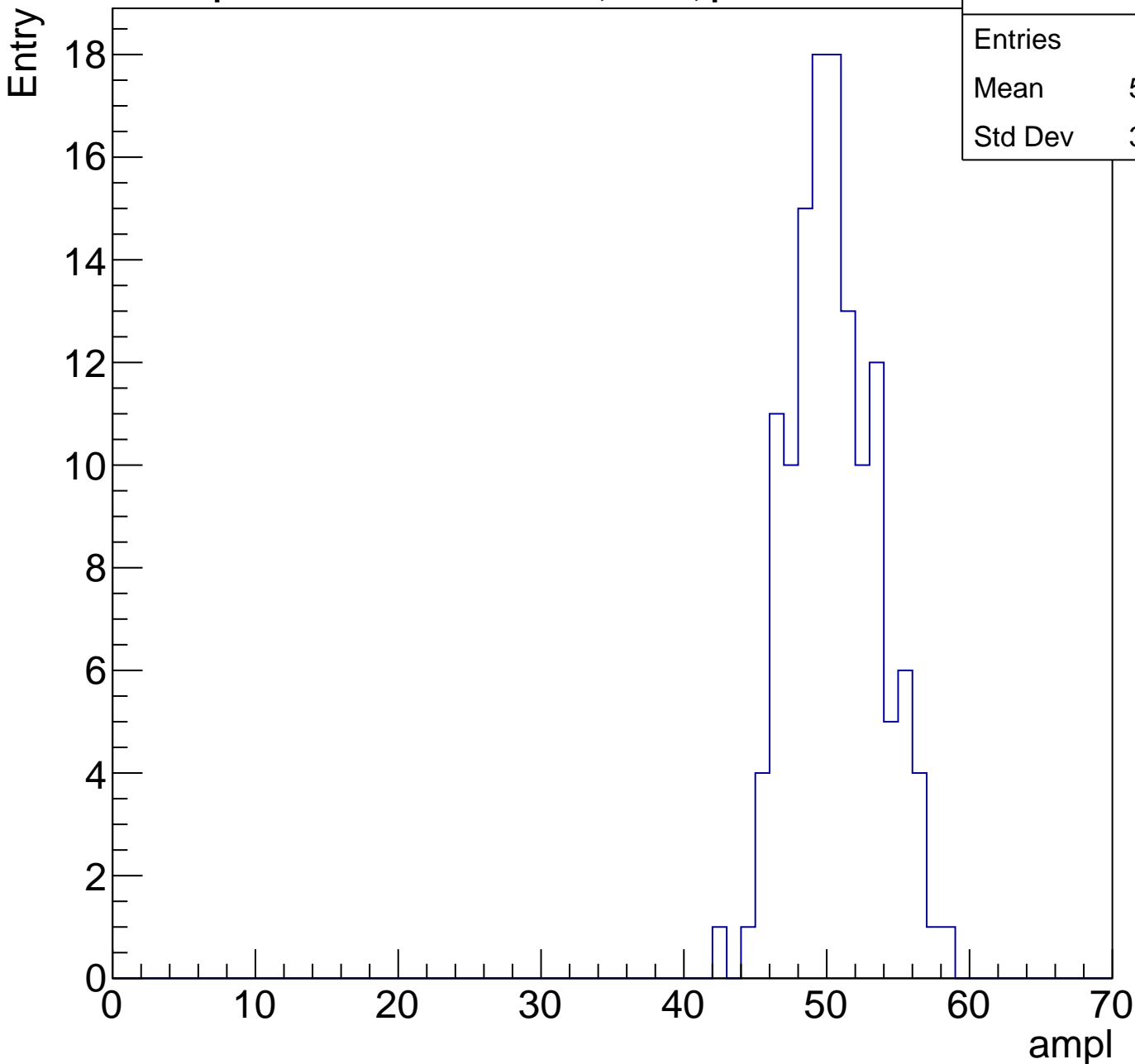
Entry



# B0L101S, U16-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 50.02 |
| Std Dev | 3.028 |

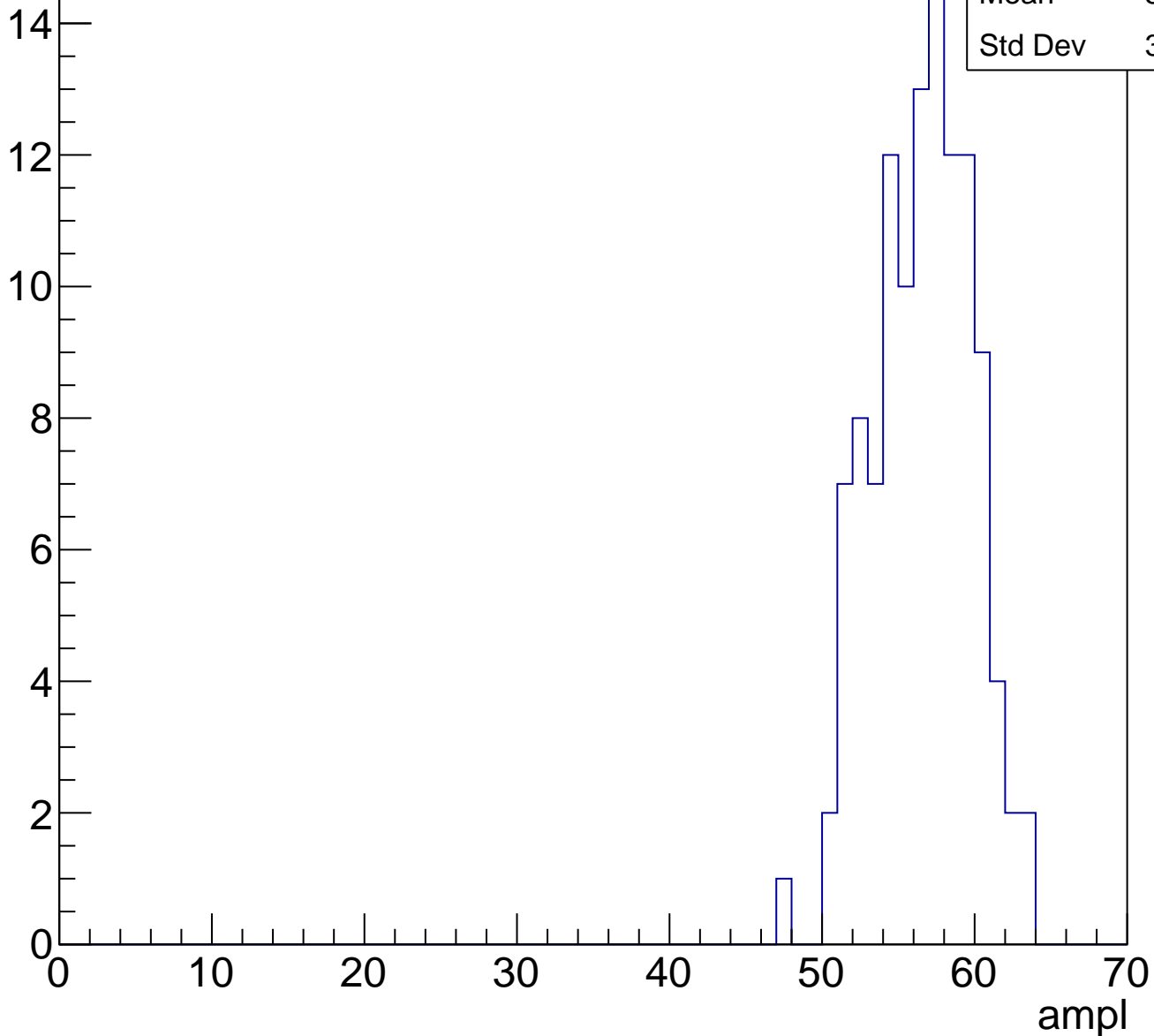


# B0L101S, U16-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 56.12 |
| Std Dev | 3.163 |

Entry



# B0L101S, U16-ch15, adc5

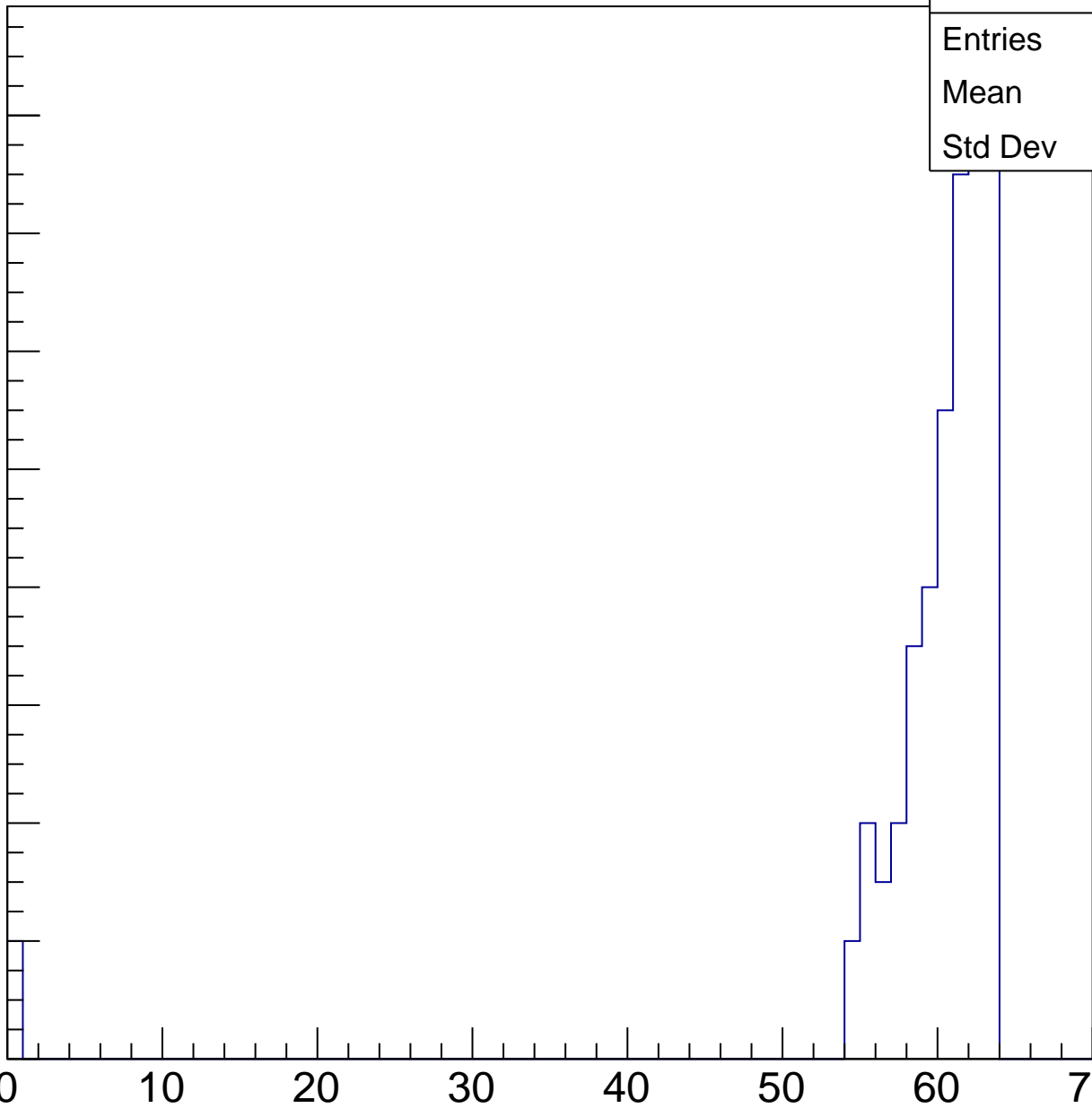
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 89    |
| Mean    | 58.87 |
| Std Dev | 9.244 |

ampl



# B0L101S, U16-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 4      |
| Mean    | 62.25  |
| Std Dev | 0.8292 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 6     |
| Std Dev | 8.485 |

# B0L101S, U16-ch16, adc0

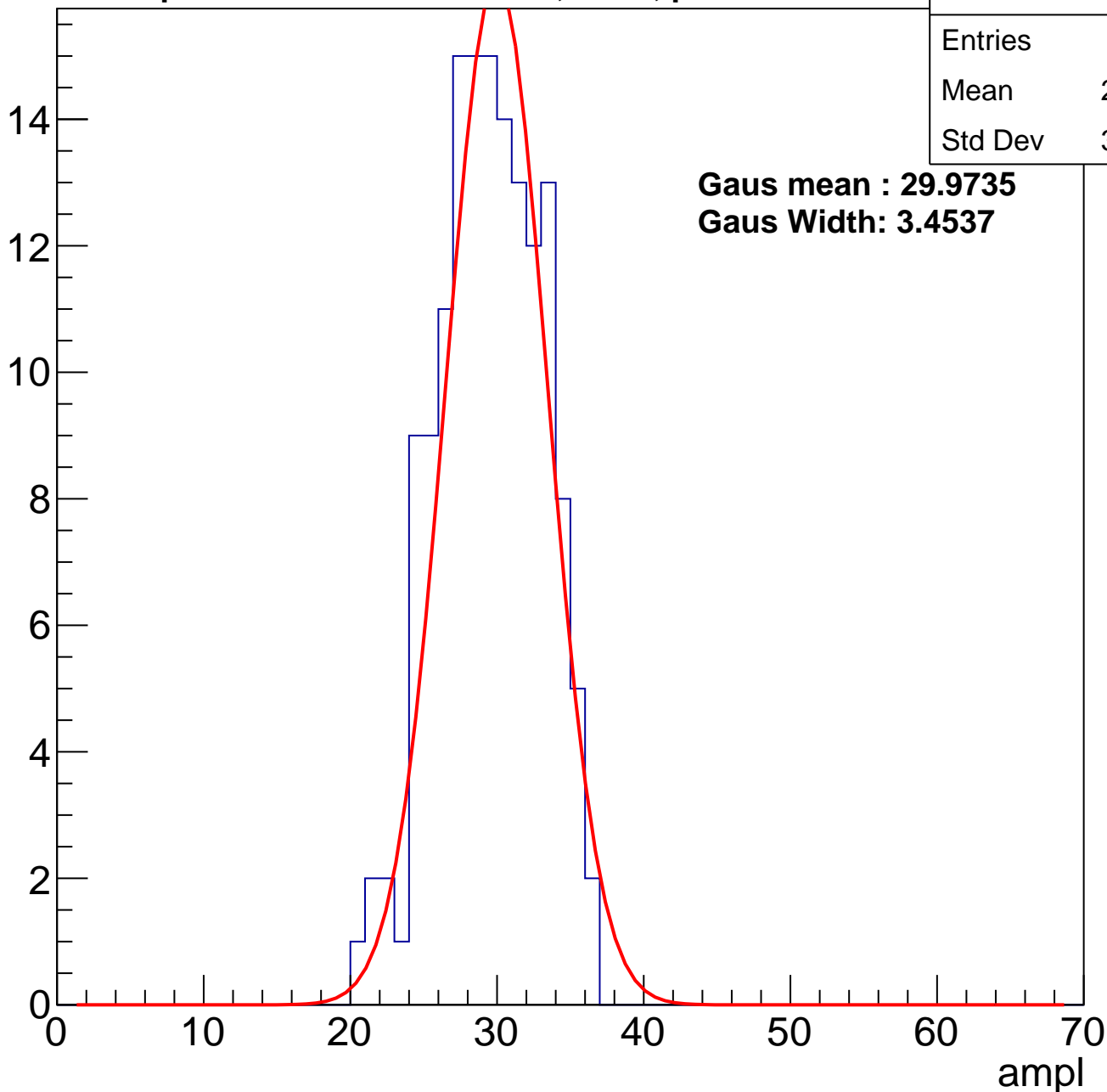
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 29.05 |
| Std Dev | 3.446 |

**Gaus mean : 29.9735**

**Gaus Width: 3.4537**

Entry



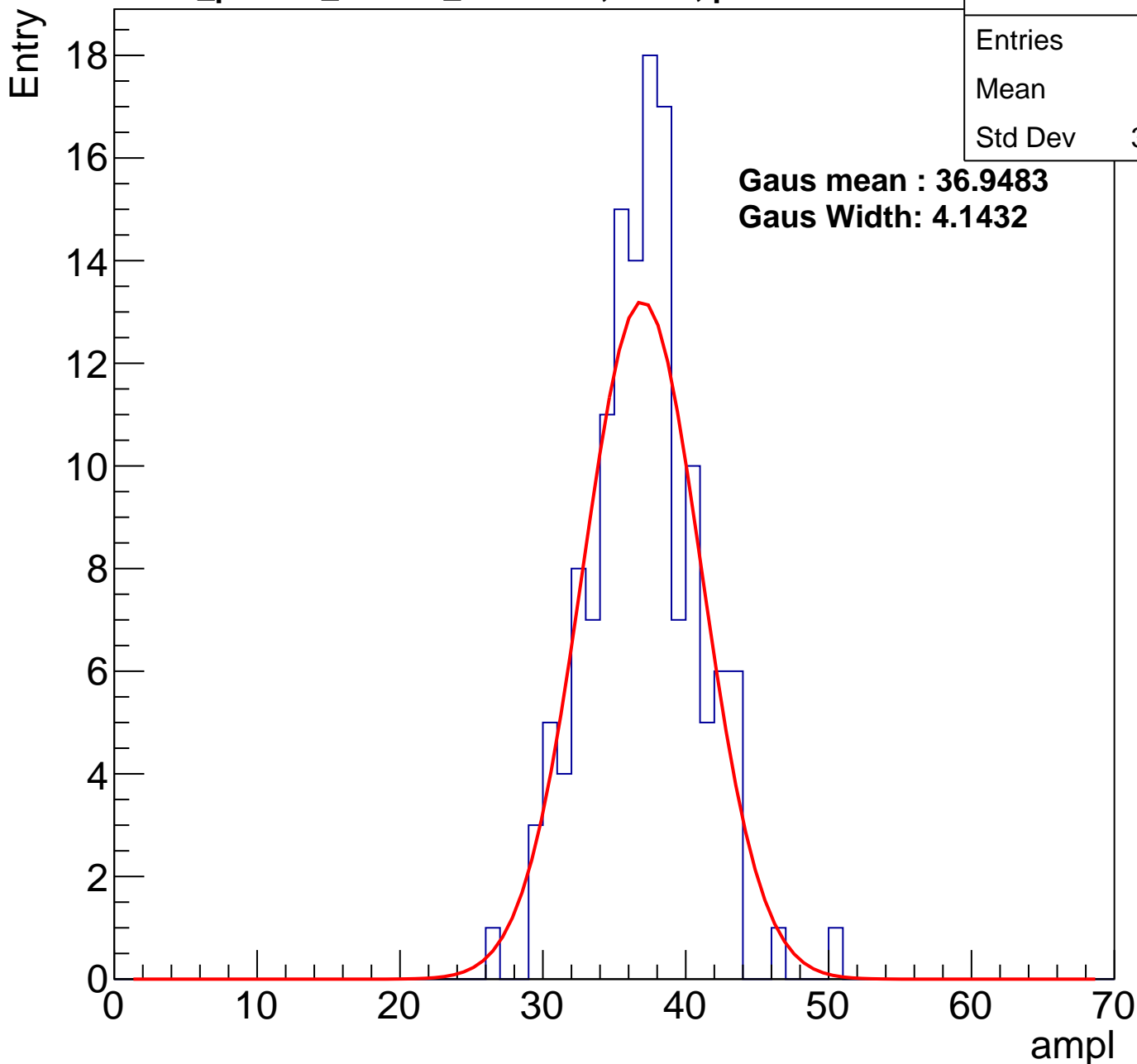
# B0L101S, U16-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 36.5  |
| Std Dev | 3.777 |

**Gaus mean : 36.9483**

**Gaus Width: 4.1432**



# B0L101S, U16-ch16, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 42.83 |
| Std Dev | 3.679 |

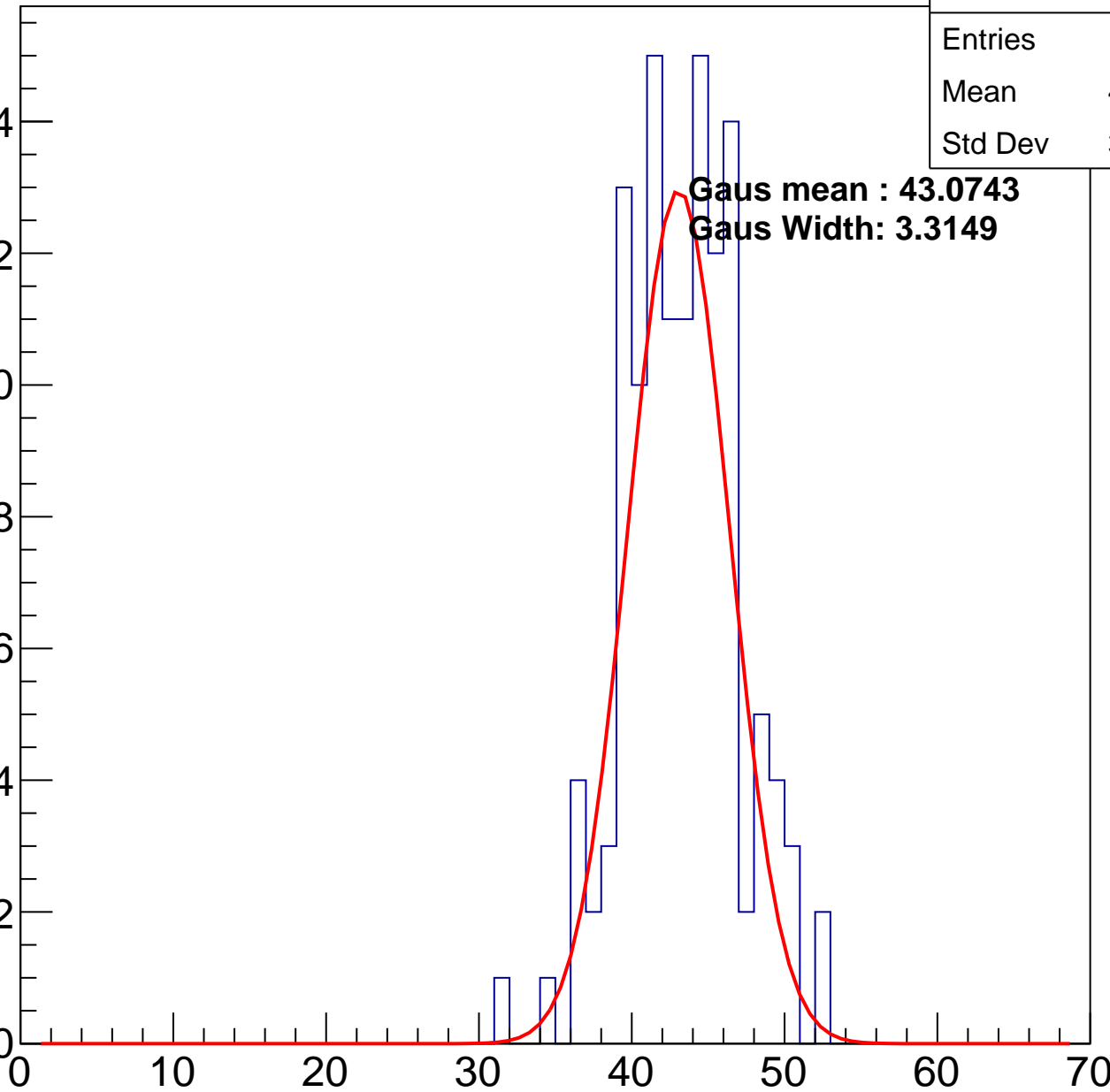
**Gaus mean : 43.0743**

**Gaus Width: 3.3149**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

133

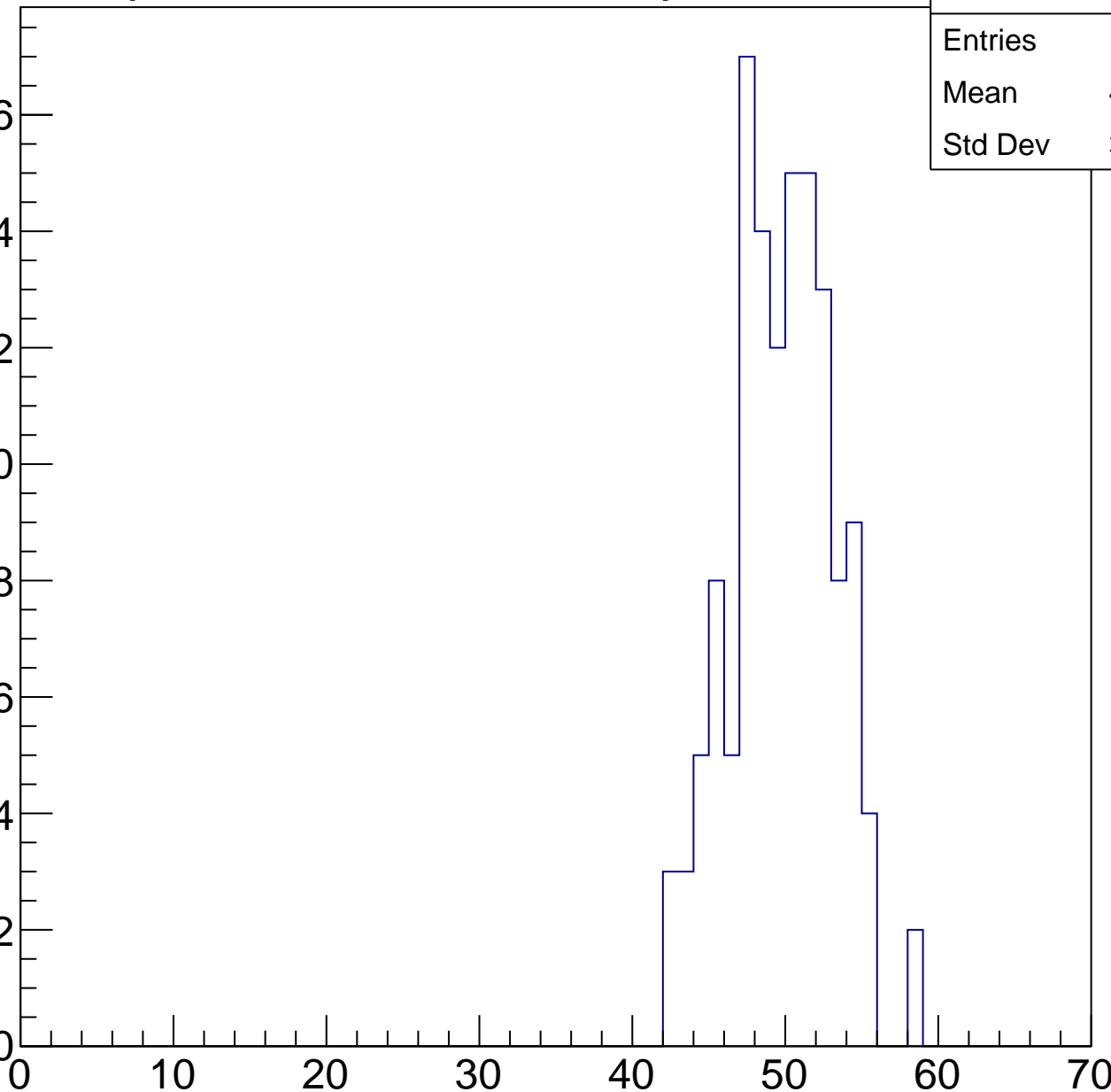
Mean

49.33

Std Dev

3.326

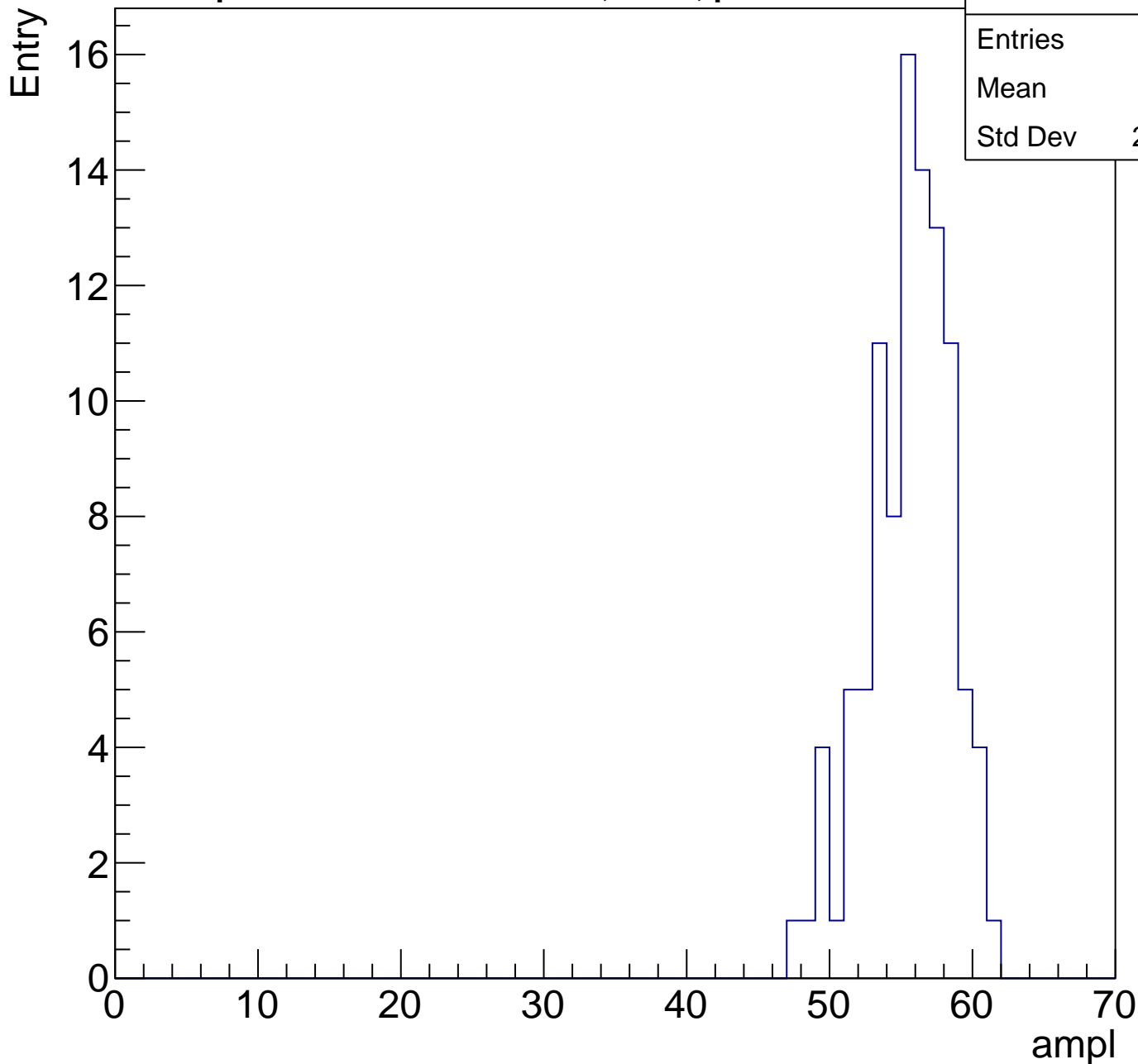
ampl



# B0L101S, U16-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 100   |
| Mean    | 55.1  |
| Std Dev | 2.897 |

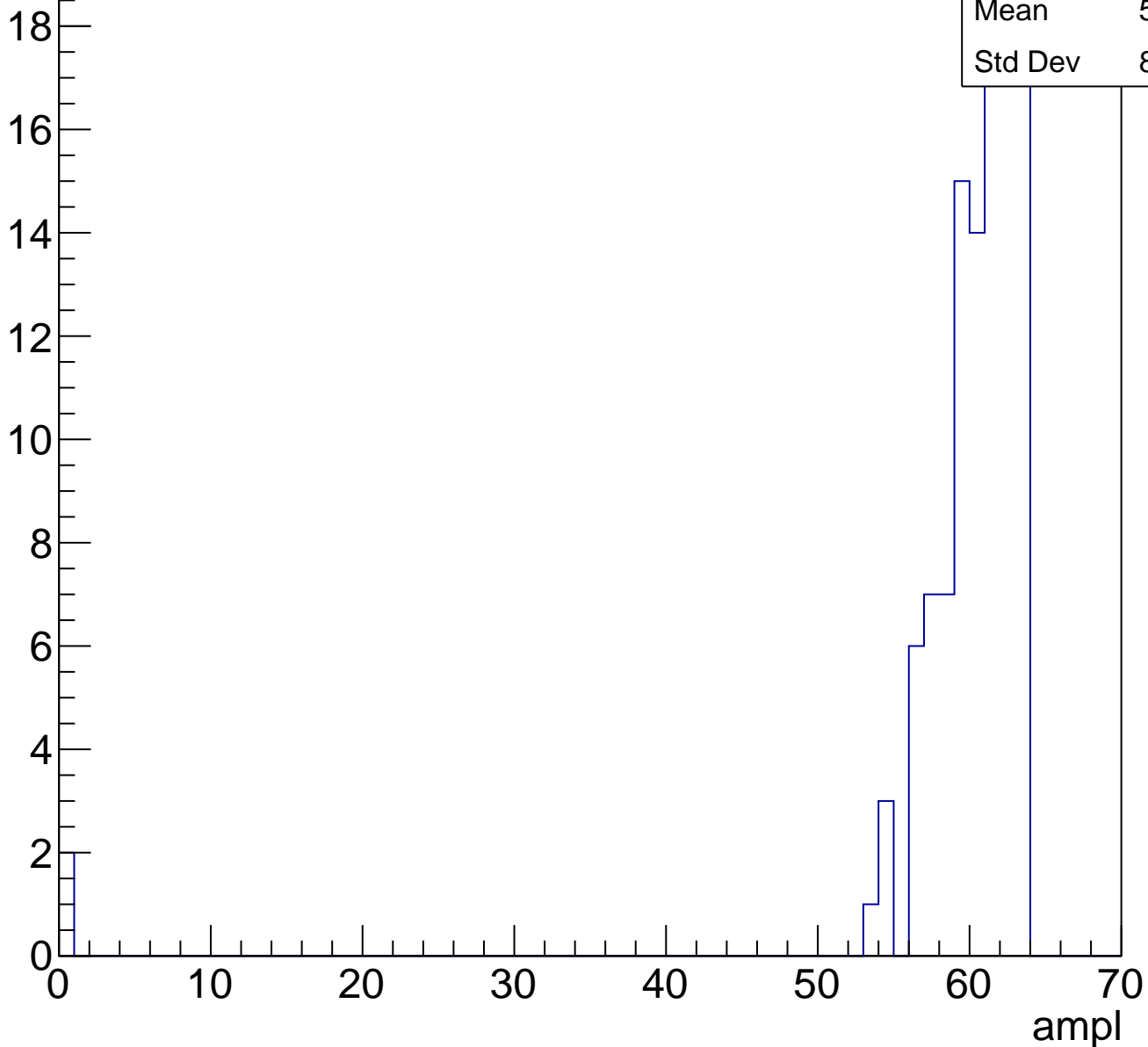


# B0L101S, U16-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 59.02 |
| Std Dev | 8.374 |

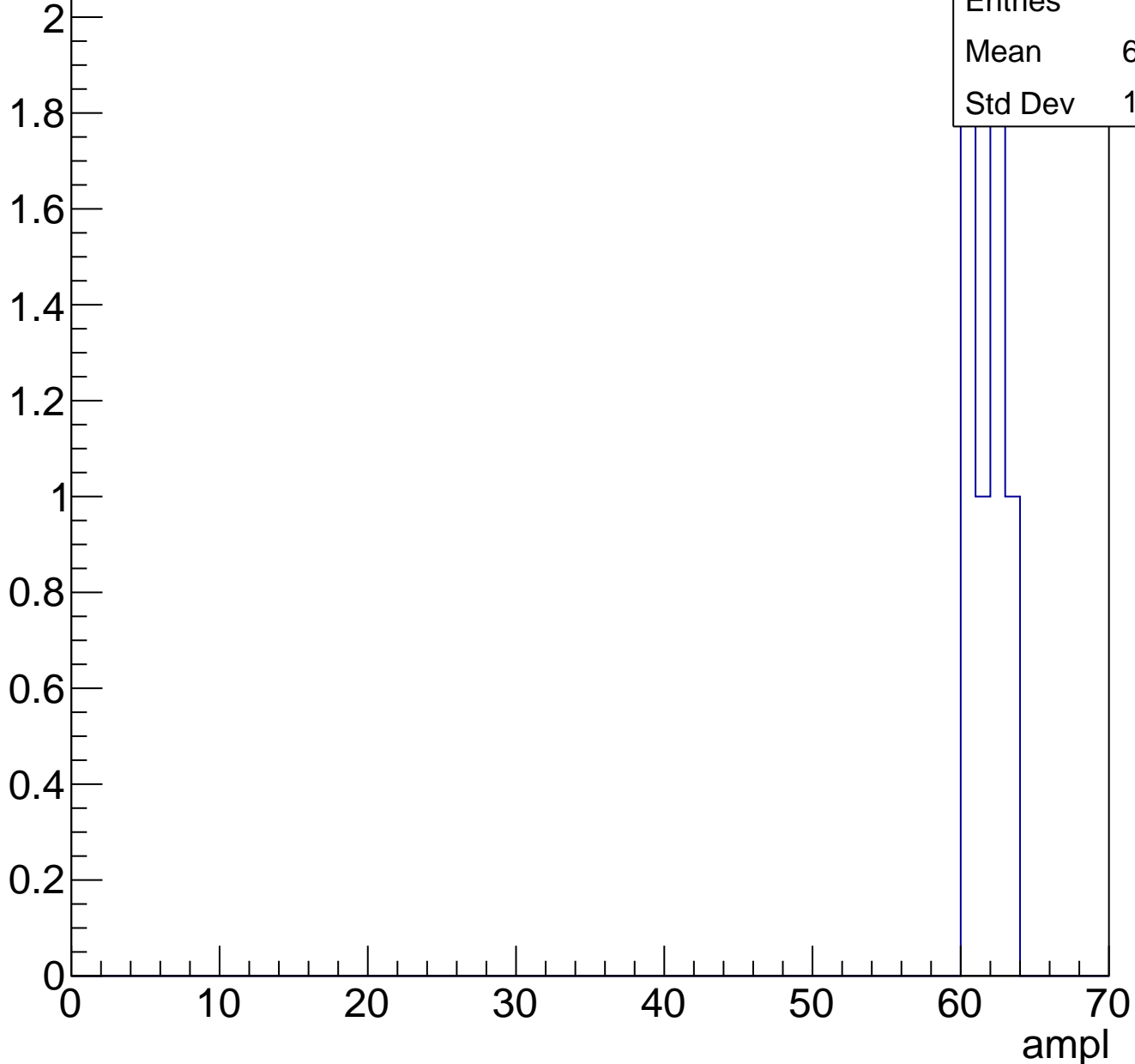
Entry



# B0L101S, U16-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

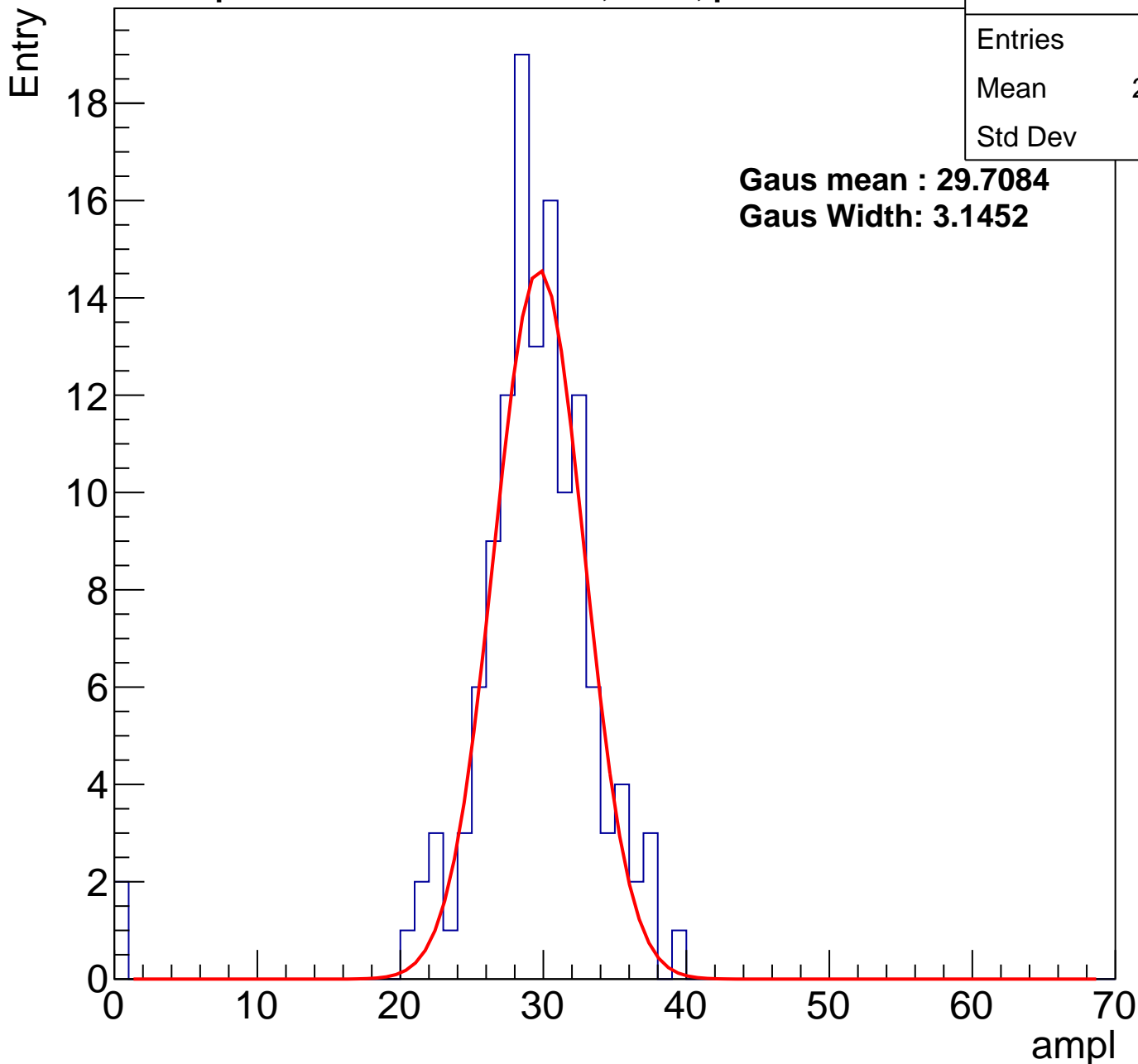
# B0L101S, U16-ch17, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 28.72 |
| Std Dev | 5.04  |

**Gaus mean : 29.7084**

**Gaus Width: 3.1452**



# B0L101S, U16-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 35.82 |
| Std Dev | 3.24  |

**Gaus mean : 36.0937**

**Gaus Width: 2.9213**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 42.79 |
| Std Dev | 3.542 |

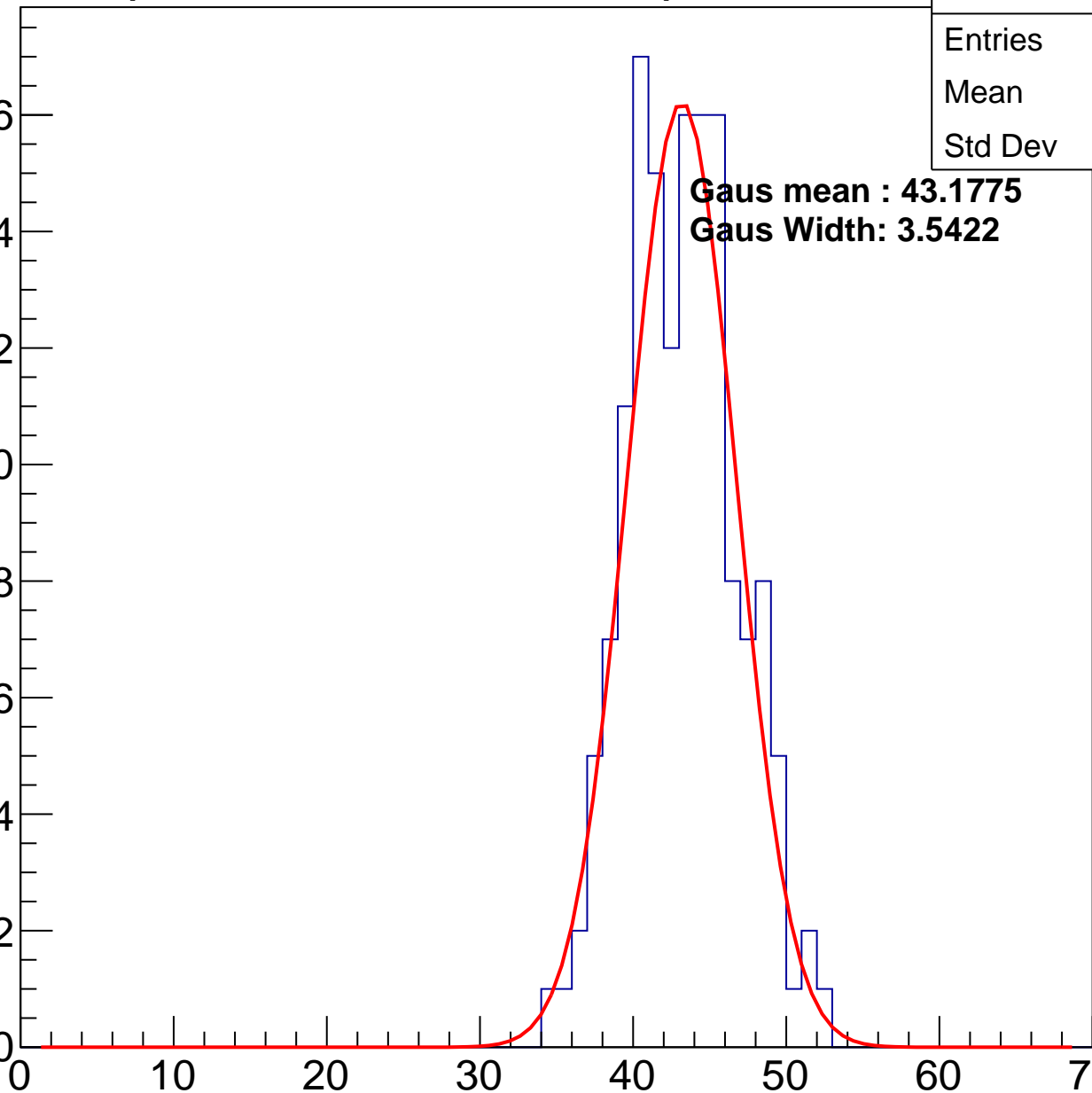
**Gaus mean : 43.1775**

**Gaus Width: 3.5422**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

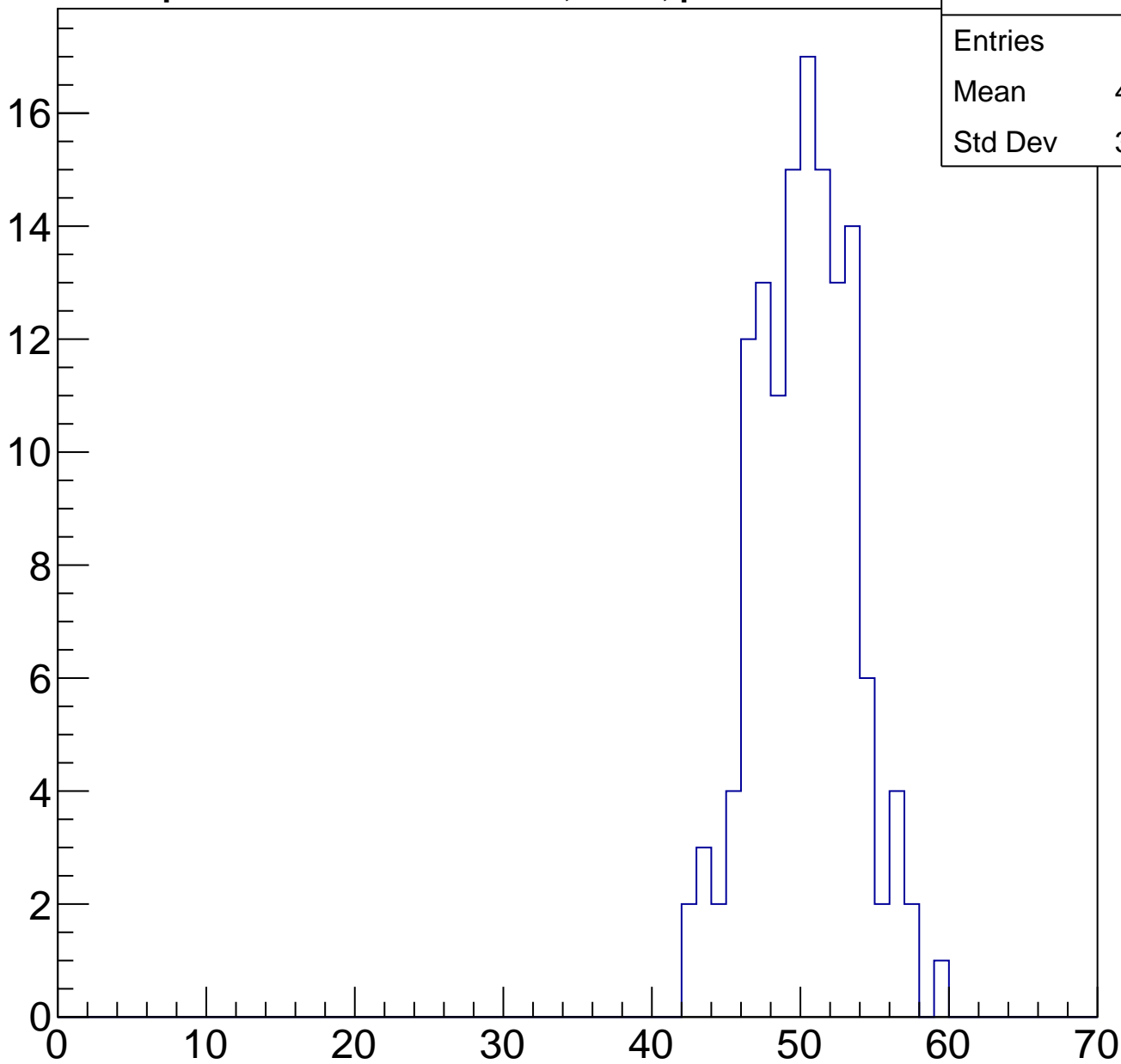
|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 49.79 |
| Std Dev | 3.289 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

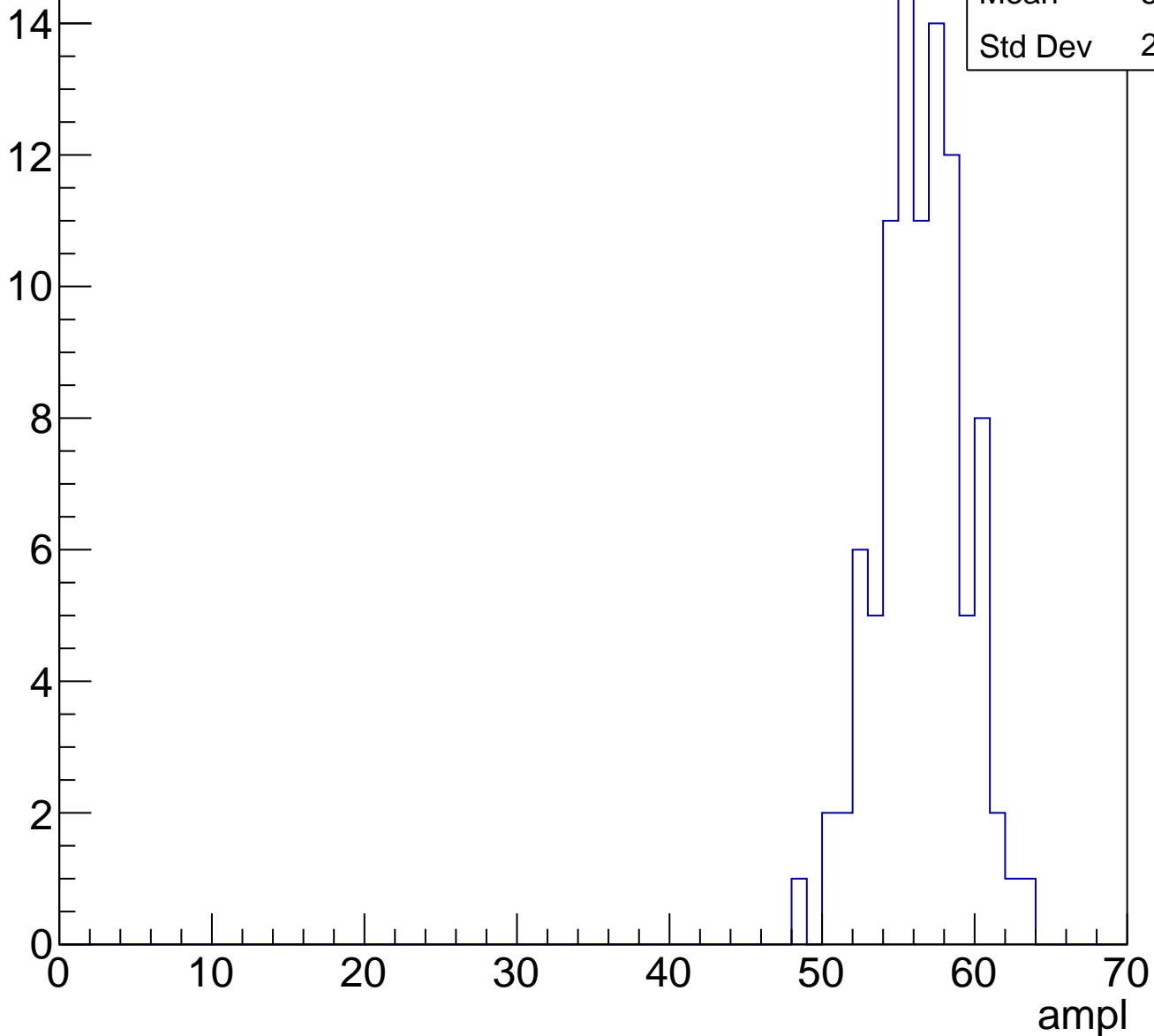


# B0L101S, U16-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 96    |
| Mean    | 56.02 |
| Std Dev | 2.806 |

Entry



# B0L101S, U16-ch17, adc5

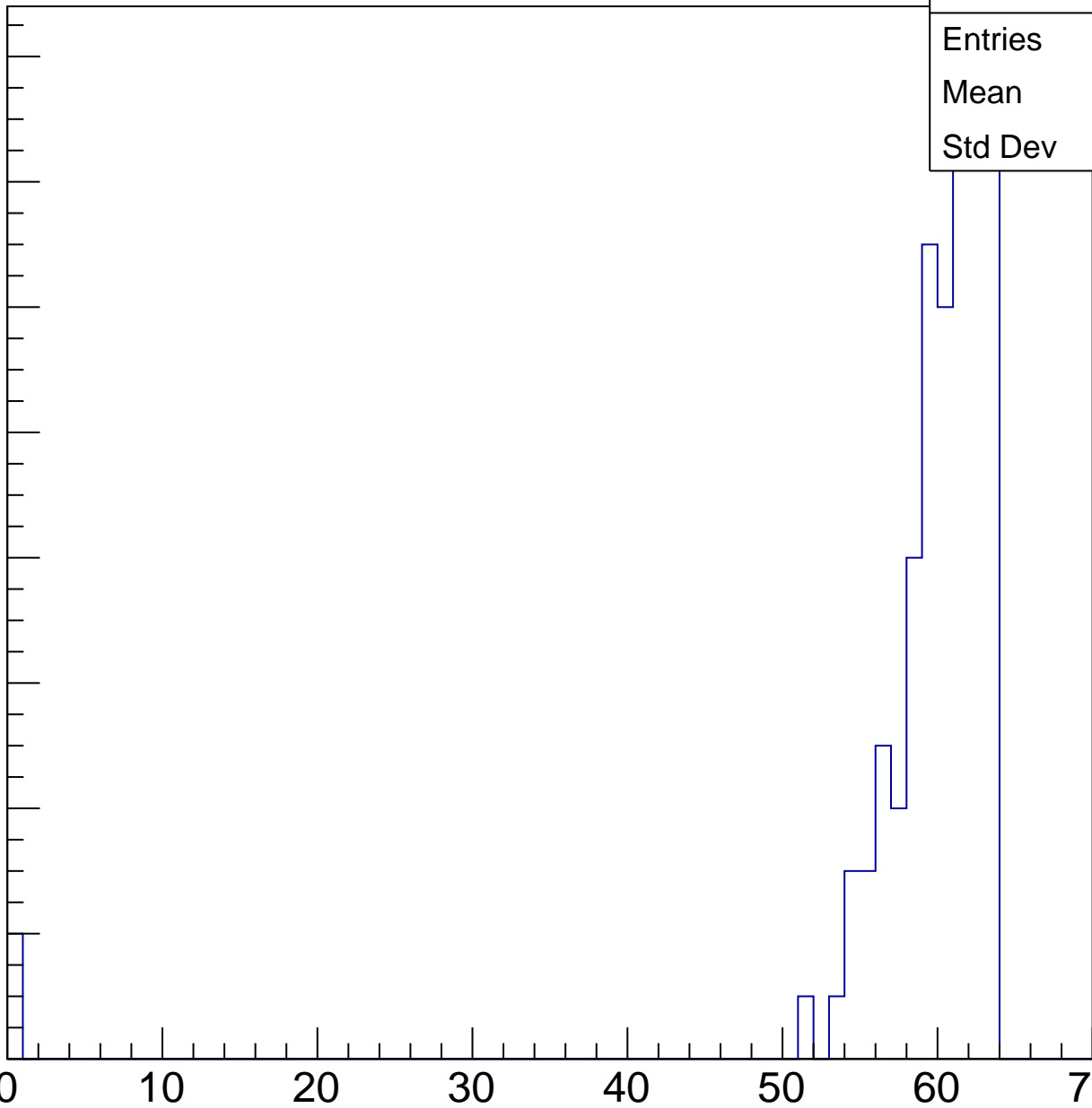
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 98    |
| Mean    | 58.58 |
| Std Dev | 8.857 |

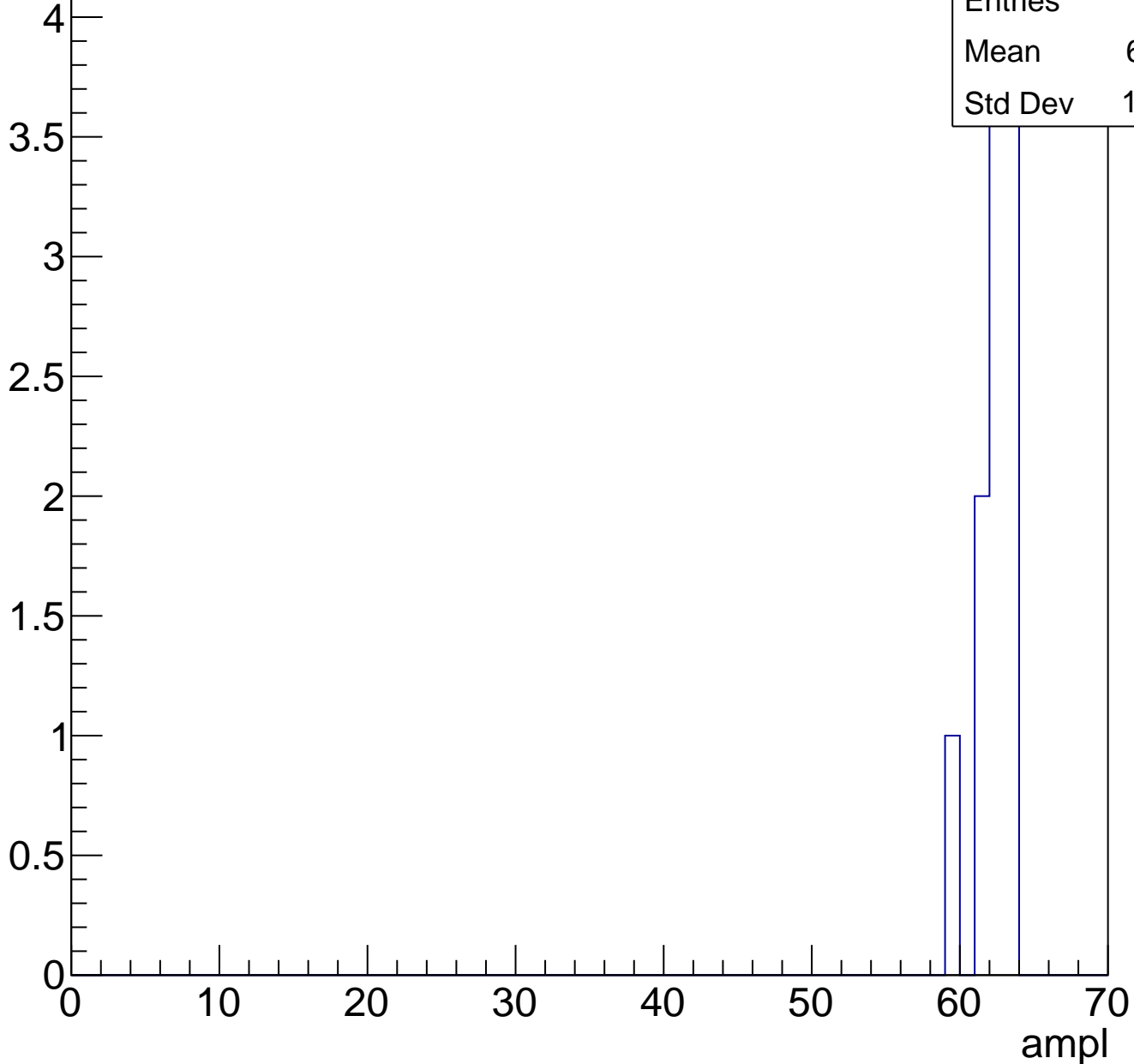
ampl



# B0L101S, U16-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch18, adc0

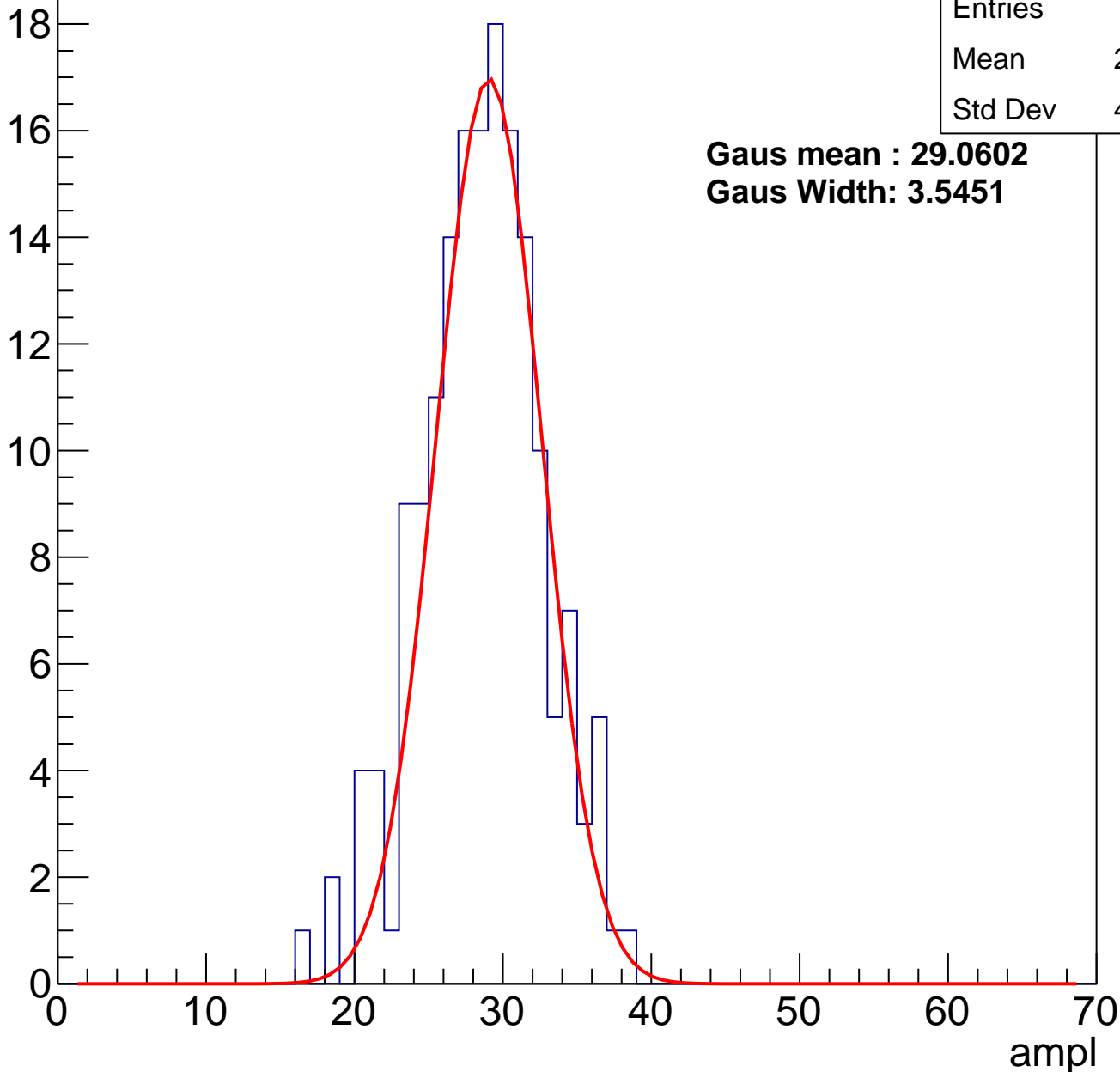
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 167   |
| Mean    | 28.14 |
| Std Dev | 4.067 |

**Gaus mean : 29.0602**

**Gaus Width: 3.5451**

Entry



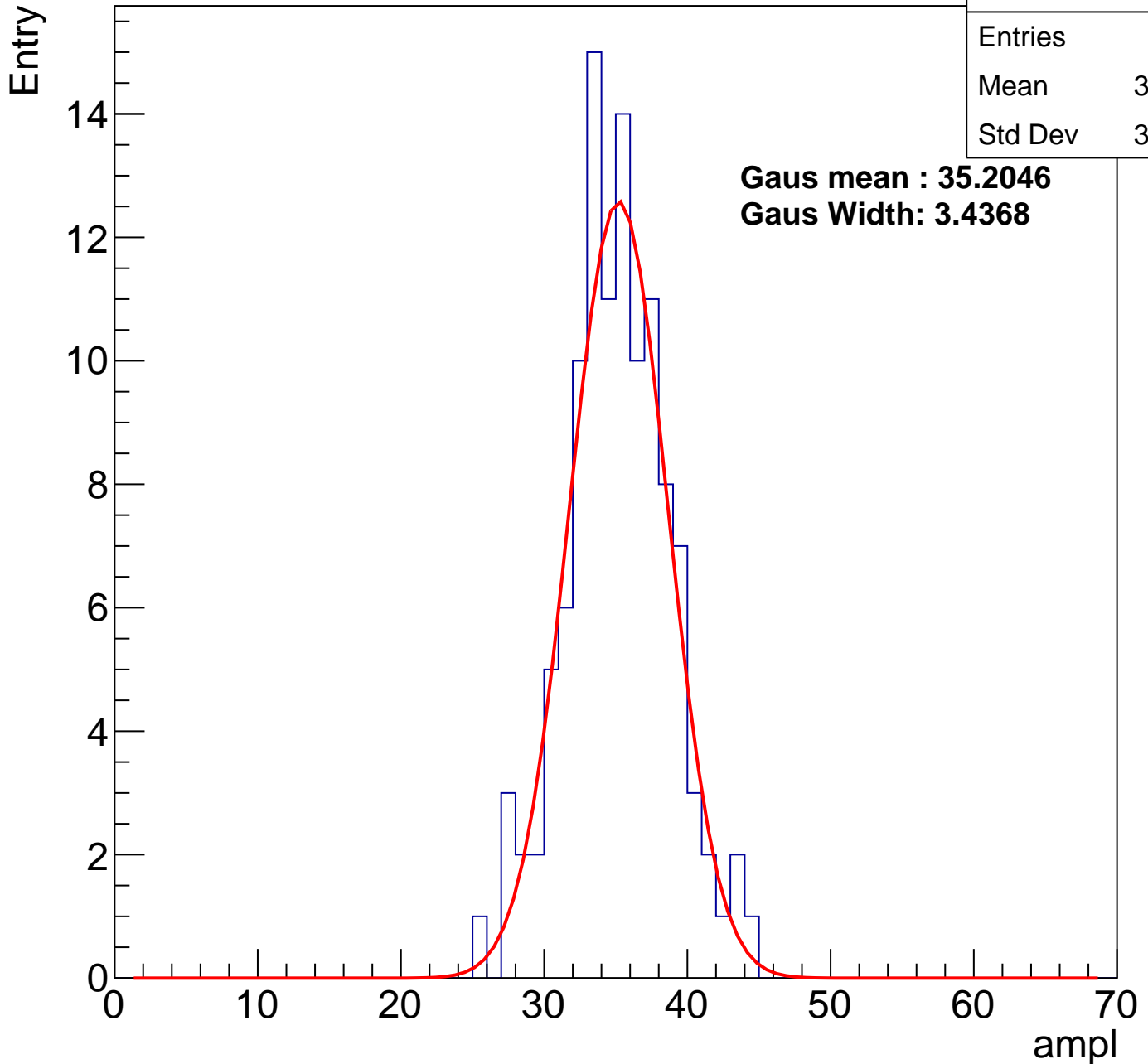
# B0L101S, U16-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 34.68 |
| Std Dev | 3.597 |

**Gaus mean : 35.2046**

**Gaus Width: 3.4368**



# B0L101S, U16-ch18, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 41.27 |
| Std Dev | 3.354 |

**Gaus mean : 41.7301**

**Gaus Width: 3.6188**

0

10

20

30

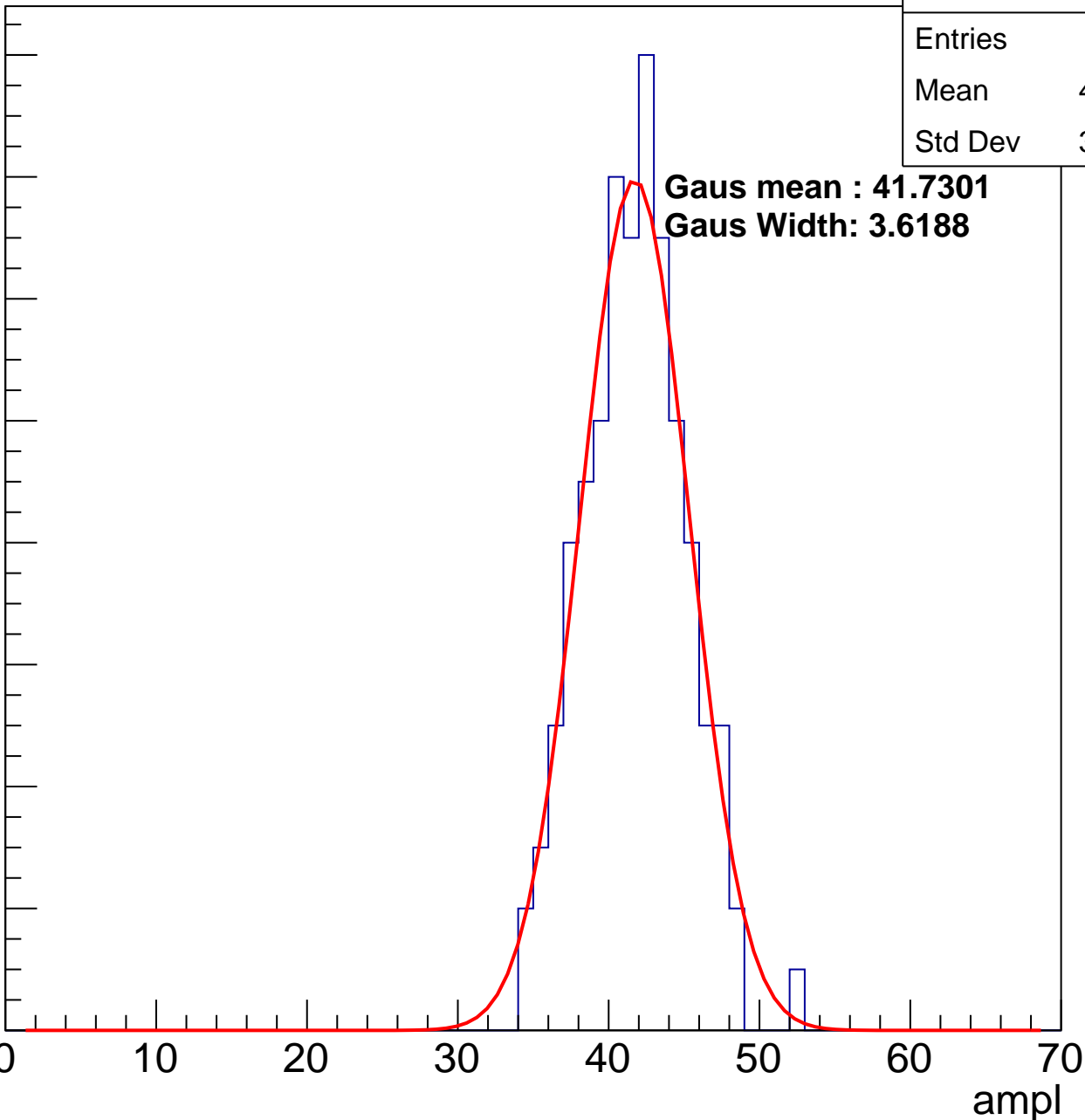
40

50

60

70

ampl

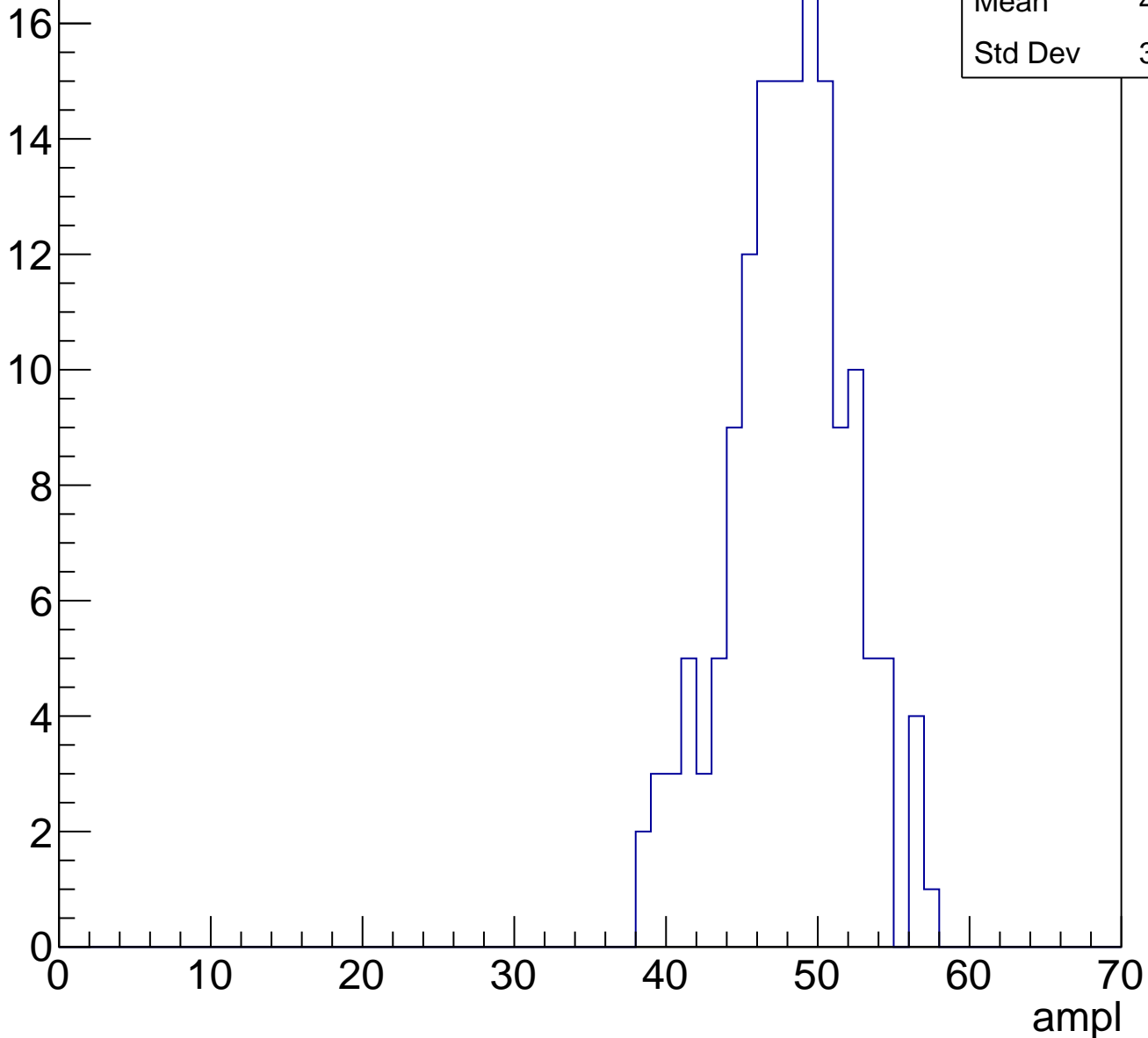


# B0L101S, U16-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 153   |
| Mean    | 47.63 |
| Std Dev | 3.944 |



# B0L101S, U16-ch18, adc4

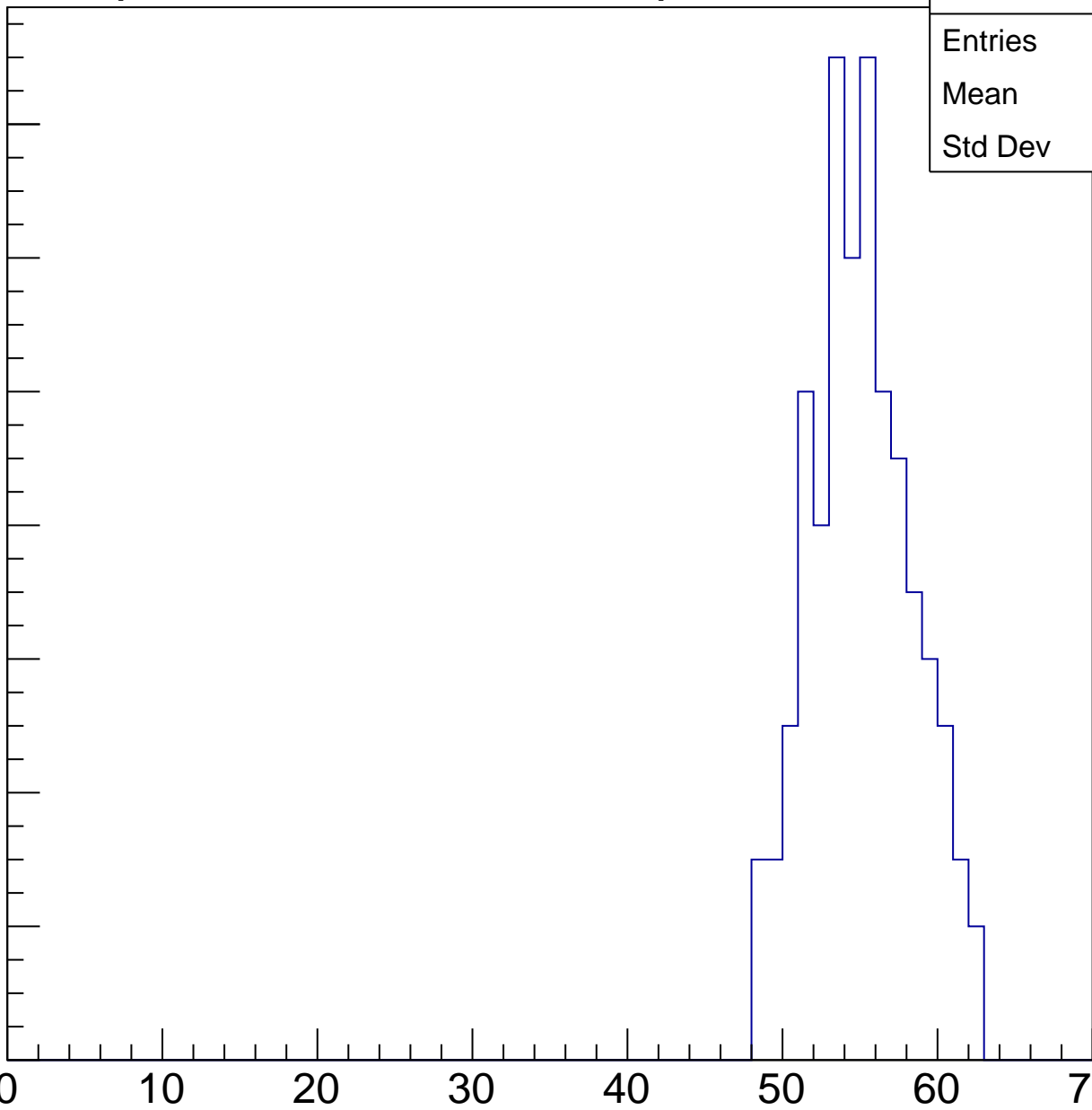
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 54.65 |
| Std Dev | 3.267 |

ampl



# B0L101S, U16-ch18, adc5

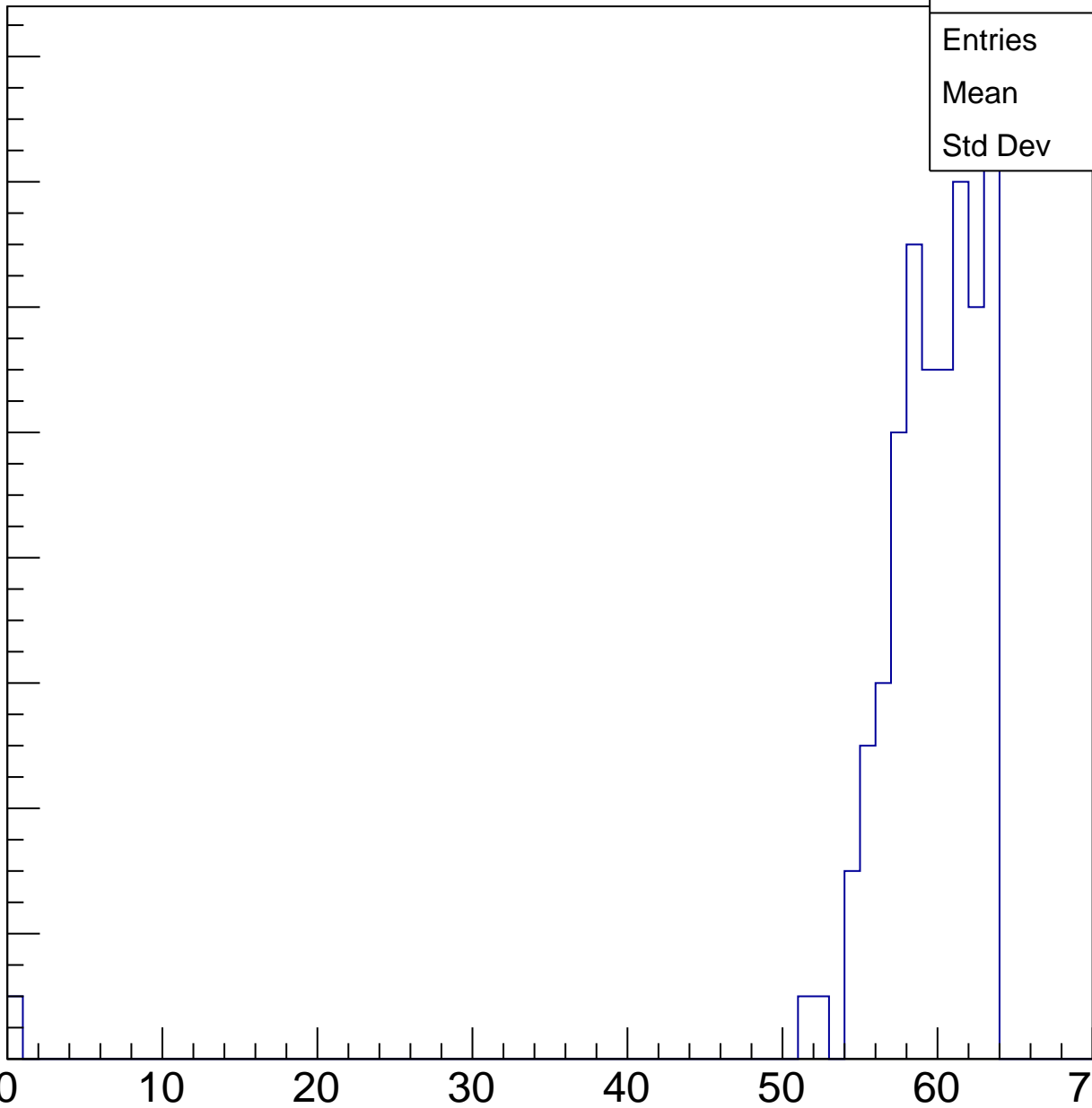
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 58.8  |
| Std Dev | 6.417 |

ampl

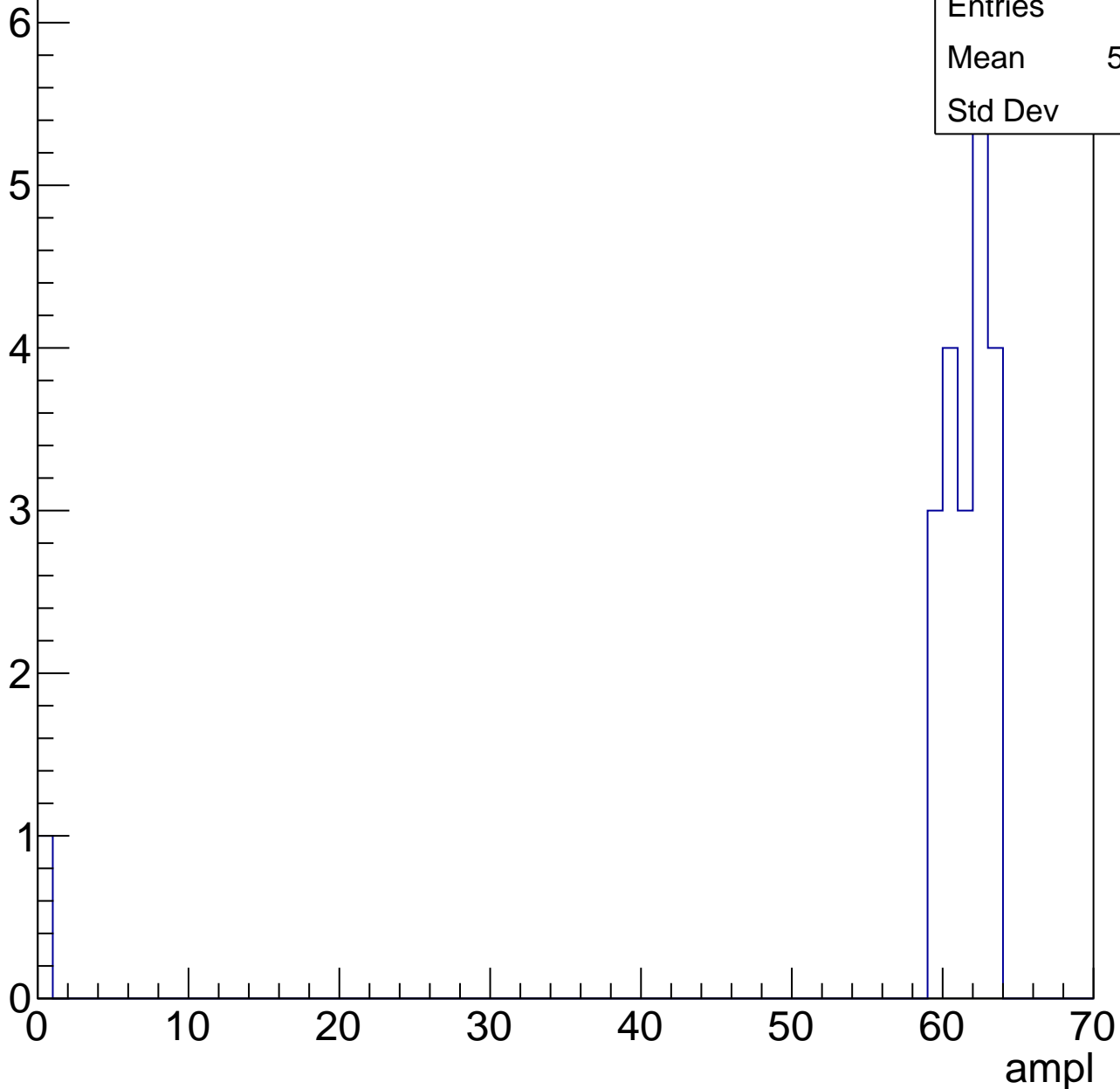


# B0L101S, U16-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 58.29 |
| Std Dev | 13.1  |





# B0L101S, U16-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch19, adc0

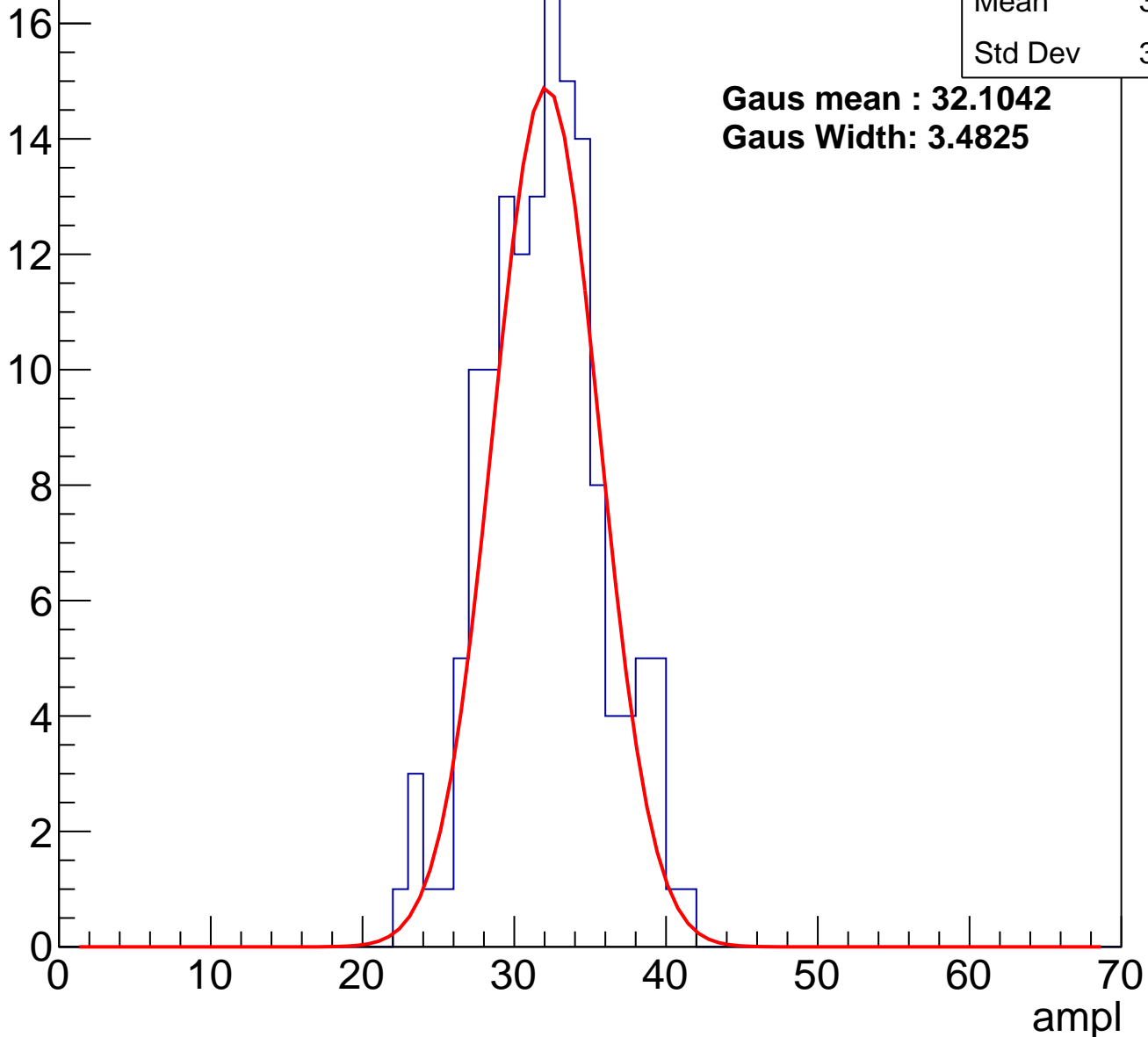
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 31.56 |
| Std Dev | 3.808 |

**Gaus mean : 32.1042**

**Gaus Width: 3.4825**

Entry



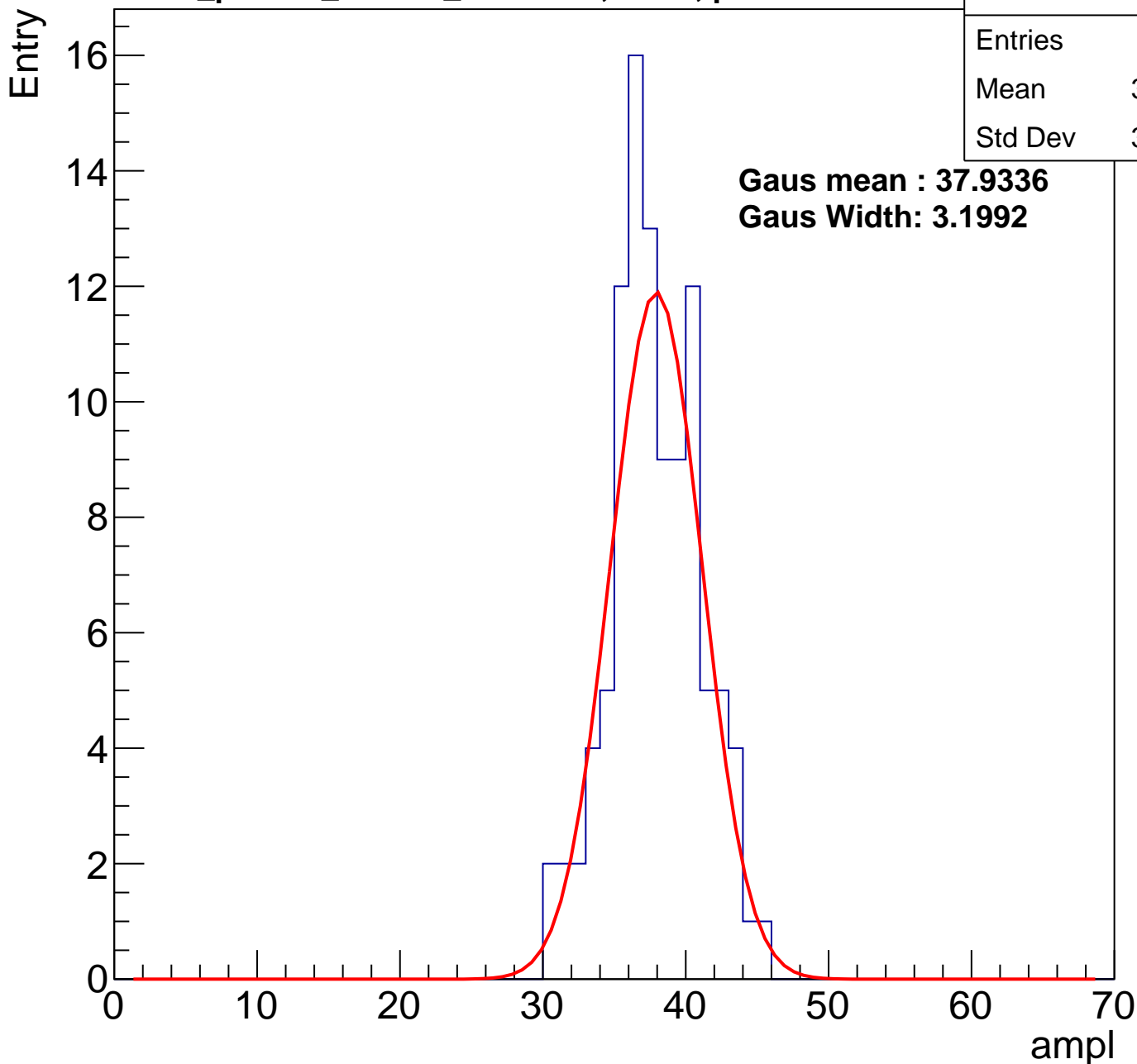
# B0L101S, U16-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 102   |
| Mean    | 37.39 |
| Std Dev | 3.119 |

**Gaus mean : 37.9336**

**Gaus Width: 3.1992**



# B0L101S, U16-ch19, adc2

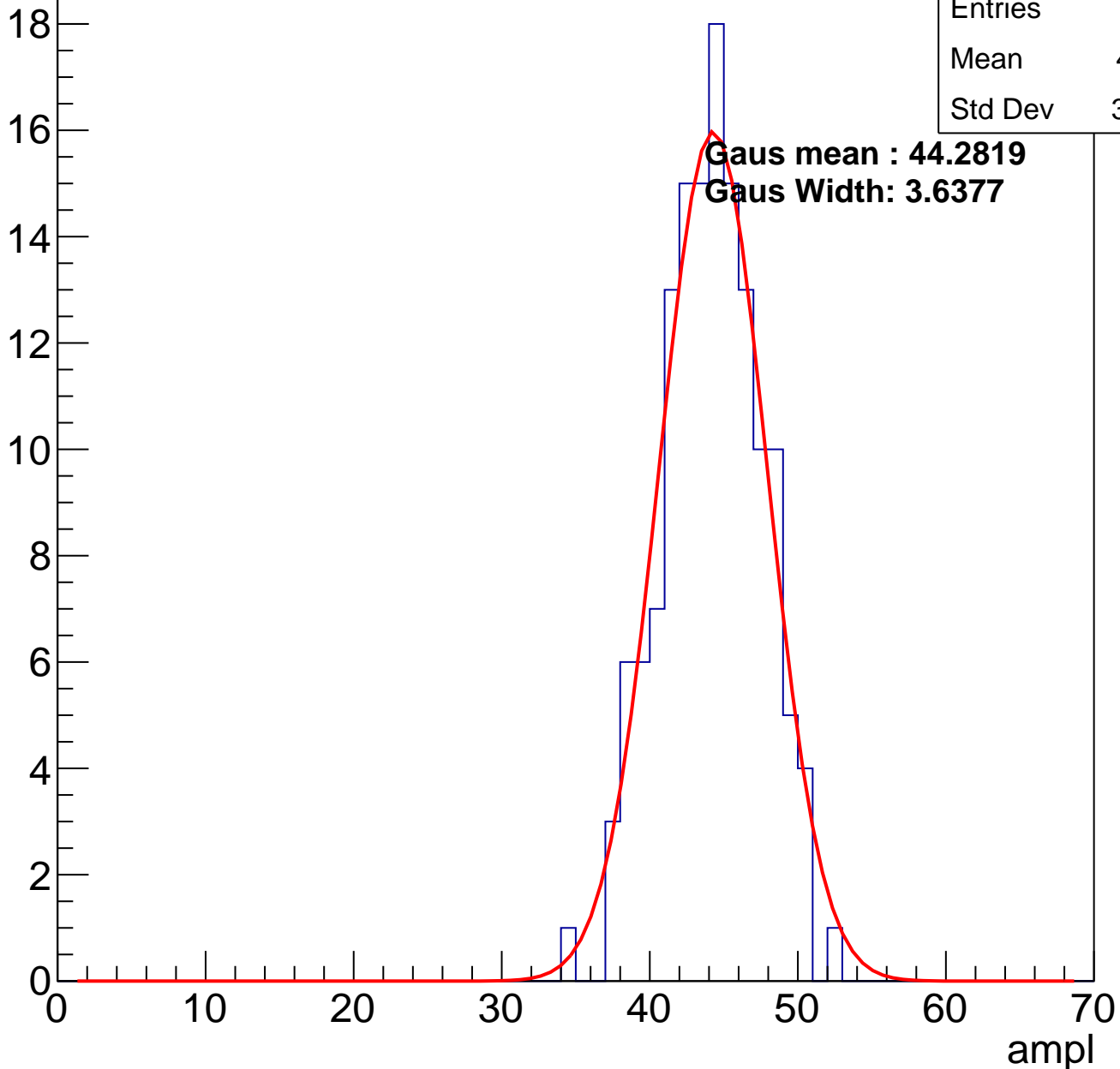
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 43.71 |
| Std Dev | 3.309 |

**Gaus mean : 44.2819**

**Gaus Width: 3.6377**

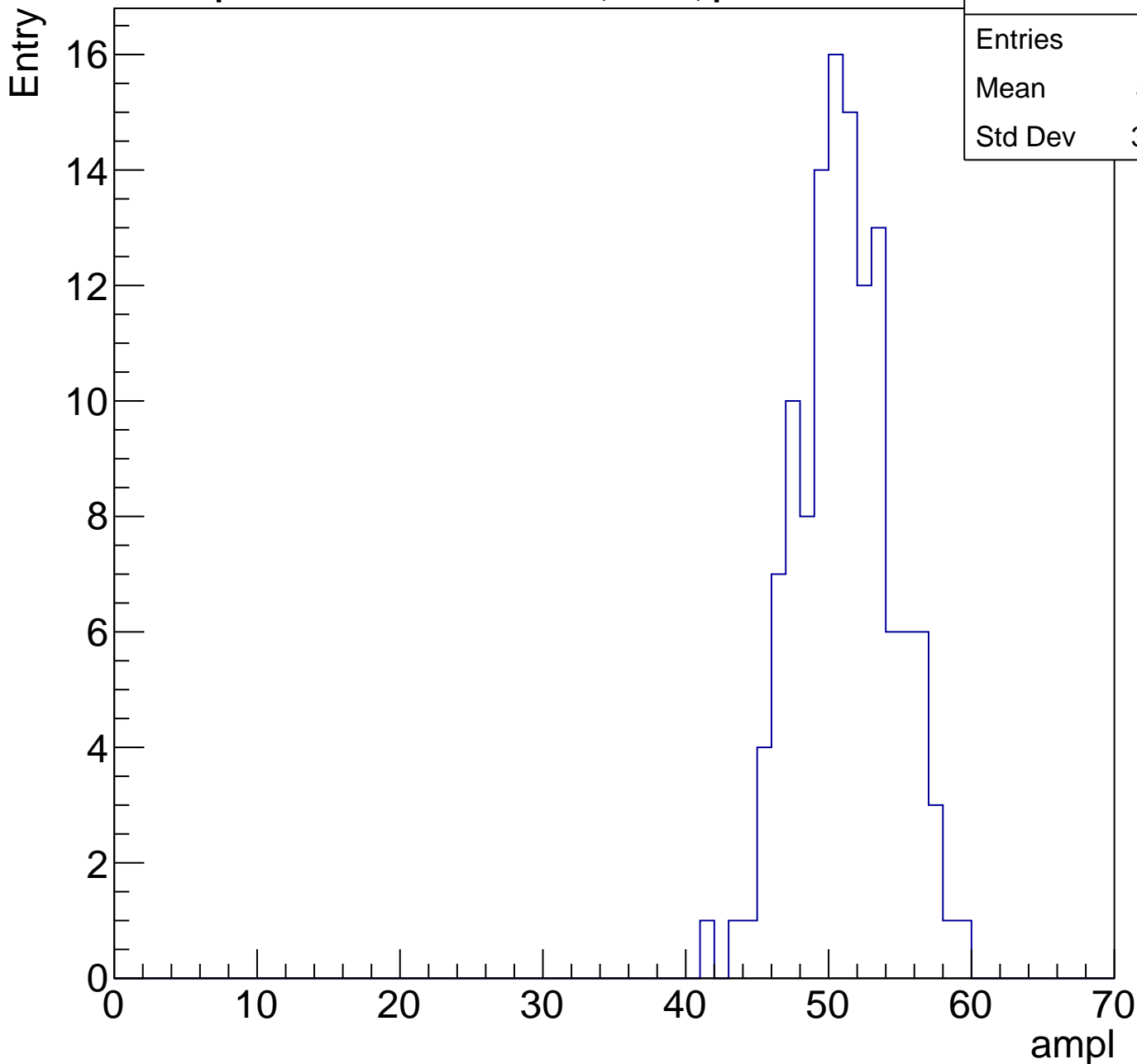
Entry



# B0L101S, U16-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 50.61 |
| Std Dev | 3.349 |



# B0L101S, U16-ch19, adc4

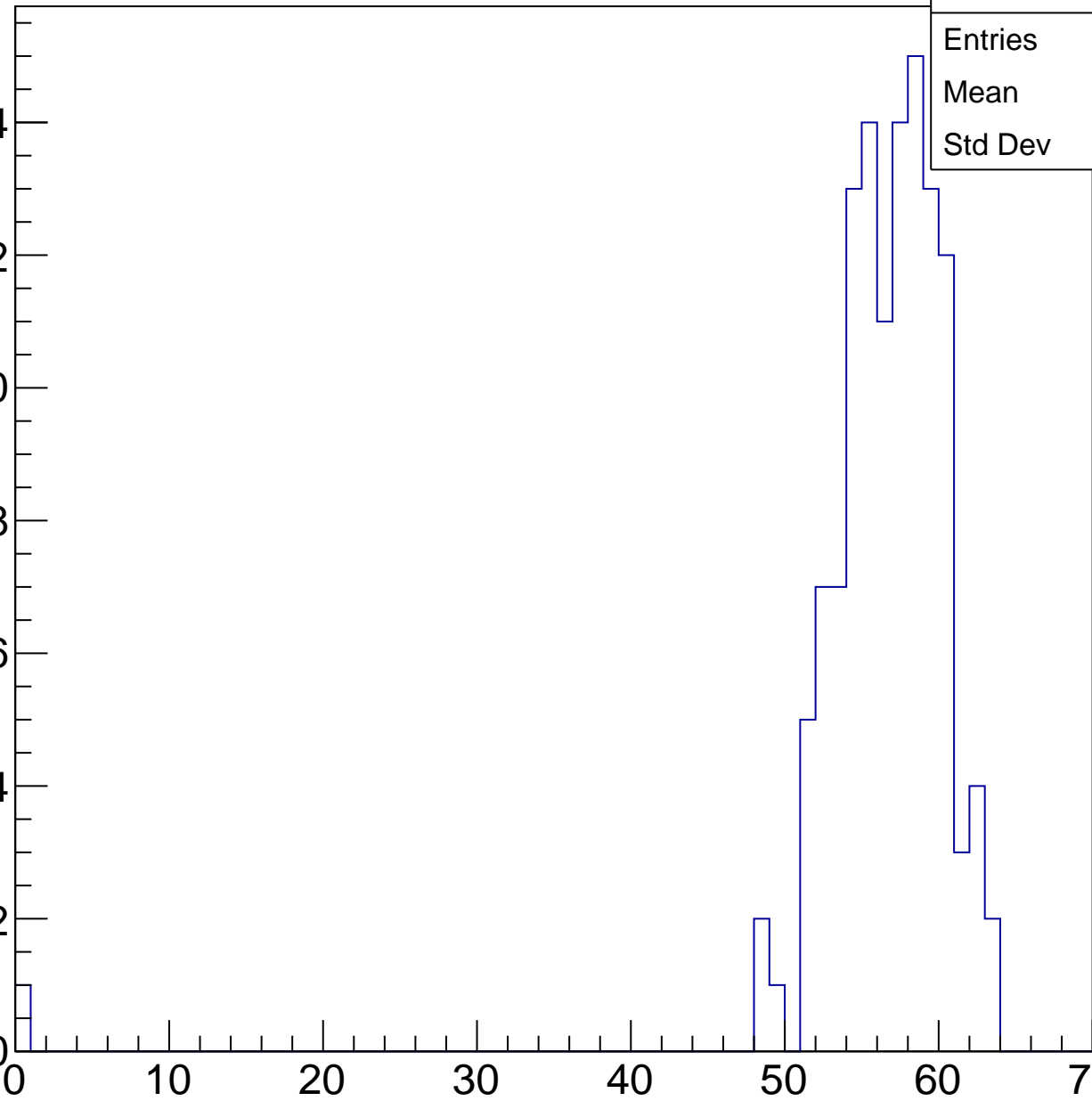
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 55.93 |
| Std Dev | 5.942 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch19, adc5

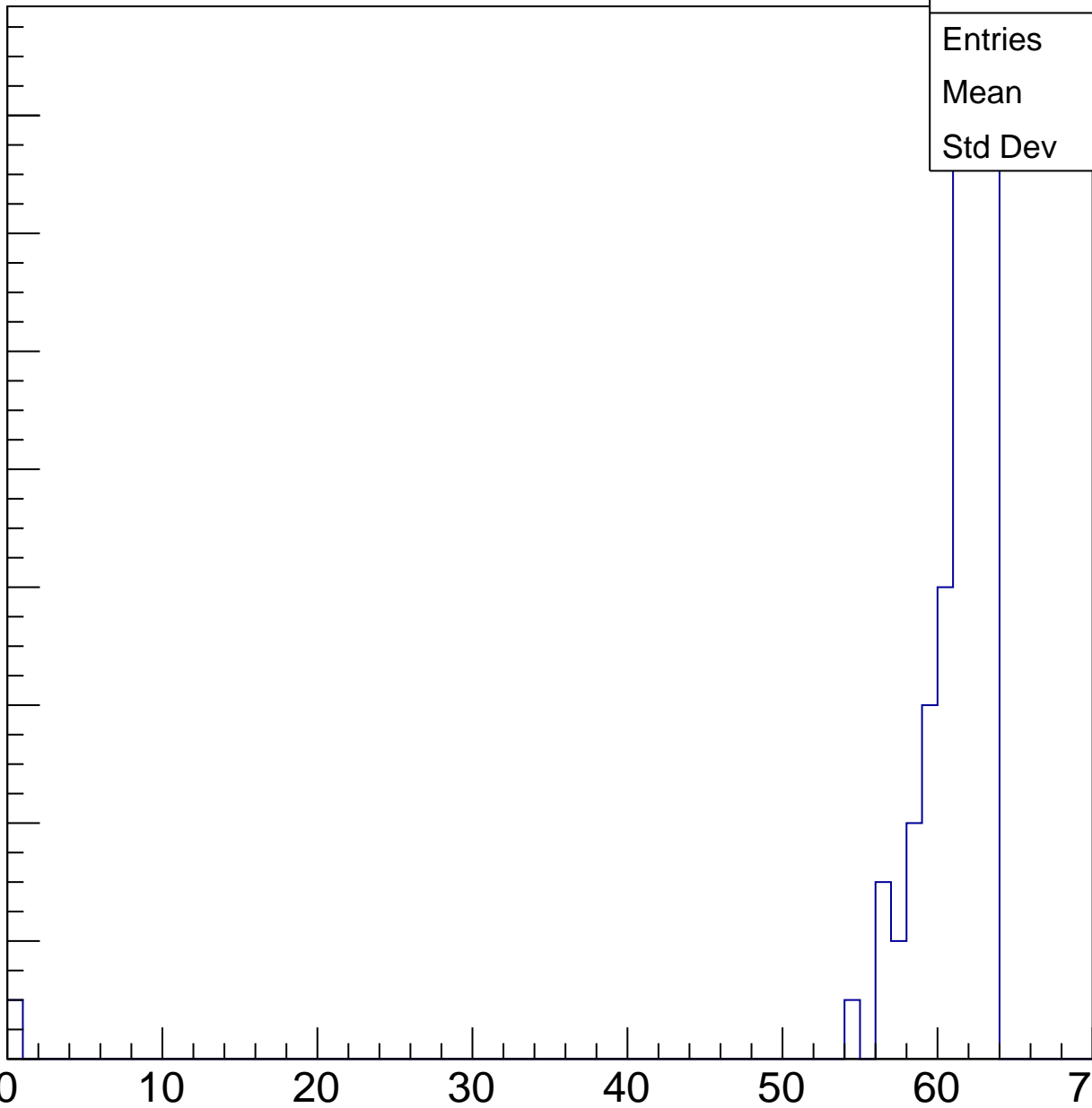
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 60.01 |
| Std Dev | 7.313 |

ampl



# B0L101S, U16-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

60.33

Std Dev

2.494



# B0L101S, U16-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 28.94 |
| Std Dev | 3.901 |

**Gaus mean : 28.9744**

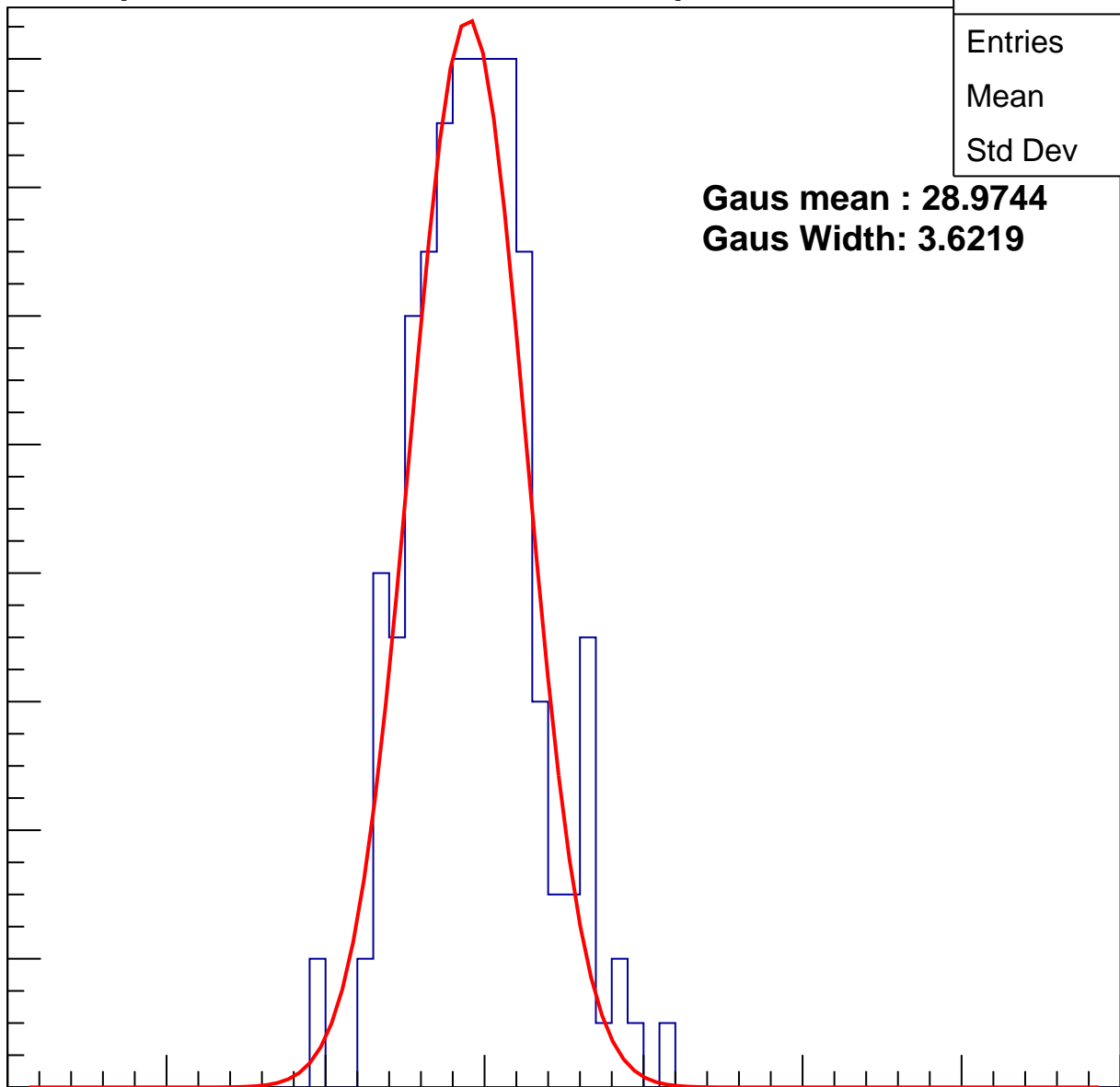
**Gaus Width: 3.6219**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch20, adc1

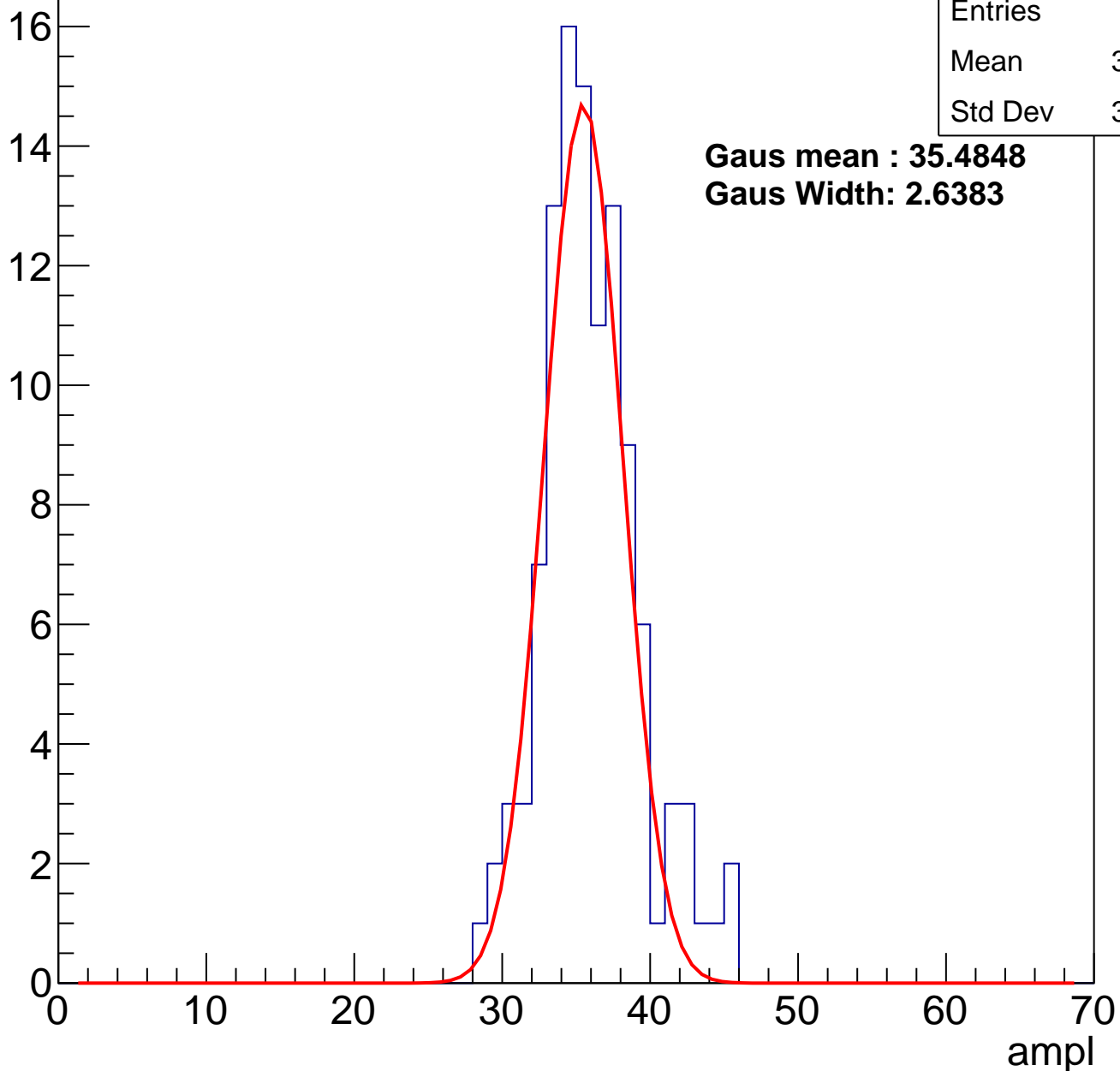
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 35.55 |
| Std Dev | 3.313 |

**Gaus mean : 35.4848**

**Gaus Width: 2.6383**

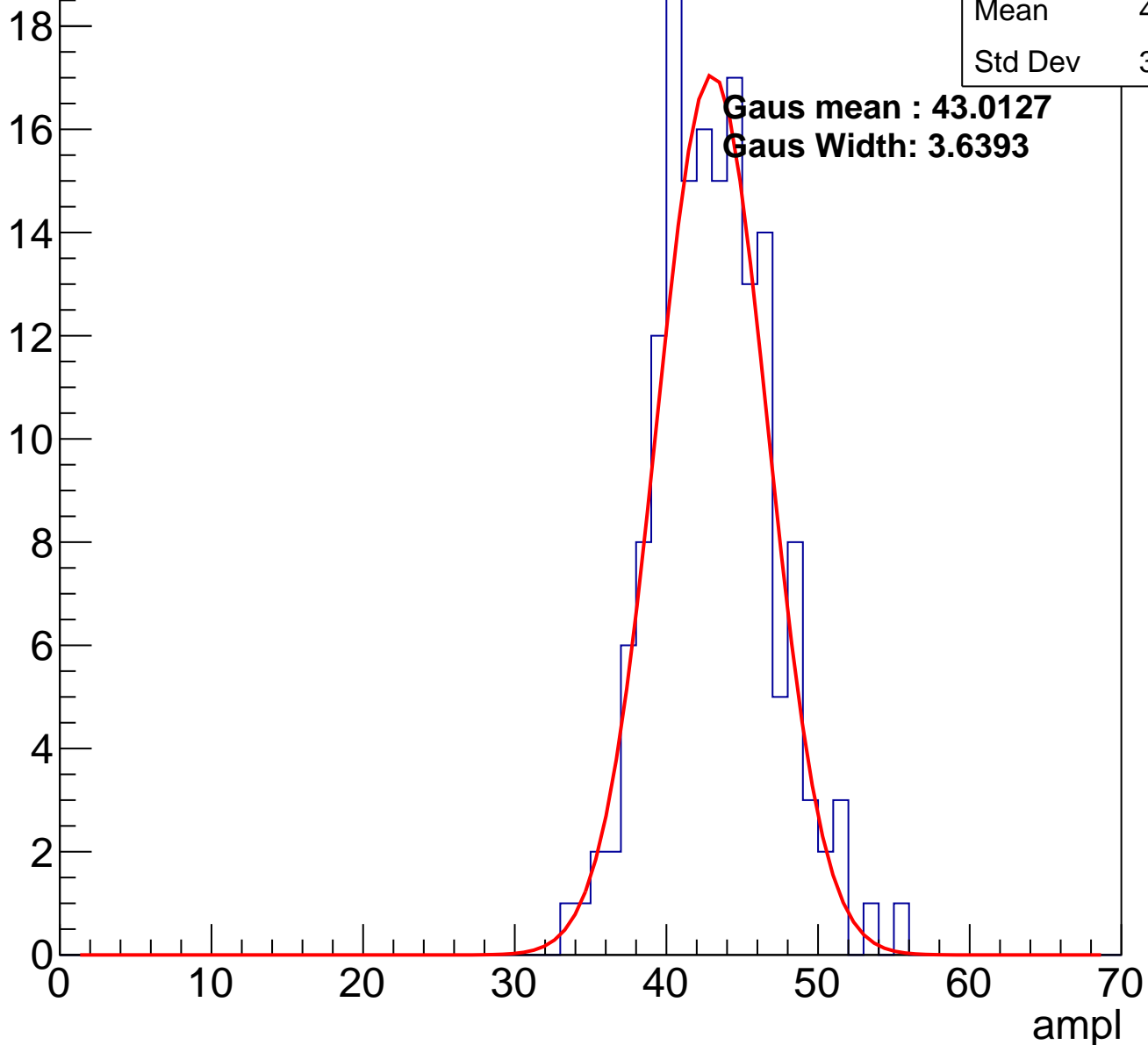
Entry



# B0L101S, U16-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

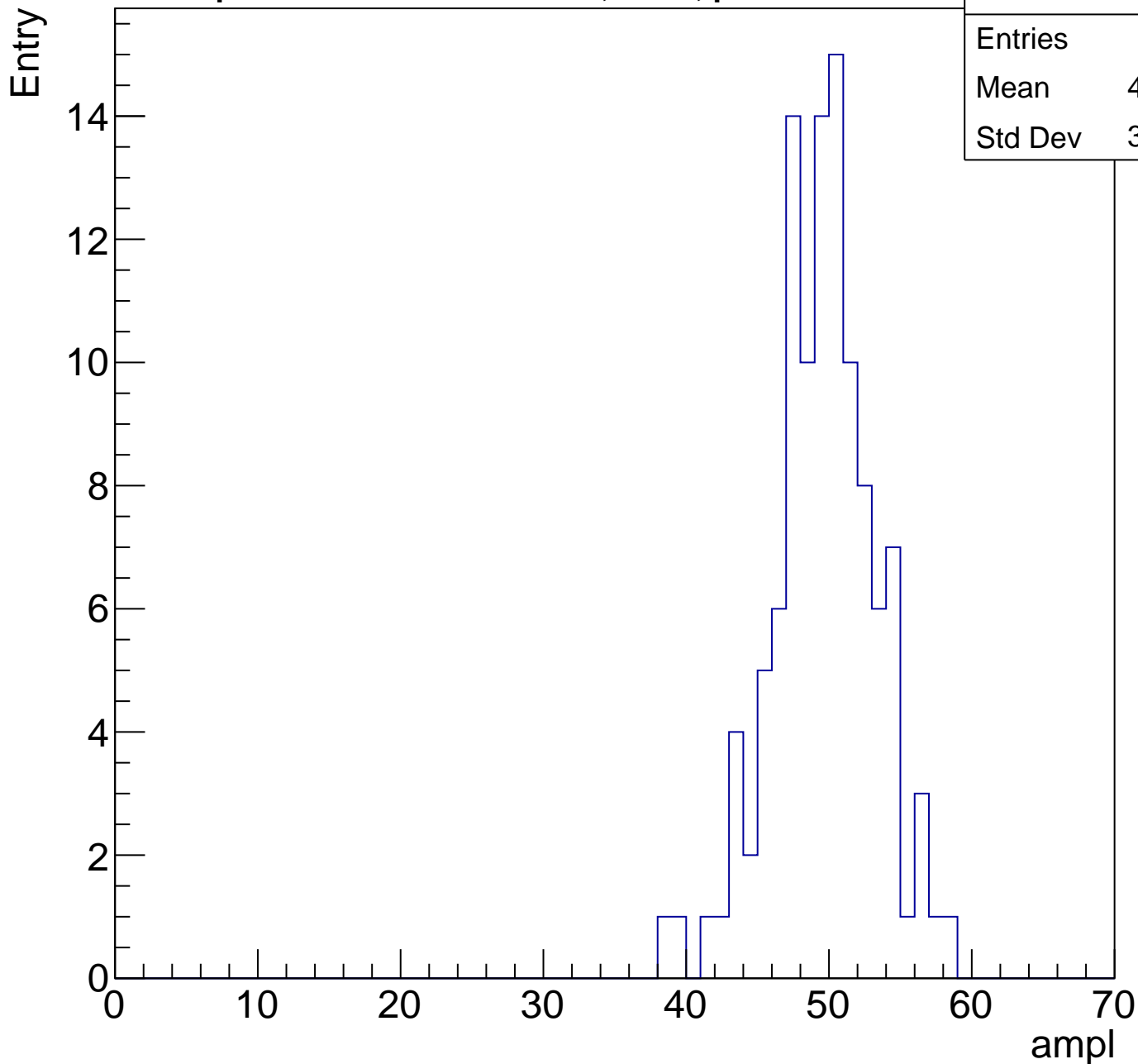
Entry



# B0L101S, U16-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 49.14 |
| Std Dev | 3.616 |



# B0L101S, U16-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

55.45

Std Dev

3.476

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch20, adc5

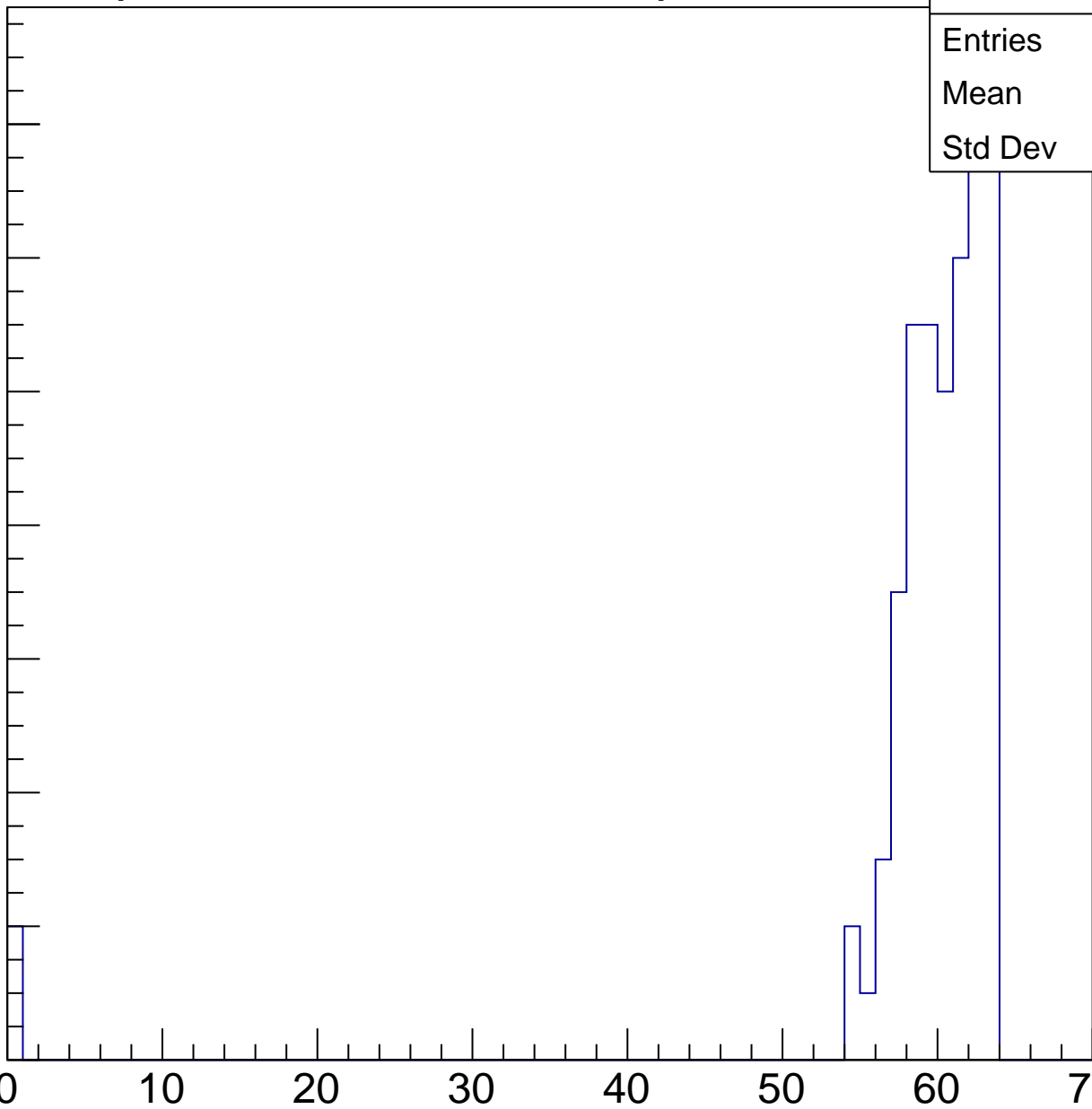
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 58.66 |
| Std Dev | 9.237 |

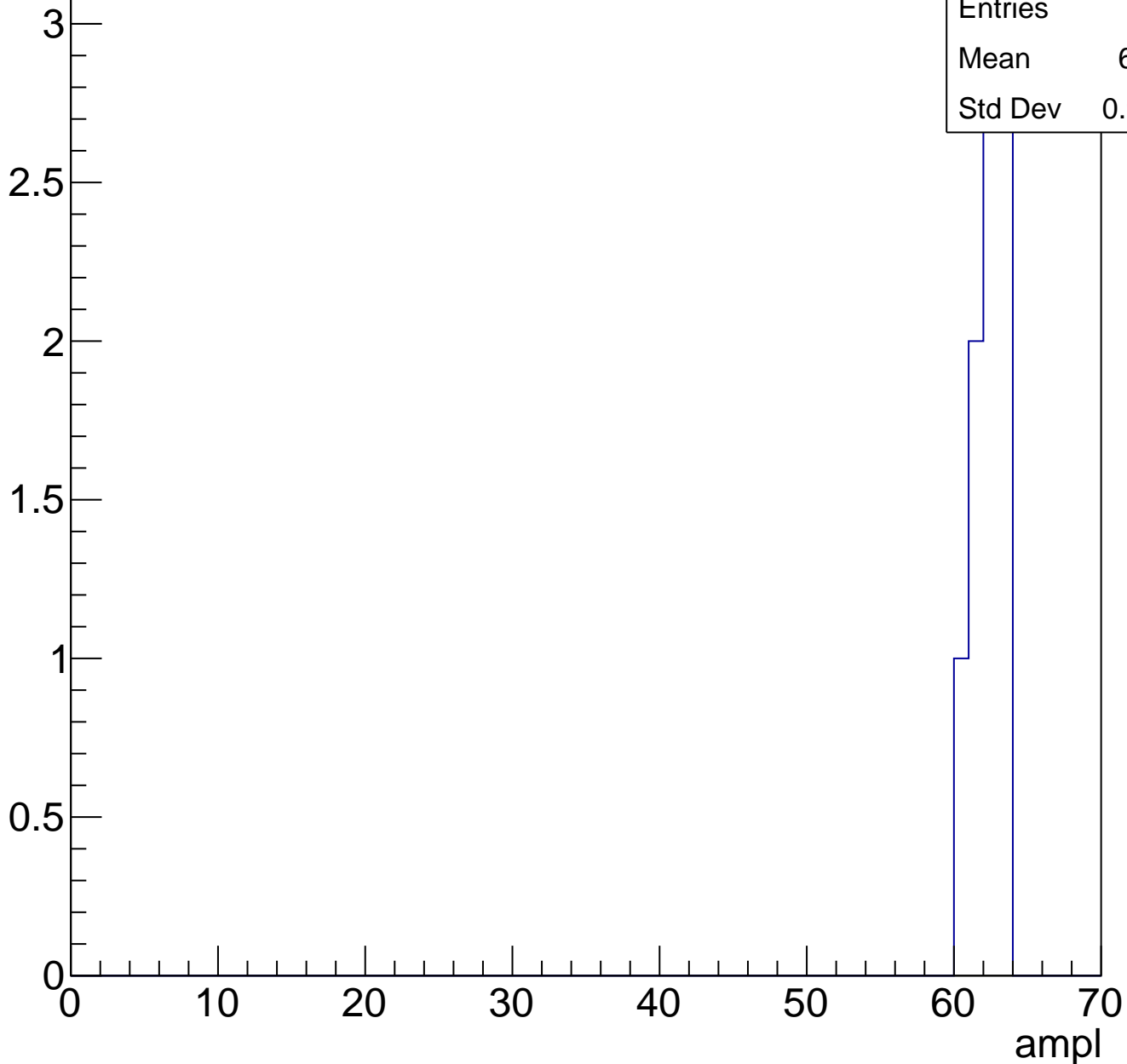
ampl



# B0L101S, U16-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch21, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 159   |
| Mean    | 30.51 |
| Std Dev | 3.821 |

**Gaus mean : 30.7056**

**Gaus Width: 3.6116**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

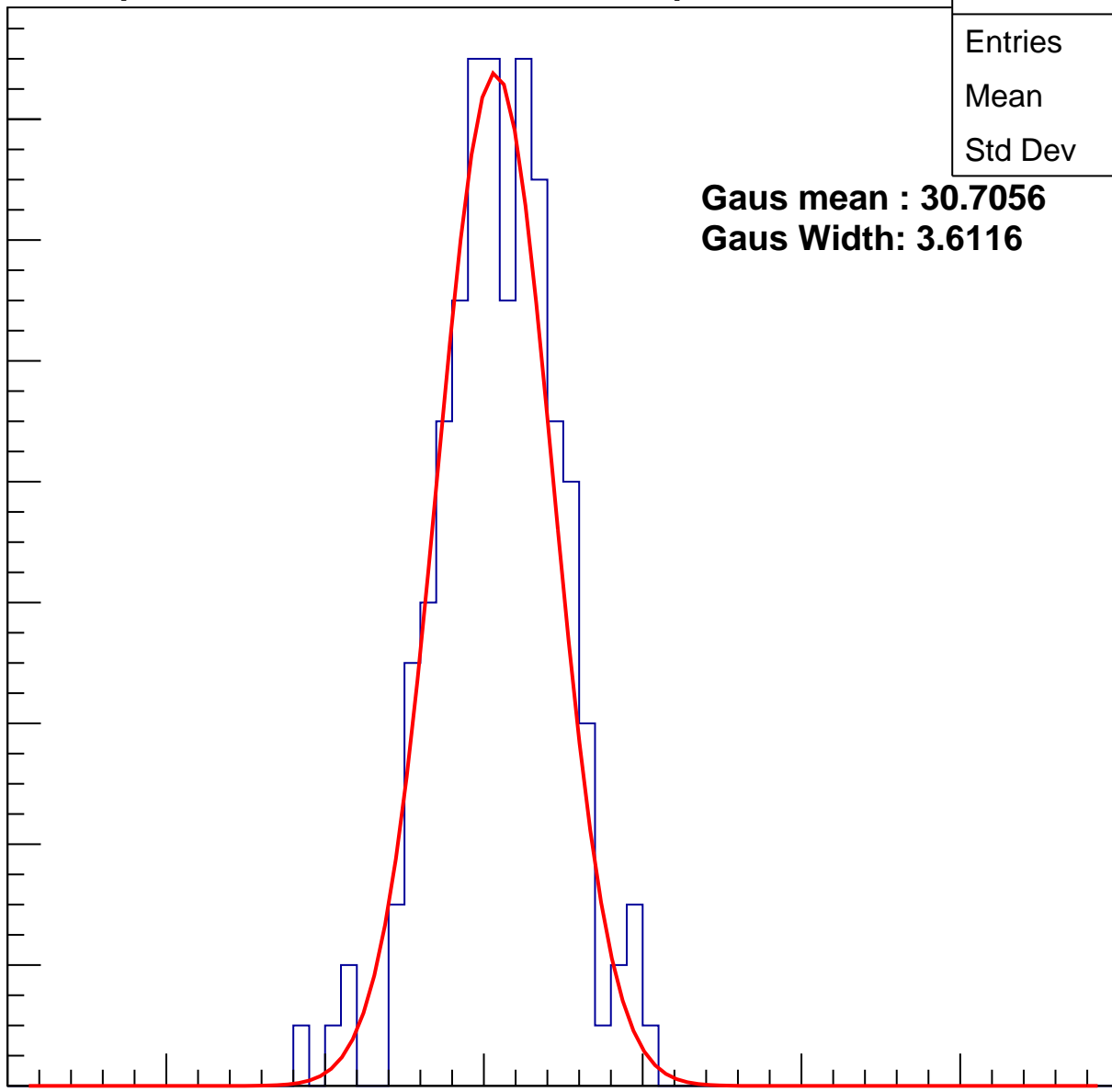
40

50

60

70

ampl



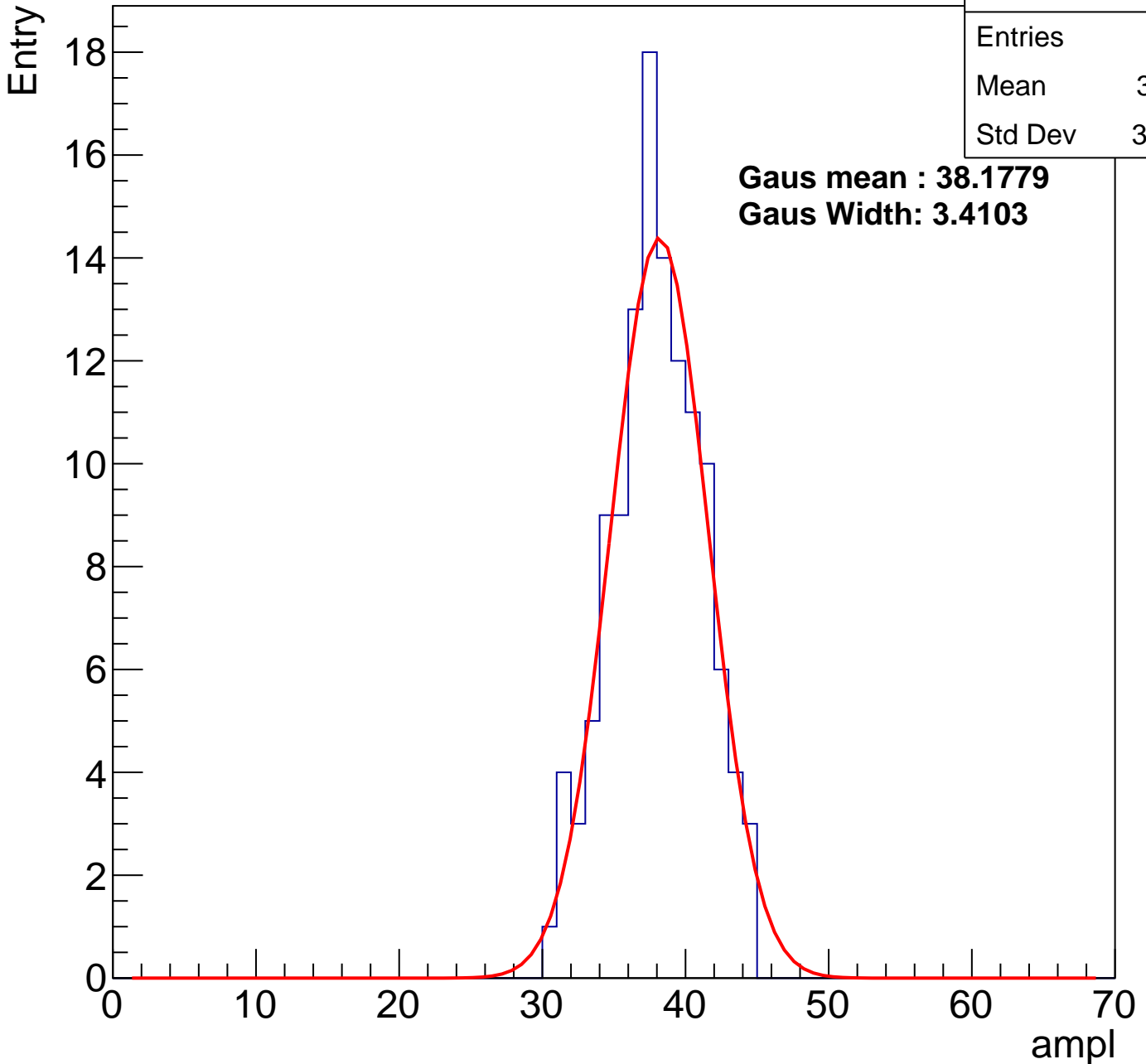
# B0L101S, U16-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 37.51 |
| Std Dev | 3.147 |

**Gaus mean : 38.1779**

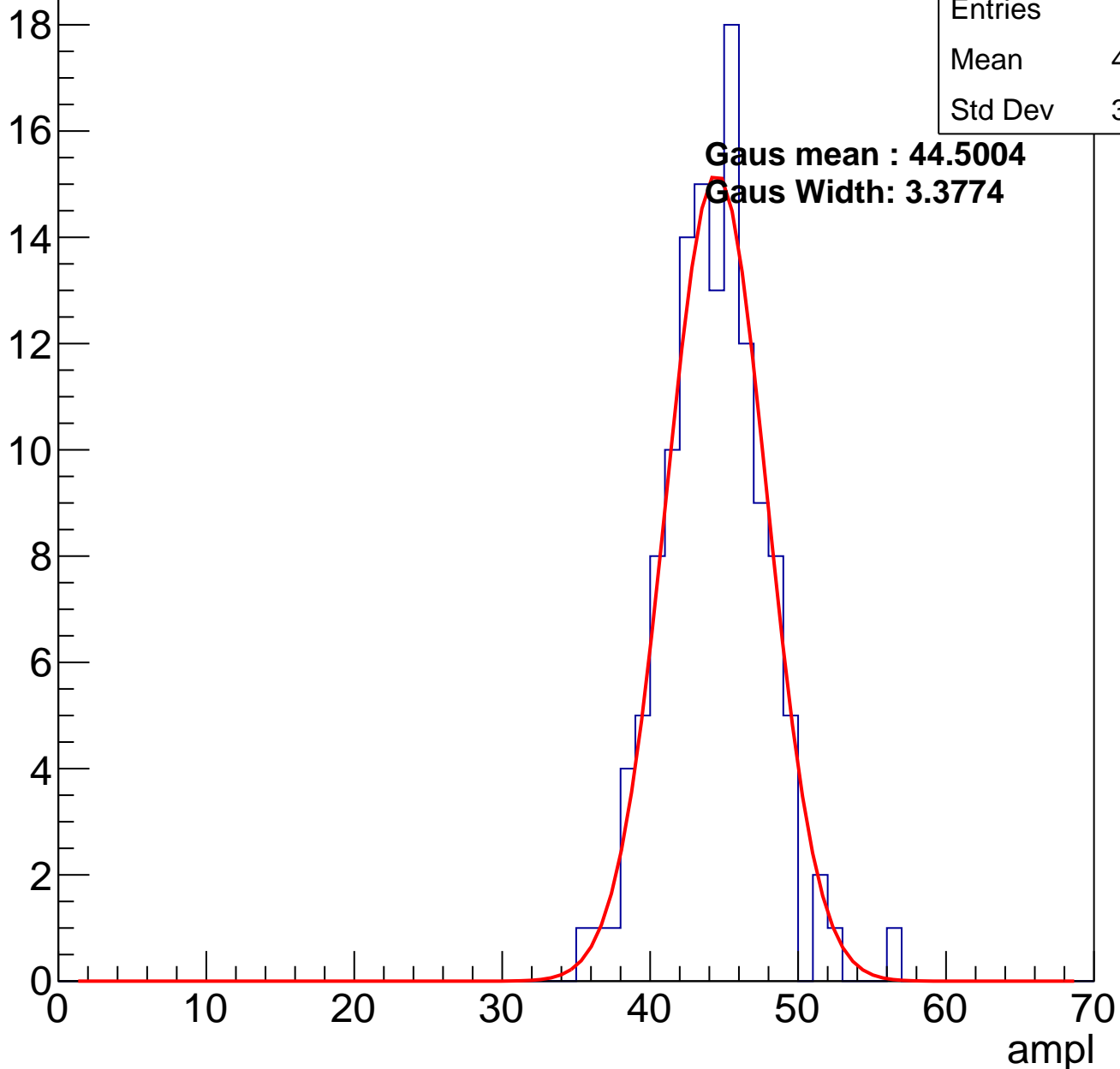
**Gaus Width: 3.4103**



# B0L101S, U16-ch21, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

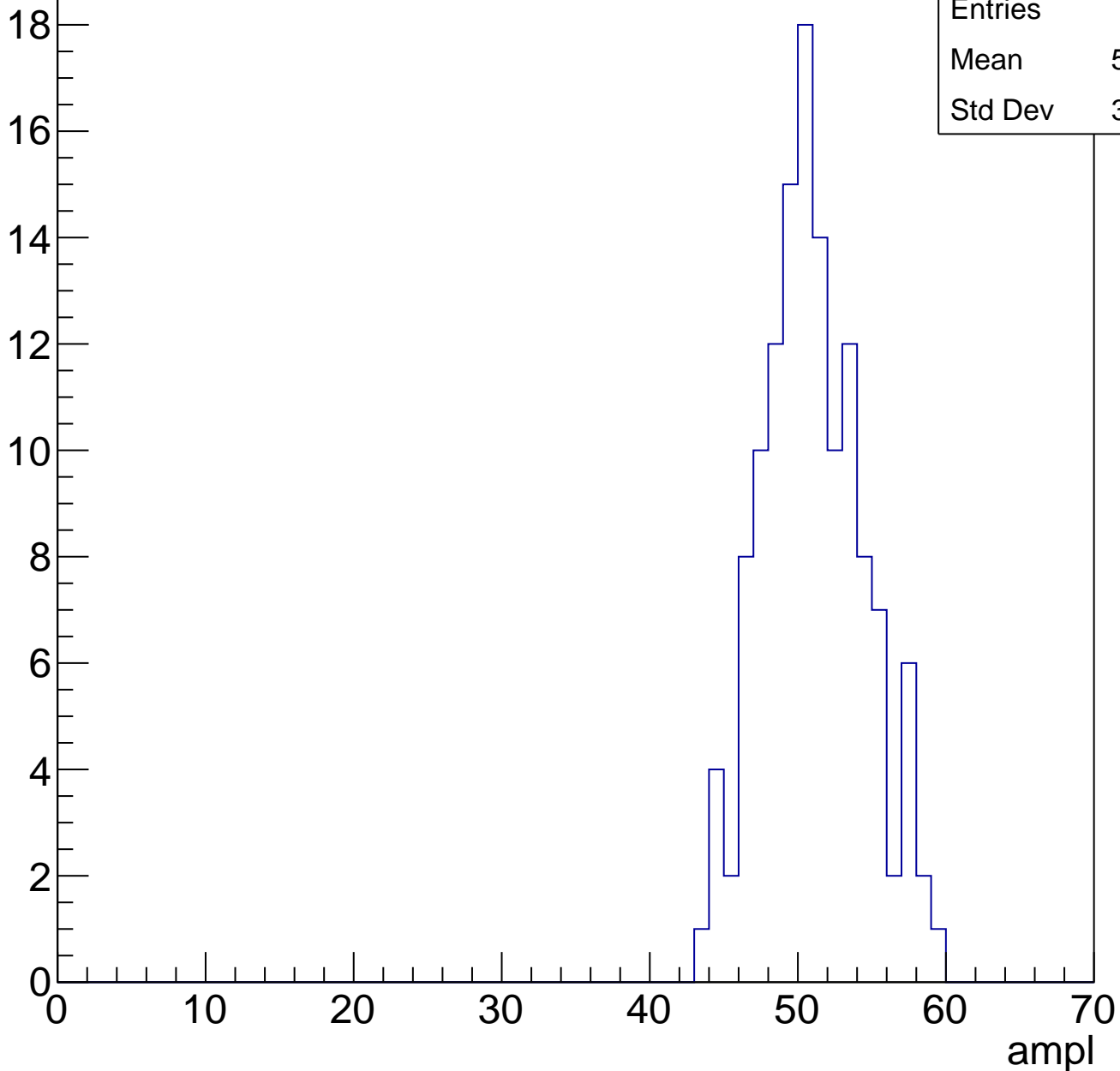


# B0L101S, U16-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 50.56 |
| Std Dev | 3.396 |

Entry



# B0L101S, U16-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

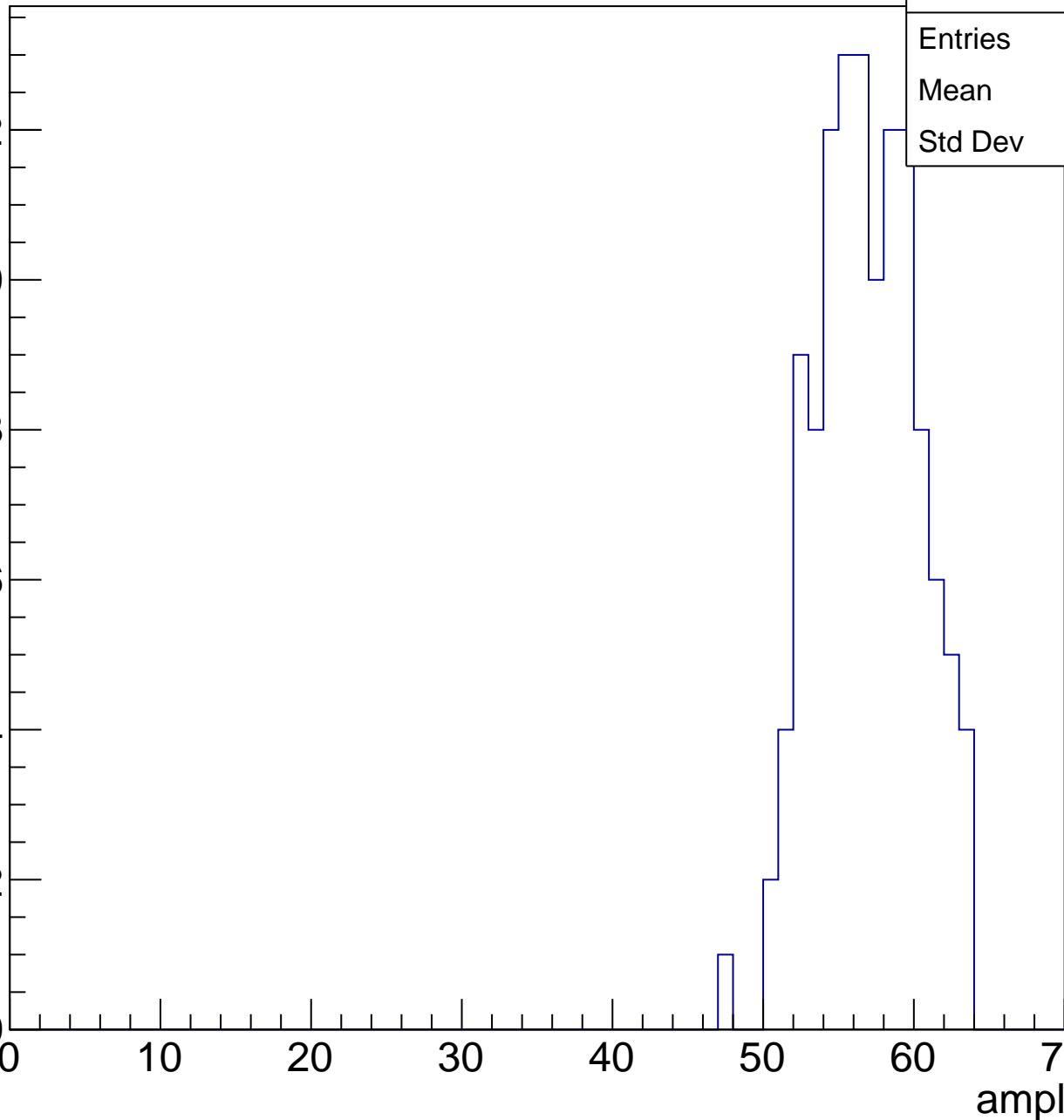
40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 56.44 |
| Std Dev | 3.34  |



# B0L101S, U16-ch21, adc5

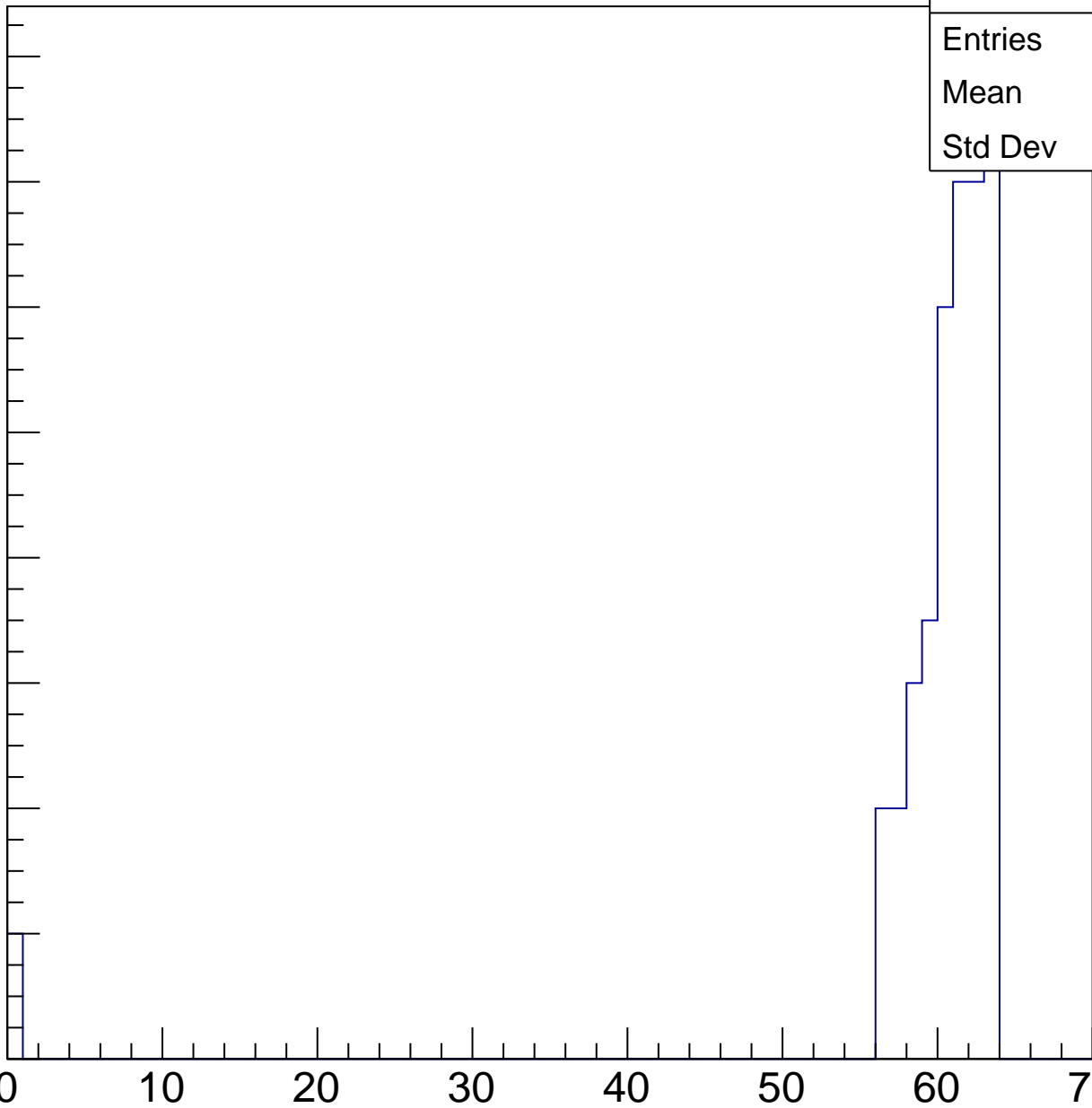
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 59.03 |
| Std Dev | 9.724 |

ampl



# B0L101S, U16-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

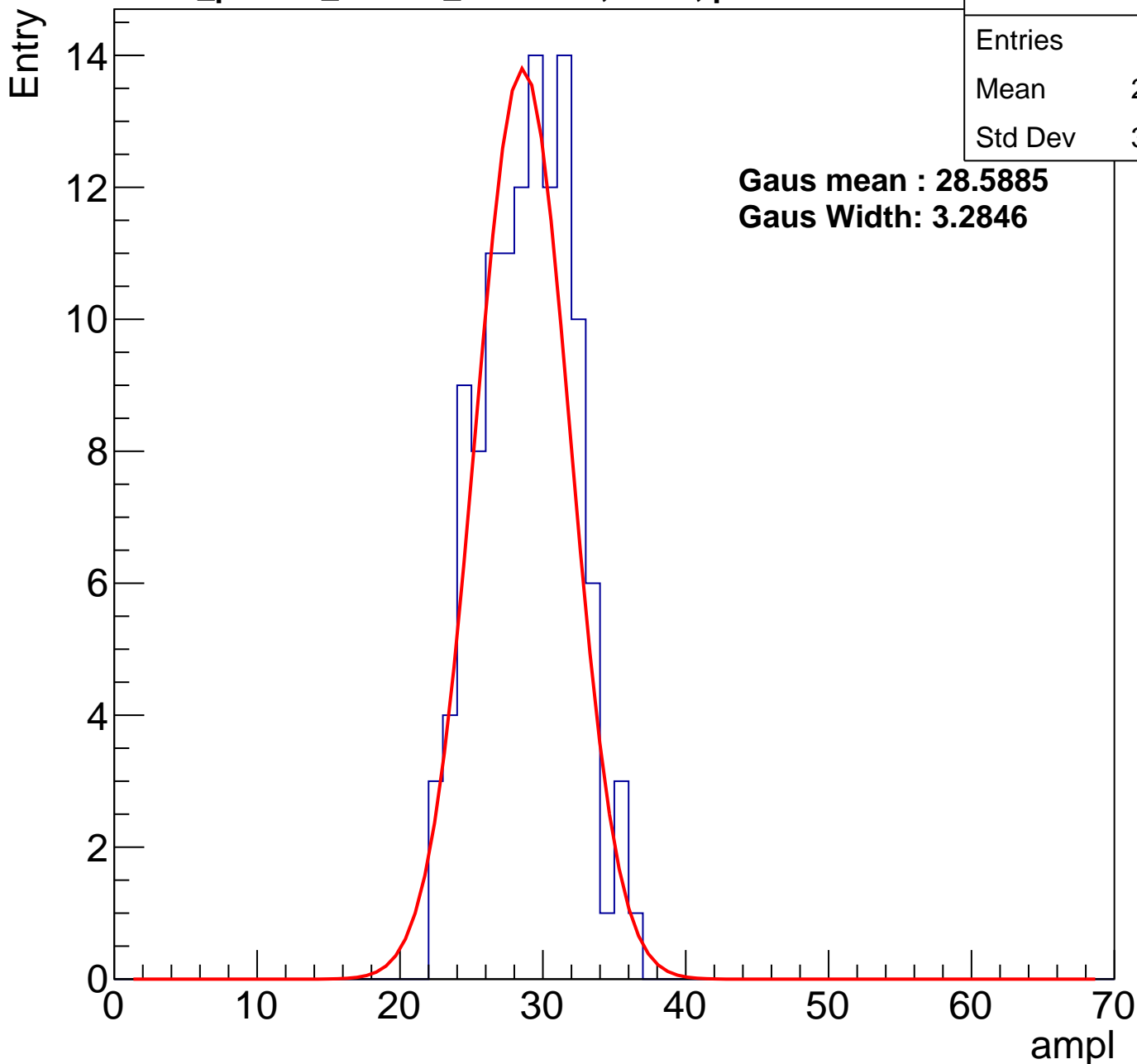
# B0L101S, U16-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 28.45 |
| Std Dev | 3.175 |

**Gaus mean : 28.5885**

**Gaus Width: 3.2846**



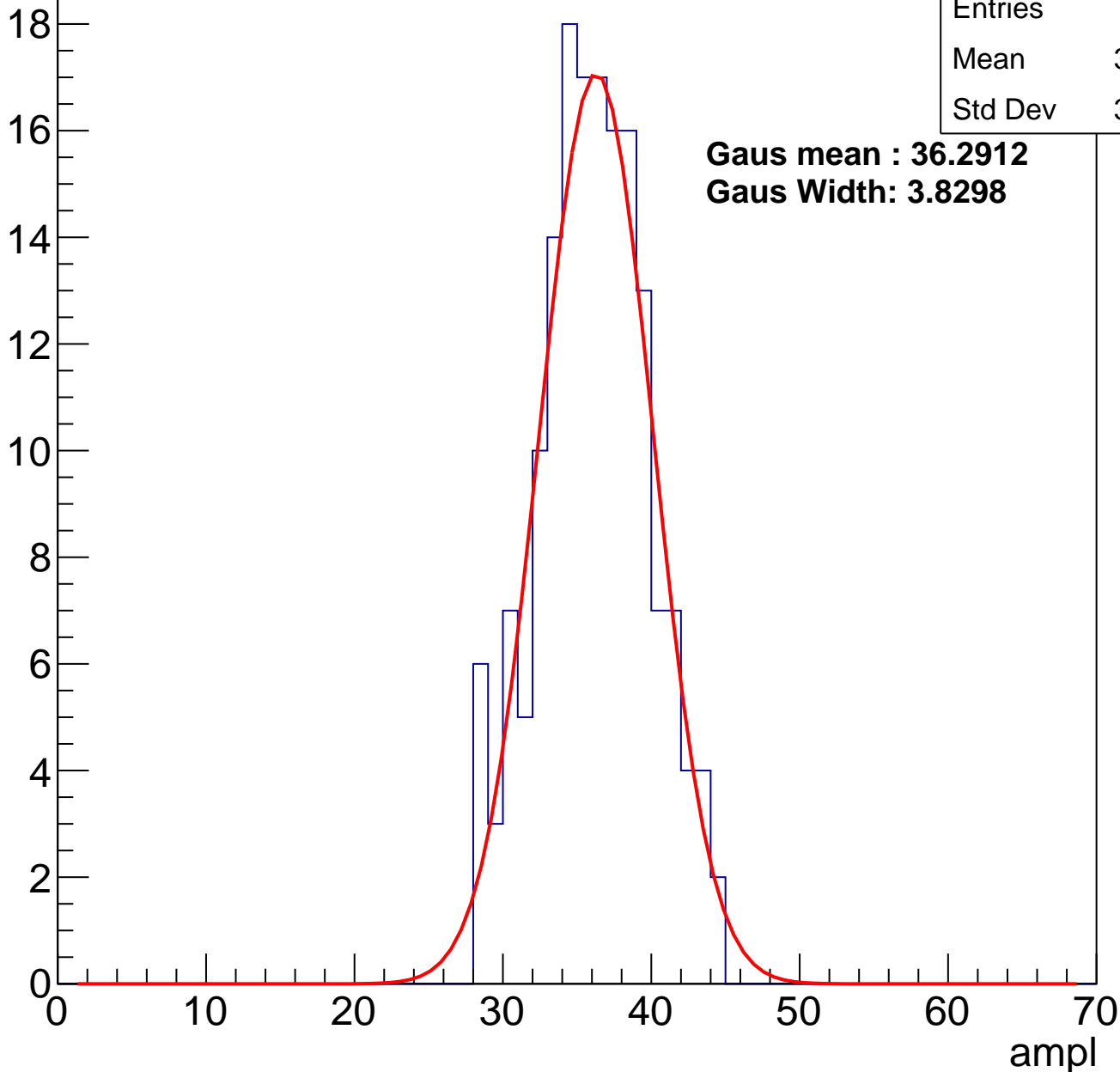
# B0L101S, U16-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 166   |
| Mean    | 35.68 |
| Std Dev | 3.655 |

**Gaus mean : 36.2912**  
**Gaus Width: 3.8298**

Entry



# B0L101S, U16-ch22, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 42.92 |
| Std Dev | 3.227 |

**Gaus mean : 43.2536**

**Gaus Width: 3.2600**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

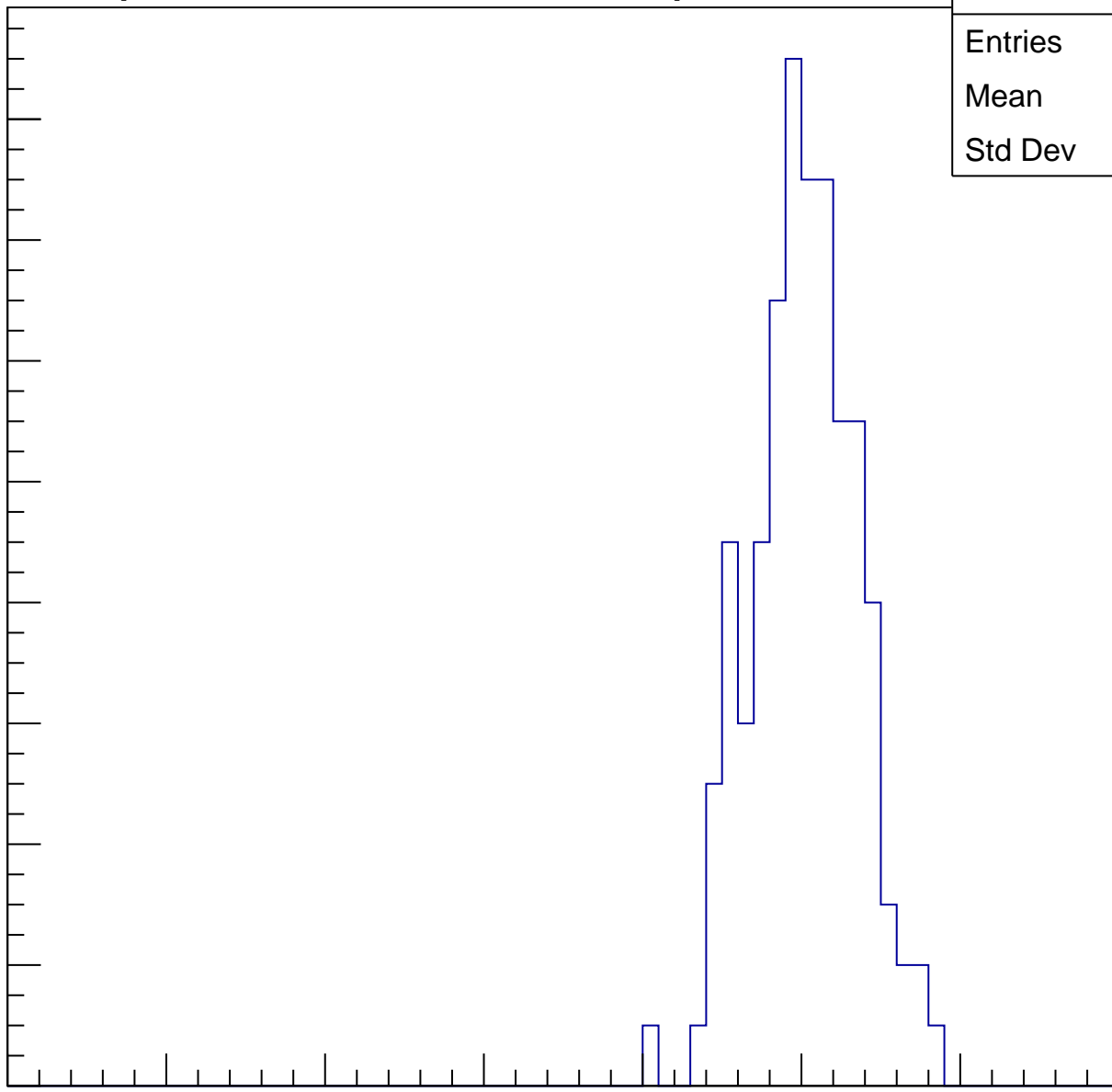
|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 49.73 |
| Std Dev | 3.277 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

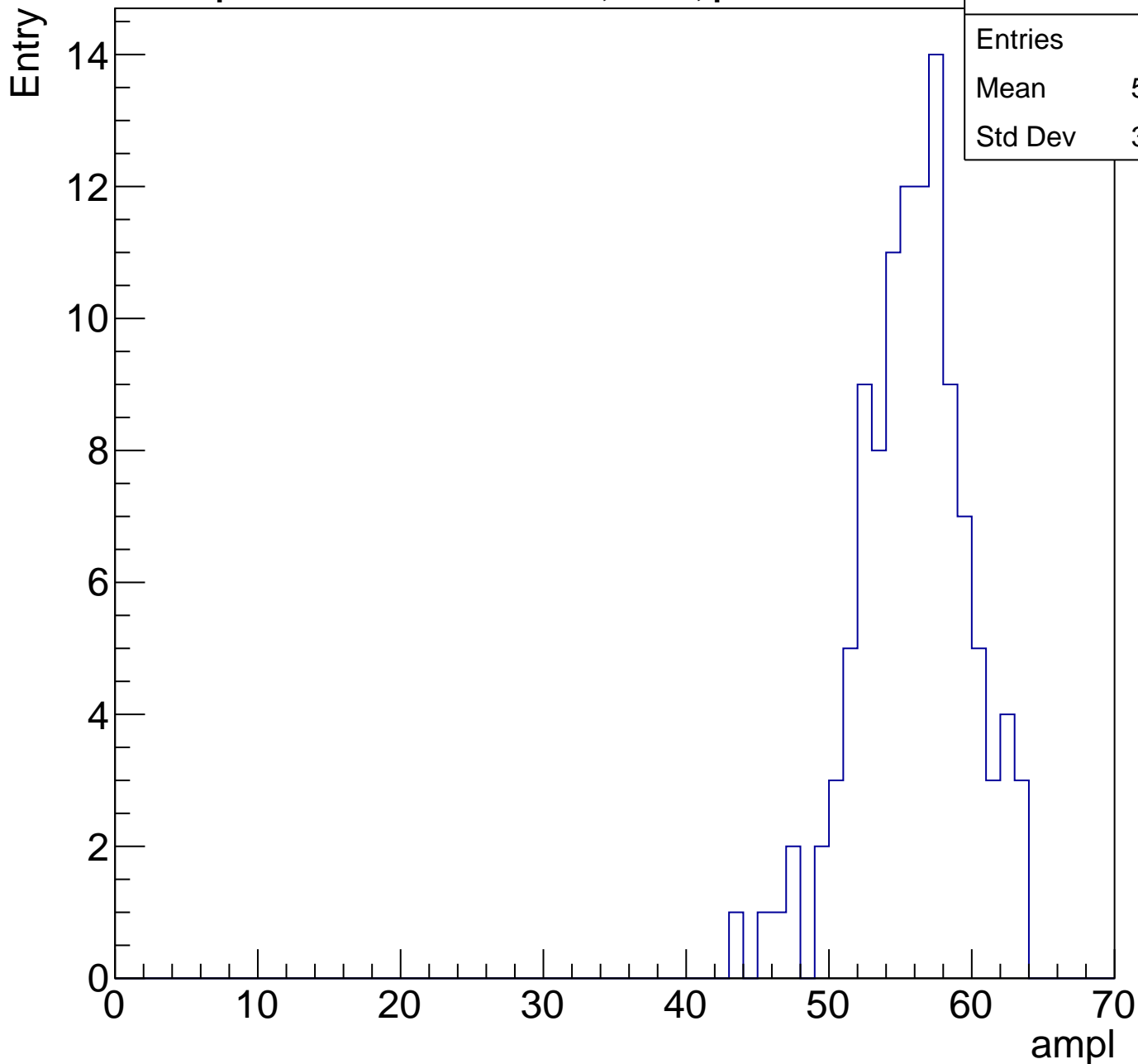
ampl



# B0L101S, U16-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

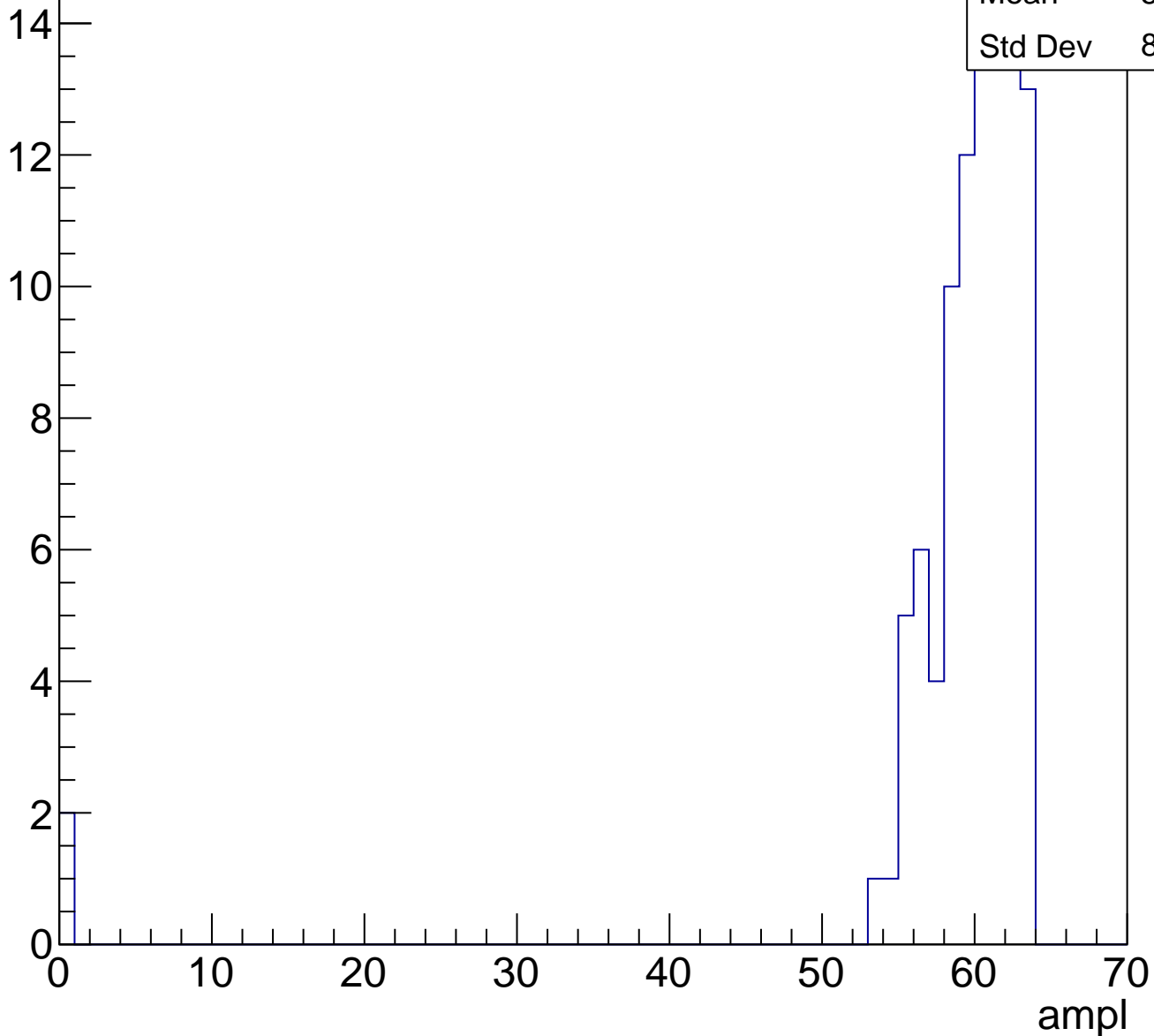
|         |       |
|---------|-------|
| Entries | 112   |
| Mean    | 55.38 |
| Std Dev | 3.862 |



# B0L101S, U16-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

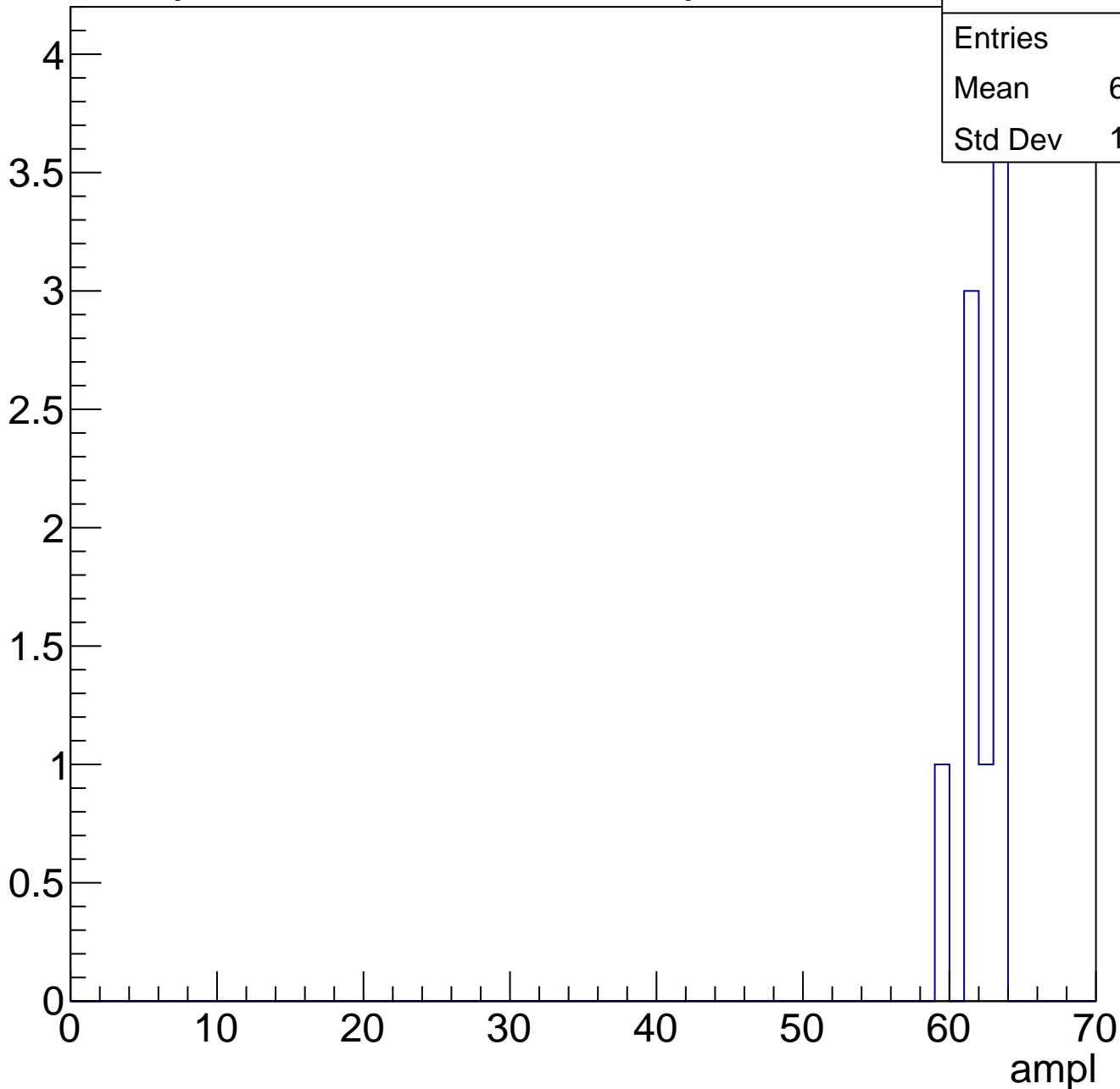


|         |       |
|---------|-------|
| Entries | 98    |
| Mean    | 58.54 |
| Std Dev | 8.793 |

# B0L101S, U16-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 10.5 |
| Std Dev | 10.5 |

# B0L101S, U16-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

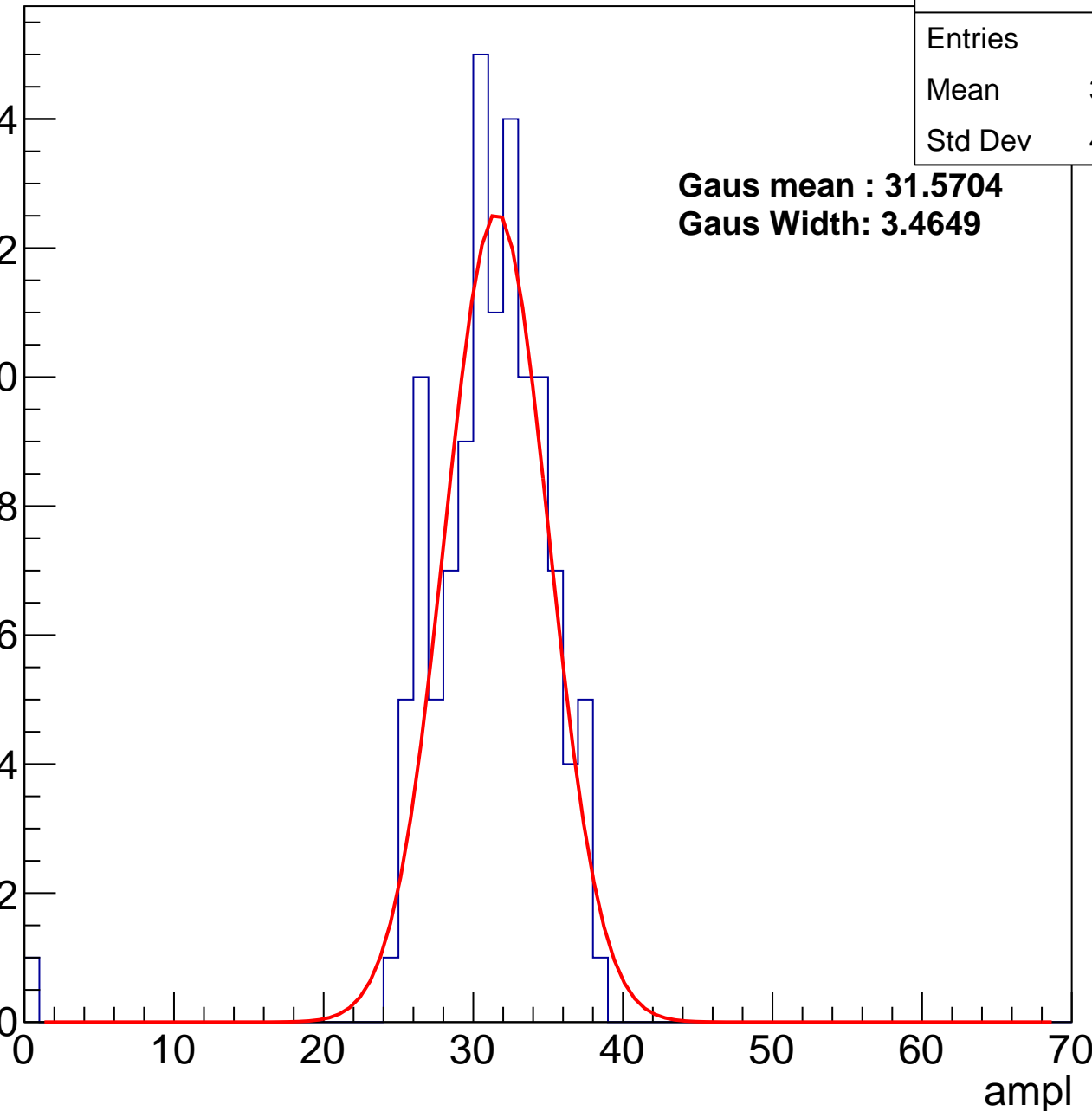
Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 30.63 |
| Std Dev | 4.377 |

**Gaus mean : 31.5704**

**Gaus Width: 3.4649**



# B0L101S, U16-ch23, adc1

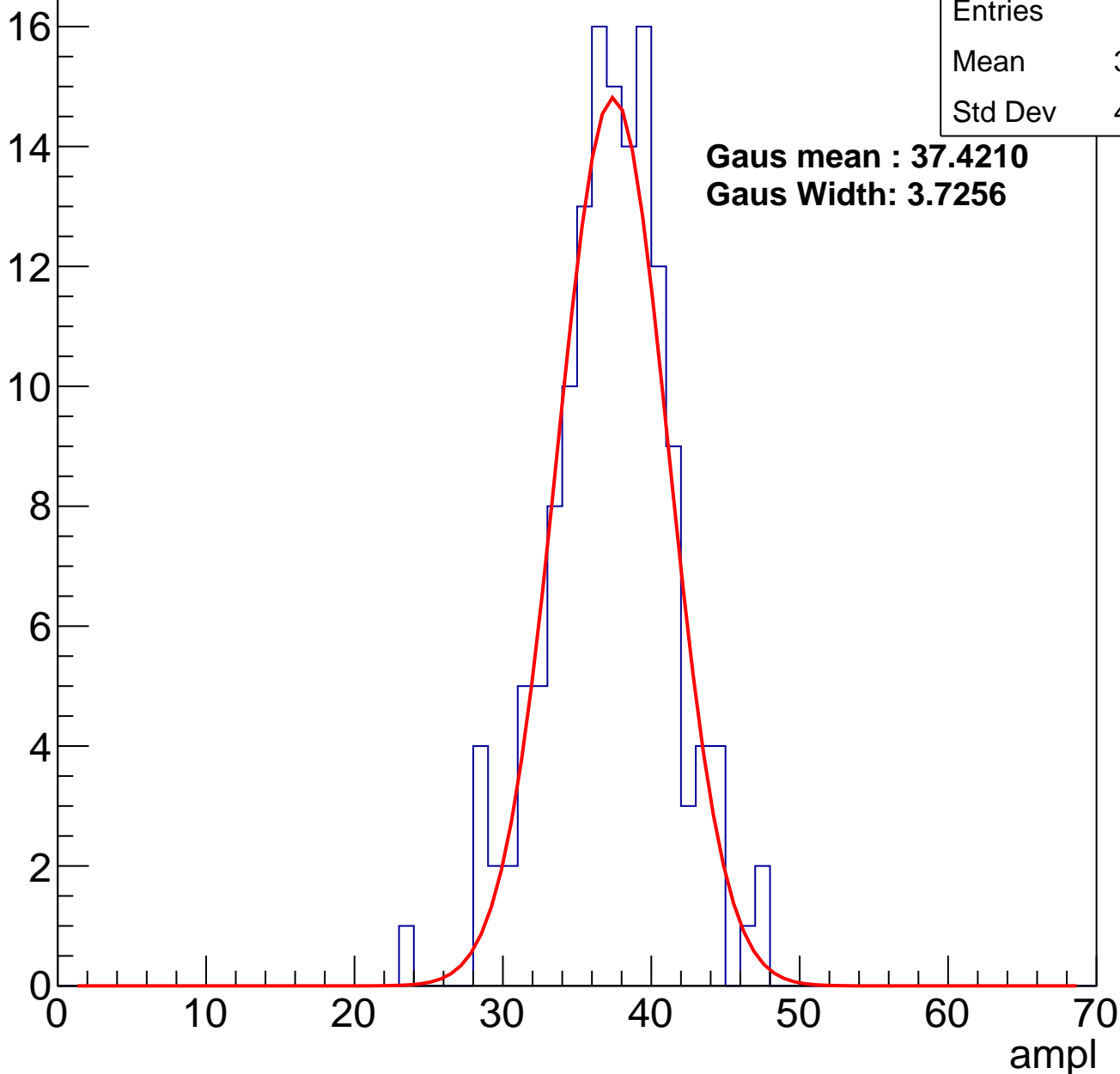
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 36.83 |
| Std Dev | 4.019 |

**Gaus mean : 37.4210**

**Gaus Width: 3.7256**

Entry



# B0L101S, U16-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 44.36 |
| Std Dev | 3.372 |

**Gaus mean : 44.6657**

**Gaus Width: 3.5728**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

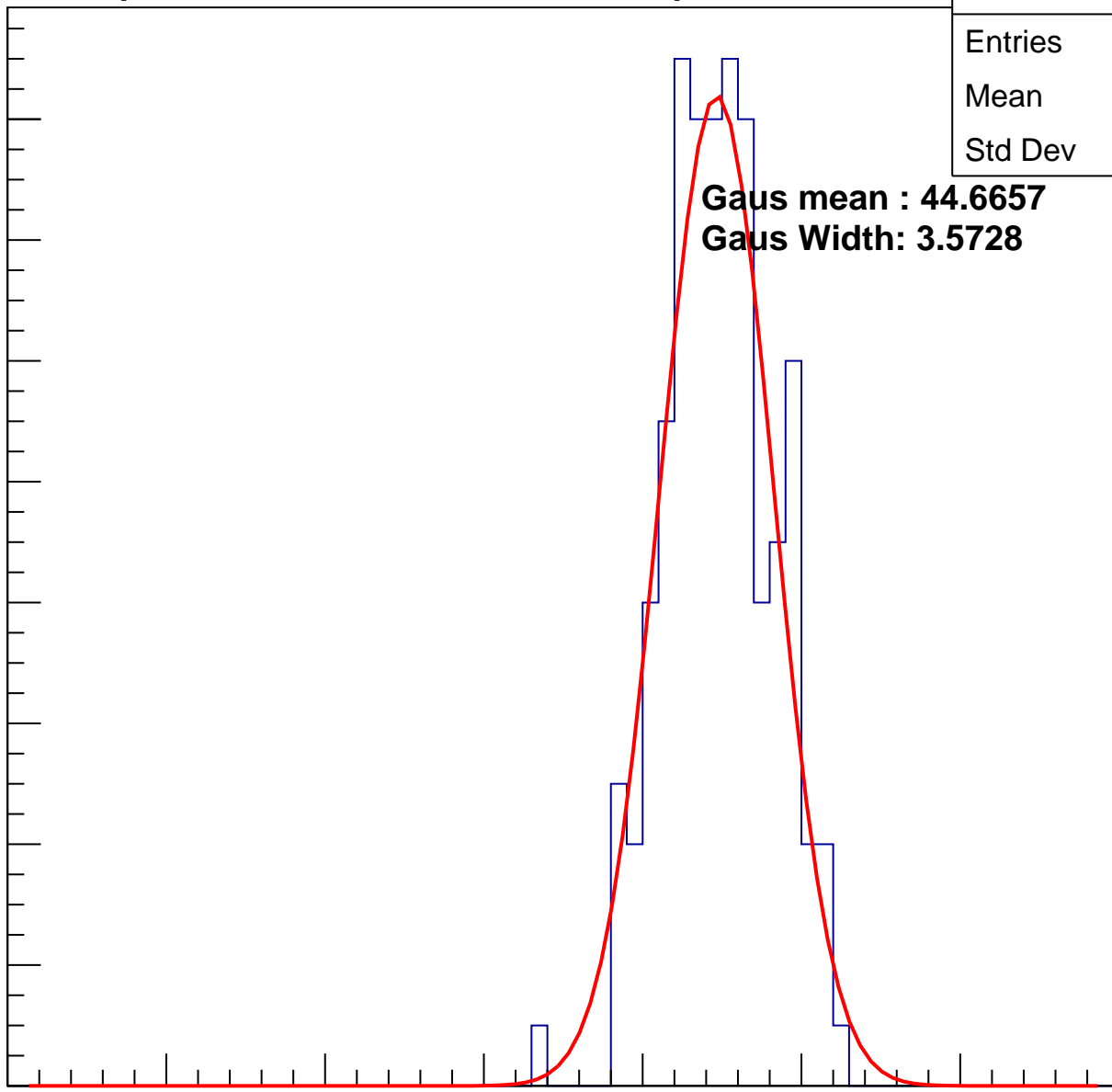
40

50

60

70

ampl



# B0L101S, U16-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 103   |
| Mean    | 50.83 |
| Std Dev | 2.956 |

Entry

12

10

8

6

4

2

0

0

10

20

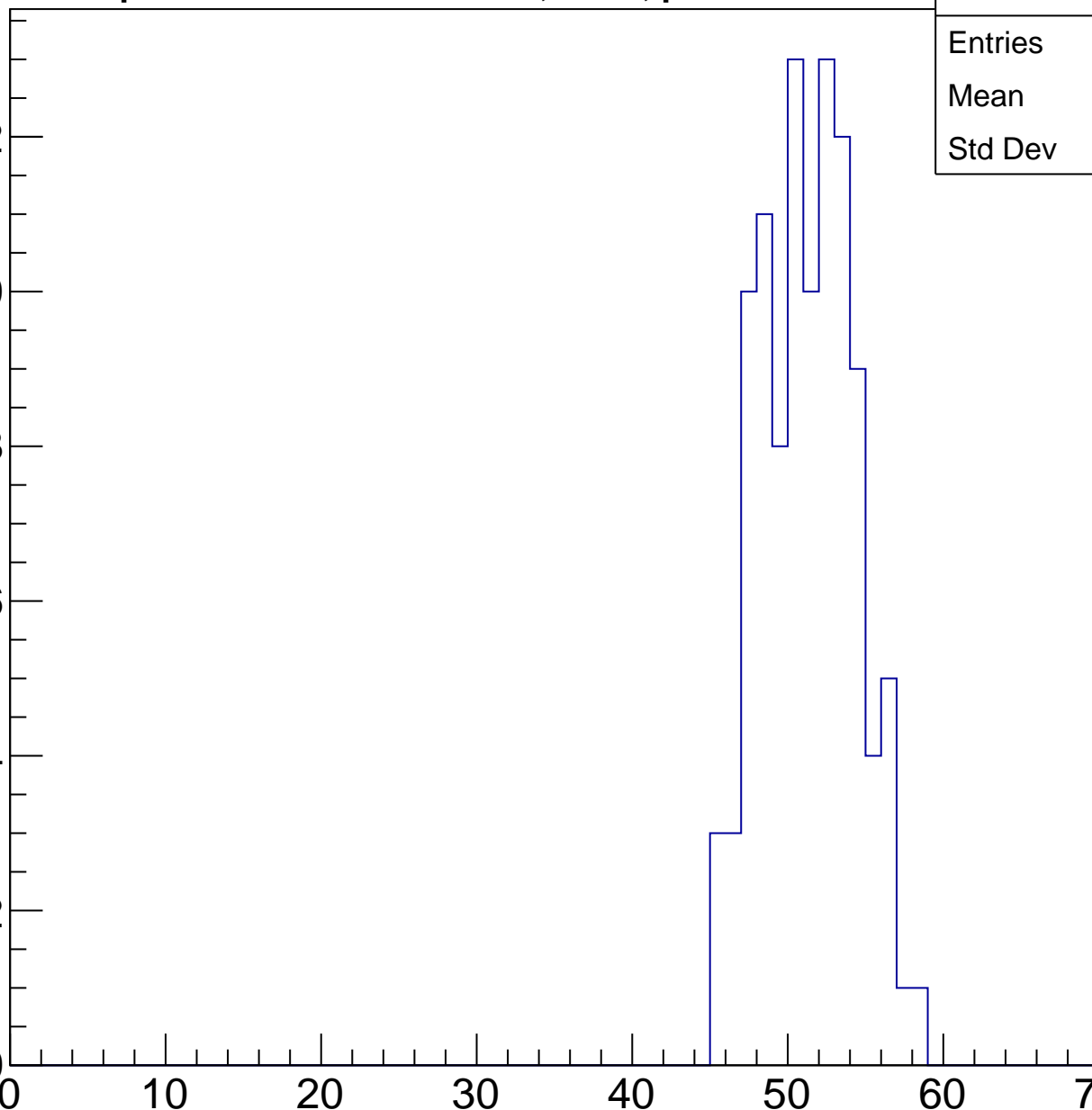
30

40

50

60

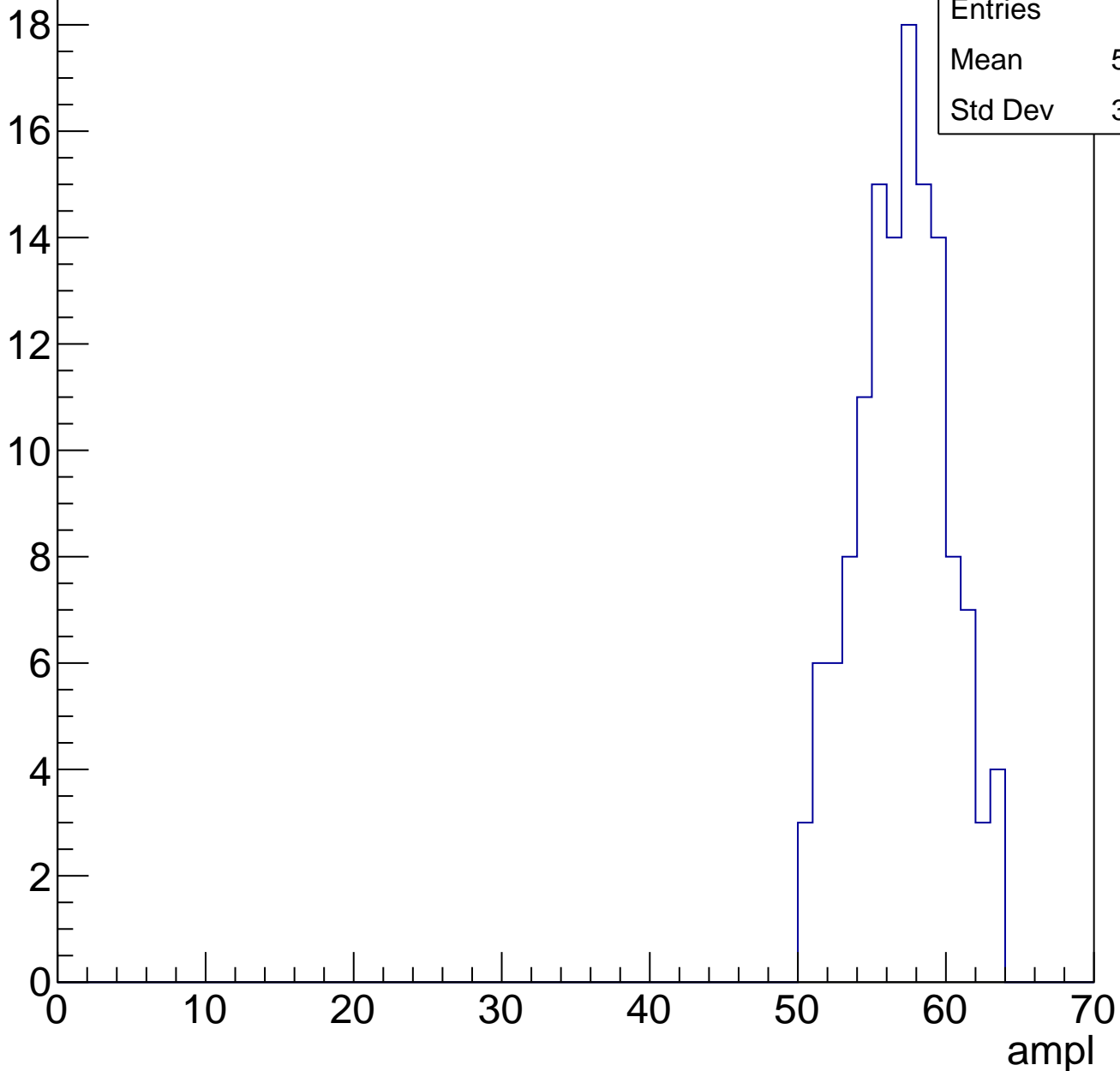
ampl



# B0L101S, U16-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch23, adc5

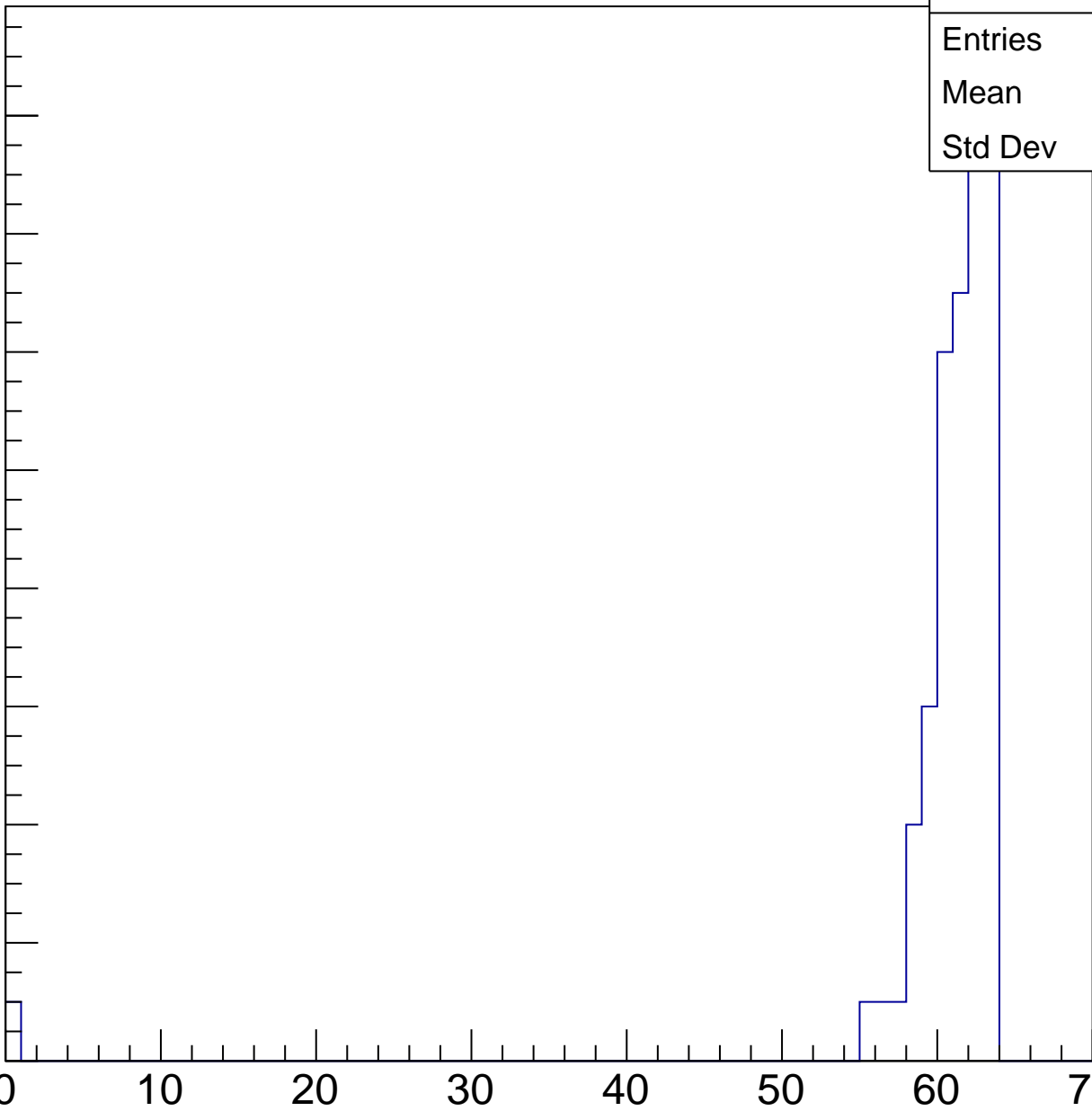
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 60.12 |
| Std Dev | 7.356 |

ampl



# B0L101S, U16-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L101S, U16-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch24, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 28.28 |
| Std Dev | 4.902 |

**Gaus mean : 29.4575**

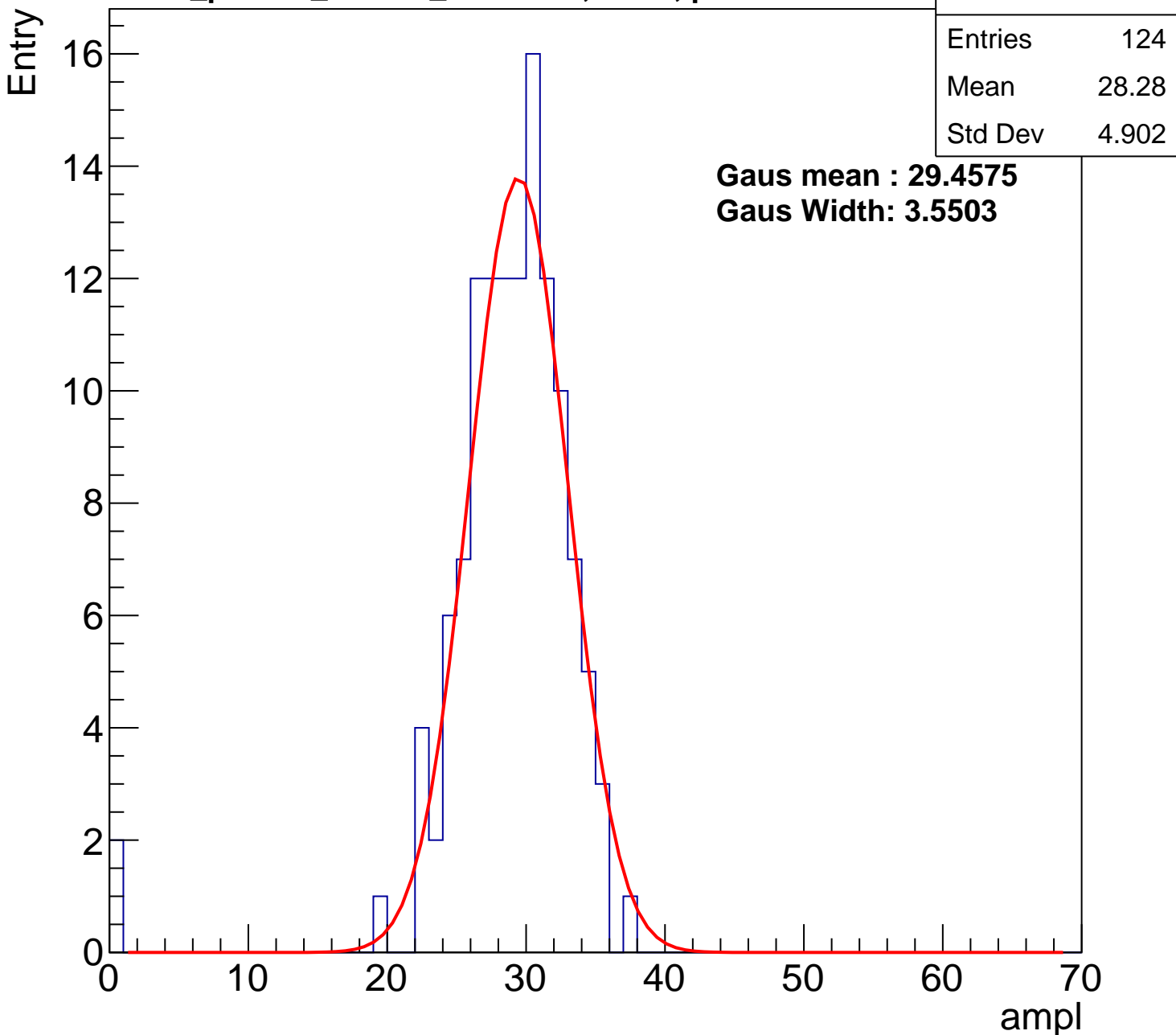
**Gaus Width: 3.5503**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

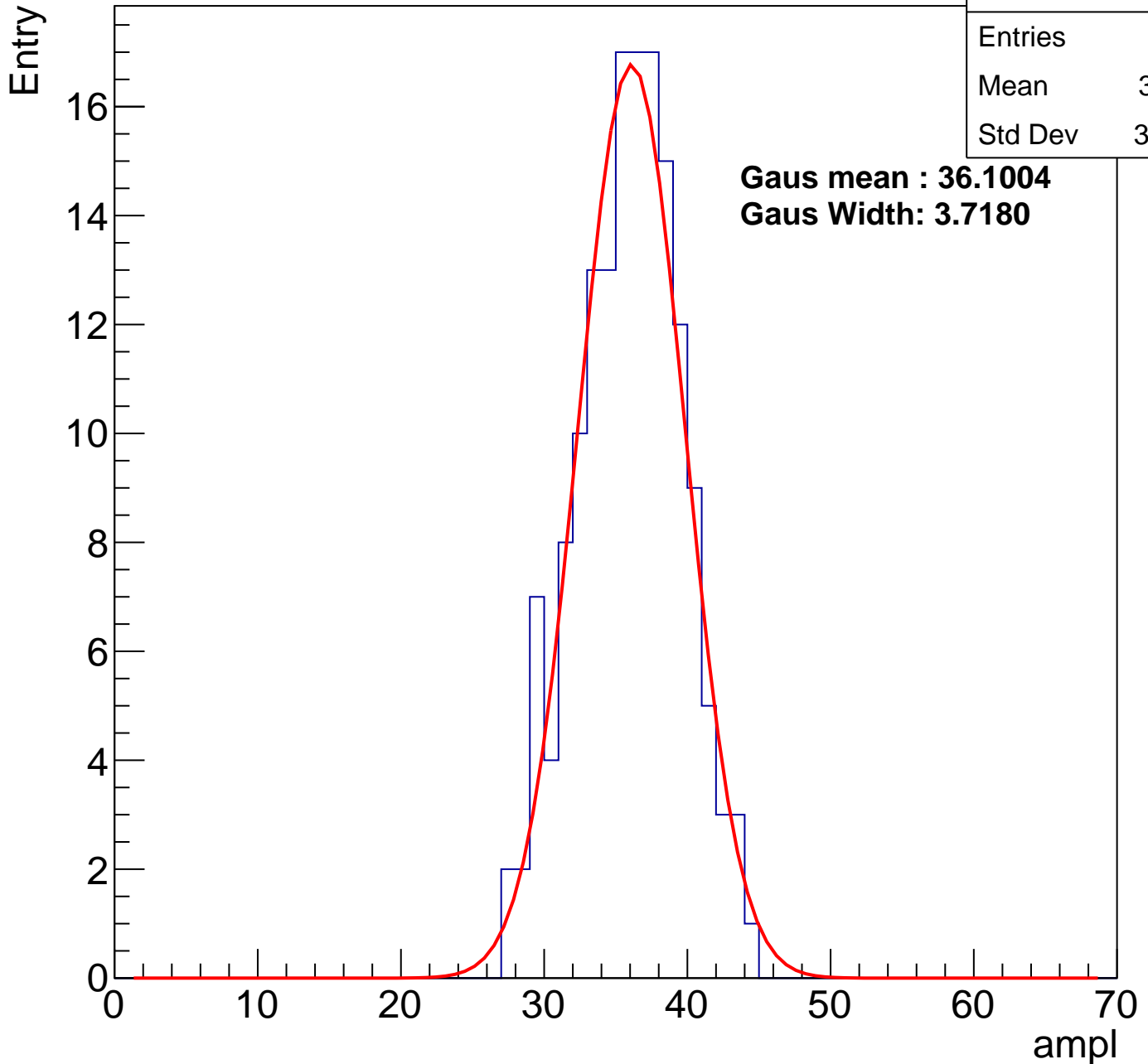


# B0L101S, U16-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 35.51 |
| Std Dev | 3.609 |

**Gaus mean : 36.1004**  
**Gaus Width: 3.7180**



# B0L101S, U16-ch24, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

42.7

Std Dev

3.302

**Gaus mean : 43.3154**

**Gaus Width: 3.3749**

0

10

20

30

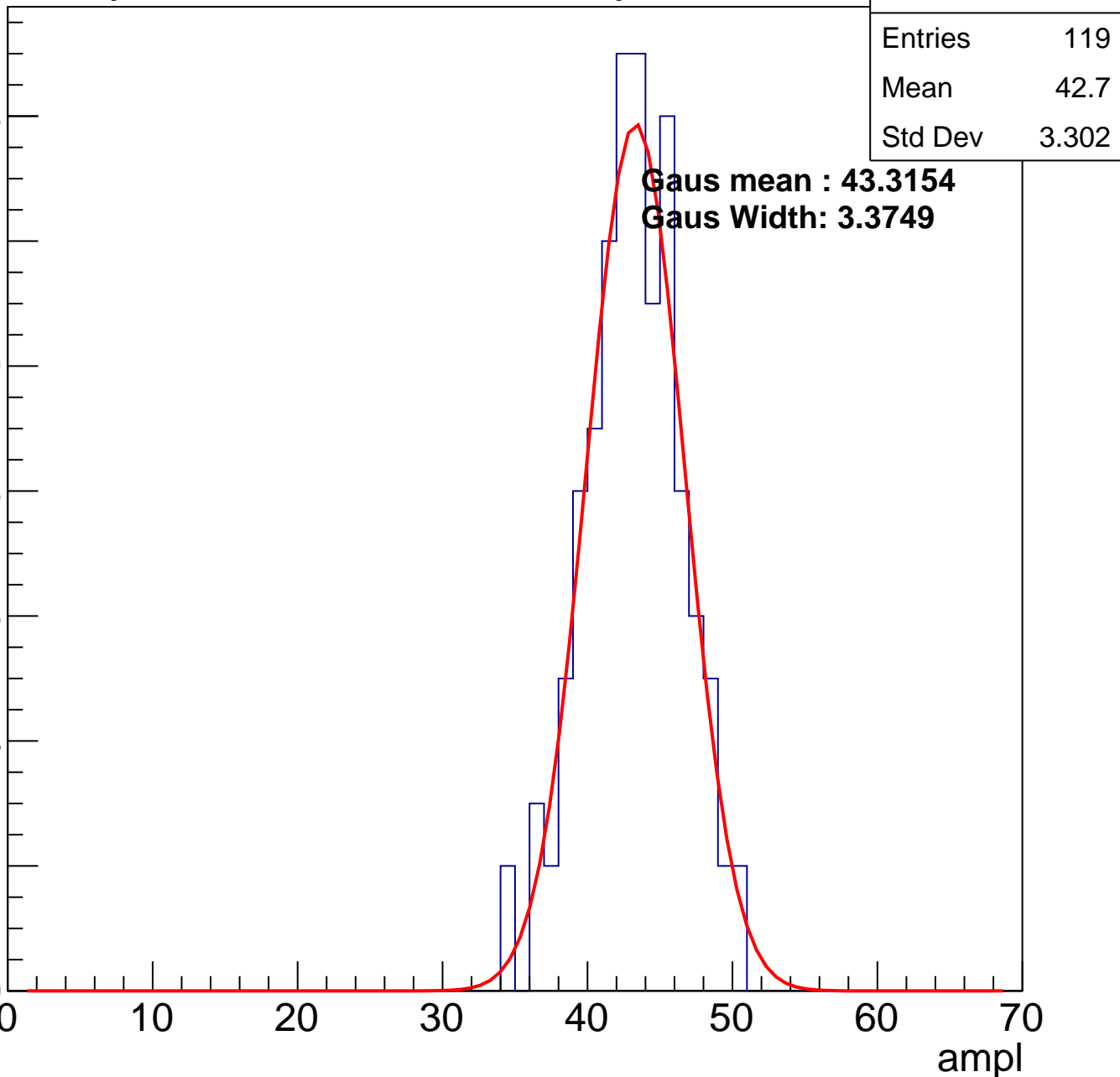
40

50

60

70

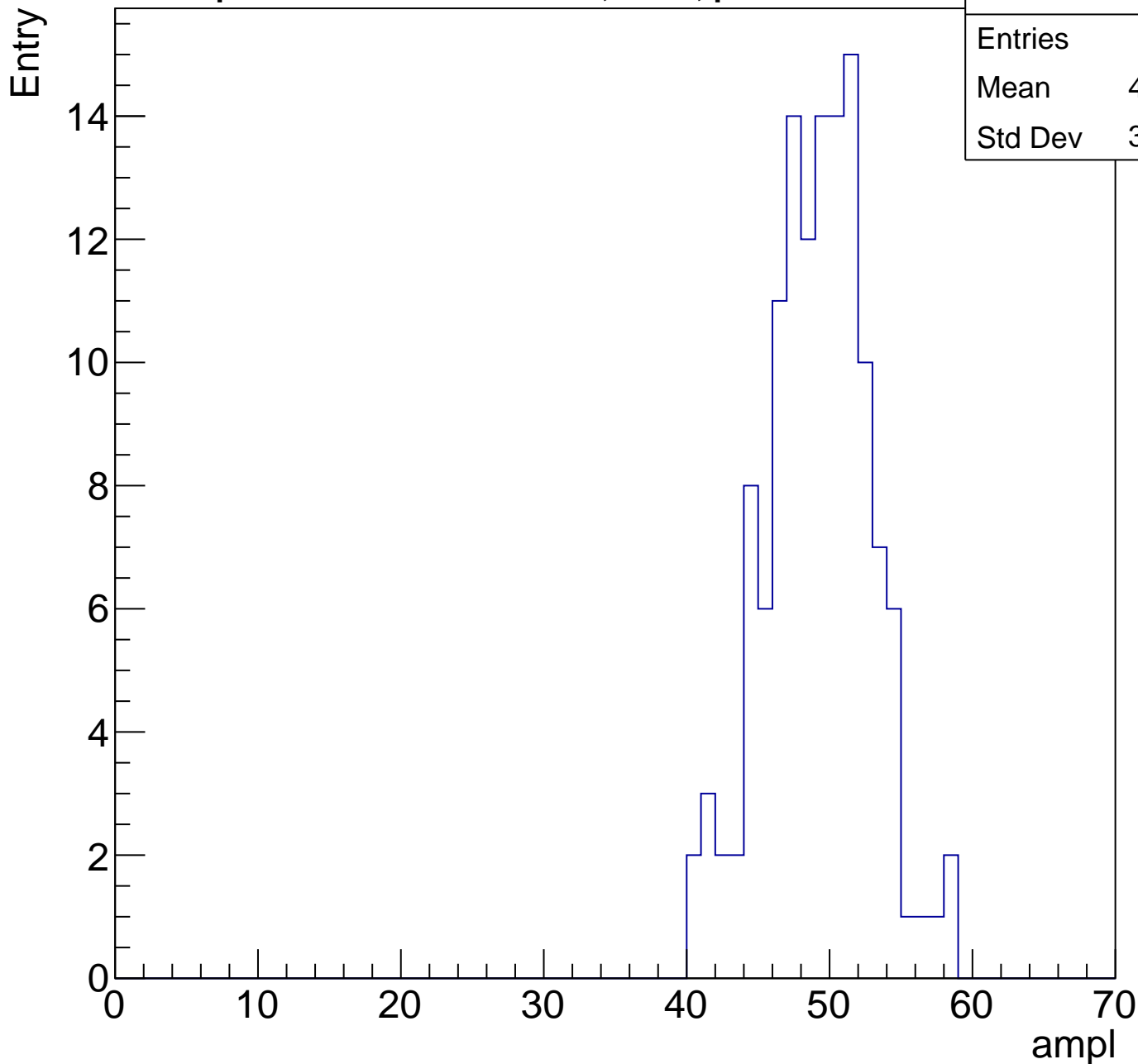
ampl



# B0L101S, U16-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 48.74 |
| Std Dev | 3.618 |

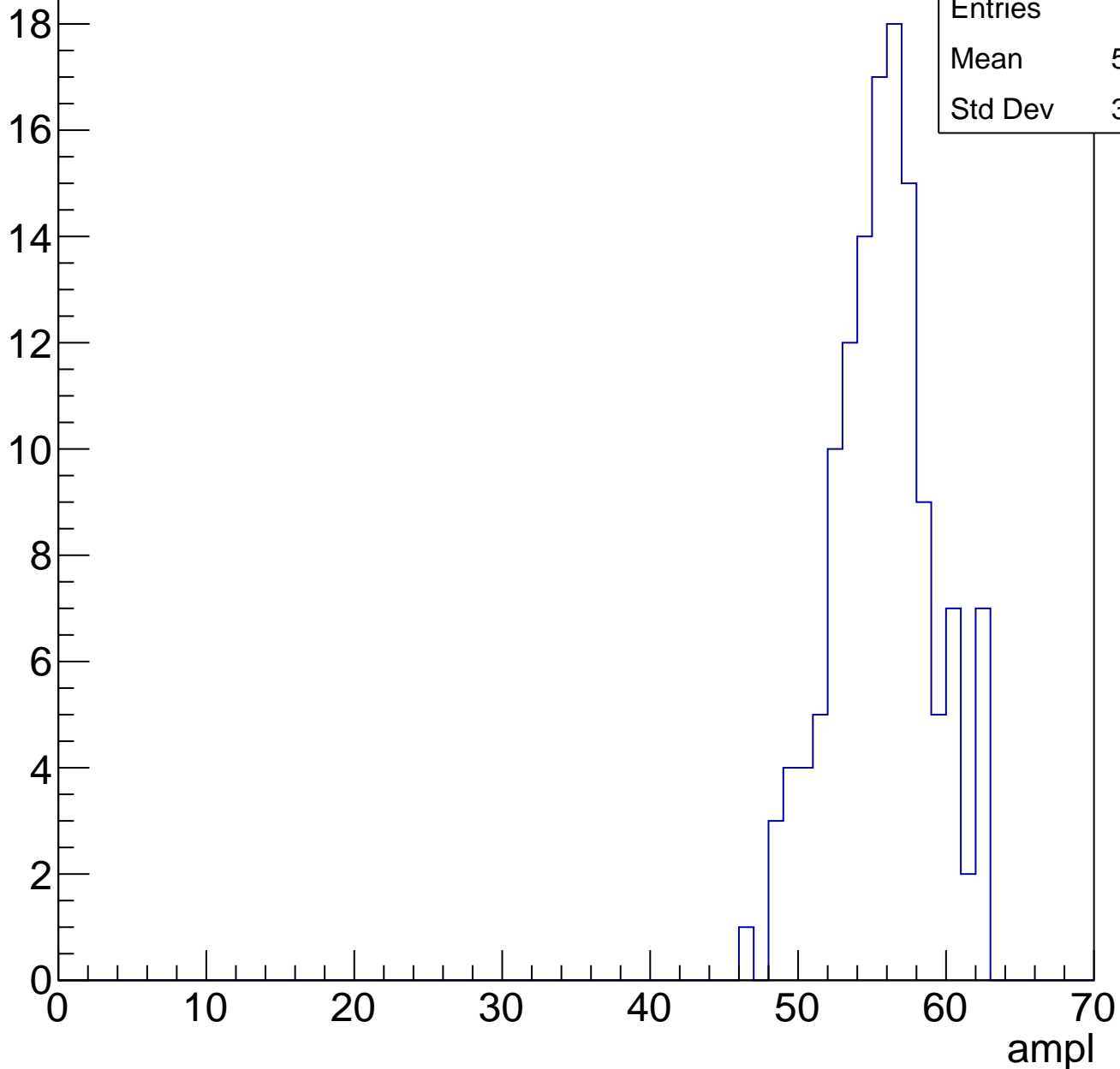


# B0L101S, U16-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 55.22 |
| Std Dev | 3.388 |

Entry



# B0L101S, U16-ch24, adc5

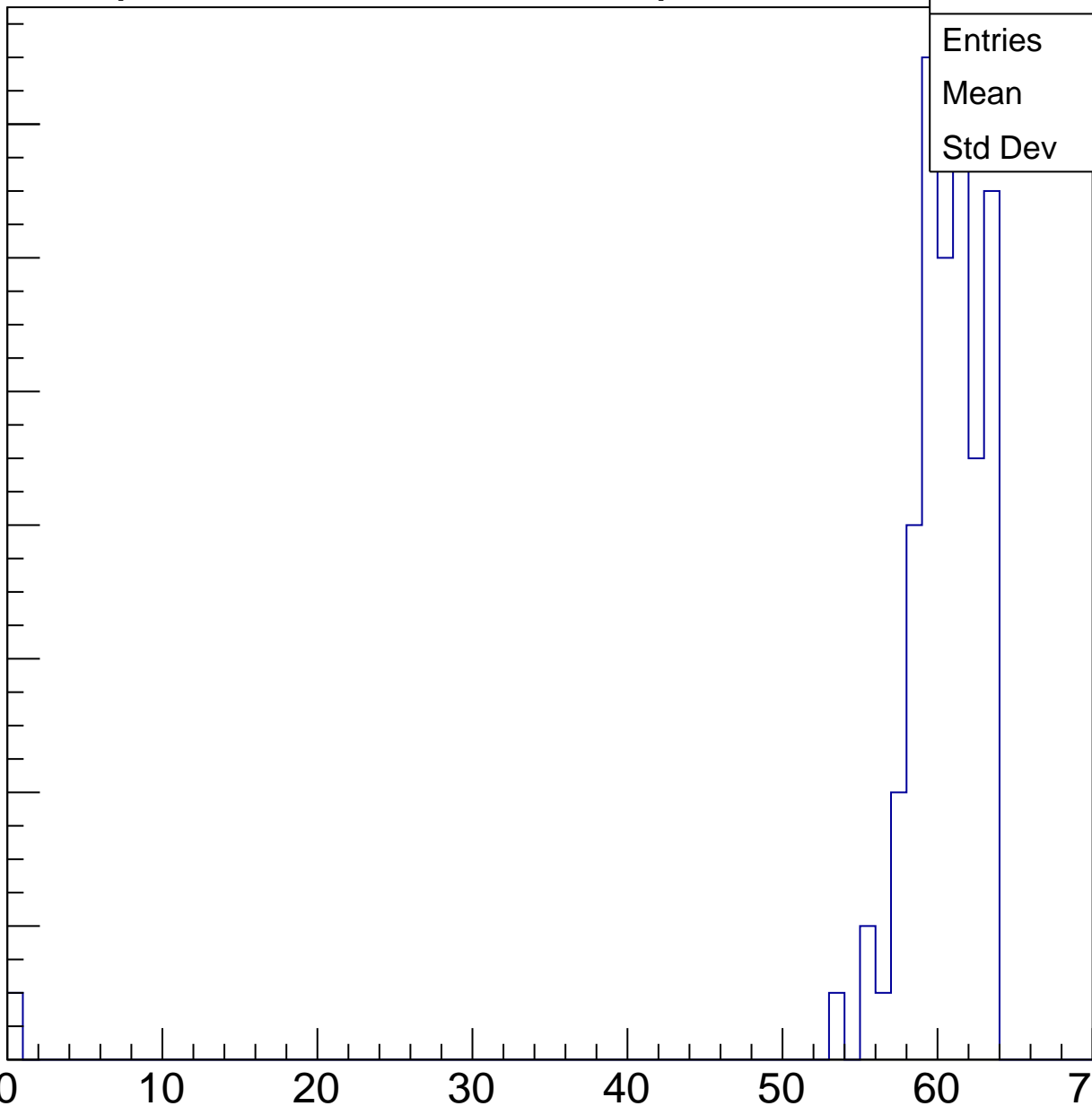
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 59.36 |
| Std Dev | 6.968 |

ampl

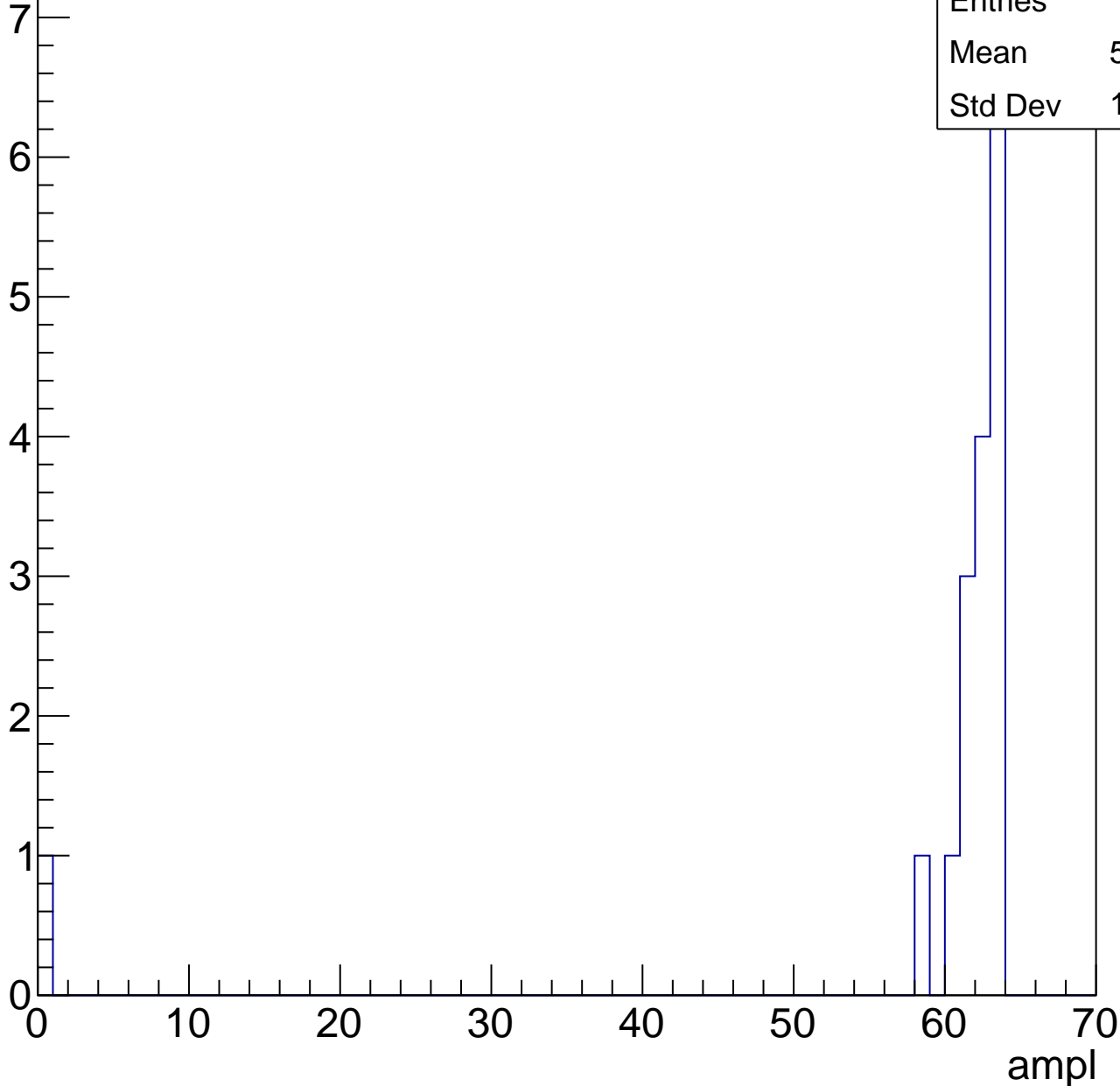


# B0L101S, U16-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 17    |
| Mean    | 58.24 |
| Std Dev | 14.62 |





# B0L101S, U16-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 27.29 |
| Std Dev | 5.276 |

**Gaus mean : 28.5094**

**Gaus Width: 3.2921**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

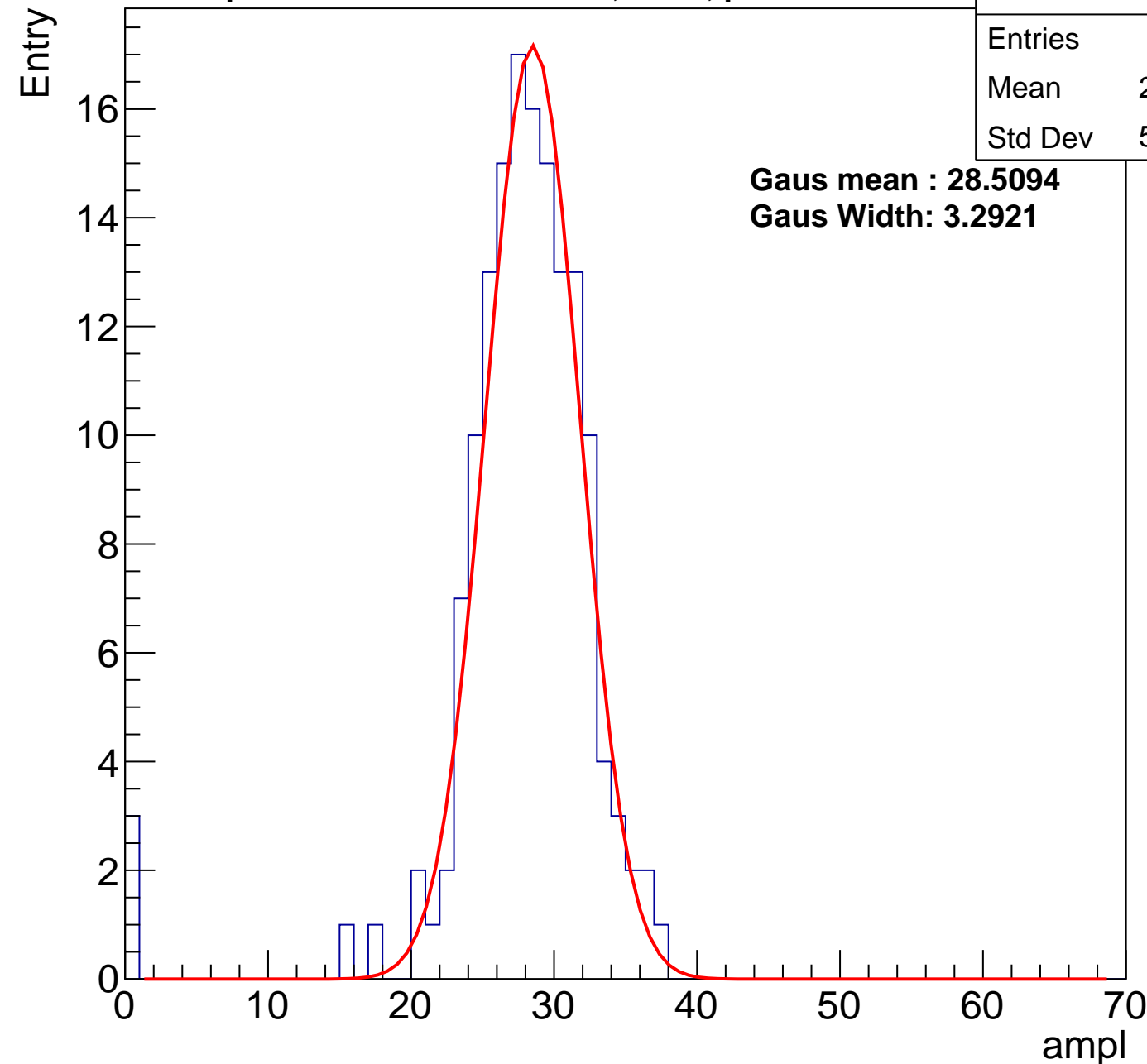
40

50

60

70

ampl

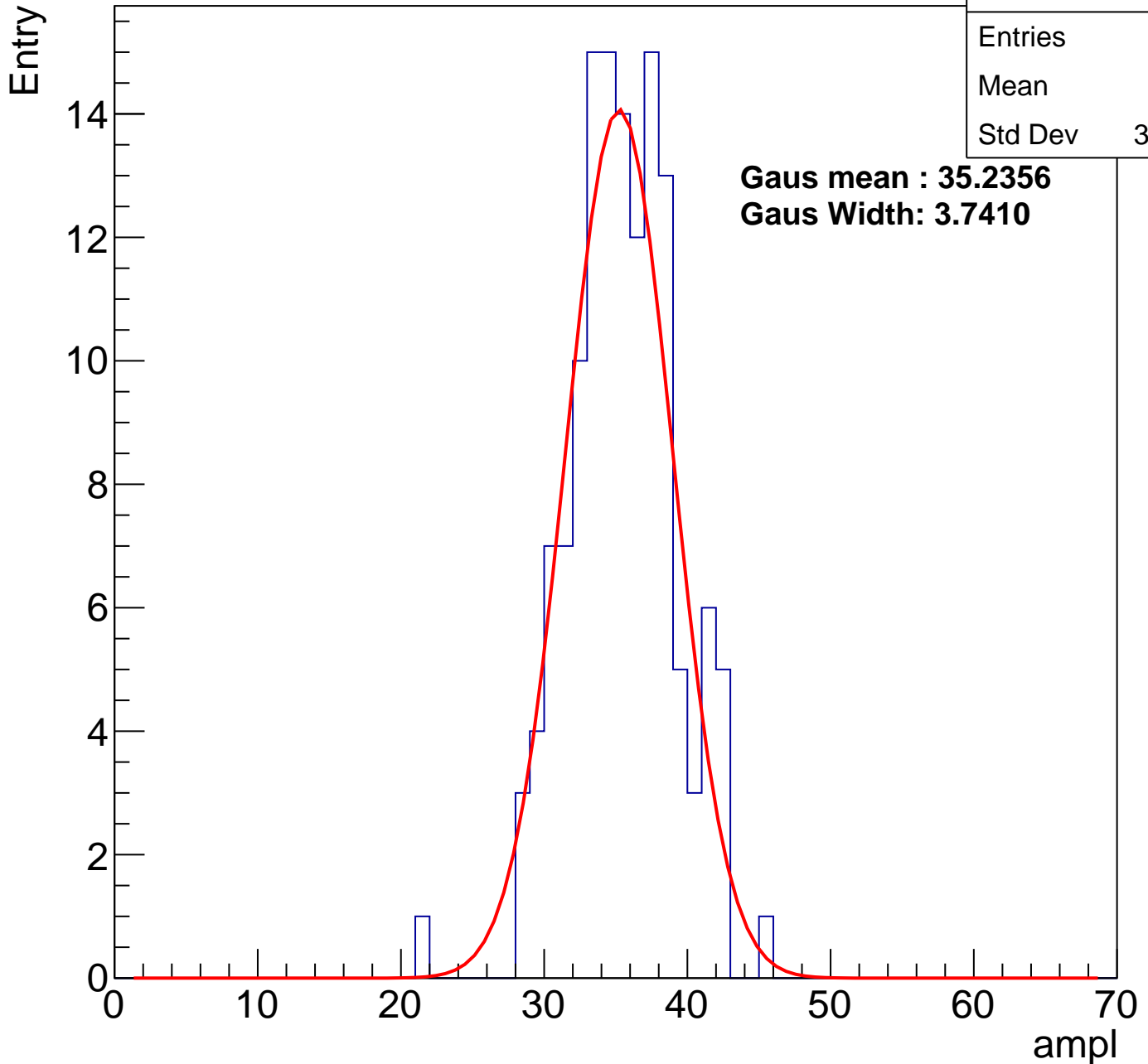


# B0L101S, U16-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 35    |
| Std Dev | 3.682 |

**Gaus mean : 35.2356**  
**Gaus Width: 3.7410**



# B0L101S, U16-ch25, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

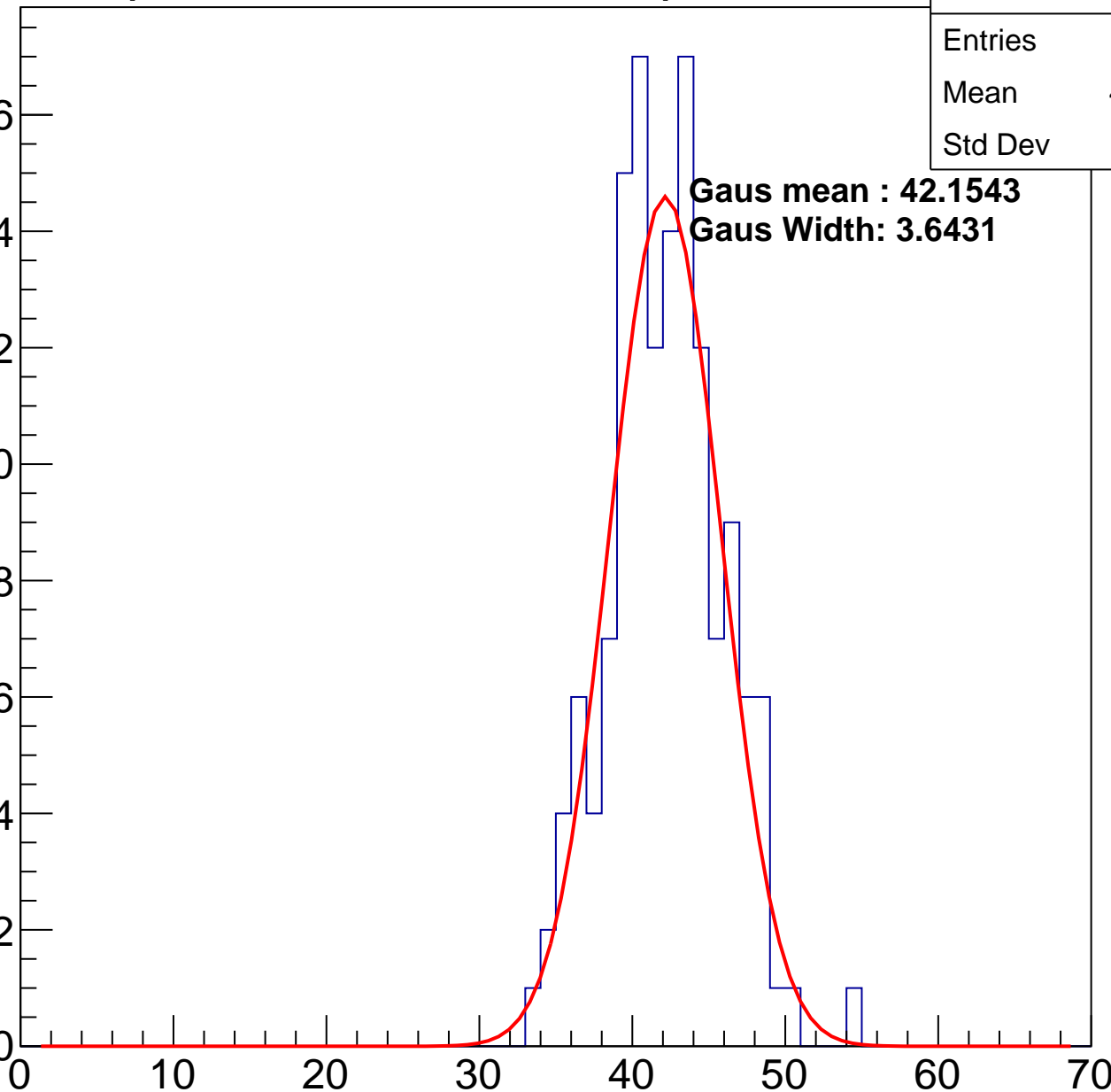
|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 41.74 |
| Std Dev | 3.69  |

**Gaus mean : 42.1543**

**Gaus Width: 3.6431**

16  
14  
12  
10  
8  
6  
4  
2  
0

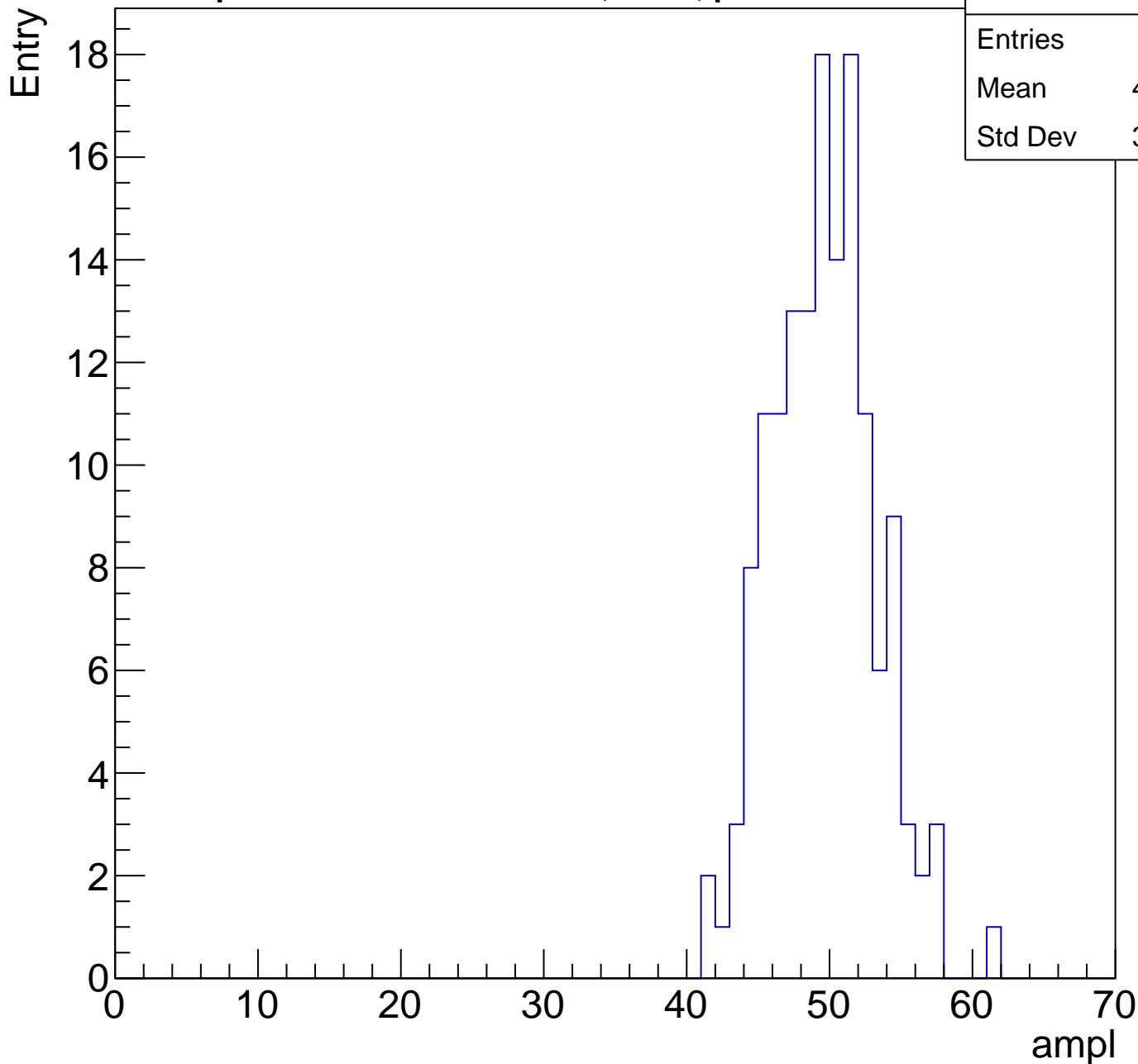
ampl



# B0L101S, U16-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 49.16 |
| Std Dev | 3.566 |



# B0L101S, U16-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

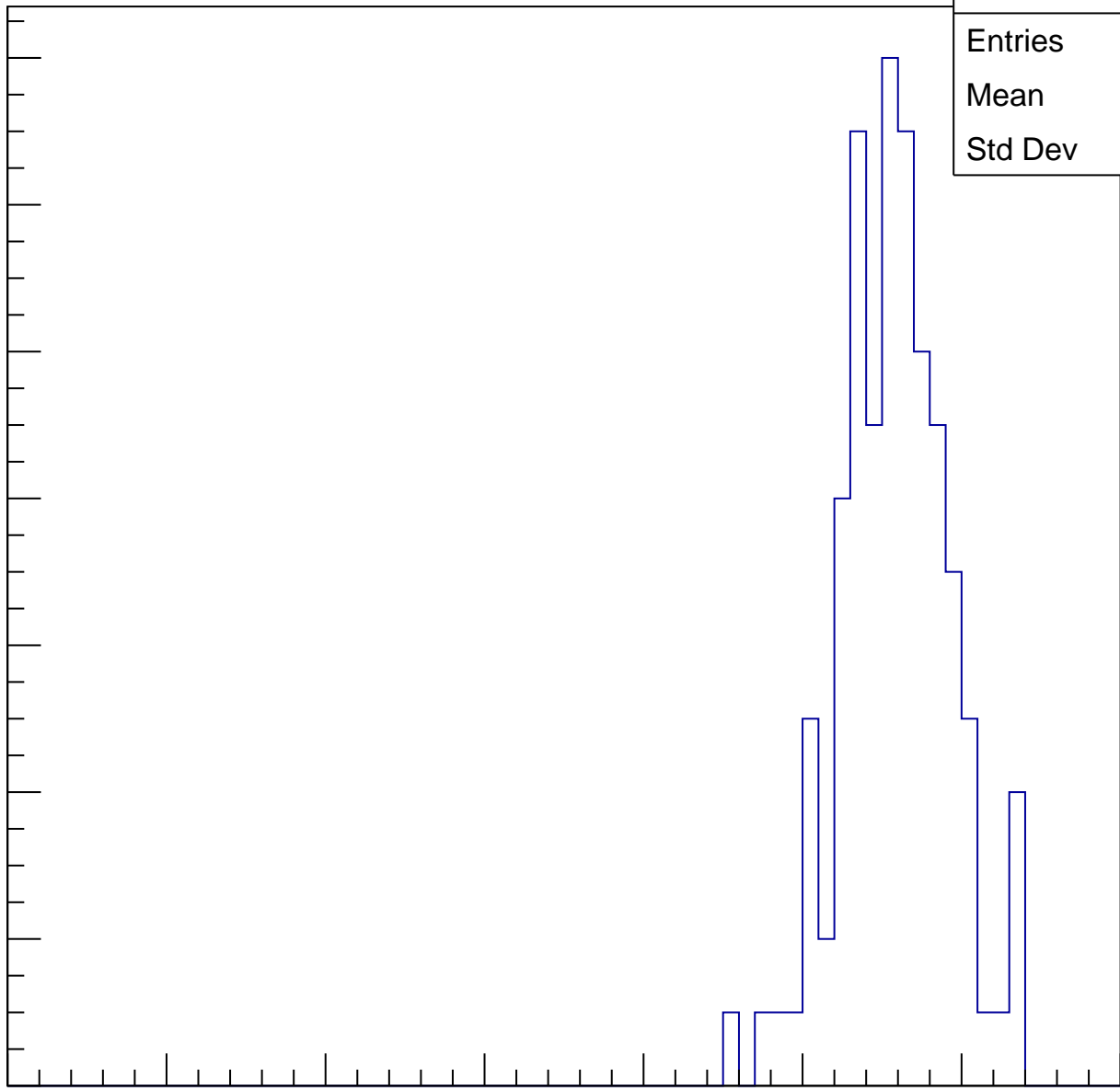
|         |       |
|---------|-------|
| Entries | 105   |
| Mean    | 55.33 |
| Std Dev | 3.436 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch25, adc5

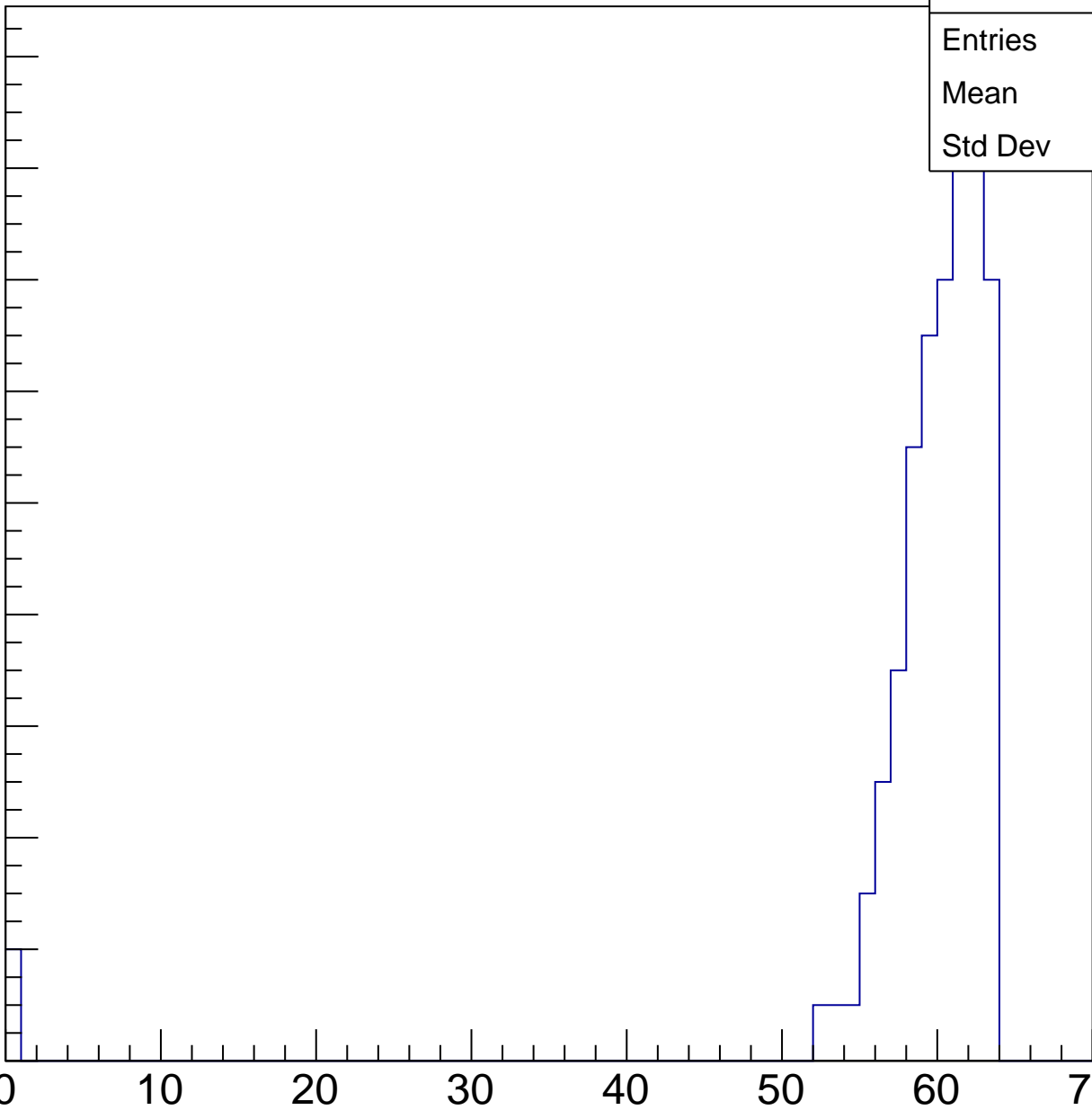
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 58.72 |
| Std Dev | 8.463 |

ampl



# B0L101S, U16-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

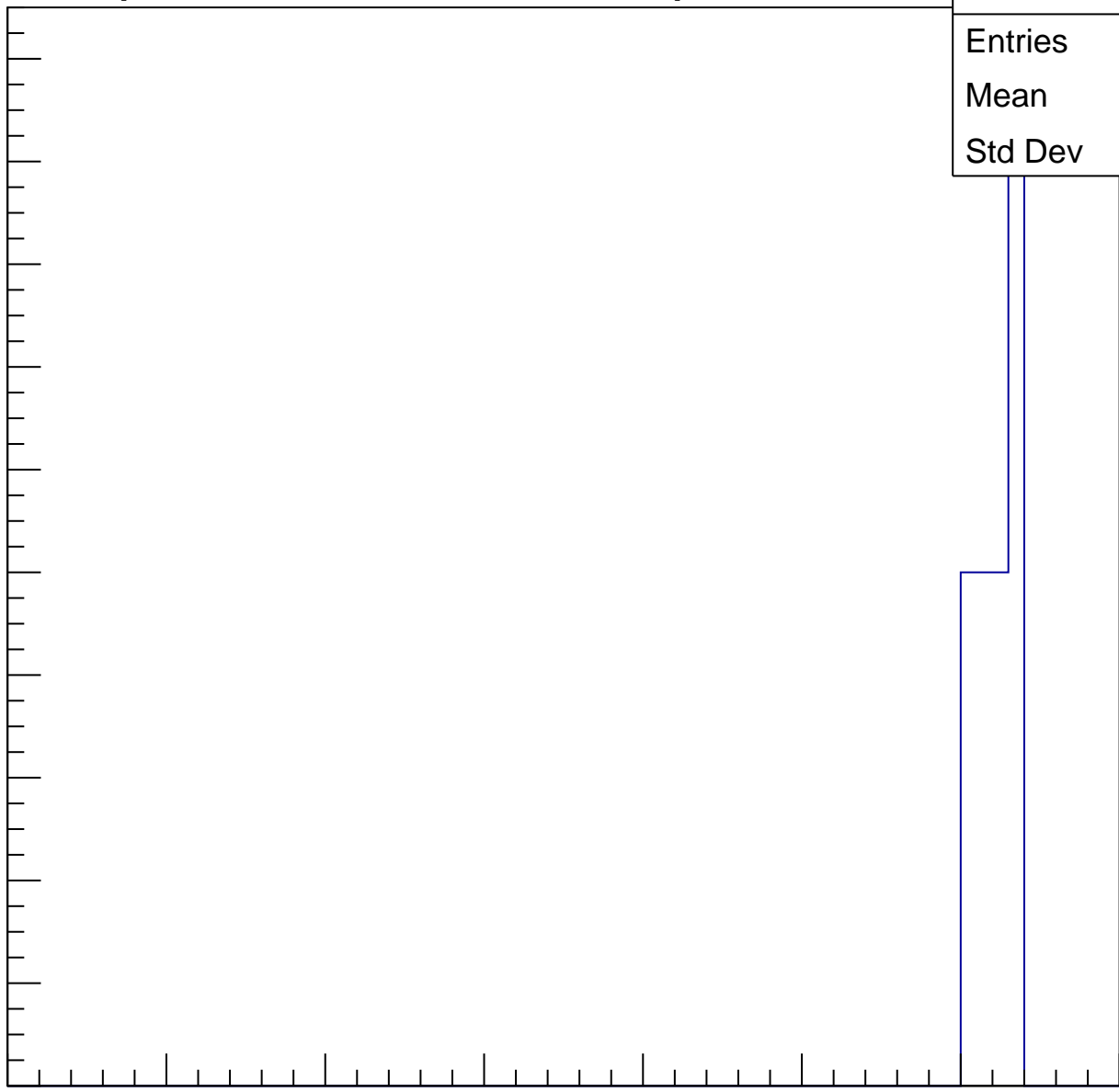
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 5     |
| Mean    | 61.8  |
| Std Dev | 1.166 |

ampl

0 10 20 30 40 50 60 70





# B0L101S, U16-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch26, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 29.1  |
| Std Dev | 5.684 |

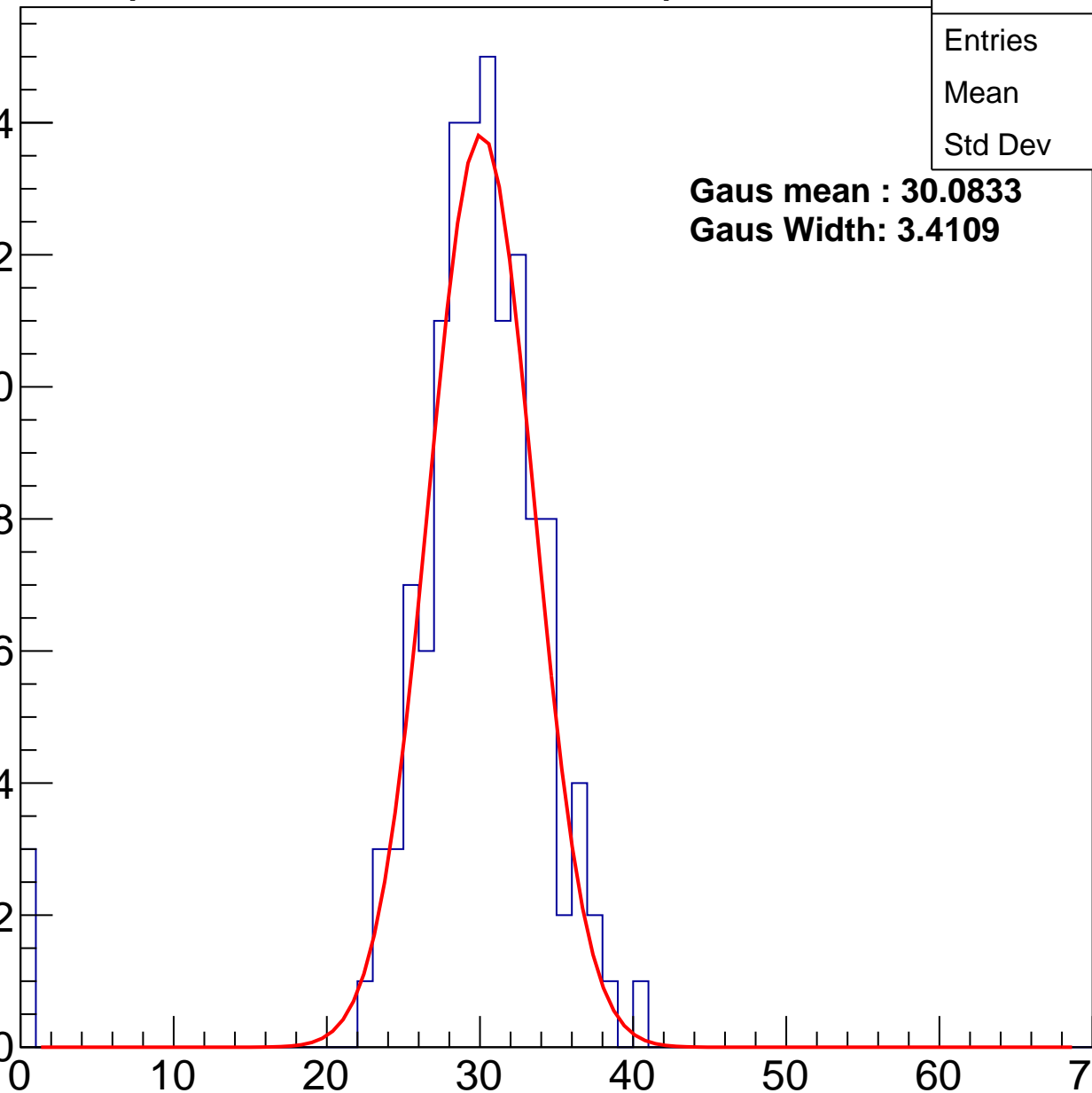
**Gaus mean : 30.0833**

**Gaus Width: 3.4109**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



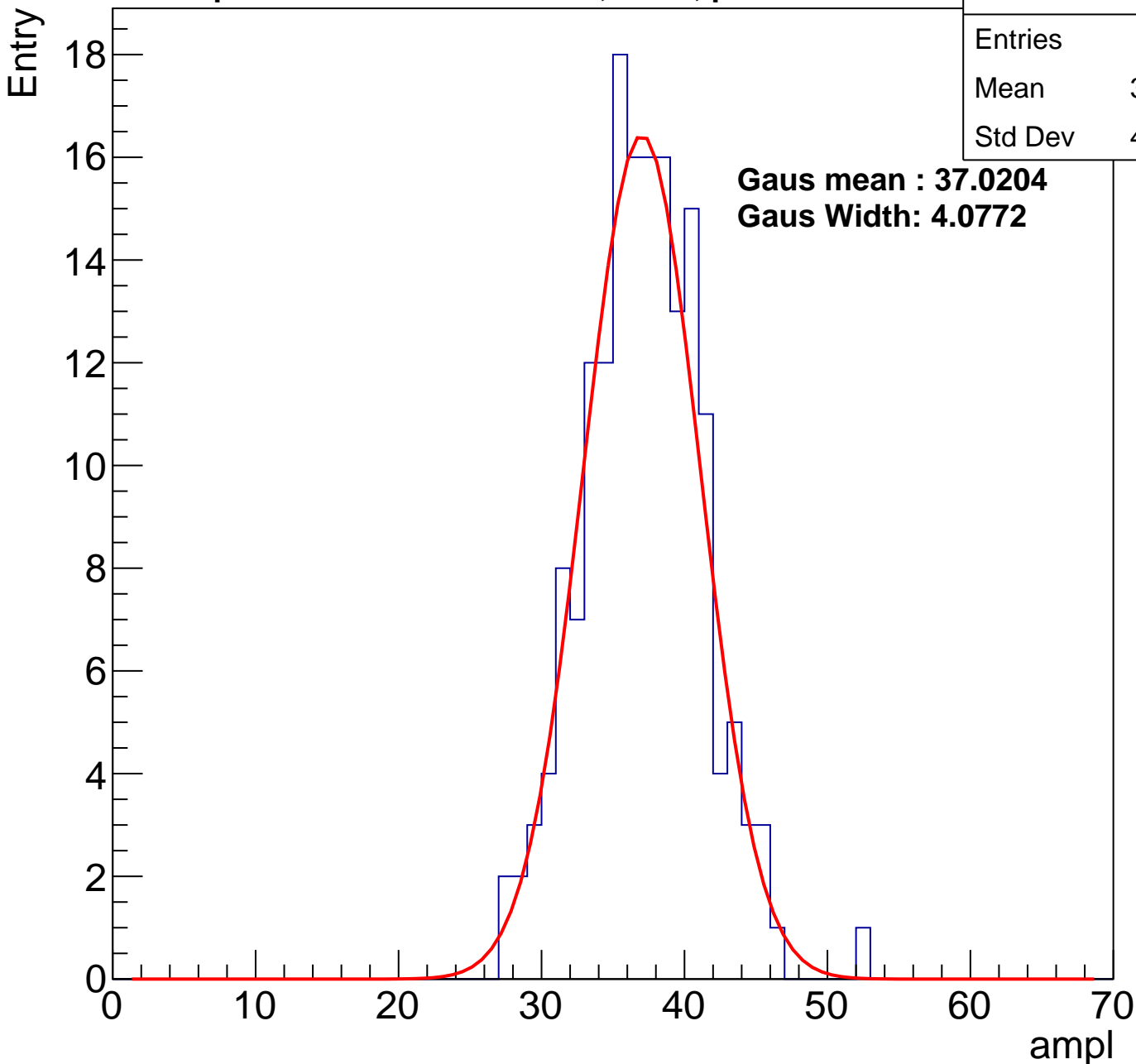
# B0L101S, U16-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 172   |
| Mean    | 36.66 |
| Std Dev | 4.095 |

**Gaus mean : 37.0204**

**Gaus Width: 4.0772**



# B0L101S, U16-ch26, adc2

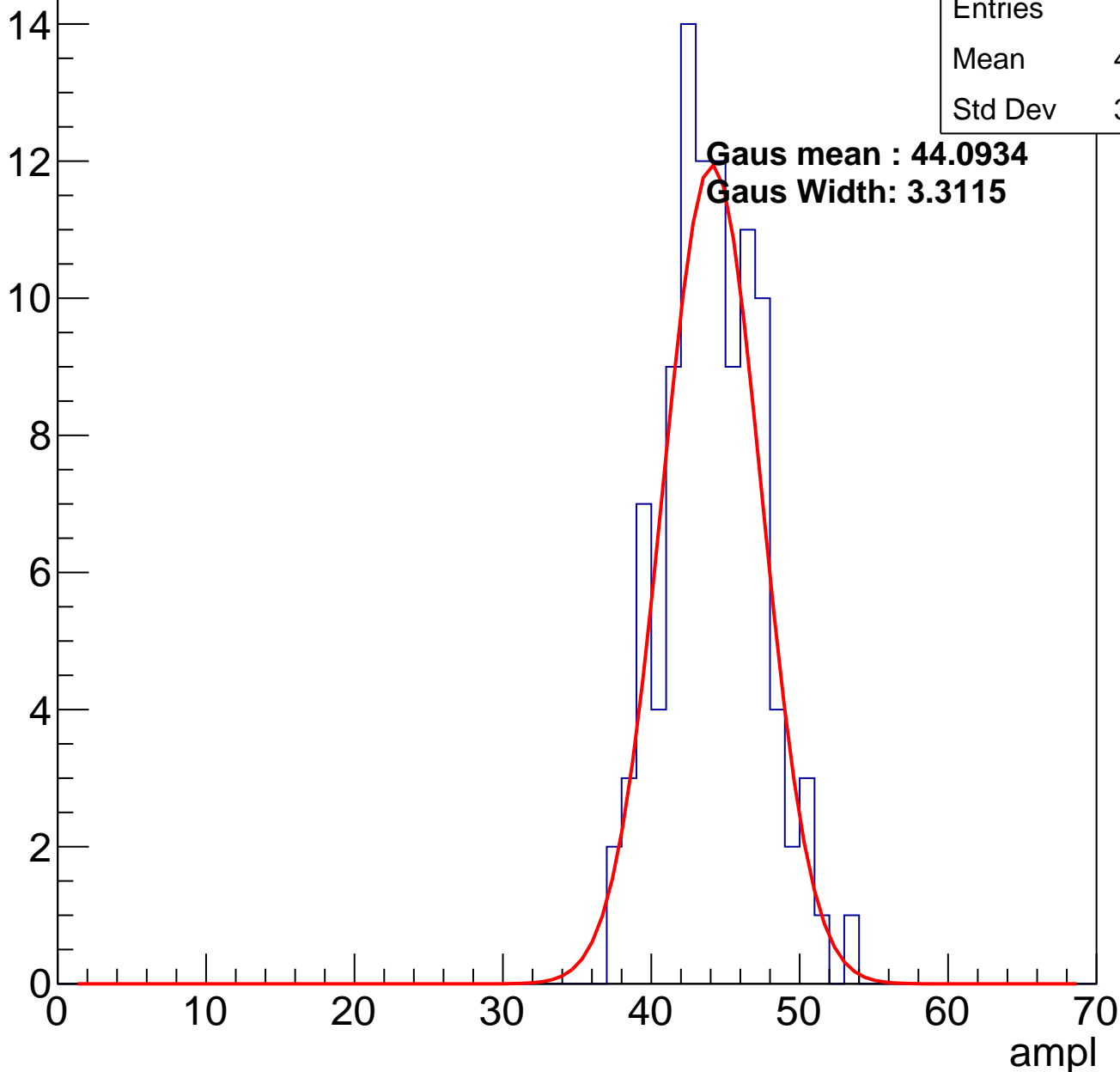
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 43.72 |
| Std Dev | 3.224 |

**Gaus mean : 44.0934**

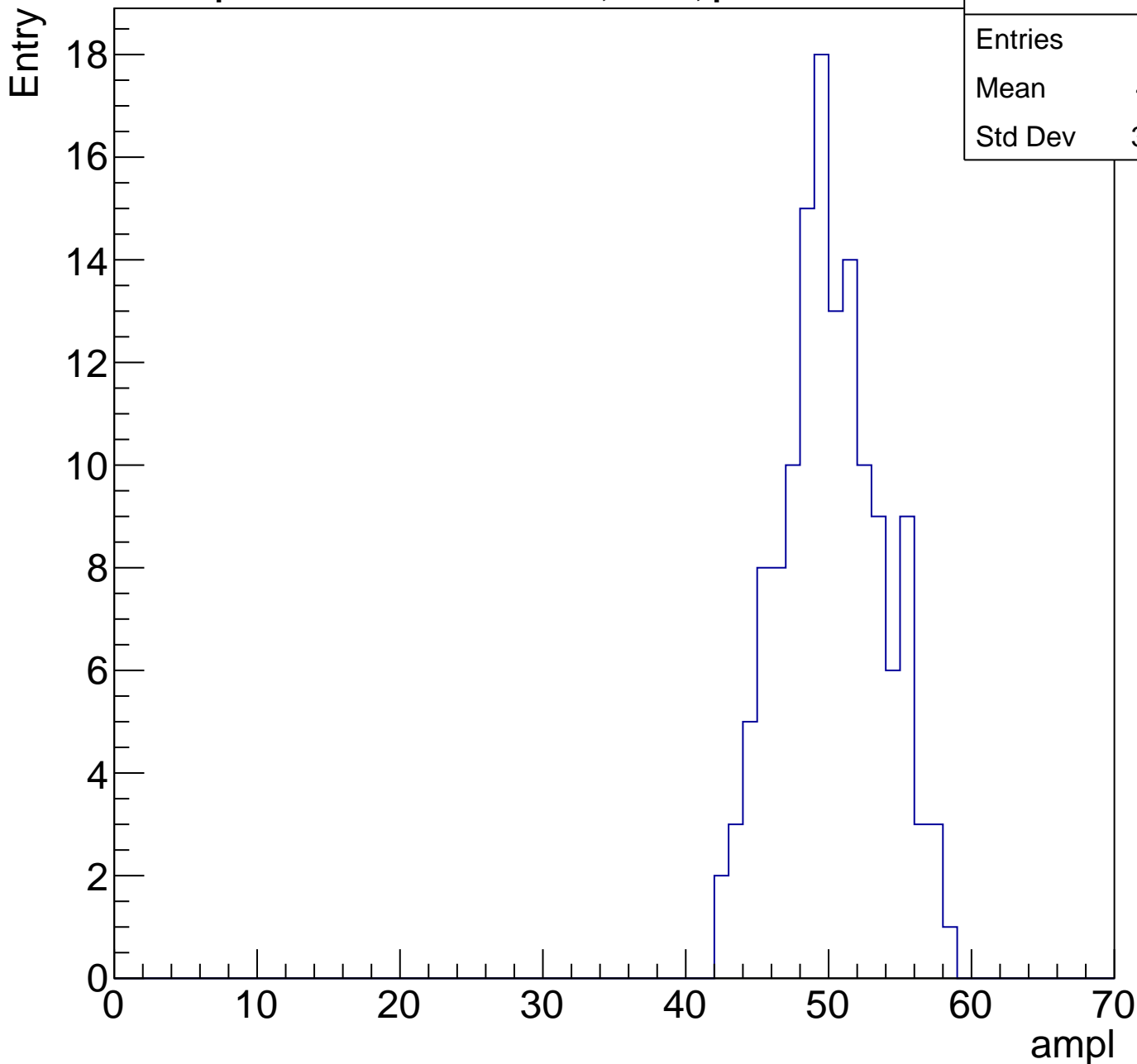
**Gaus Width: 3.3115**



# B0L101S, U16-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

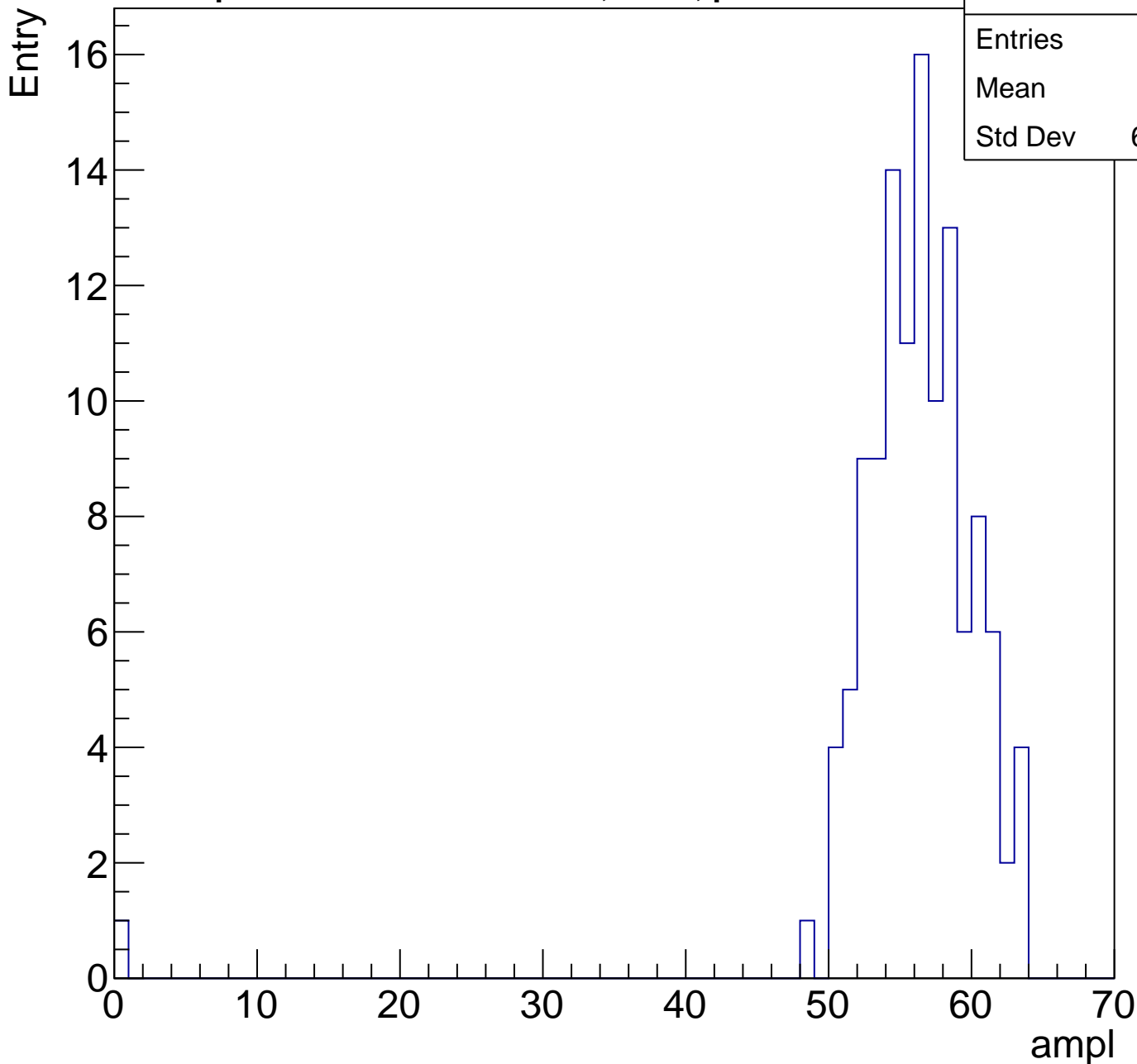
|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 49.71 |
| Std Dev | 3.537 |



# B0L101S, U16-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 55.5  |
| Std Dev | 6.076 |



# B0L101S, U16-ch26, adc5

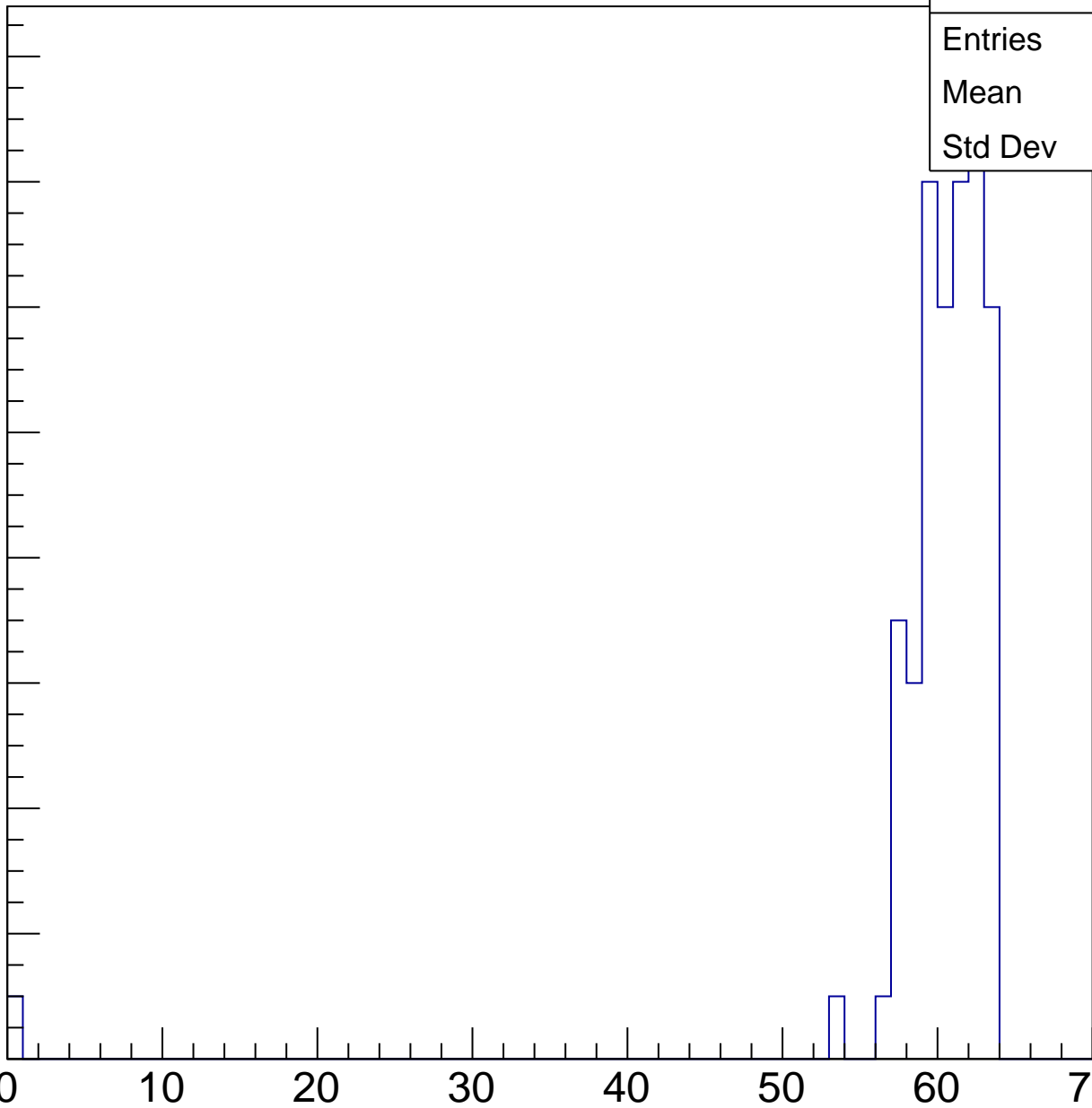
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 84    |
| Mean    | 59.57 |
| Std Dev | 6.846 |

ampl



# B0L101S, U16-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch27, adc0

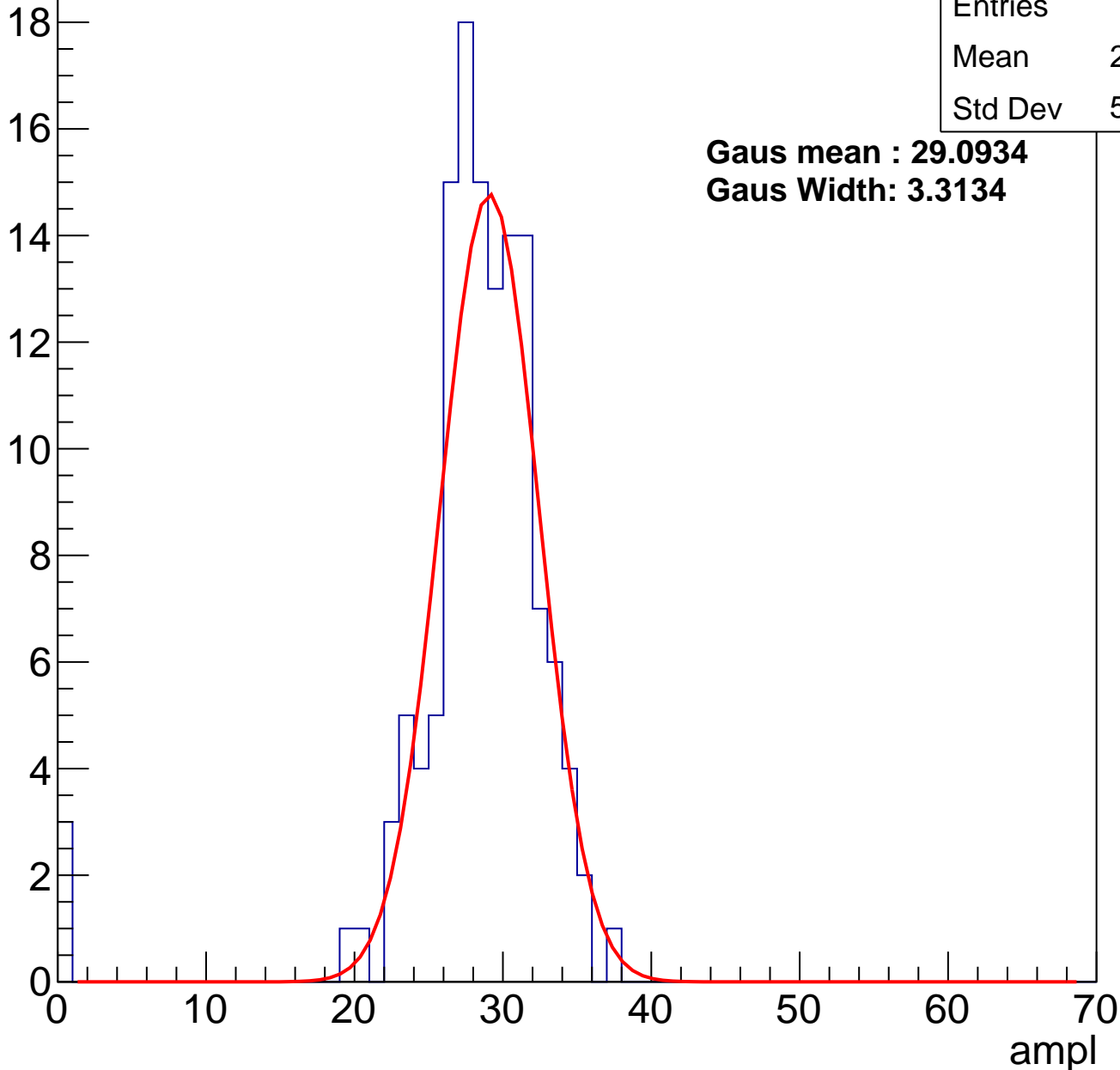
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 27.73 |
| Std Dev | 5.306 |

**Gaus mean : 29.0934**

**Gaus Width: 3.3134**

Entry



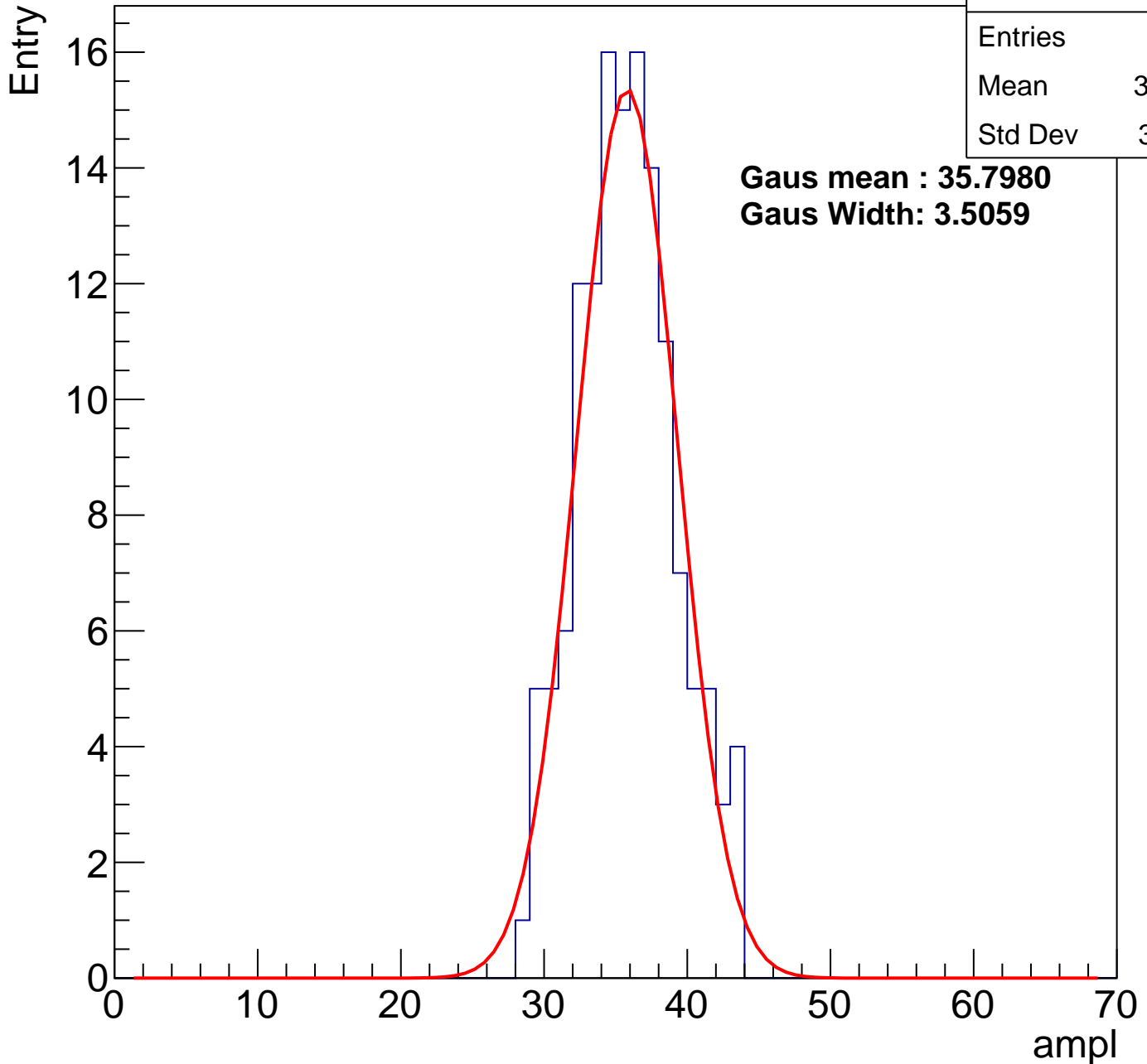
# B0L101S, U16-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 35.37 |
| Std Dev | 3.411 |

**Gaus mean : 35.7980**

**Gaus Width: 3.5059**



# B0L101S, U16-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 42.35 |
| Std Dev | 3.736 |

**Gaus mean : 42.5760**

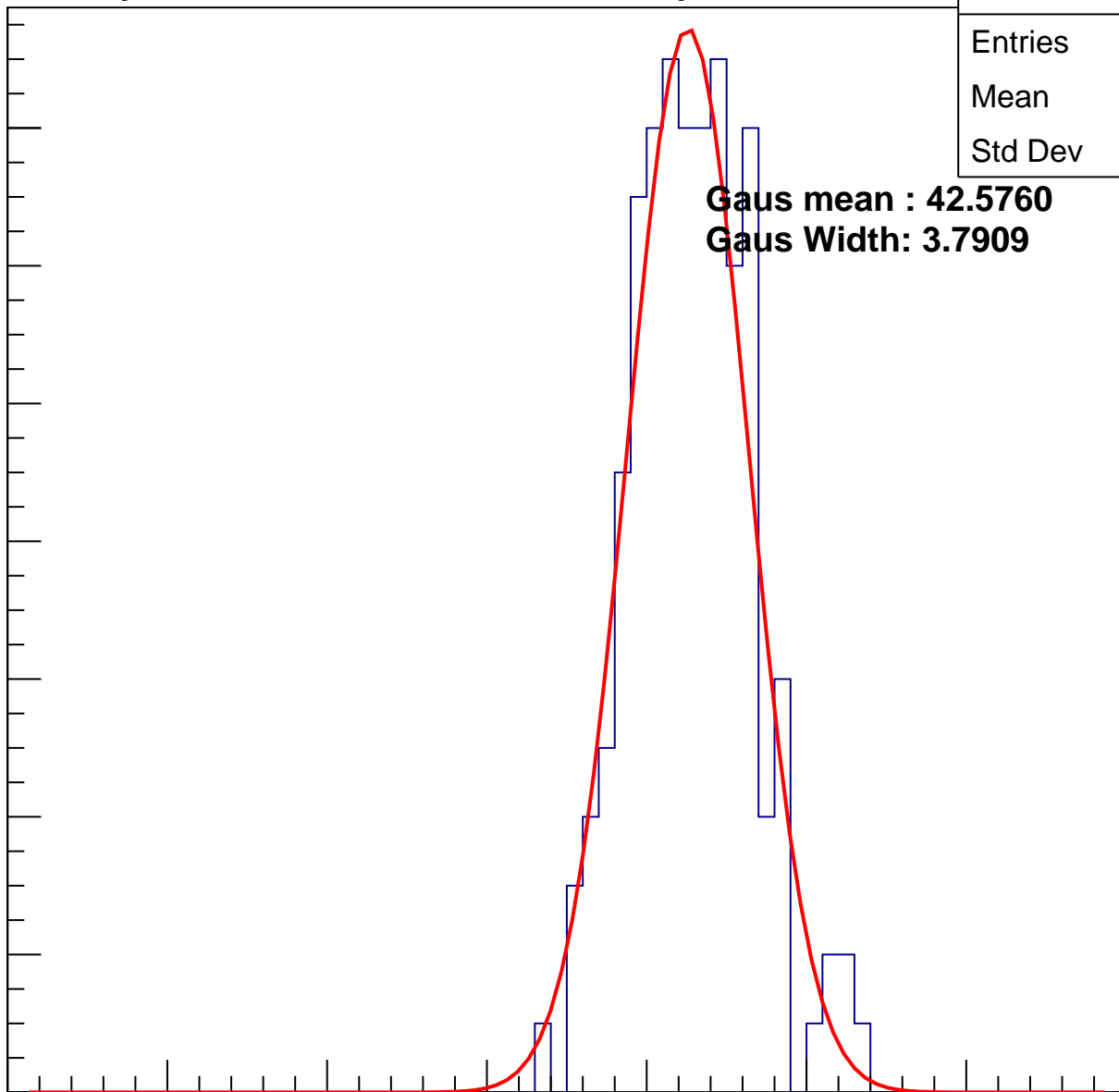
**Gaus Width: 3.7909**

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

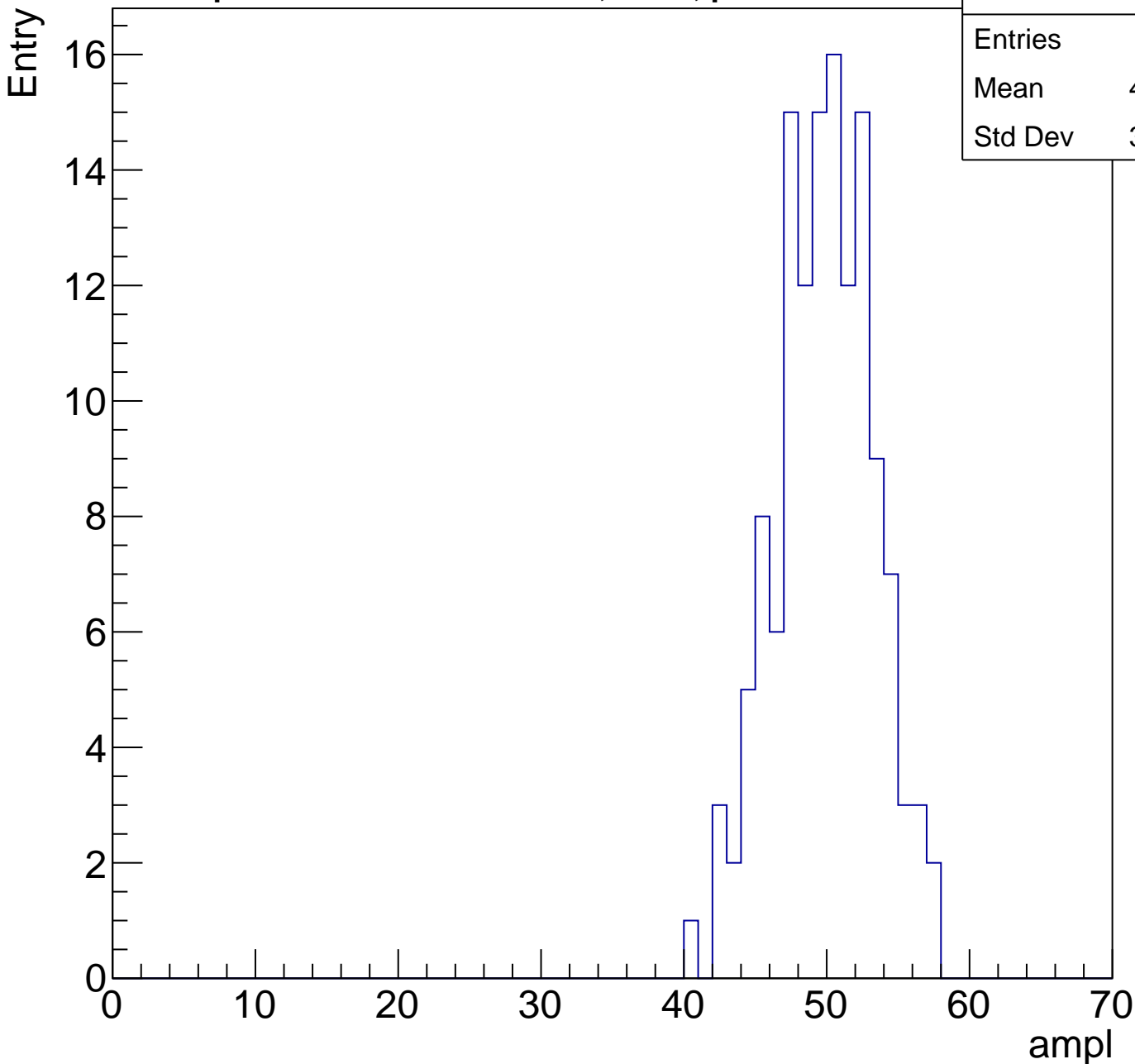
ampl



# B0L101S, U16-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

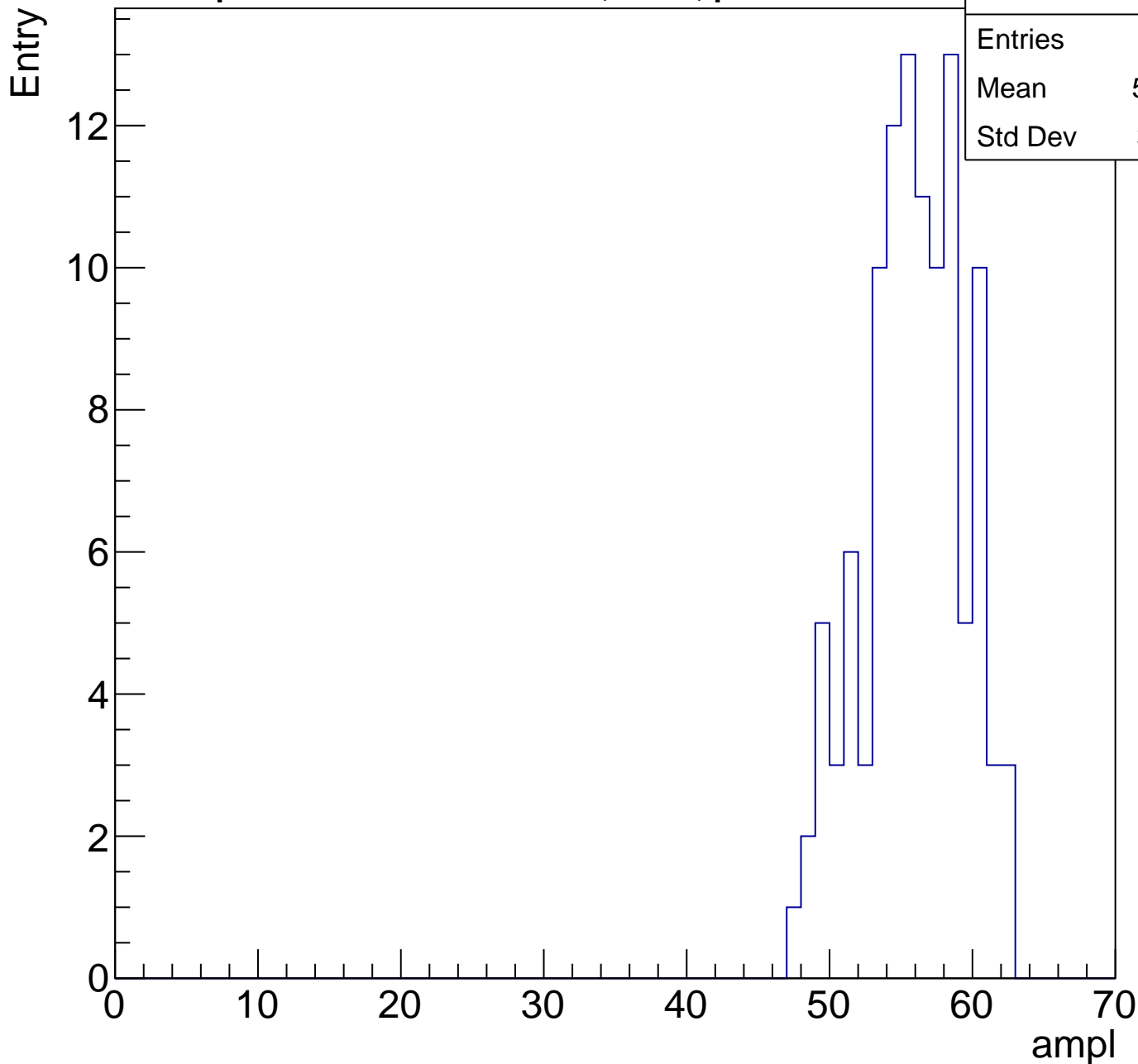
|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 49.39 |
| Std Dev | 3.412 |



# B0L101S, U16-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 55.43 |
| Std Dev | 3.471 |



# B0L101S, U16-ch27, adc5

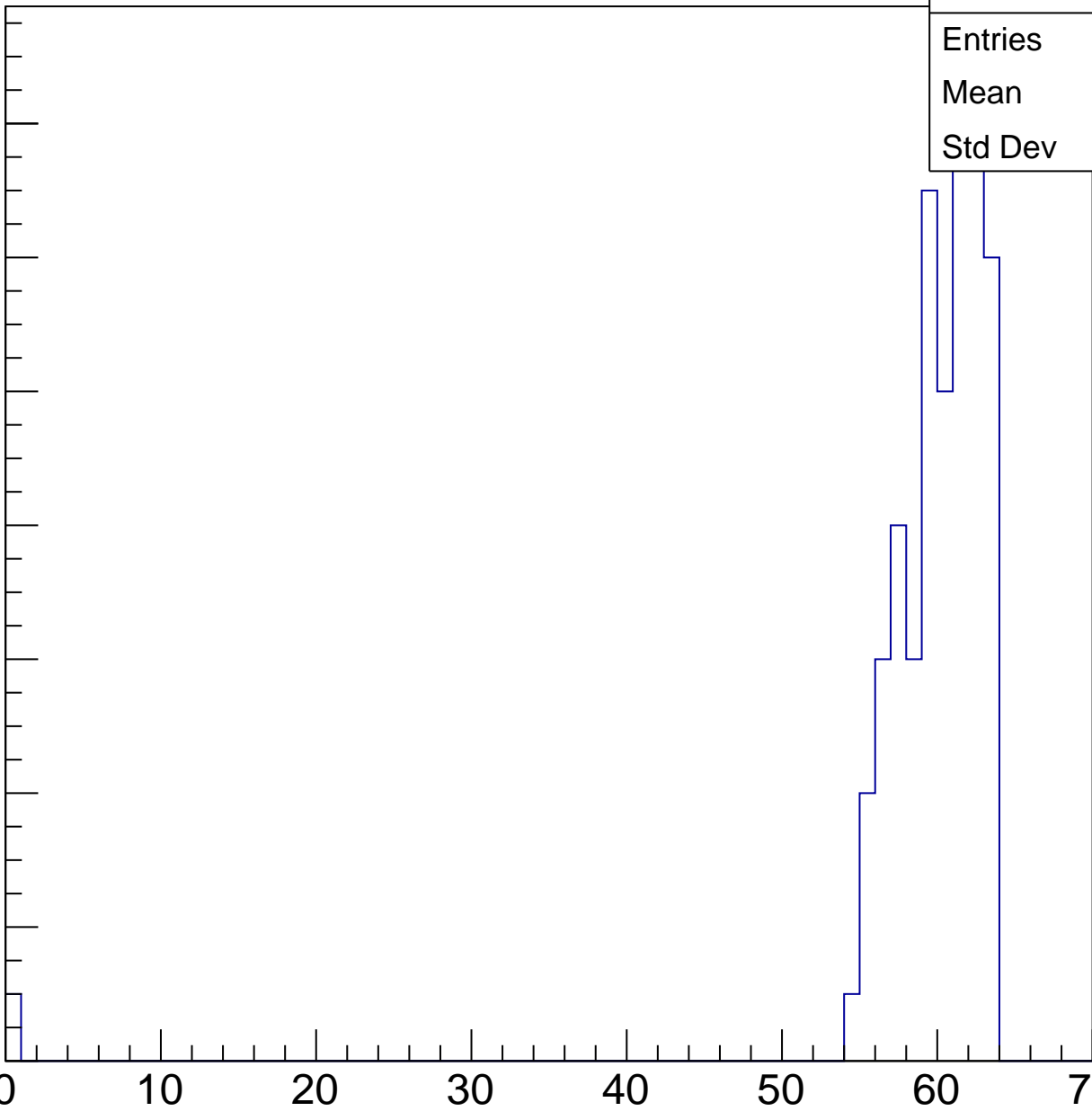
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 59.11 |
| Std Dev | 6.707 |

ampl

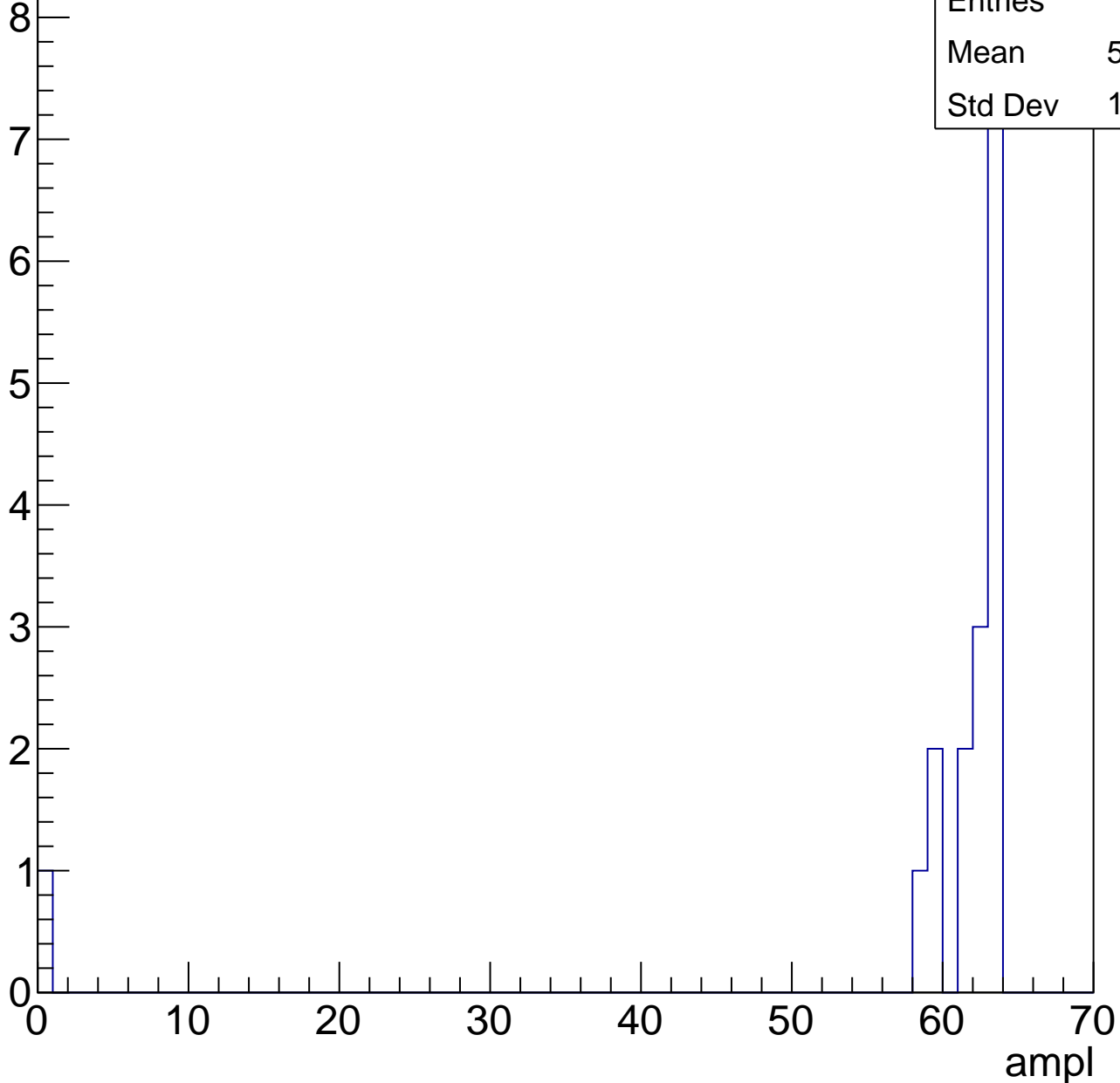


# B0L101S, U16-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 17    |
| Mean    | 58.12 |
| Std Dev | 14.62 |





# B0L101S, U16-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch28, adc0

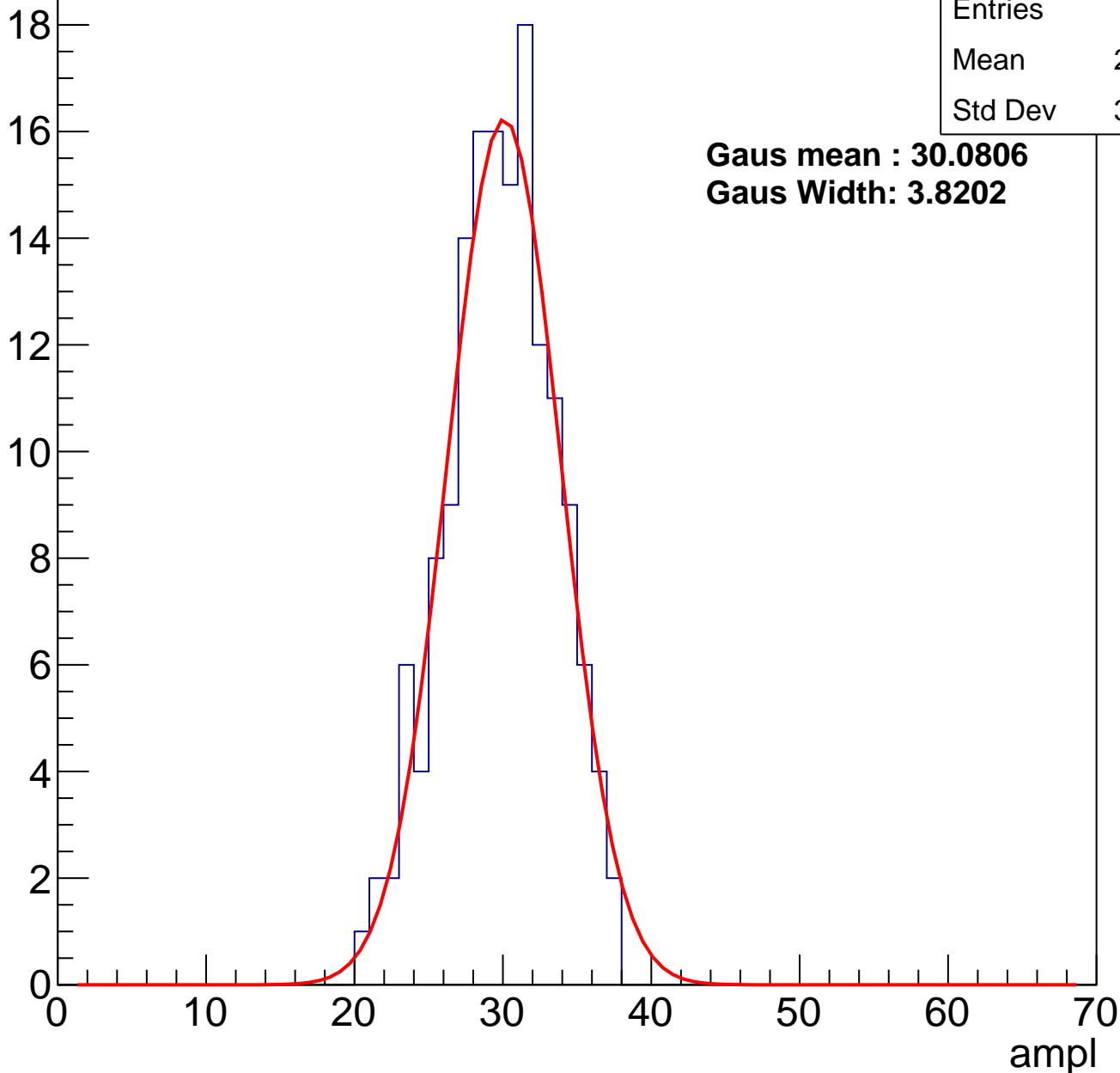
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 29.37 |
| Std Dev | 3.608 |

**Gaus mean : 30.0806**

**Gaus Width: 3.8202**

Entry



# B0L101S, U16-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 36.75 |
| Std Dev | 3.426 |

**Gaus mean : 37.2582**

**Gaus Width: 3.1314**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

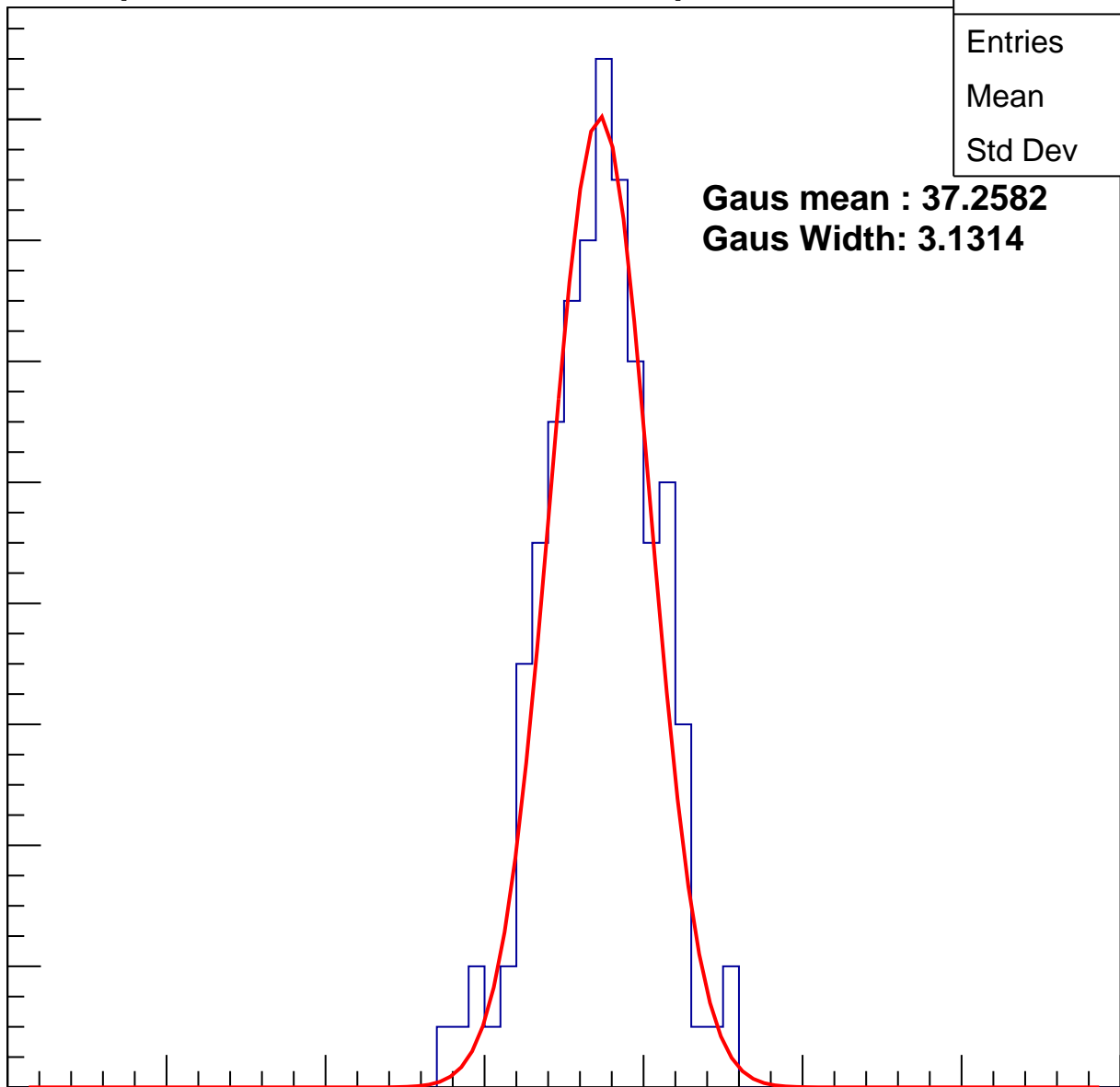
40

50

60

70

ampl



# B0L101S, U16-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

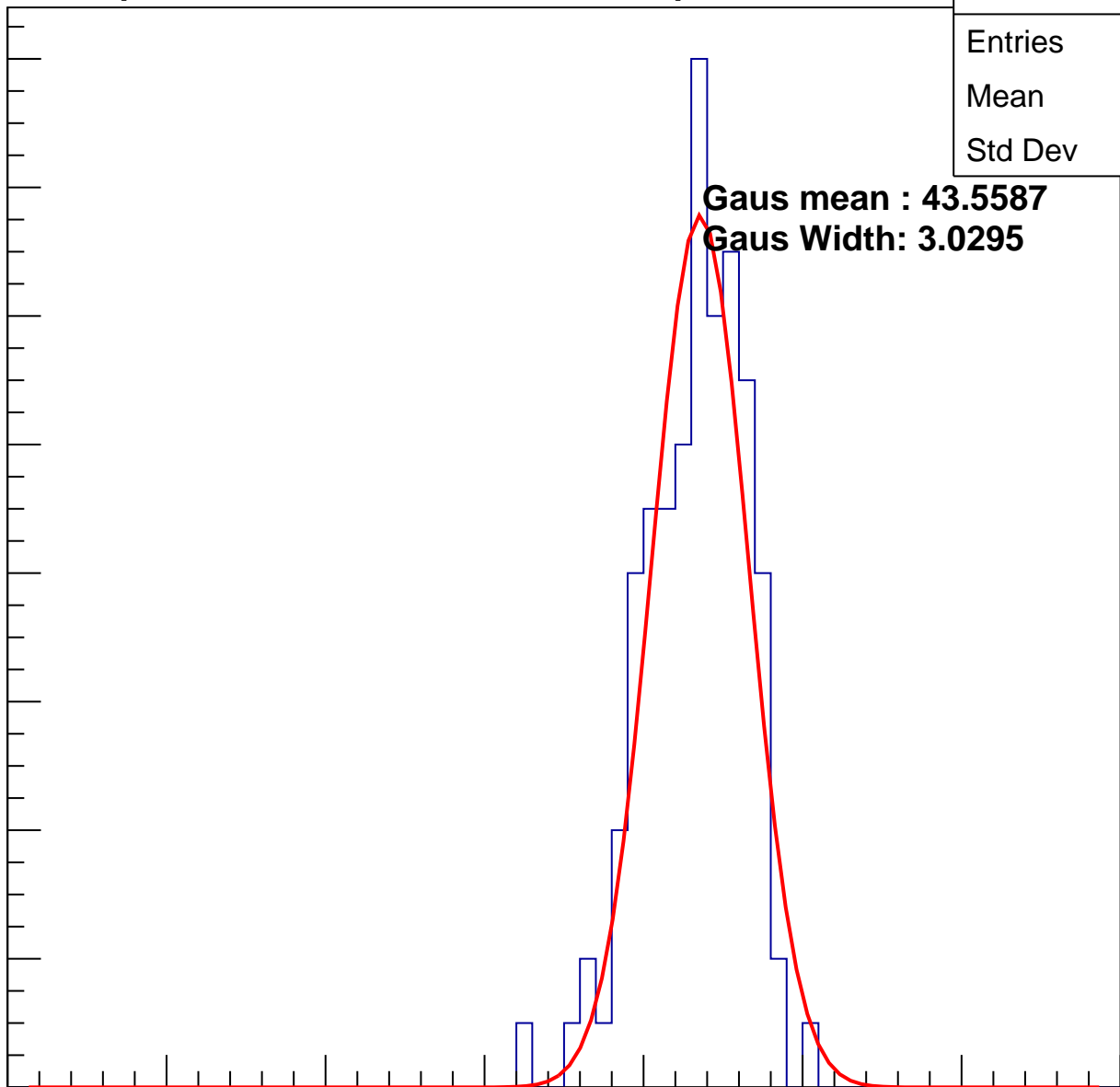
|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 42.76 |
| Std Dev | 3.13  |

**Gaus mean : 43.5587**

**Gaus Width: 3.0295**

0 10 20 30 40 50 60 70

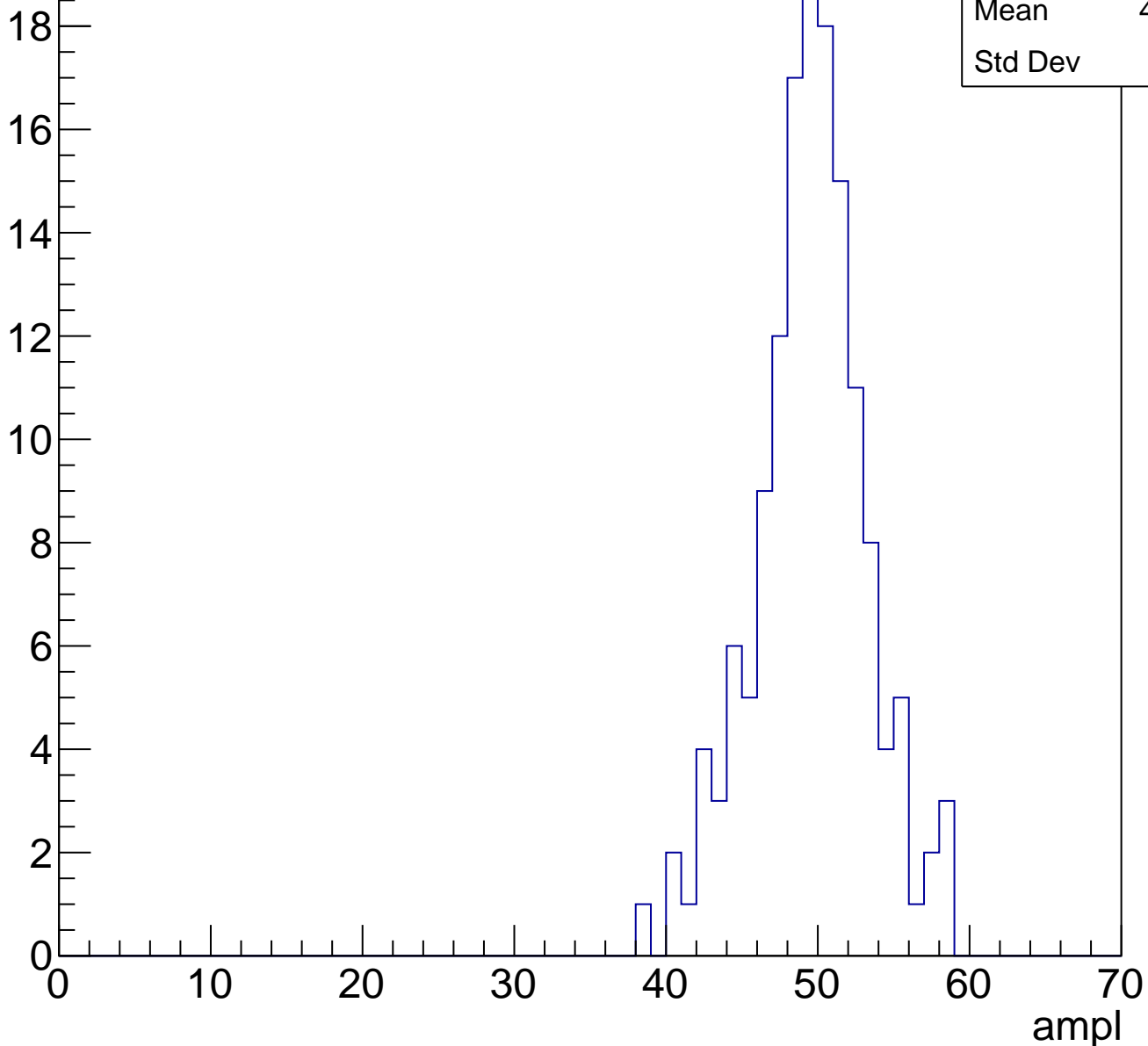
ampl



# B0L101S, U16-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

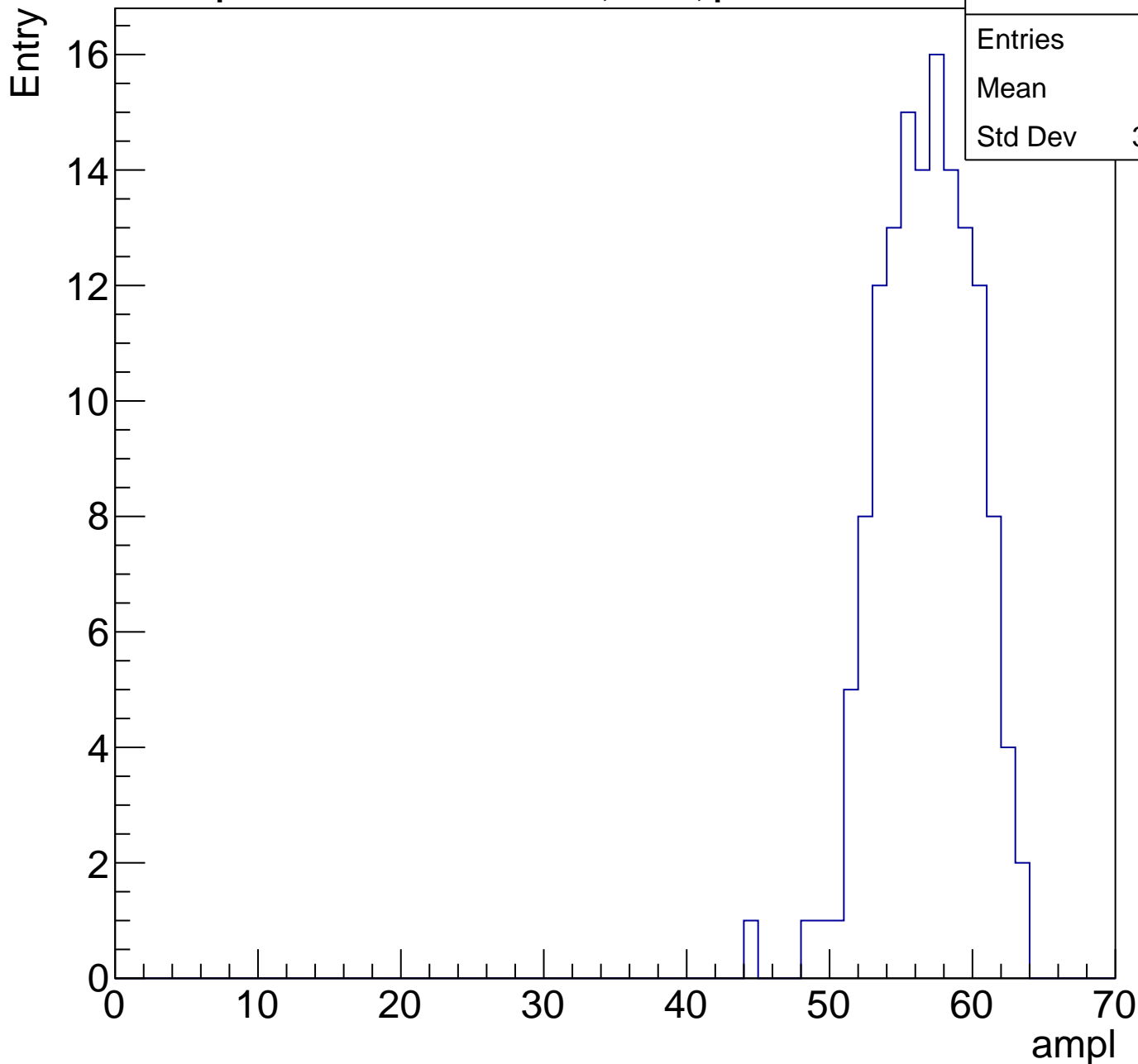


|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 49.08 |
| Std Dev | 3.73  |

# B0L101S, U16-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 56.3  |
| Std Dev | 3.307 |



# B0L101S, U16-ch28, adc5

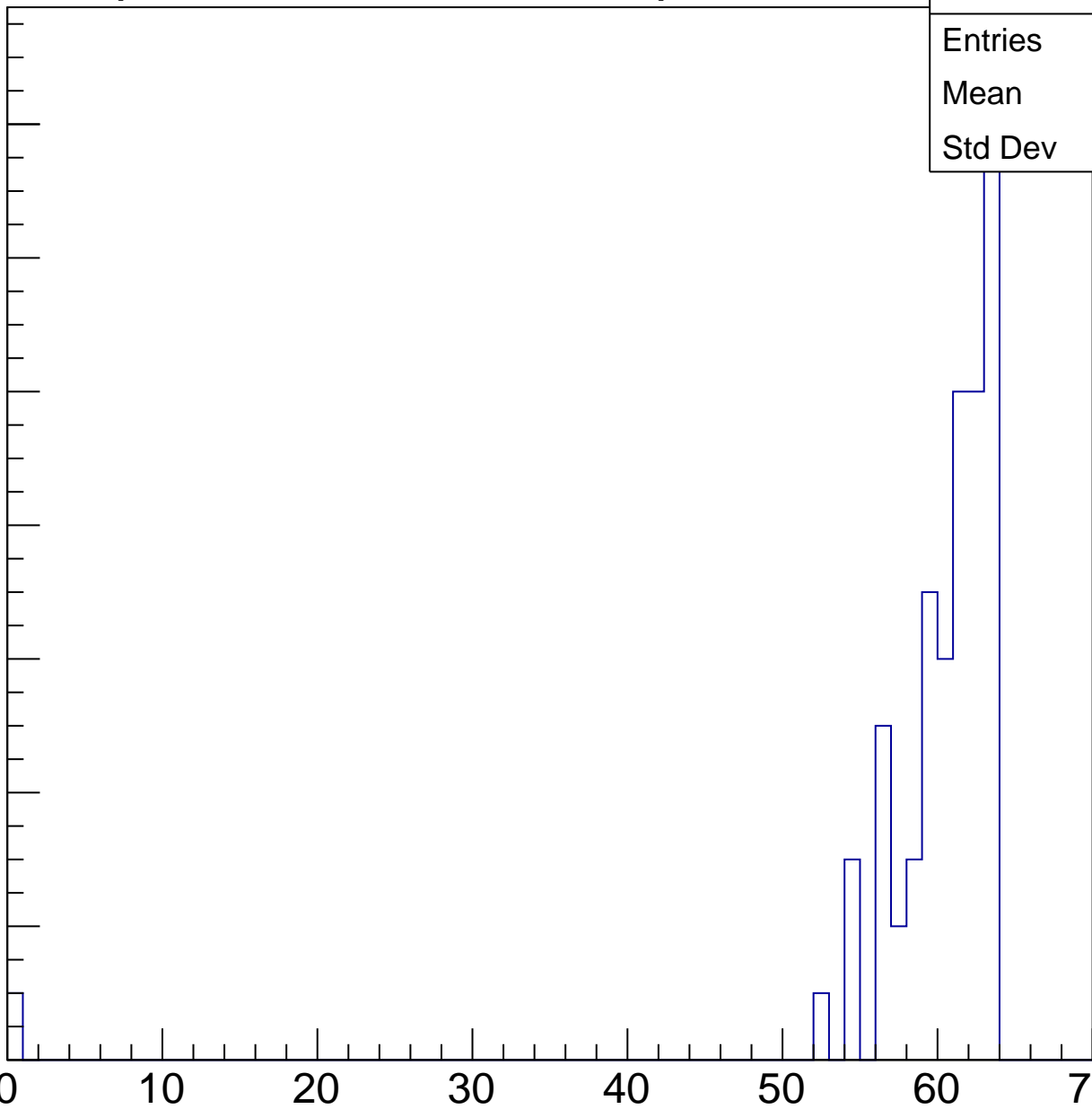
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 59.21 |
| Std Dev | 8.002 |

ampl

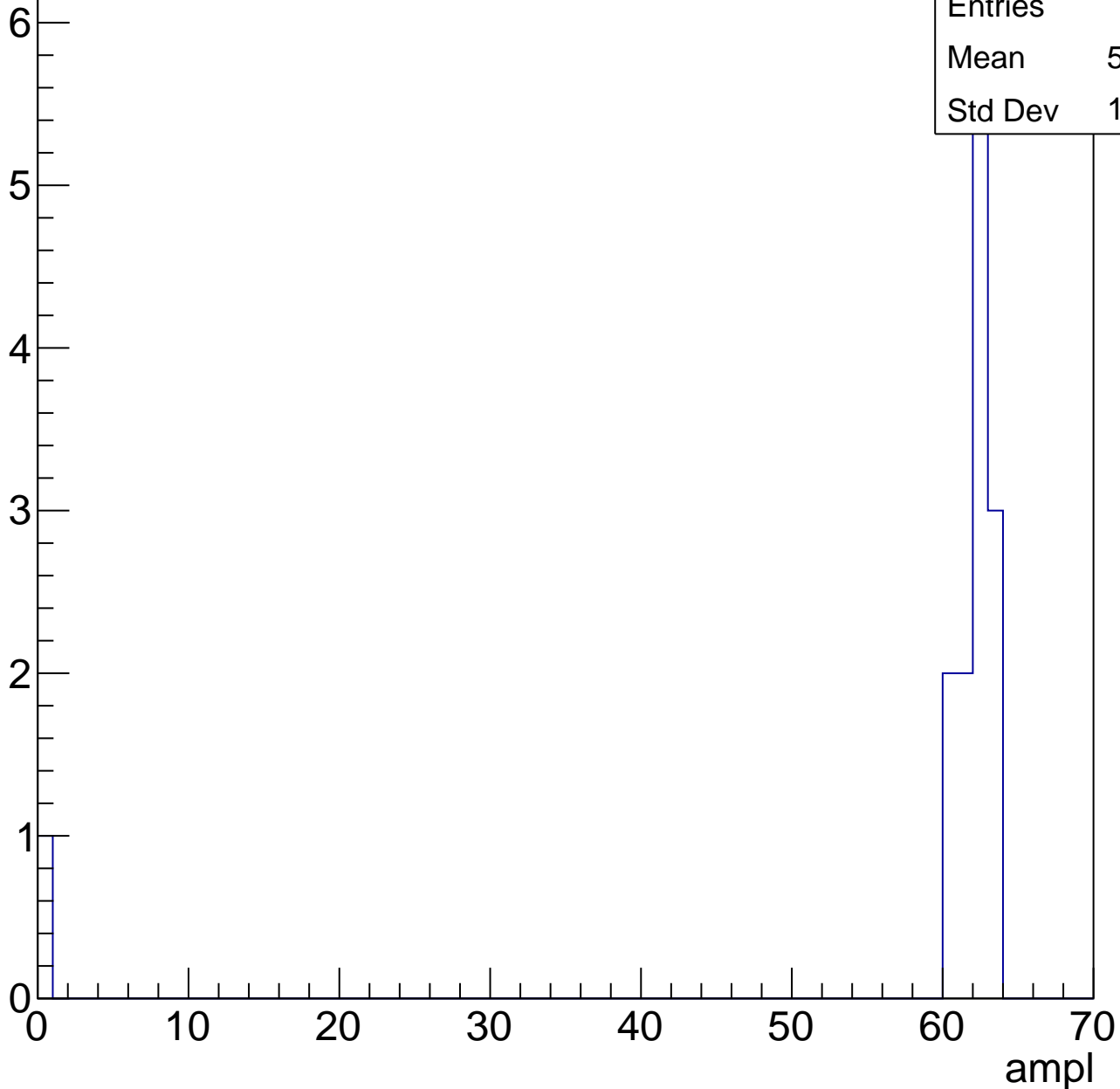


# B0L101S, U16-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 57.36 |
| Std Dev | 15.94 |





# B0L101S, U16-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 29.83 |
| Std Dev | 5.069 |

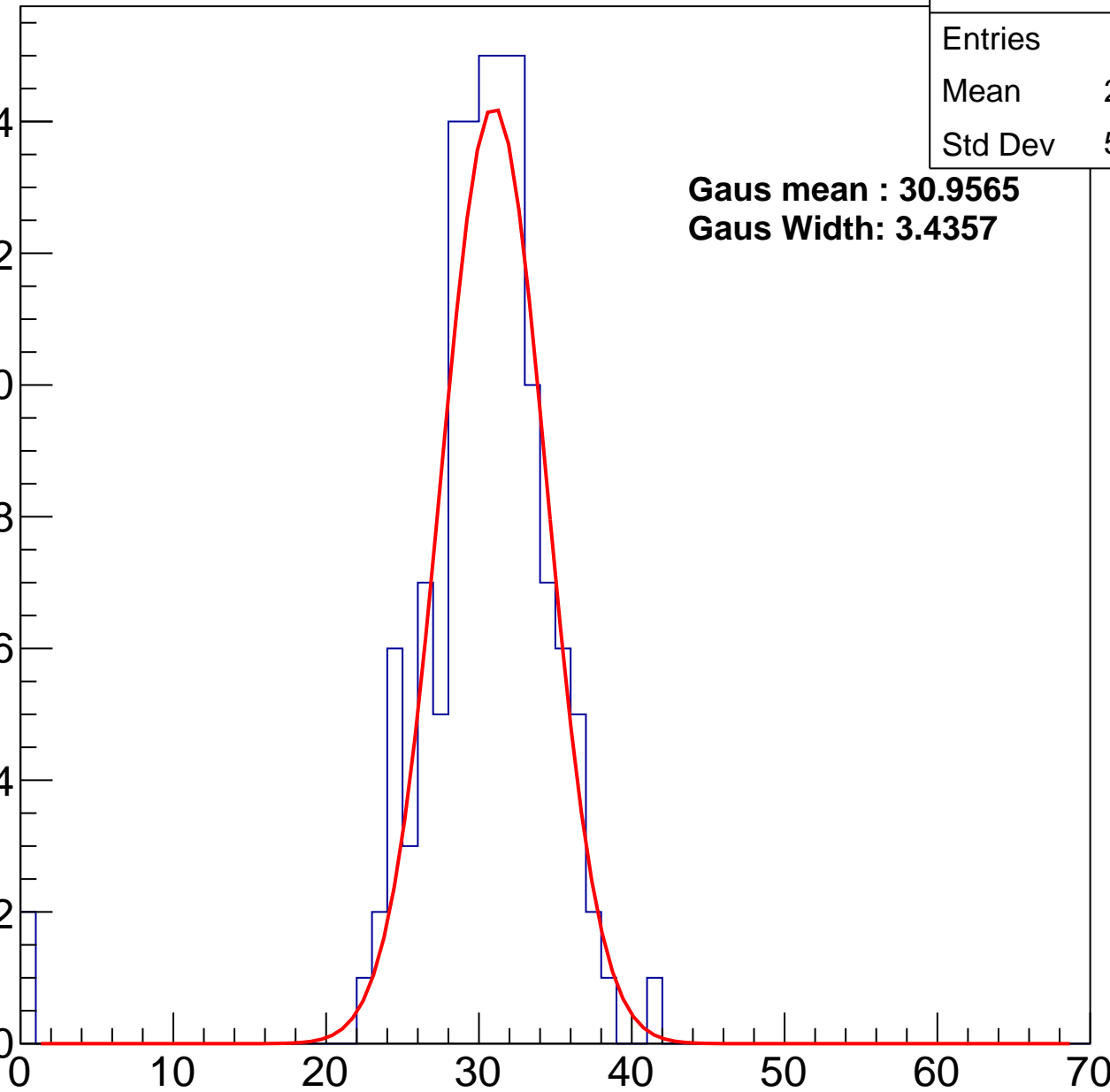
**Gaus mean : 30.9565**

**Gaus Width: 3.4357**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



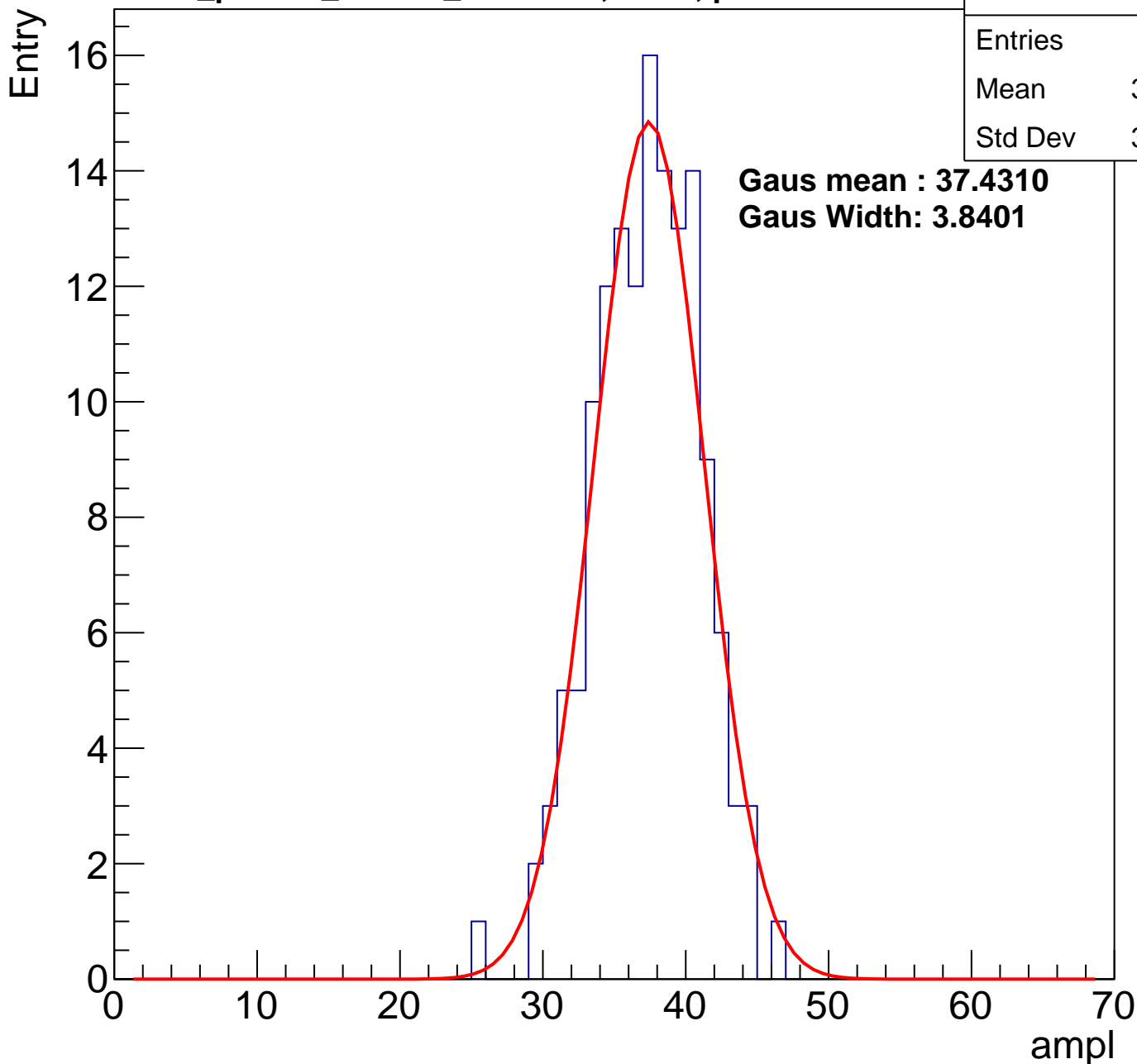
# B0L101S, U16-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 36.85 |
| Std Dev | 3.633 |

**Gaus mean : 37.4310**

**Gaus Width: 3.8401**



# B0L101S, U16-ch29, adc2

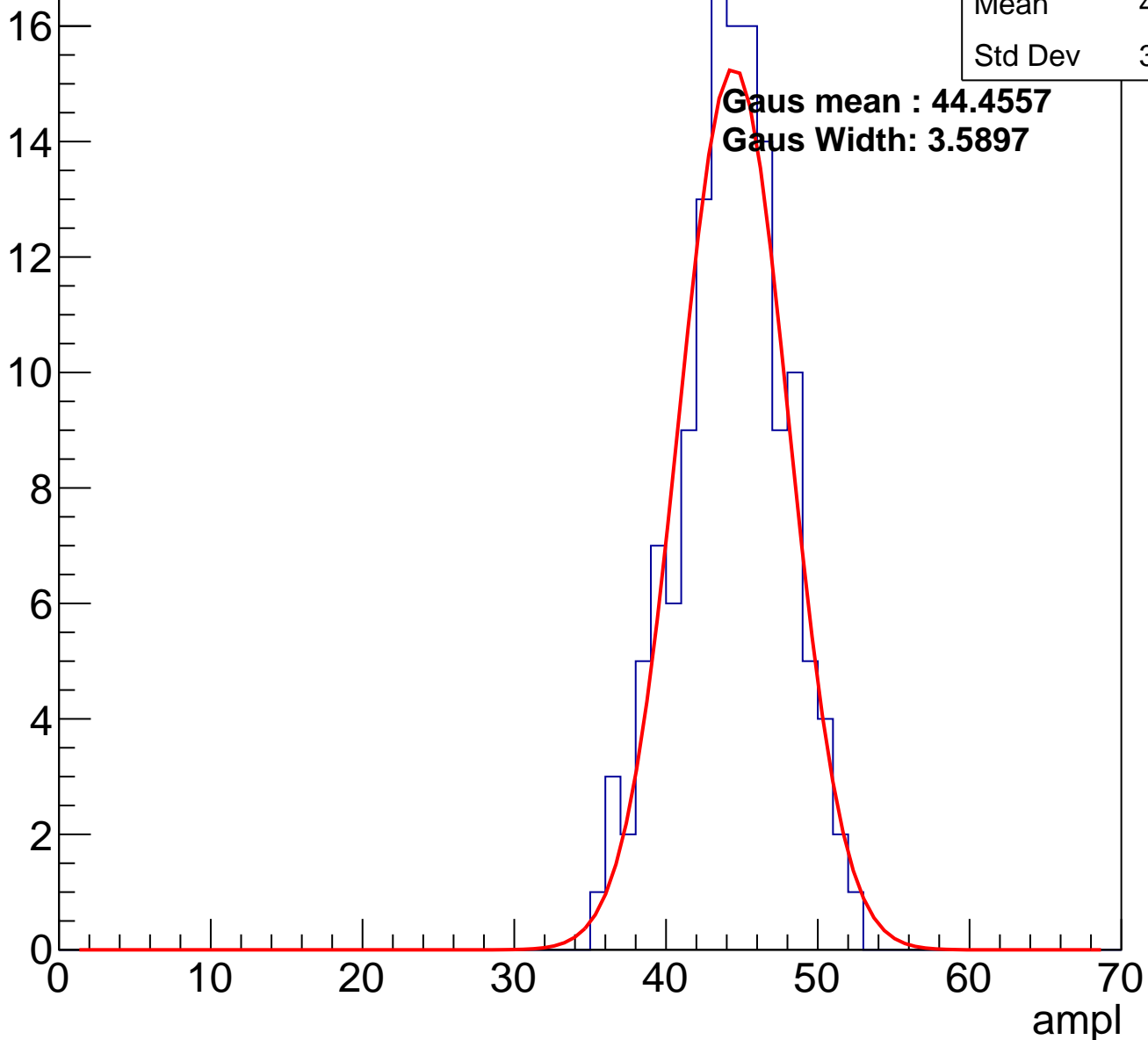
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 43.83 |
| Std Dev | 3.497 |

**Gaus mean : 44.4557**

**Gaus Width: 3.5897**



# B0L101S, U16-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 50.41 |
| Std Dev | 3.16  |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

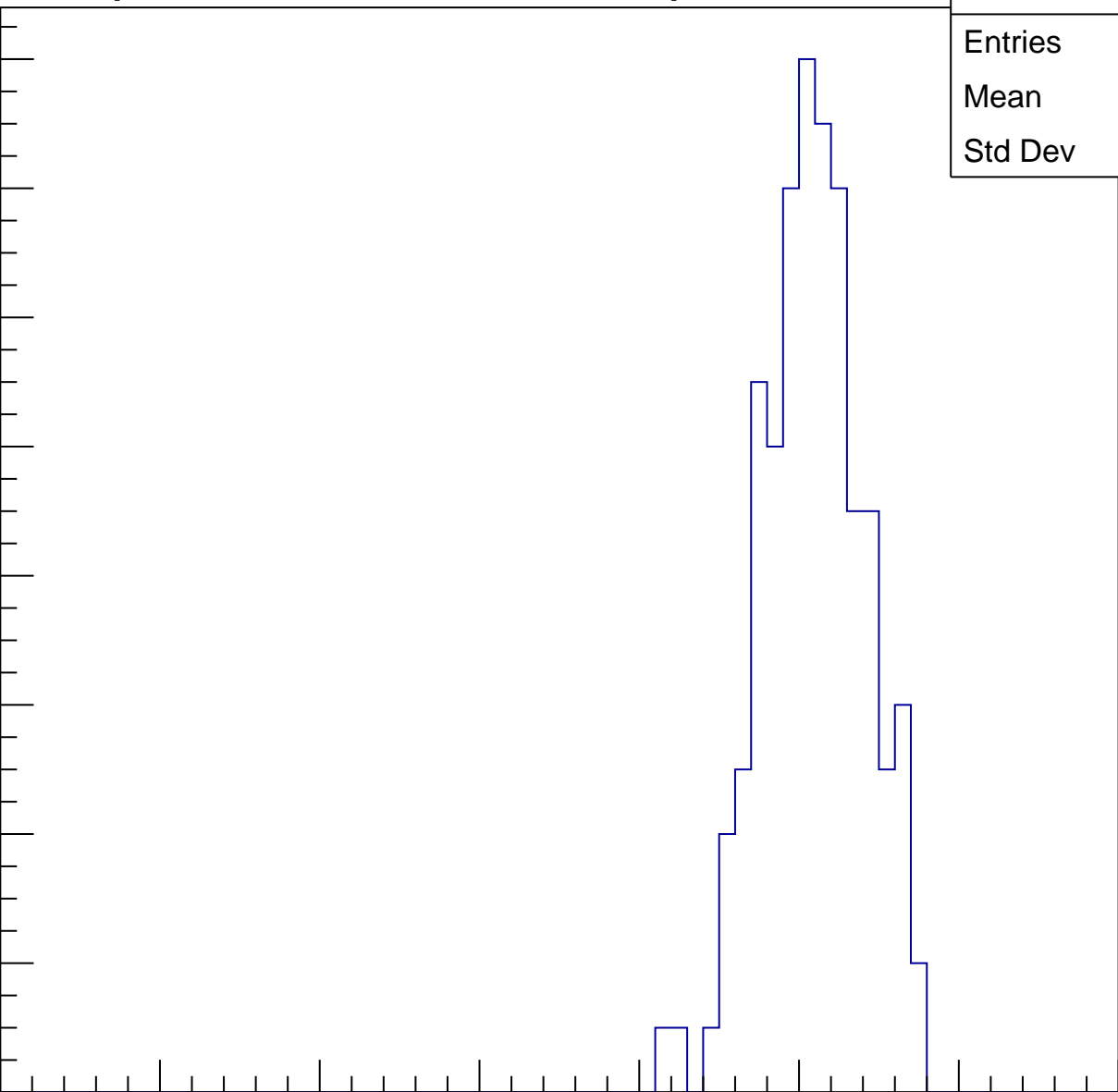
40

50

60

70

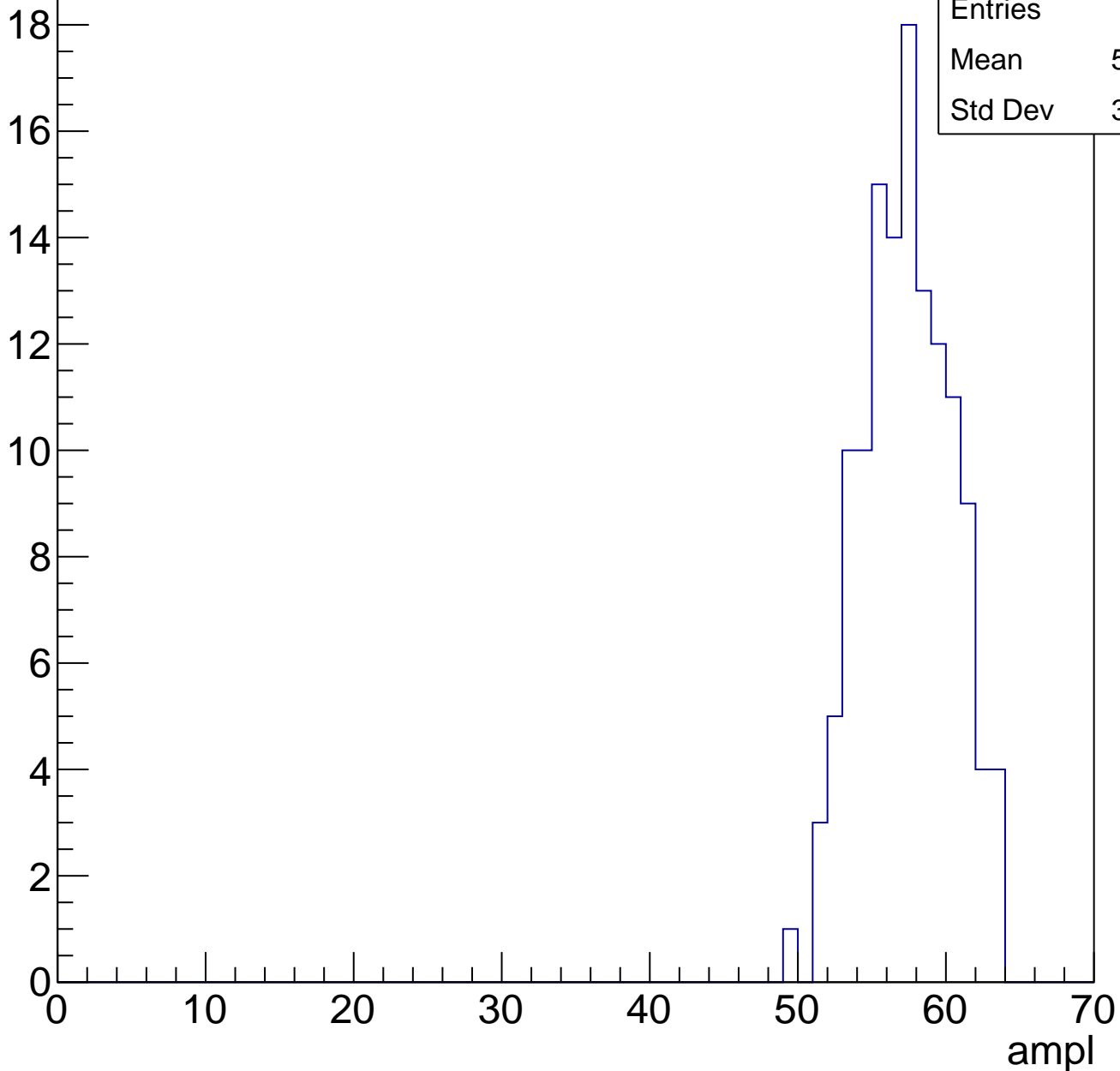
ampl



# B0L101S, U16-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch29, adc5

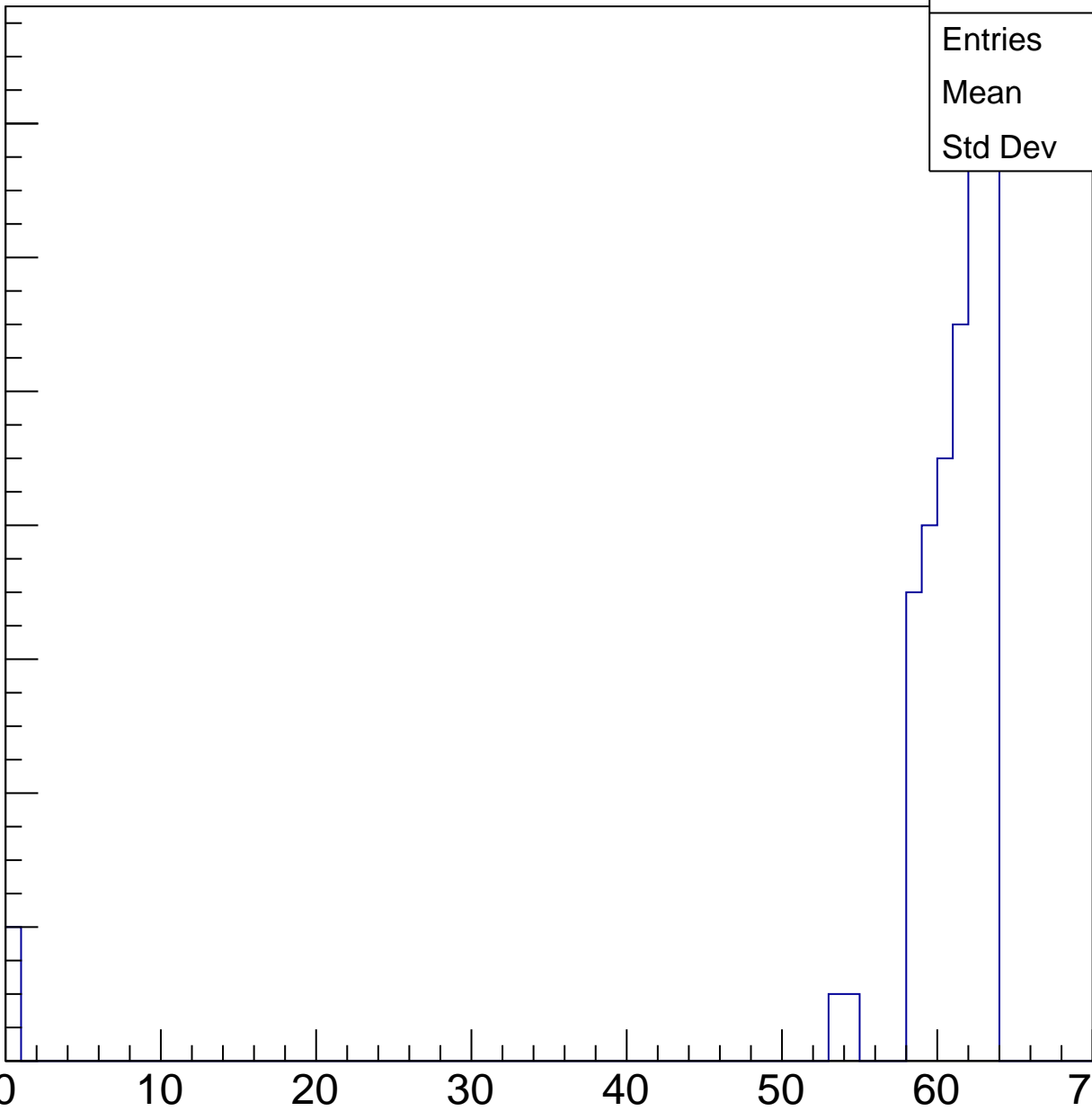
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 58.94 |
| Std Dev | 10.46 |

ampl



# B0L101S, U16-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 3      |
| Mean    | 62.67  |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch30, adc0

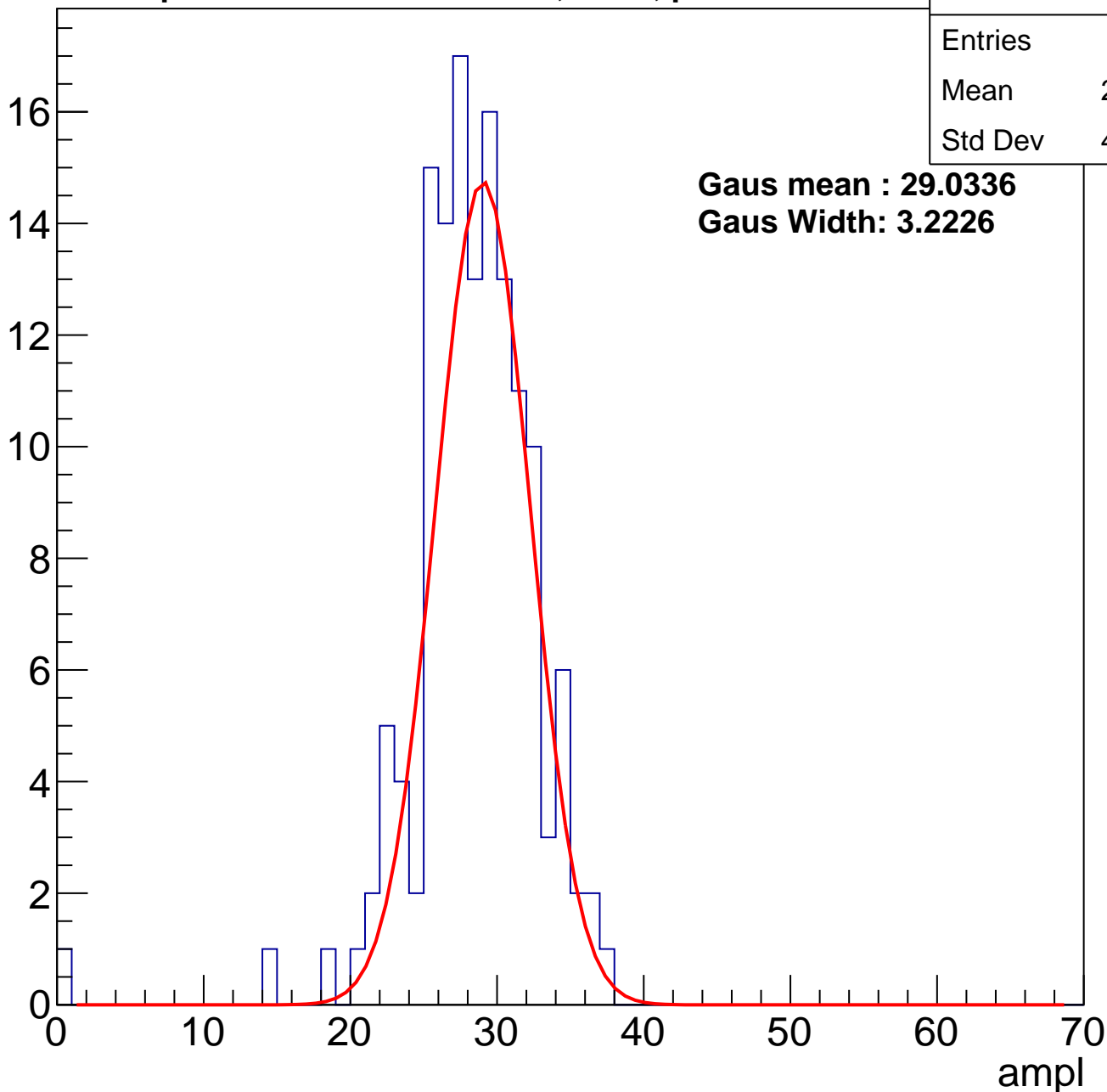
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 27.88 |
| Std Dev | 4.379 |

**Gaus mean : 29.0336**

**Gaus Width: 3.2226**

Entry



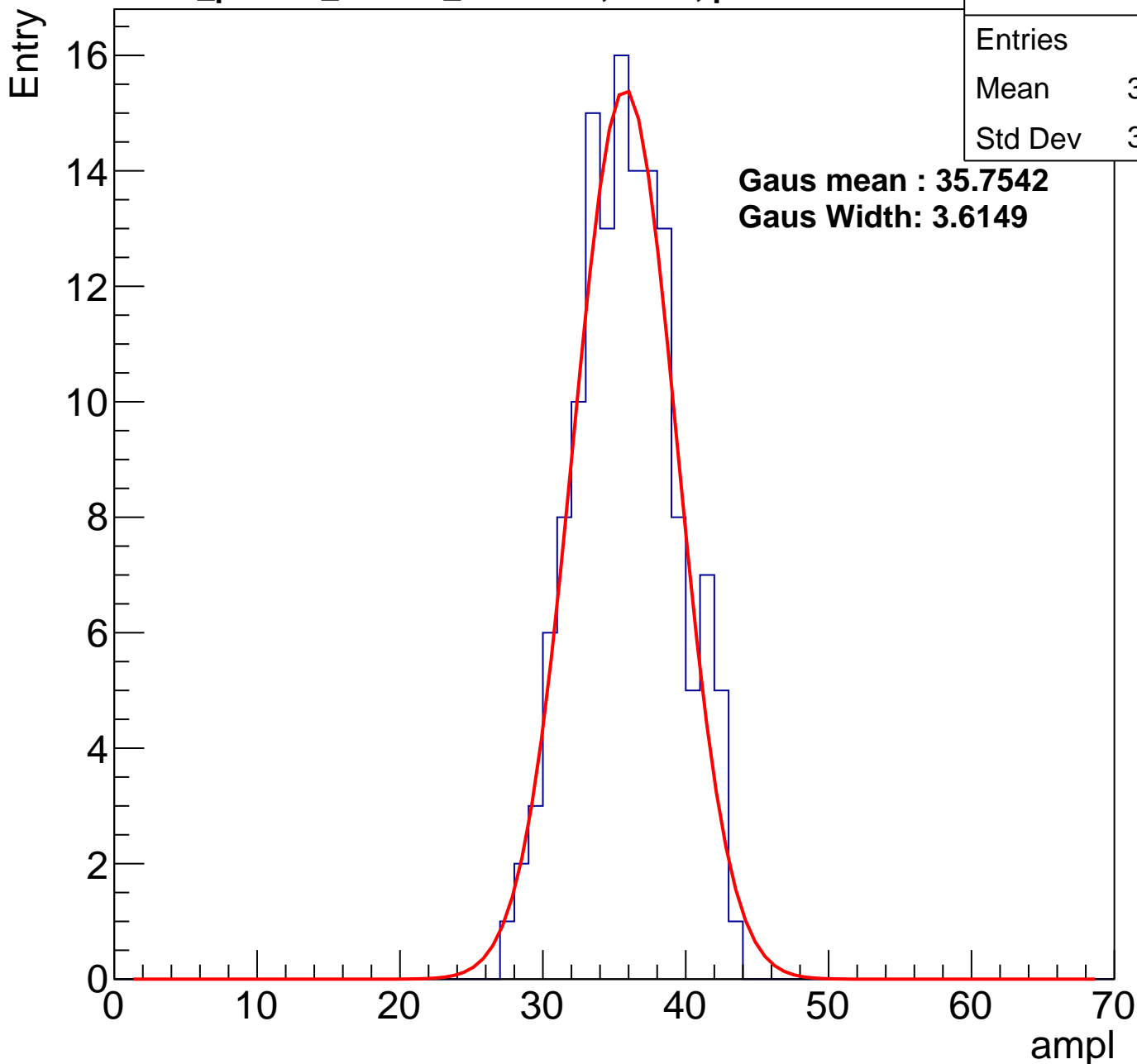
# B0L101S, U16-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 141   |
| Mean    | 35.34 |
| Std Dev | 3.468 |

**Gaus mean : 35.7542**

**Gaus Width: 3.6149**



# B0L101S, U16-ch30, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 42.53 |
| Std Dev | 3.51  |

**Gaus mean : 43.0859**

**Gaus Width: 3.8464**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

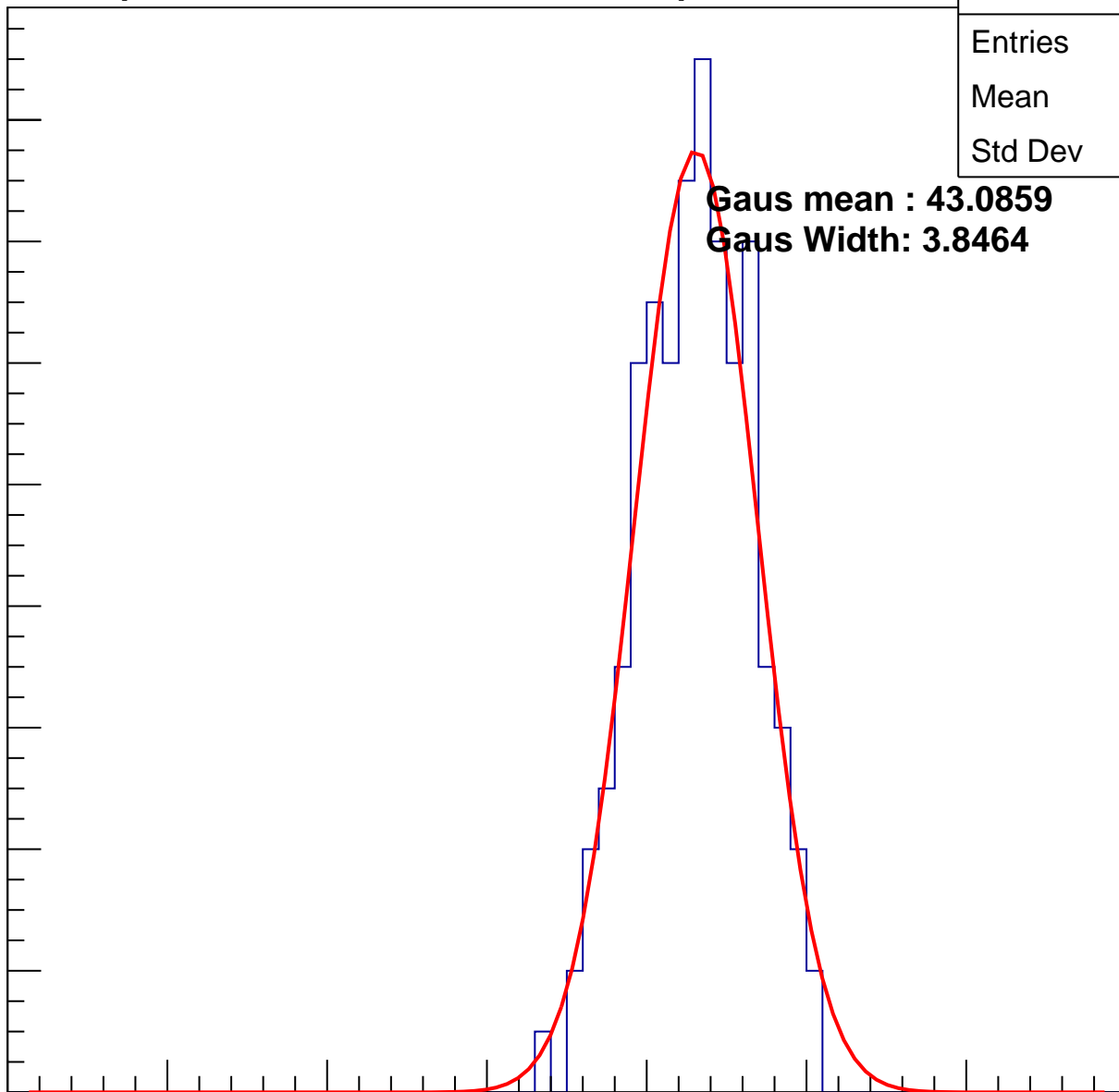
40

50

60

70

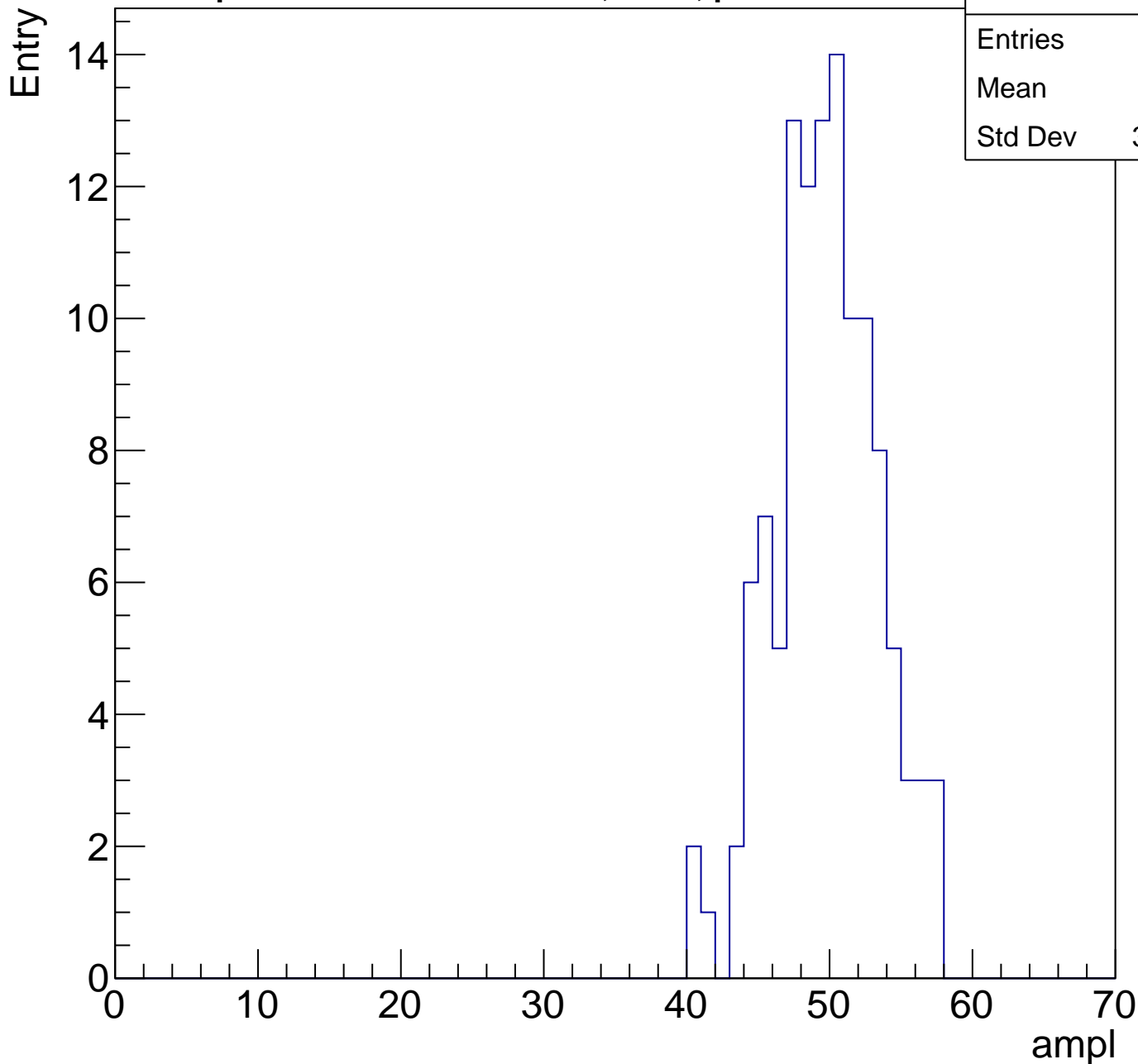
ampl



# B0L101S, U16-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 49.3  |
| Std Dev | 3.584 |



# B0L101S, U16-ch30, adc4

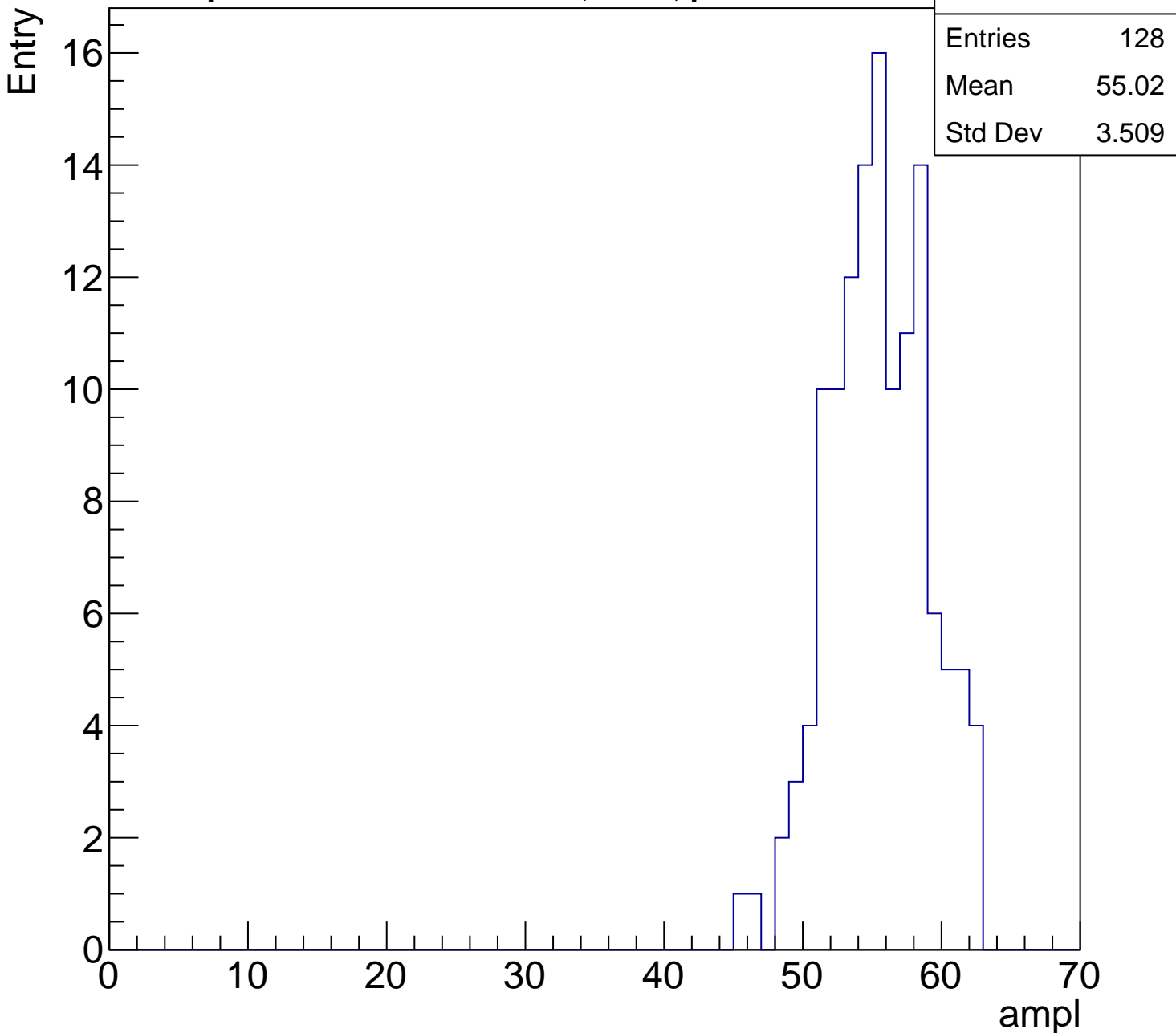
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 55.02 |
| Std Dev | 3.509 |

ampl



# B0L101S, U16-ch30, adc5

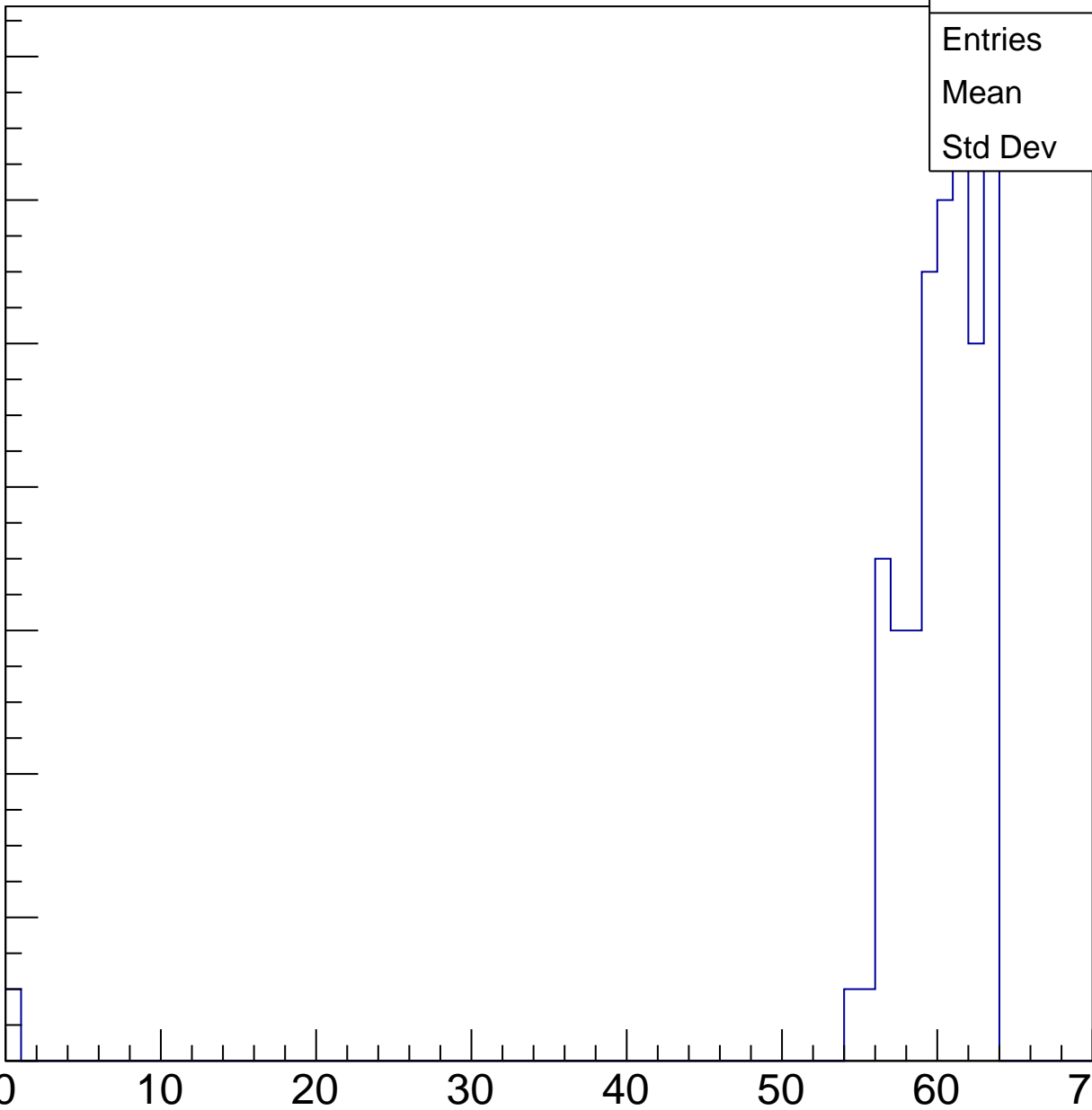
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 59.21 |
| Std Dev | 6.975 |

ampl

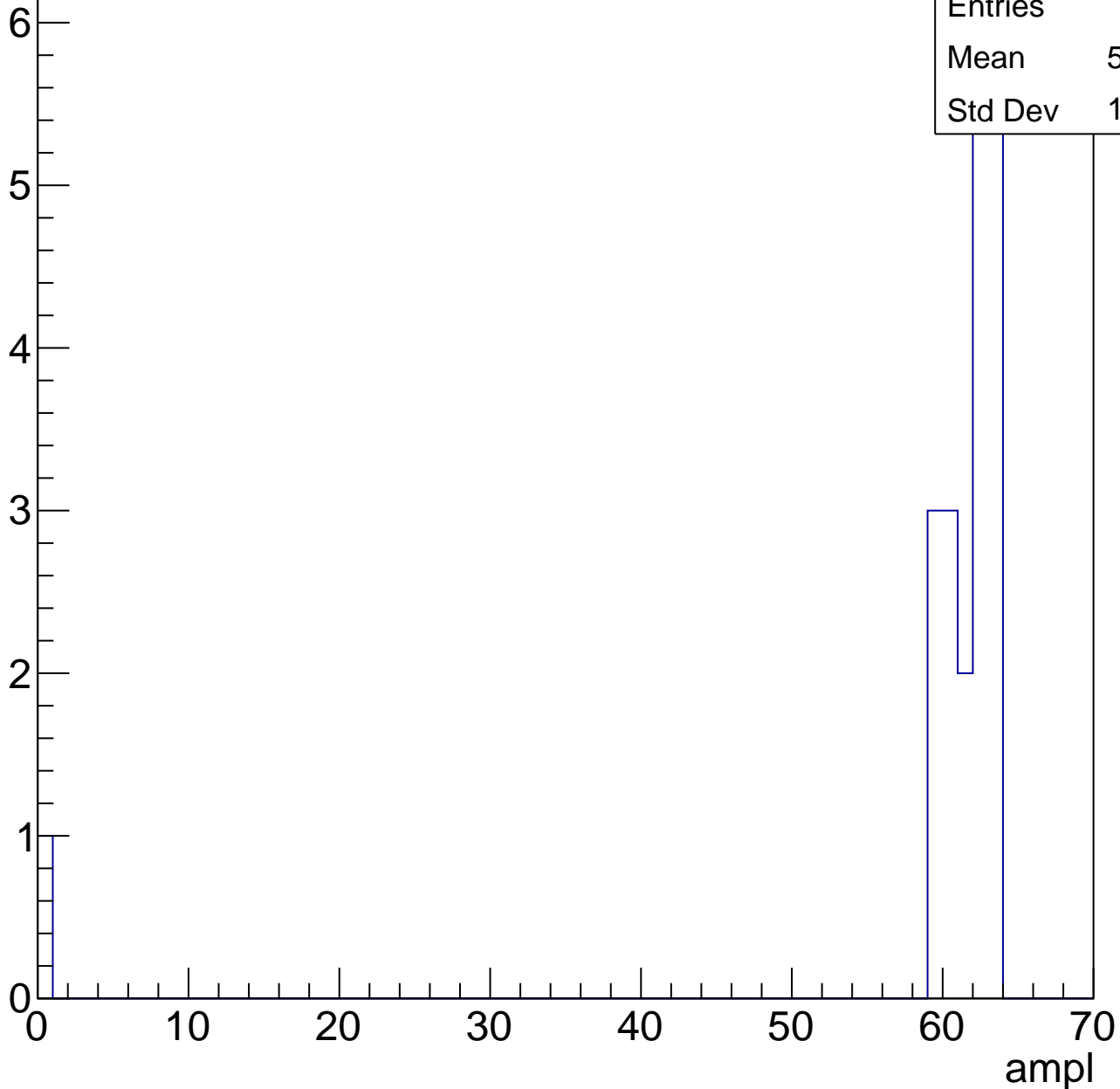


# B0L101S, U16-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 58.52 |
| Std Dev | 13.16 |





# B0L101S, U16-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch31, adc0

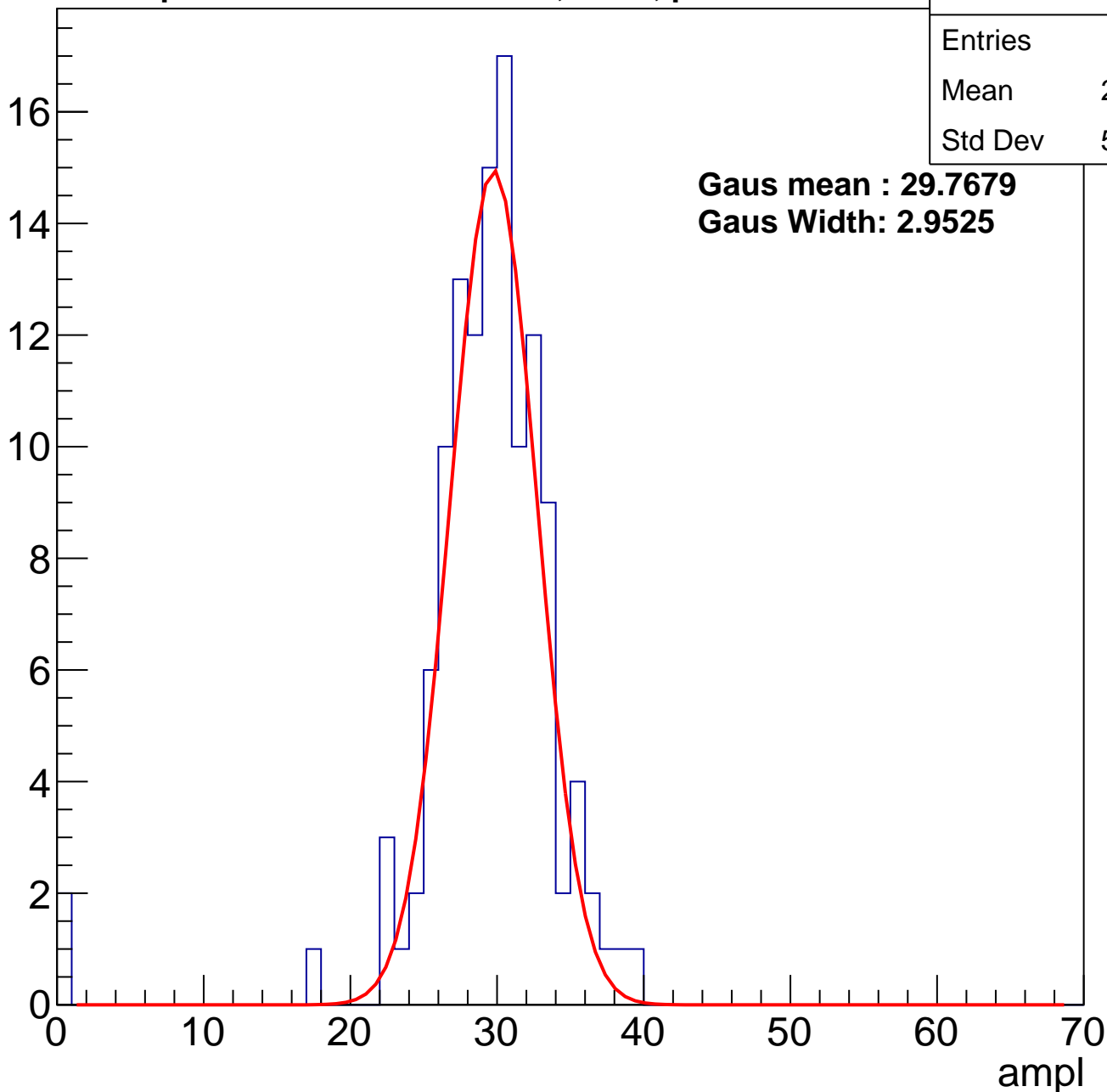
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 28.88 |
| Std Dev | 5.043 |

**Gaus mean : 29.7679**

**Gaus Width: 2.9525**

Entry



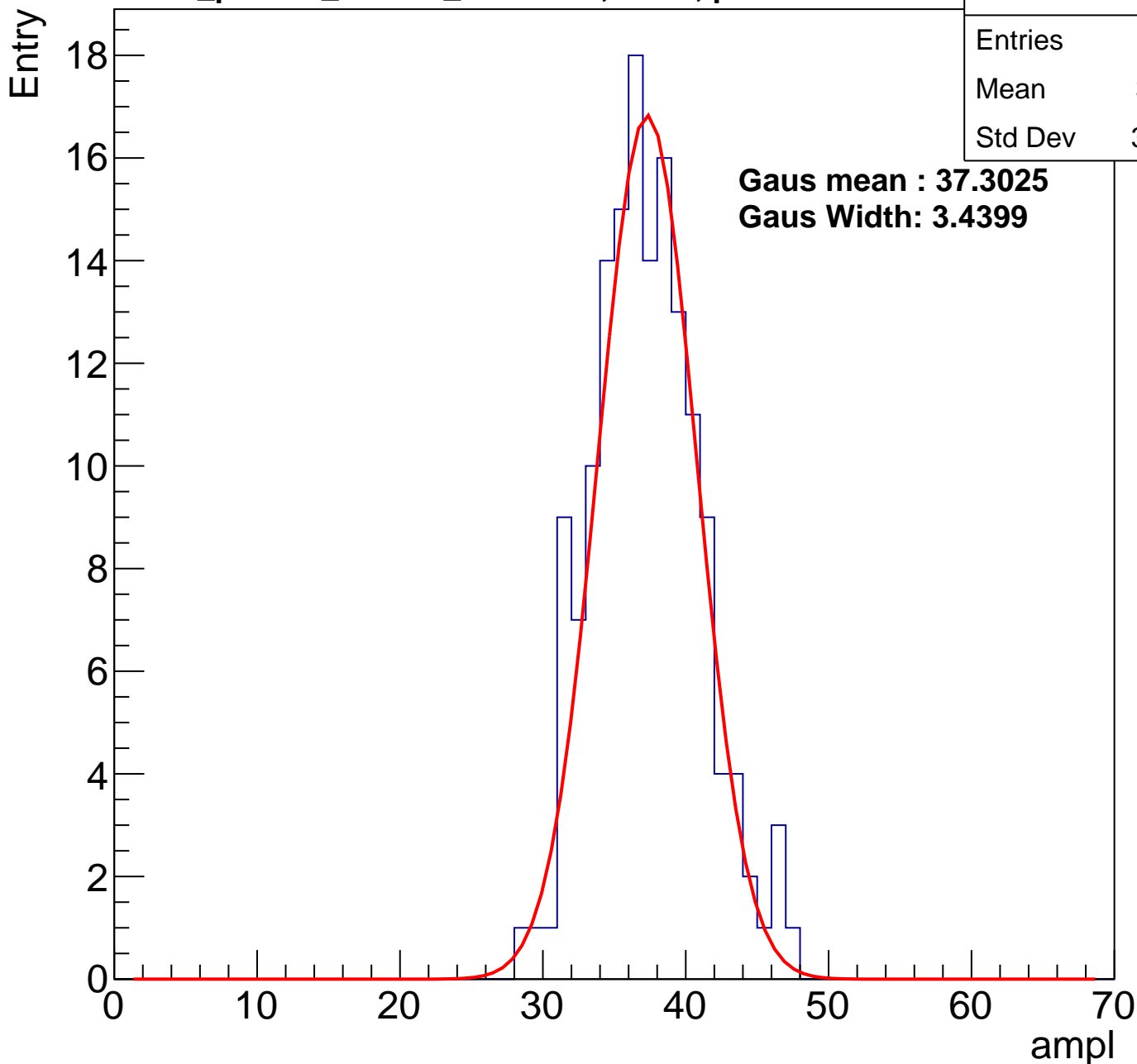
# B0L101S, U16-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 154   |
| Mean    | 36.81 |
| Std Dev | 3.703 |

**Gaus mean : 37.3025**

**Gaus Width: 3.4399**



# B0L101S, U16-ch31, adc2

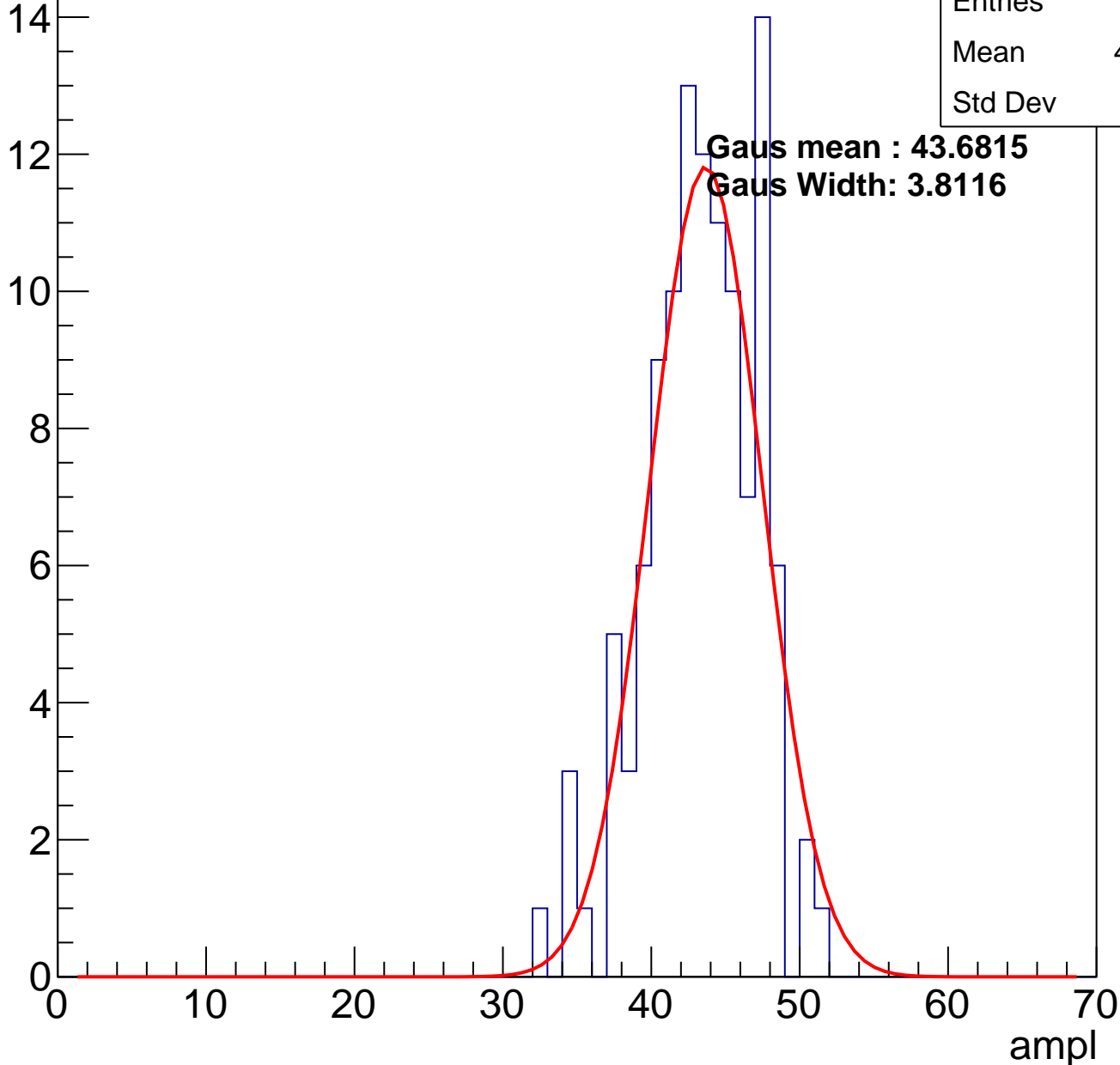
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 42.87 |
| Std Dev | 3.7   |

**Gaus mean : 43.6815**

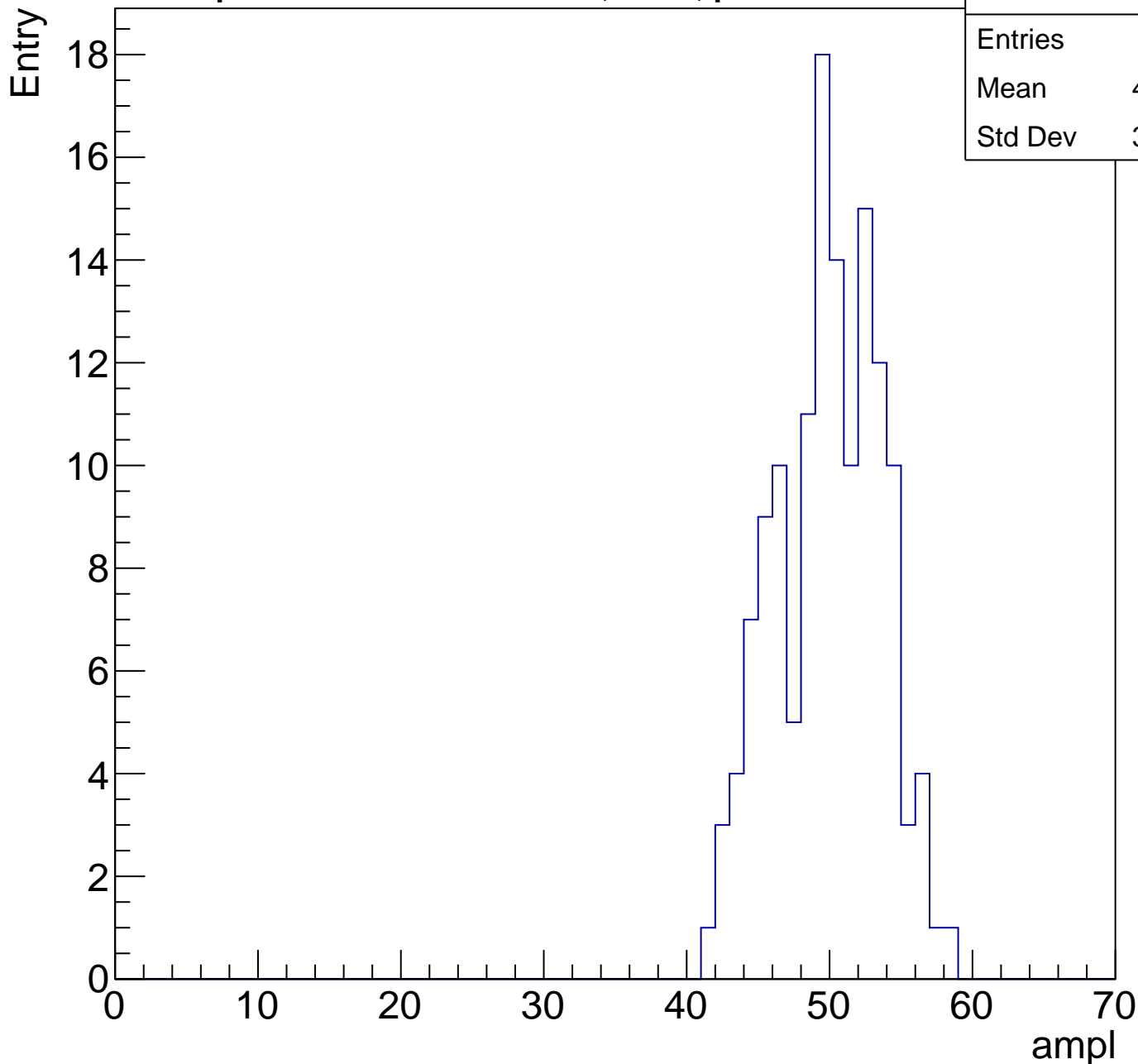
**Gaus Width: 3.8116**



# B0L101S, U16-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 49.47 |
| Std Dev | 3.656 |



# B0L101S, U16-ch31, adc4

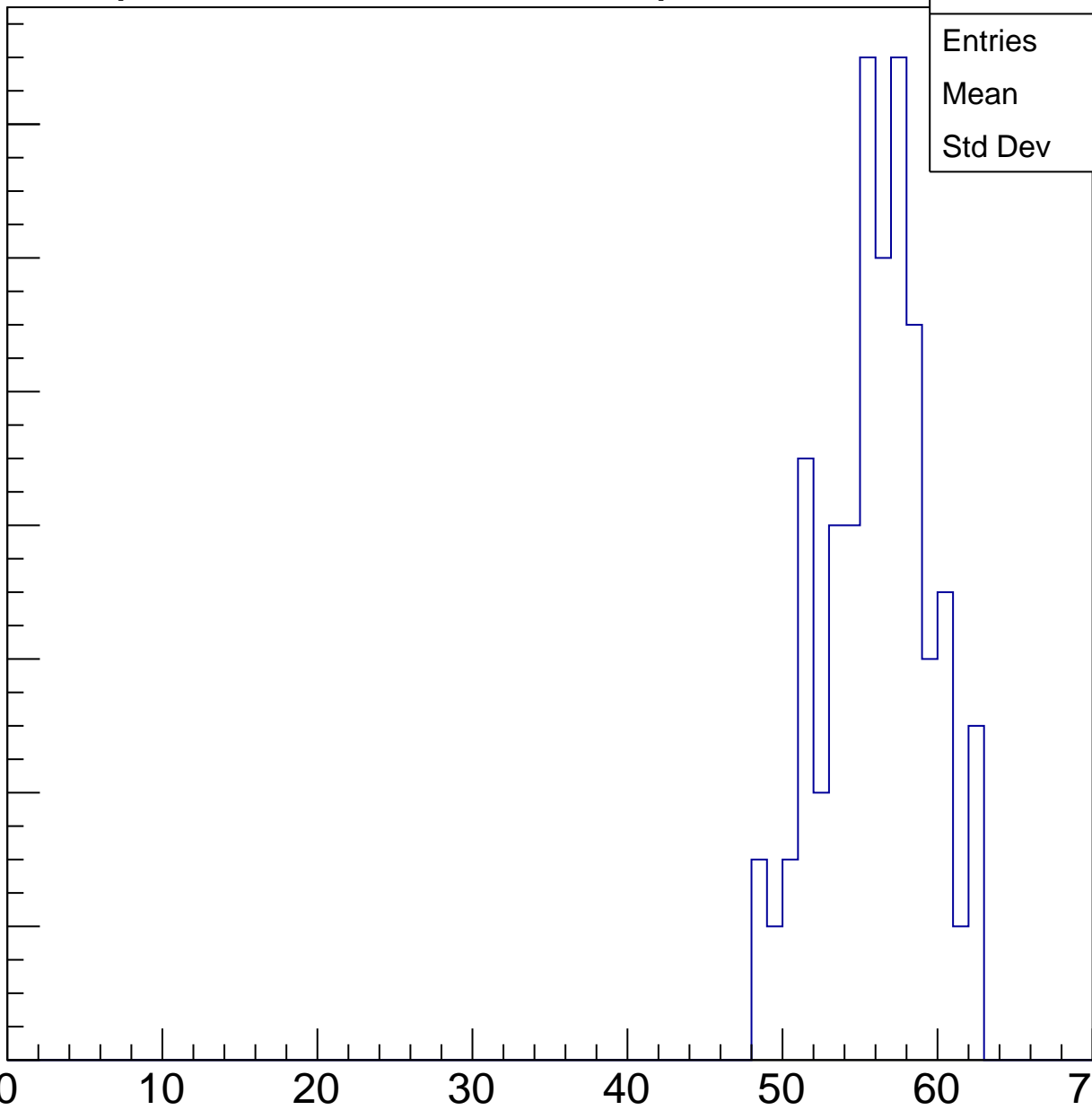
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 55.55 |
| Std Dev | 3.356 |

ampl



# B0L101S, U16-ch31, adc5

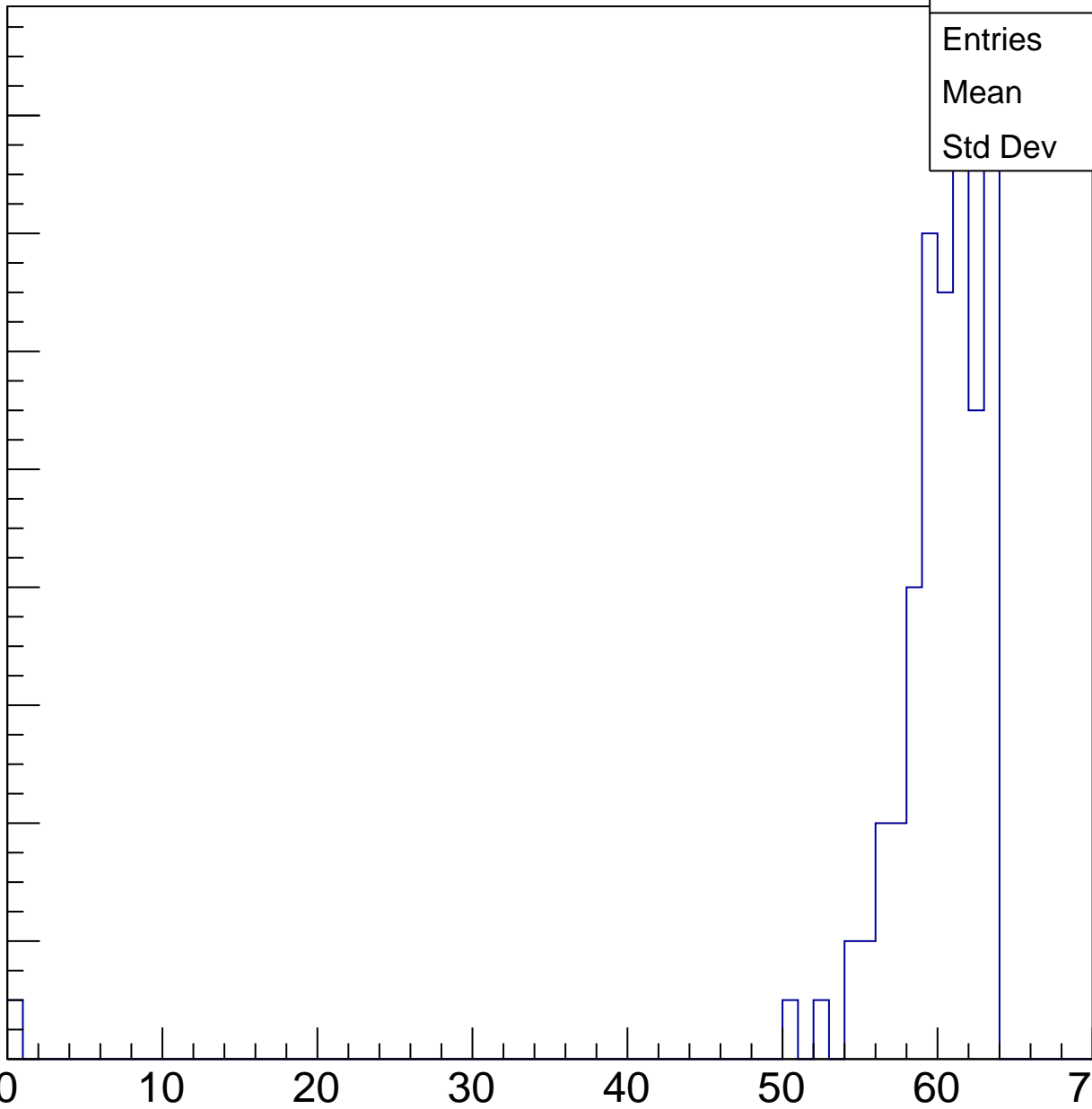
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 94    |
| Mean    | 59.24 |
| Std Dev | 6.671 |

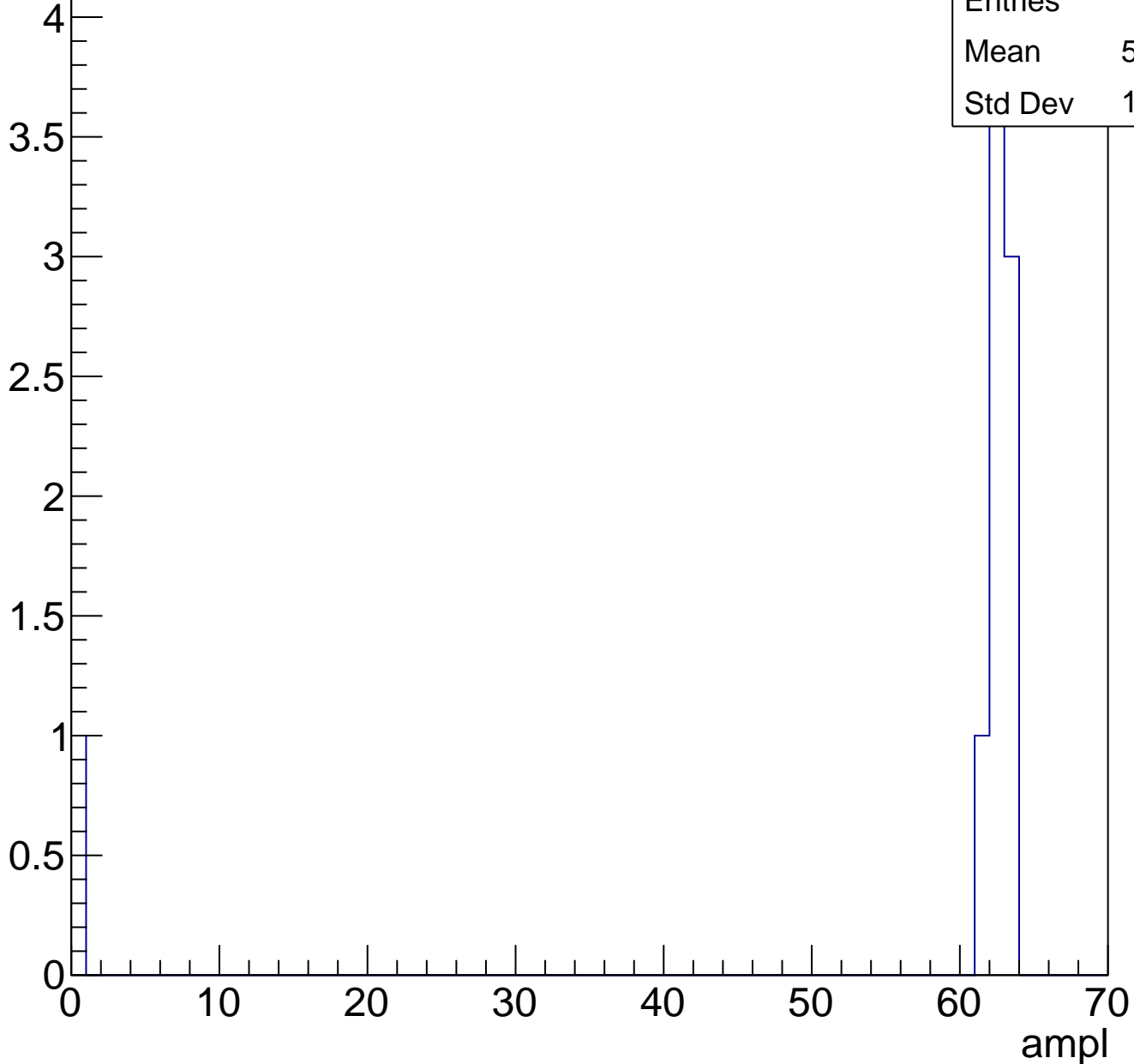
ampl



# B0L101S, U16-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

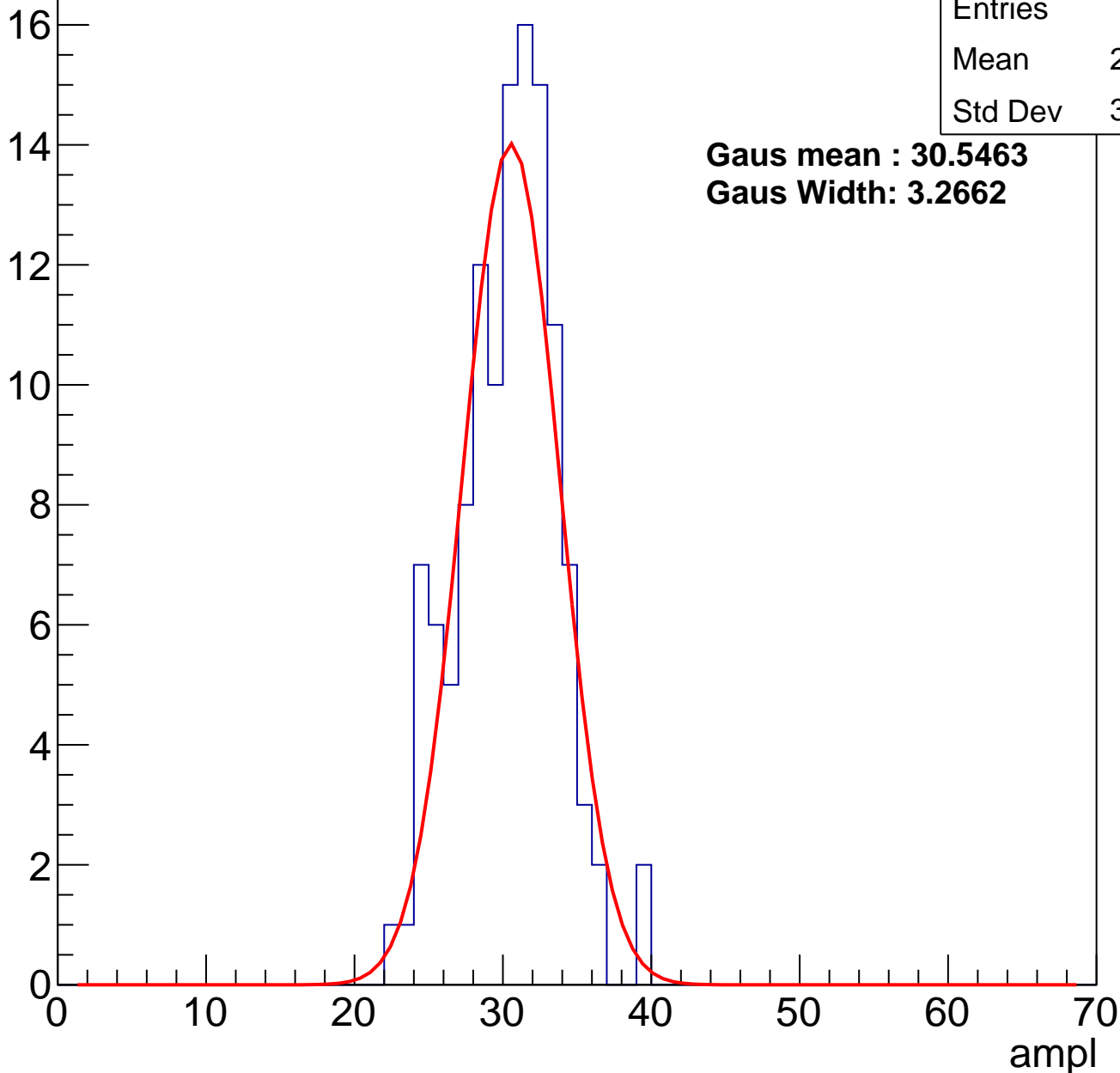
Entry



# B0L101S, U16-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



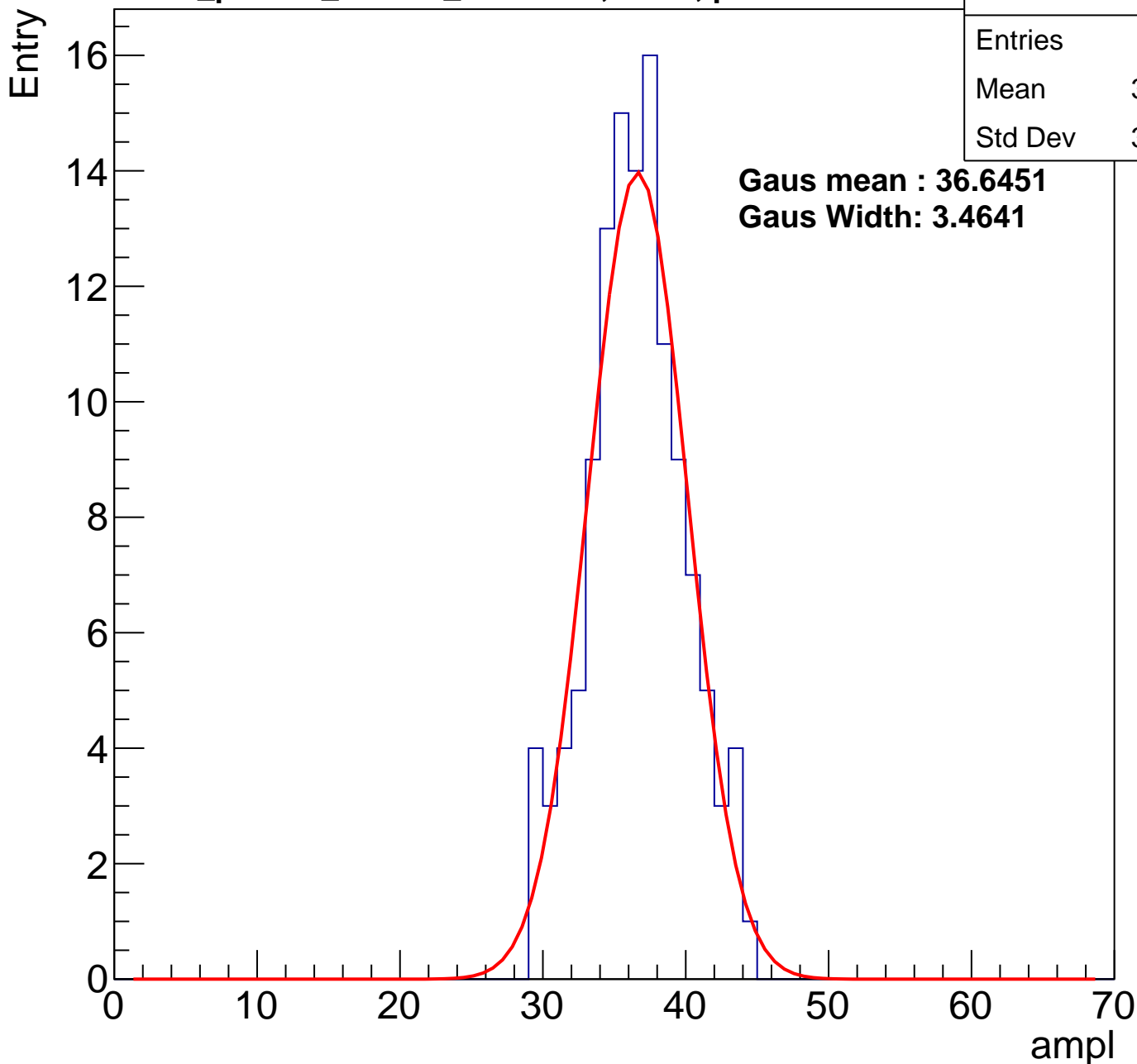
# B0L101S, U16-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 36.15 |
| Std Dev | 3.344 |

**Gaus mean : 36.6451**

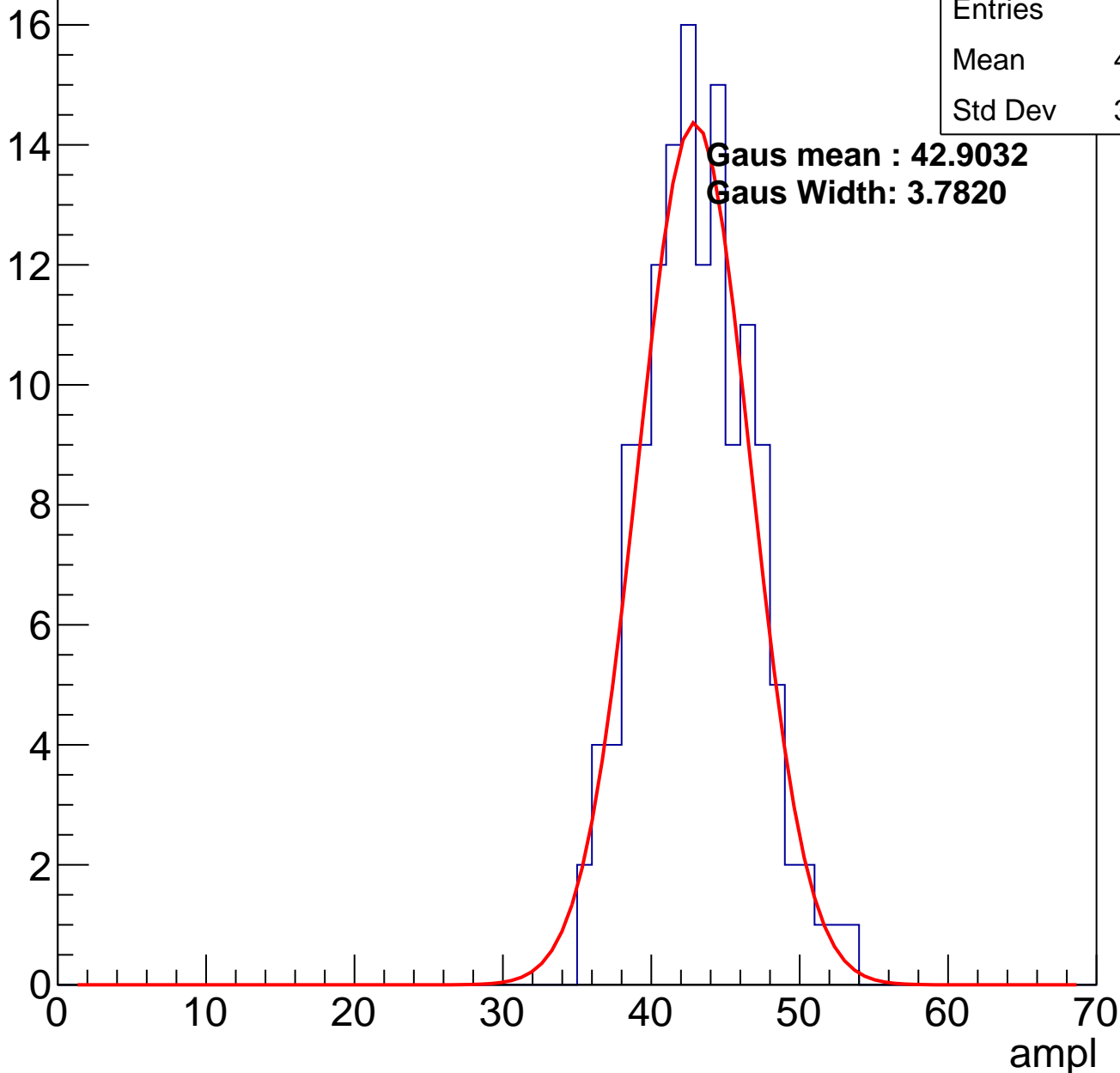
**Gaus Width: 3.4641**



# B0L101S, U16-ch32, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

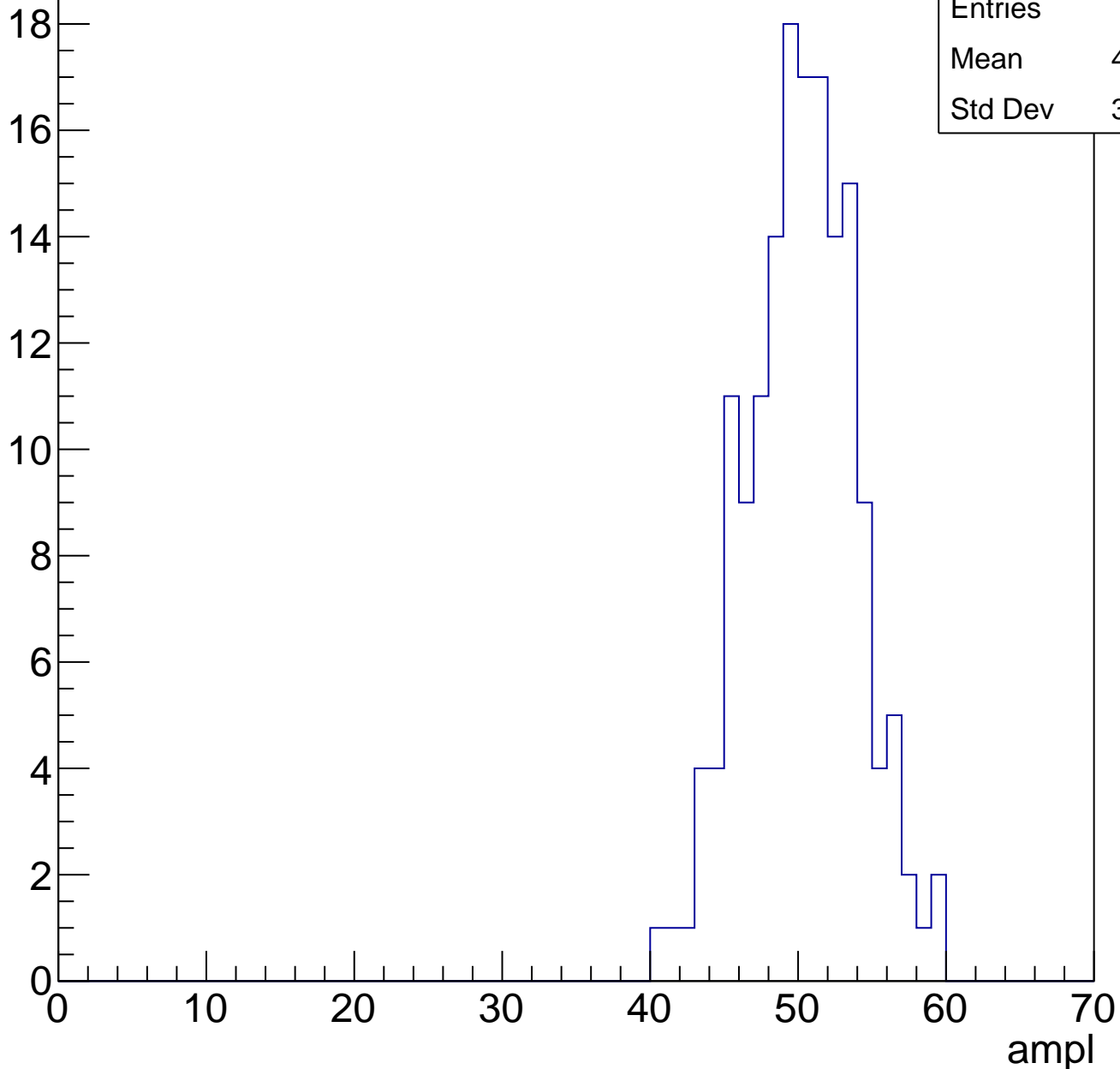


# B0L101S, U16-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 49.79 |
| Std Dev | 3.645 |

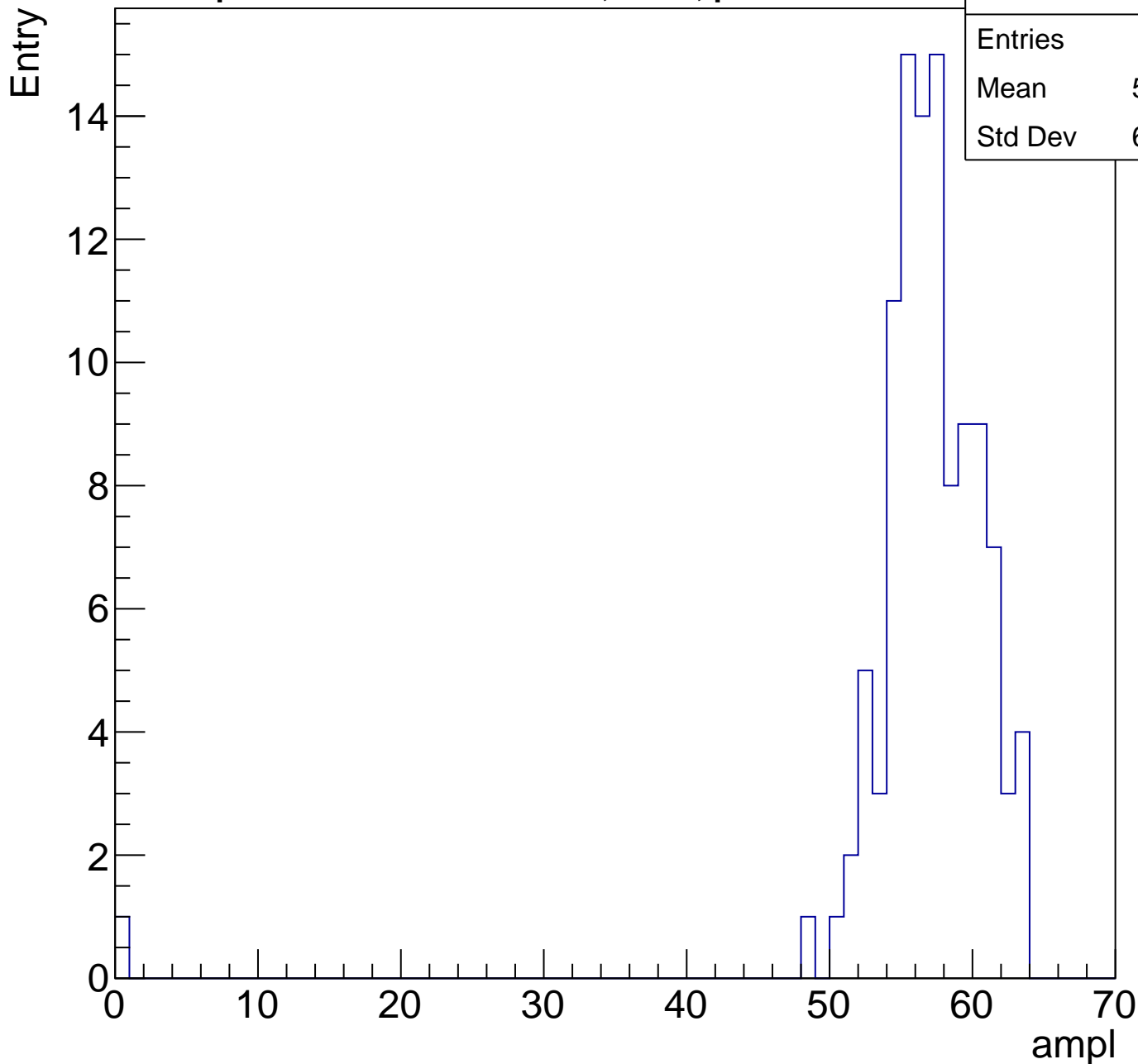
Entry



# B0L101S, U16-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 56.27 |
| Std Dev | 6.233 |



# B0L101S, U16-ch32, adc5

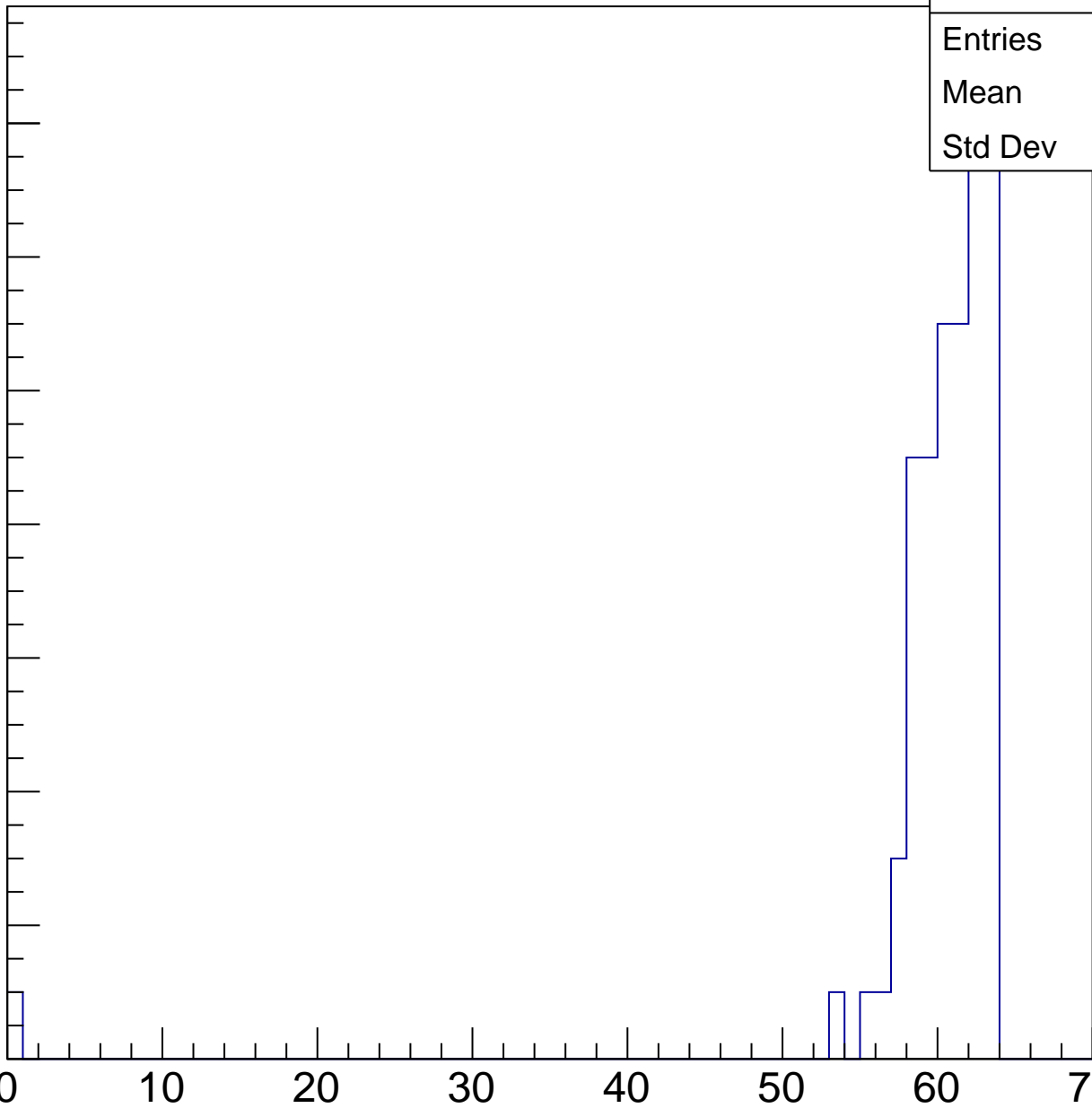
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 59.62 |
| Std Dev | 7.207 |

ampl



# B0L101S, U16-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 8     |
| Mean    | 61    |
| Std Dev | 1.871 |

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch33, adc0

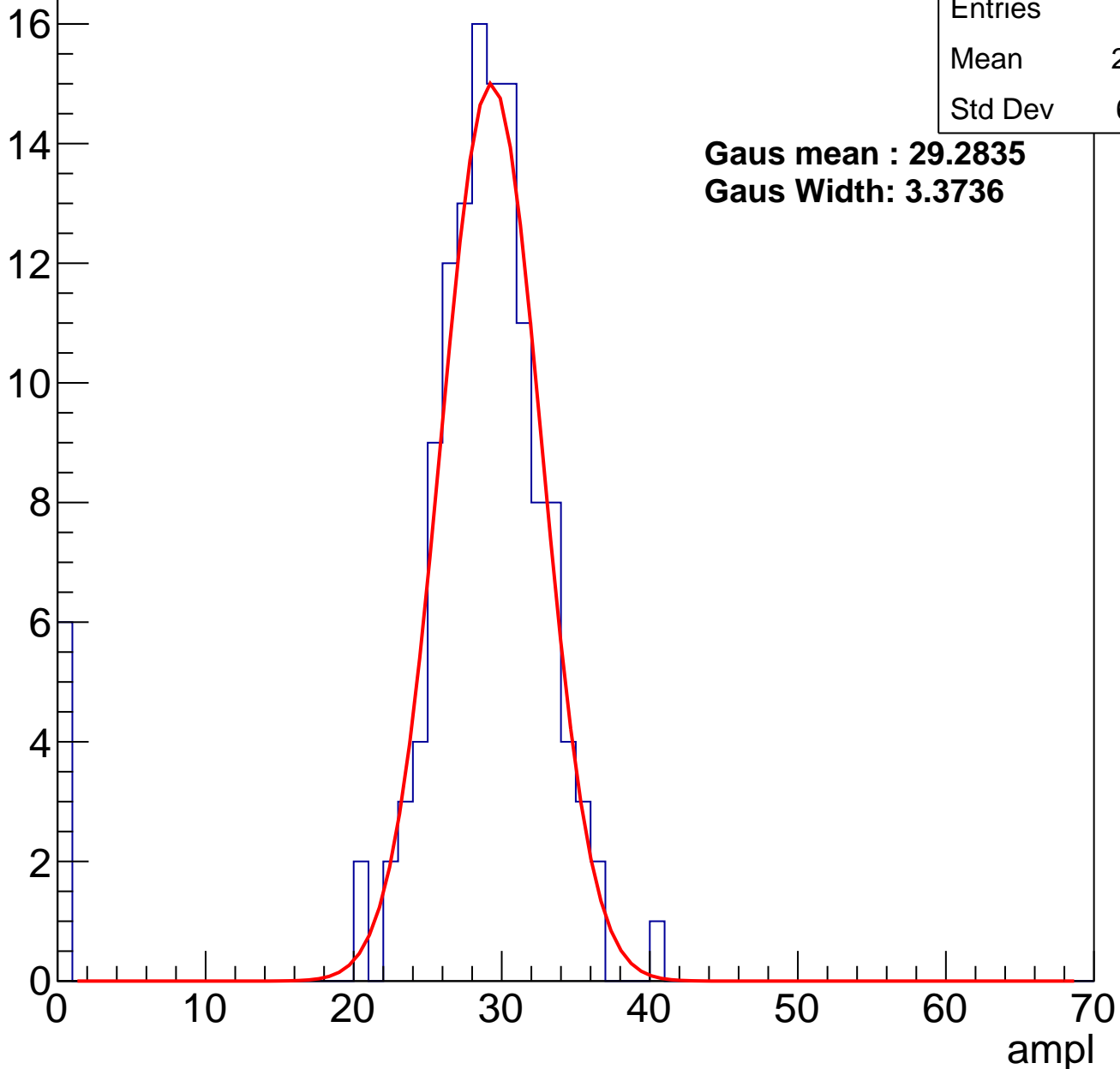
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 27.49 |
| Std Dev | 6.811 |

**Gaus mean : 29.2835**

**Gaus Width: 3.3736**

Entry



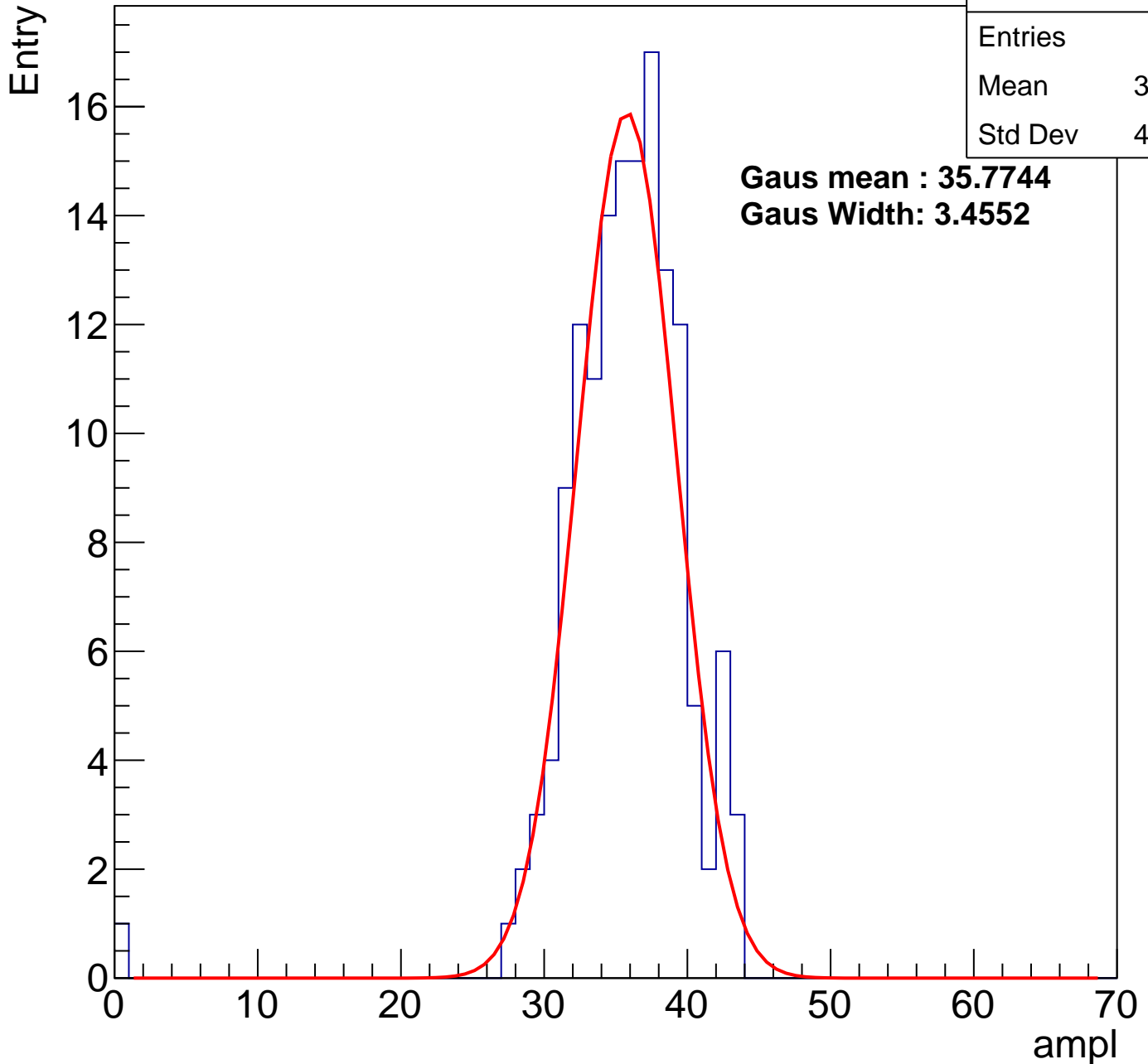
# B0L101S, U16-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 145   |
| Mean    | 35.25 |
| Std Dev | 4.527 |

**Gaus mean : 35.7744**

**Gaus Width: 3.4552**



# B0L101S, U16-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 42.81 |
| Std Dev | 3.557 |

**Gaus mean : 43.3808**

**Gaus Width: 3.5671**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

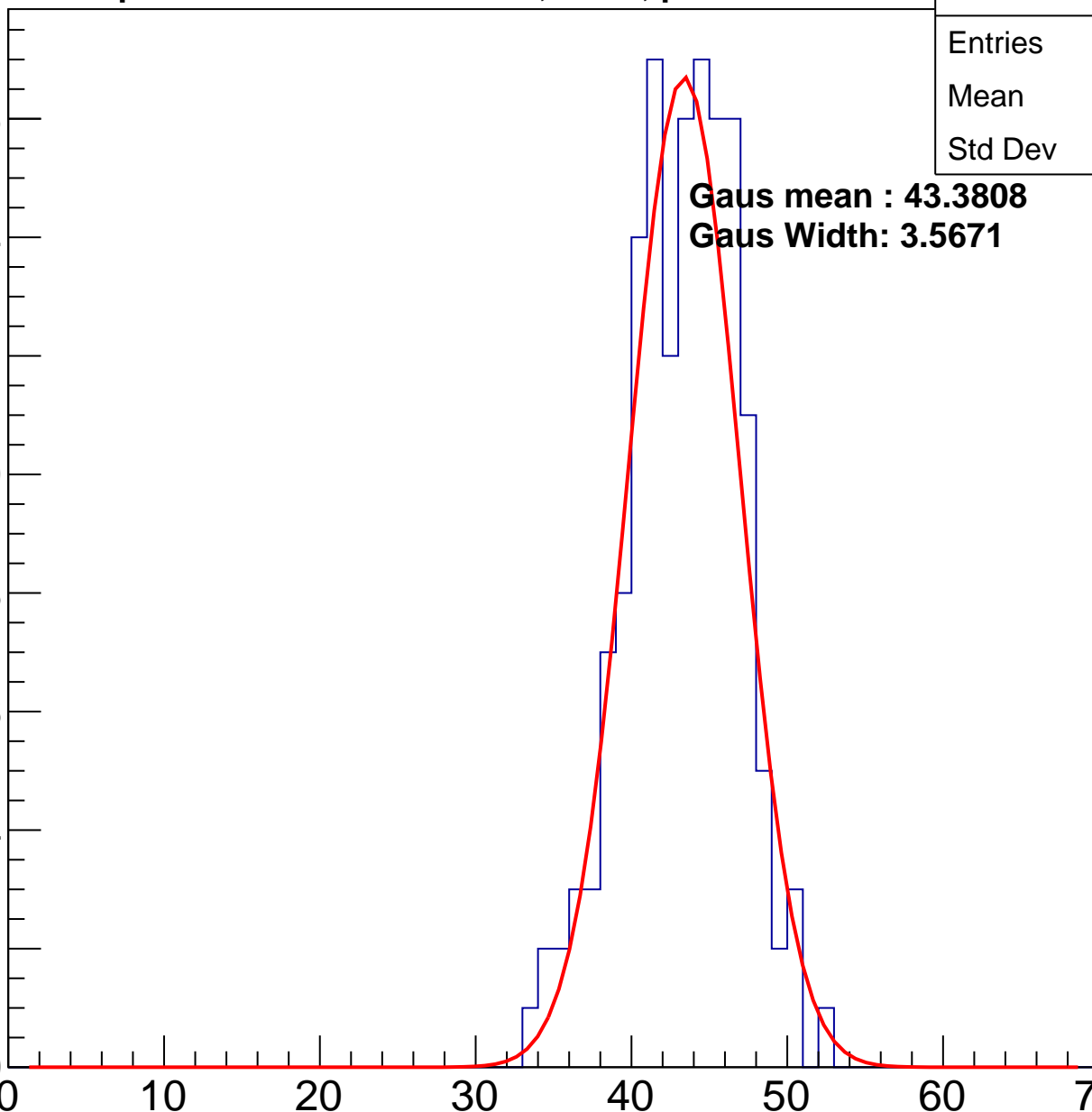
40

50

60

70

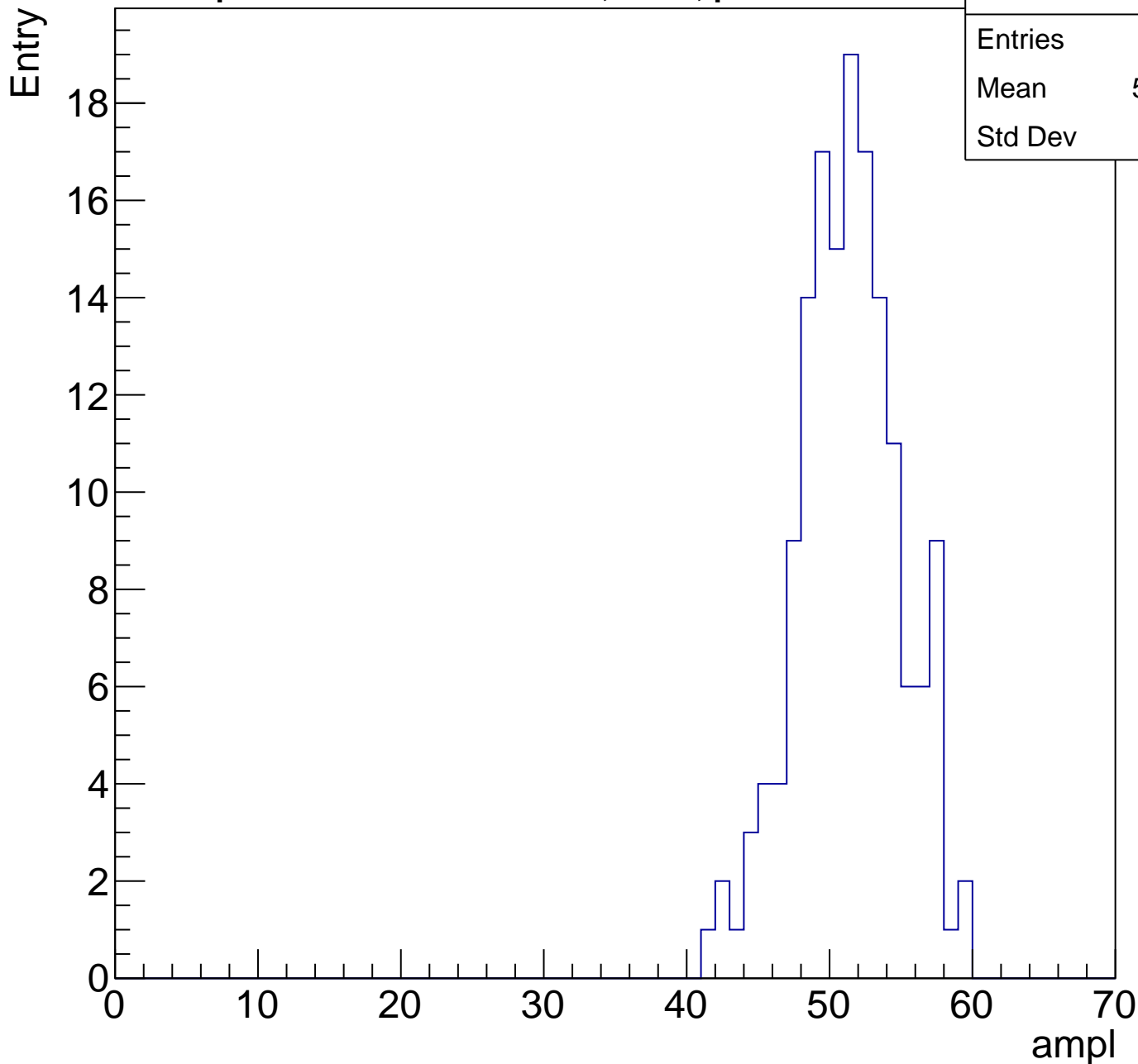
ampl



# B0L101S, U16-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 50.88 |
| Std Dev | 3.58  |



# B0L101S, U16-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

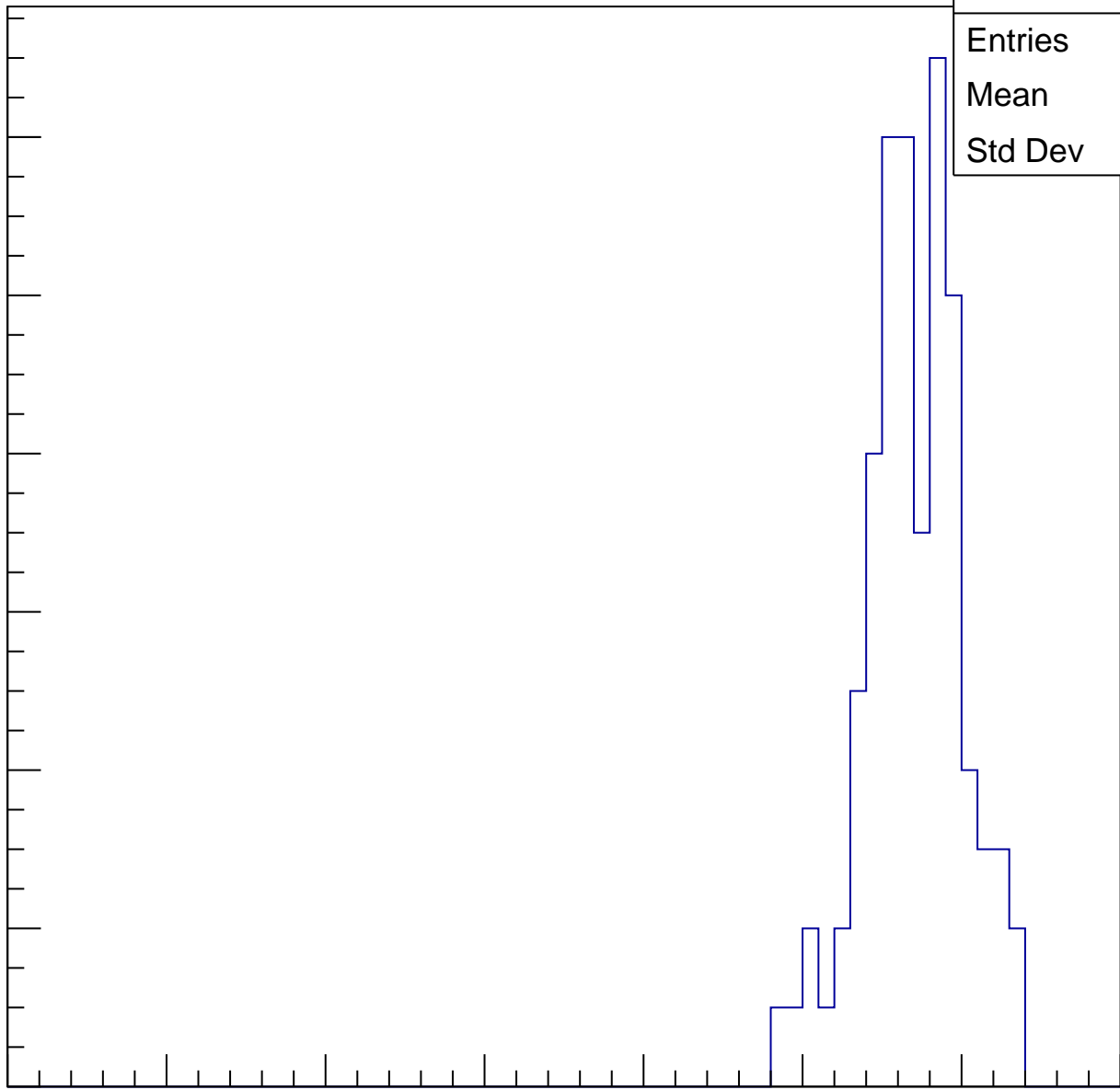
2

0

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 56.5  |
| Std Dev | 3.057 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch33, adc5

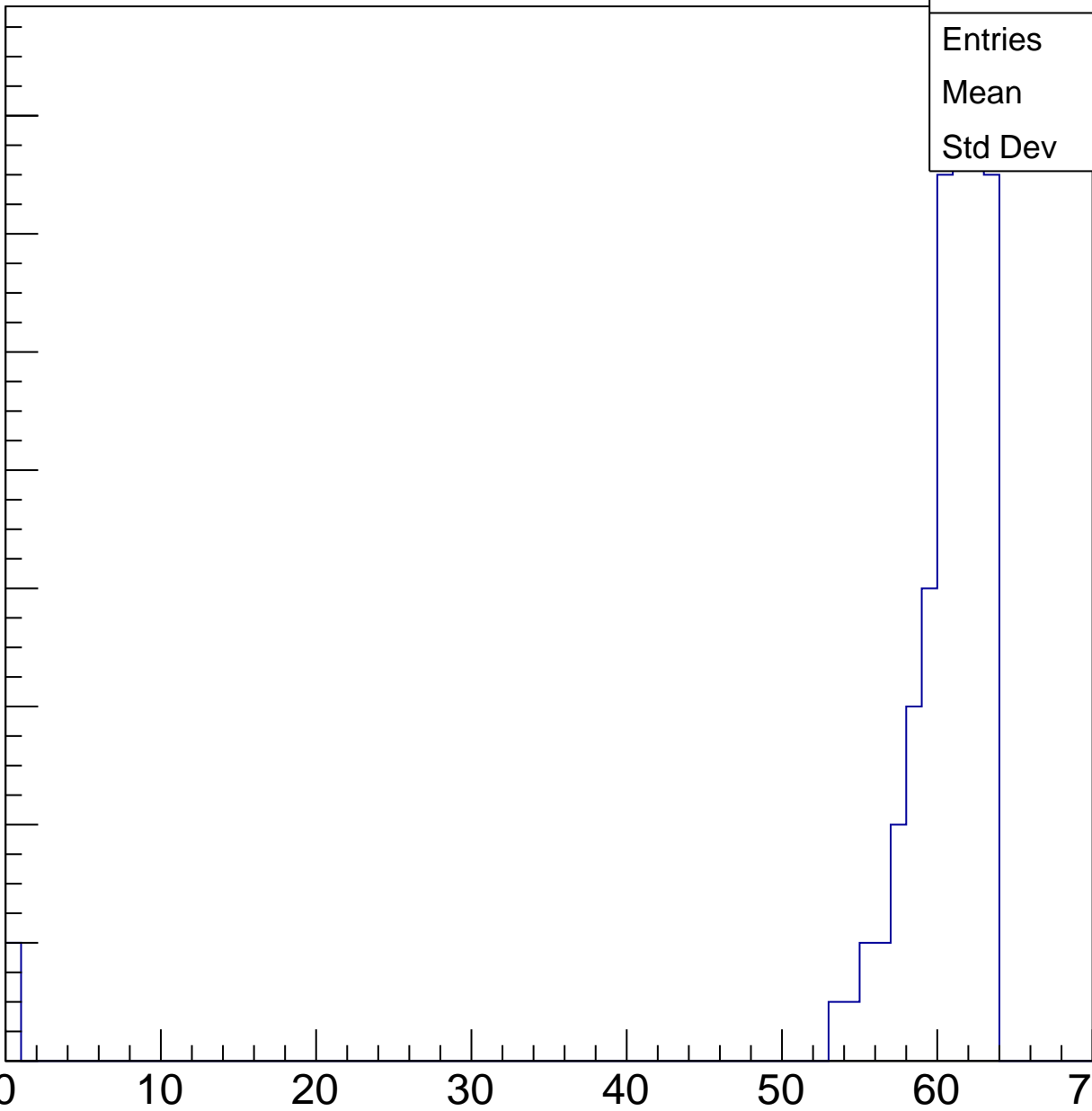
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 59.03 |
| Std Dev | 9.172 |

ampl



# B0L101S, U16-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch34, adc0

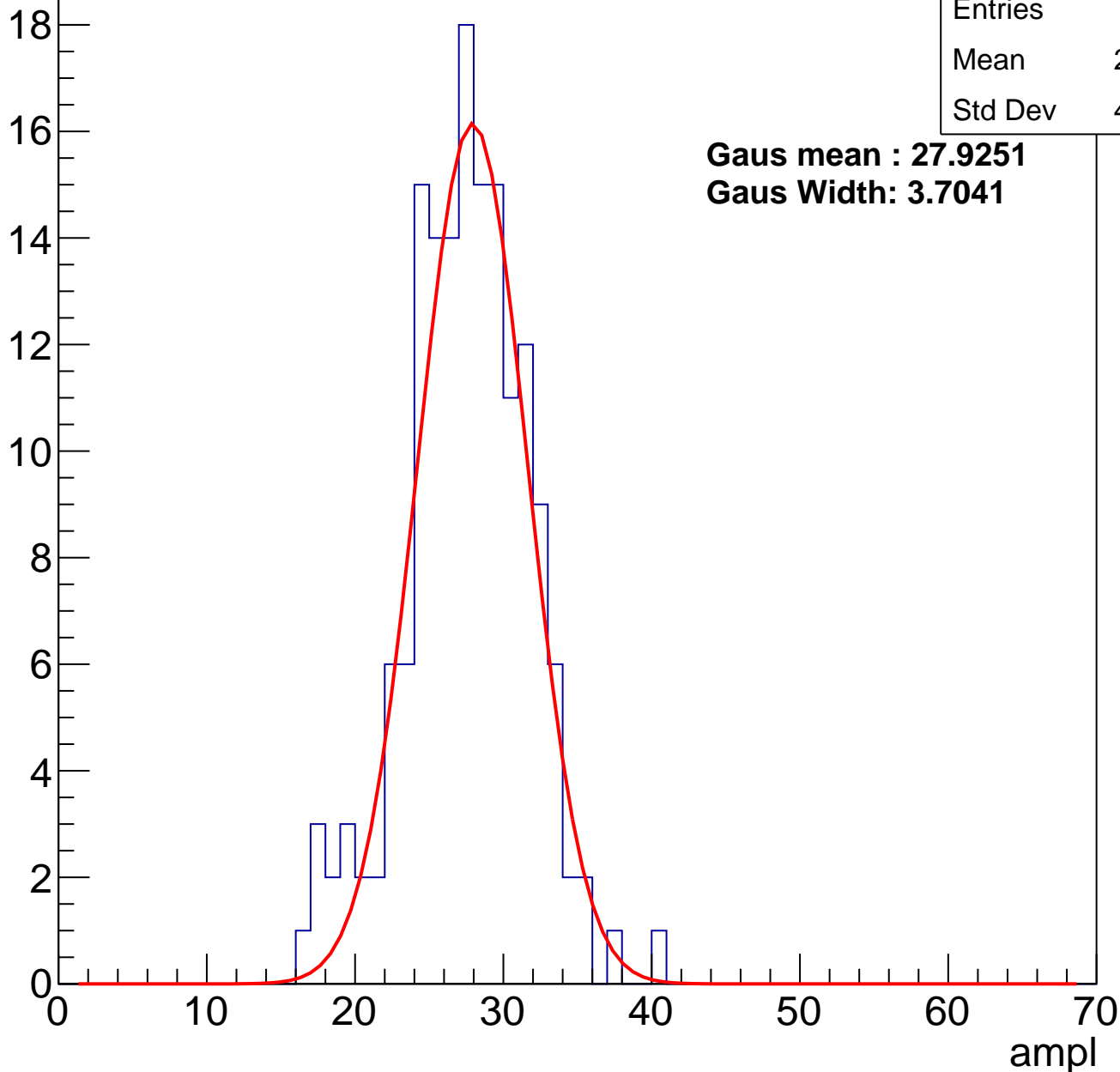
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 27.06 |
| Std Dev | 4.106 |

**Gaus mean : 27.9251**

**Gaus Width: 3.7041**

Entry



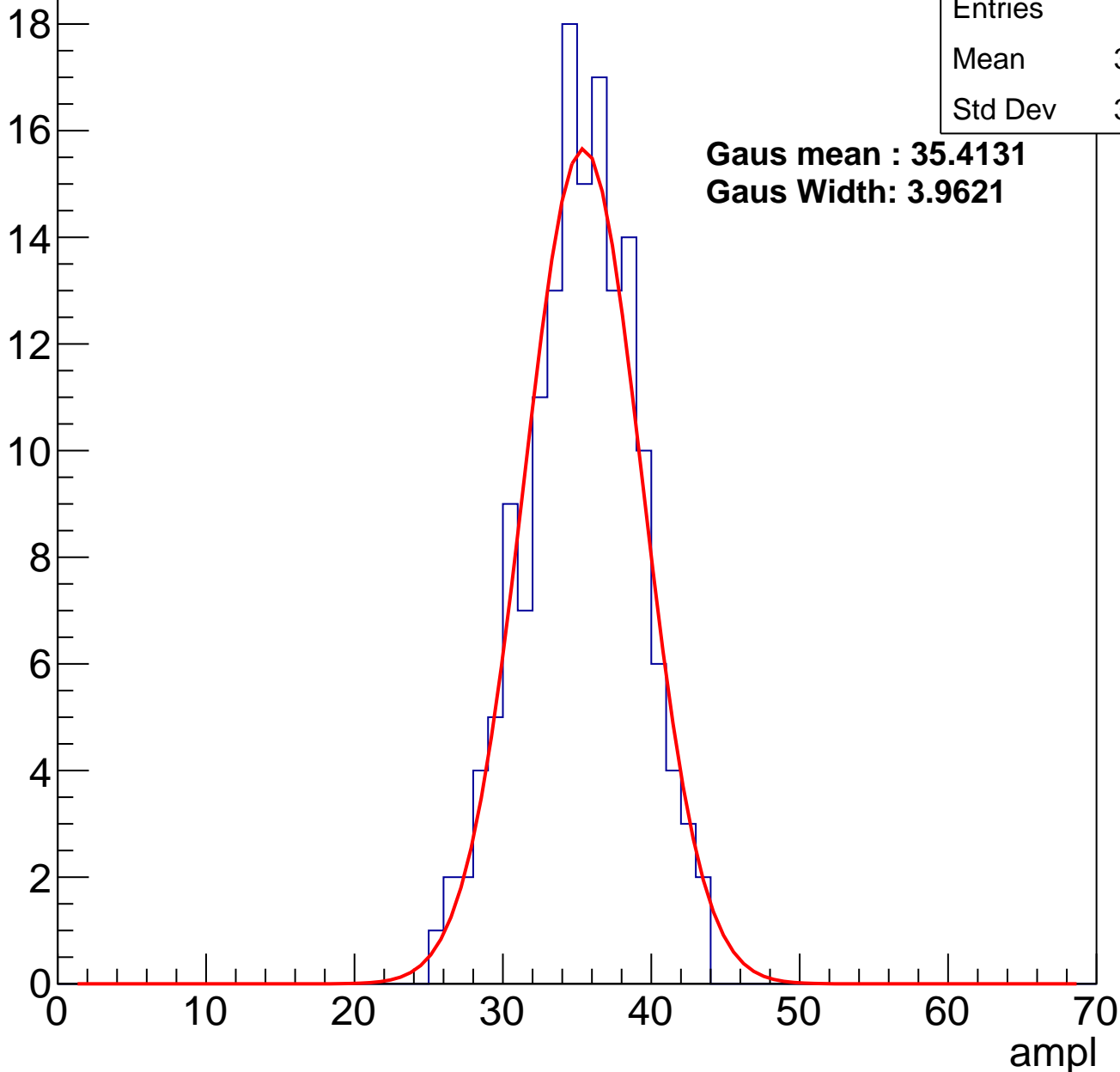
# B0L101S, U16-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 34.77 |
| Std Dev | 3.745 |

**Gaus mean : 35.4131**  
**Gaus Width: 3.9621**

Entry



# B0L101S, U16-ch34, adc2

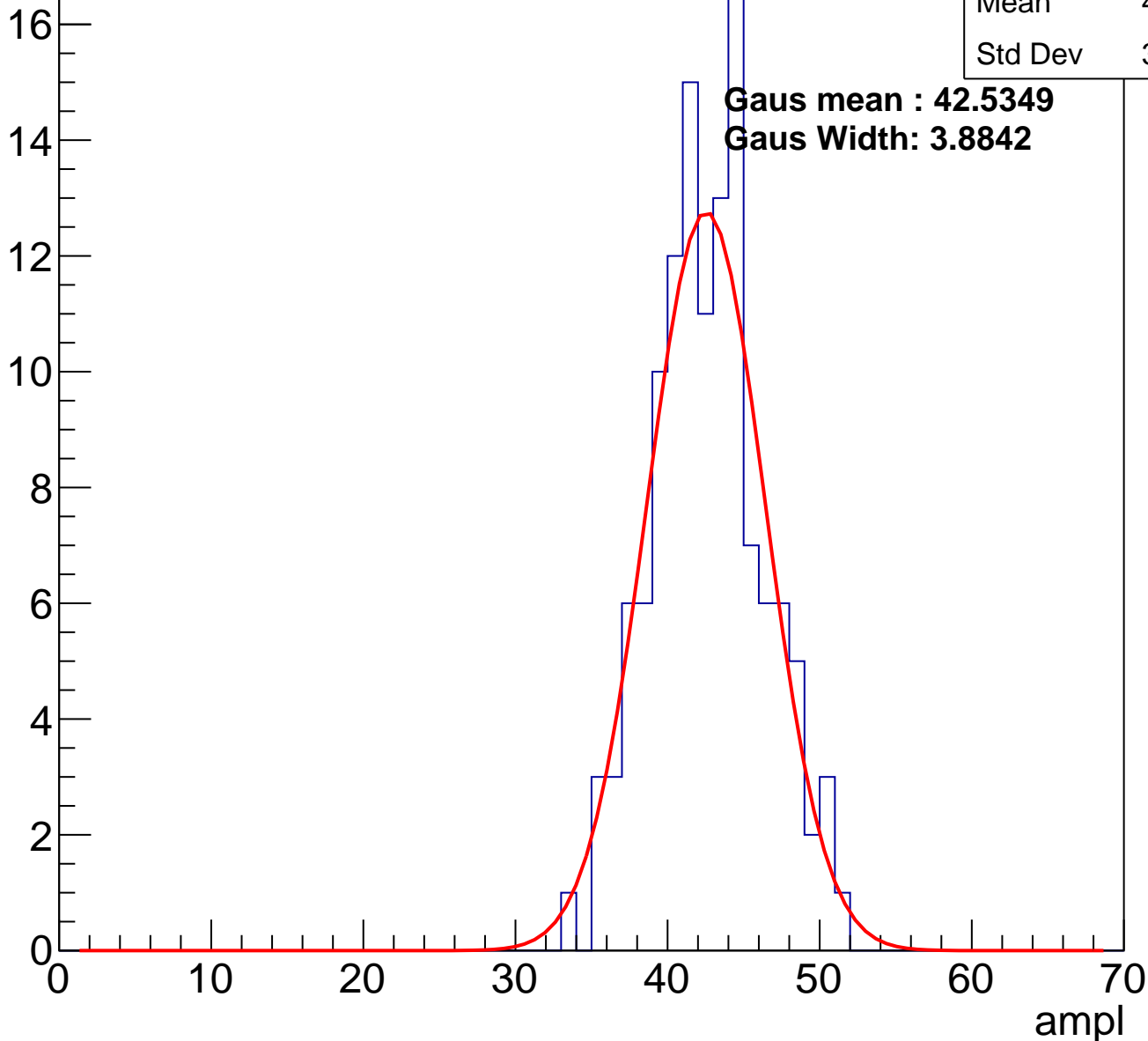
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 127   |
| Mean    | 42.22 |
| Std Dev | 3.626 |

**Gaus mean : 42.5349**

**Gaus Width: 3.8842**

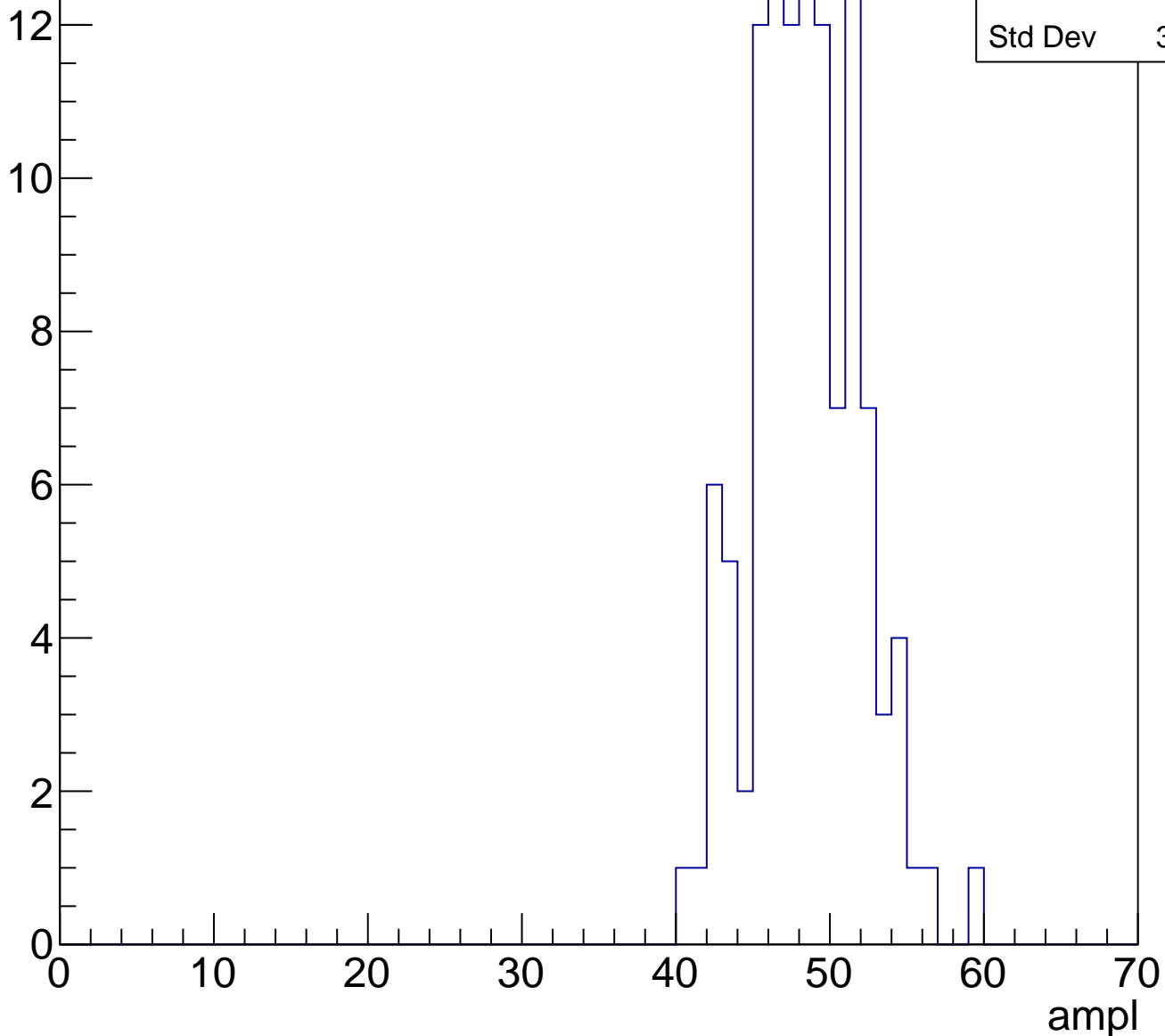


# B0L101S, U16-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 48    |
| Std Dev | 3.479 |

Entry



# B0L101S, U16-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

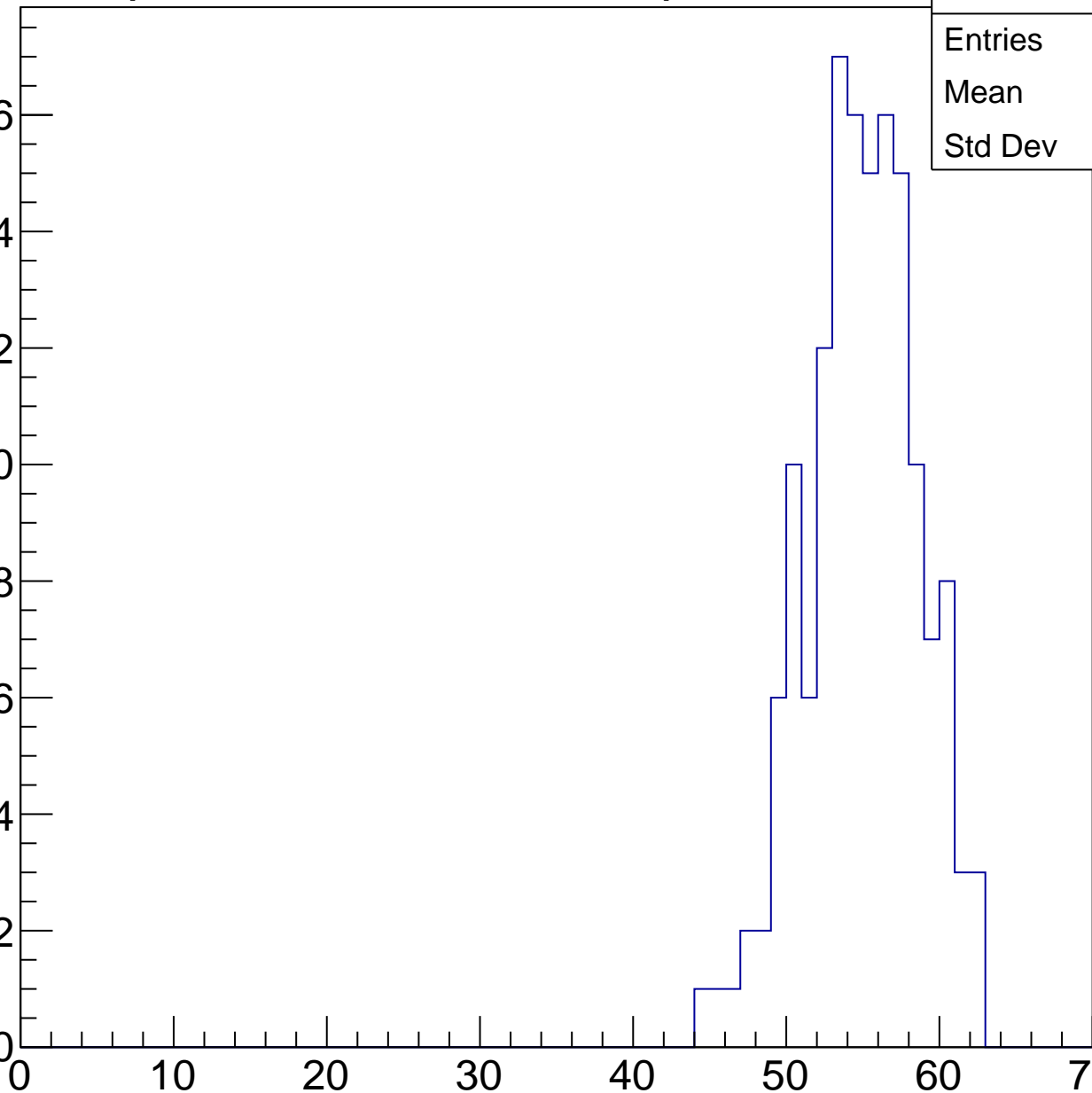
0

Entries 151

Mean 54.52

Std Dev 3.621

ampl



# B0L101S, U16-ch34, adc5

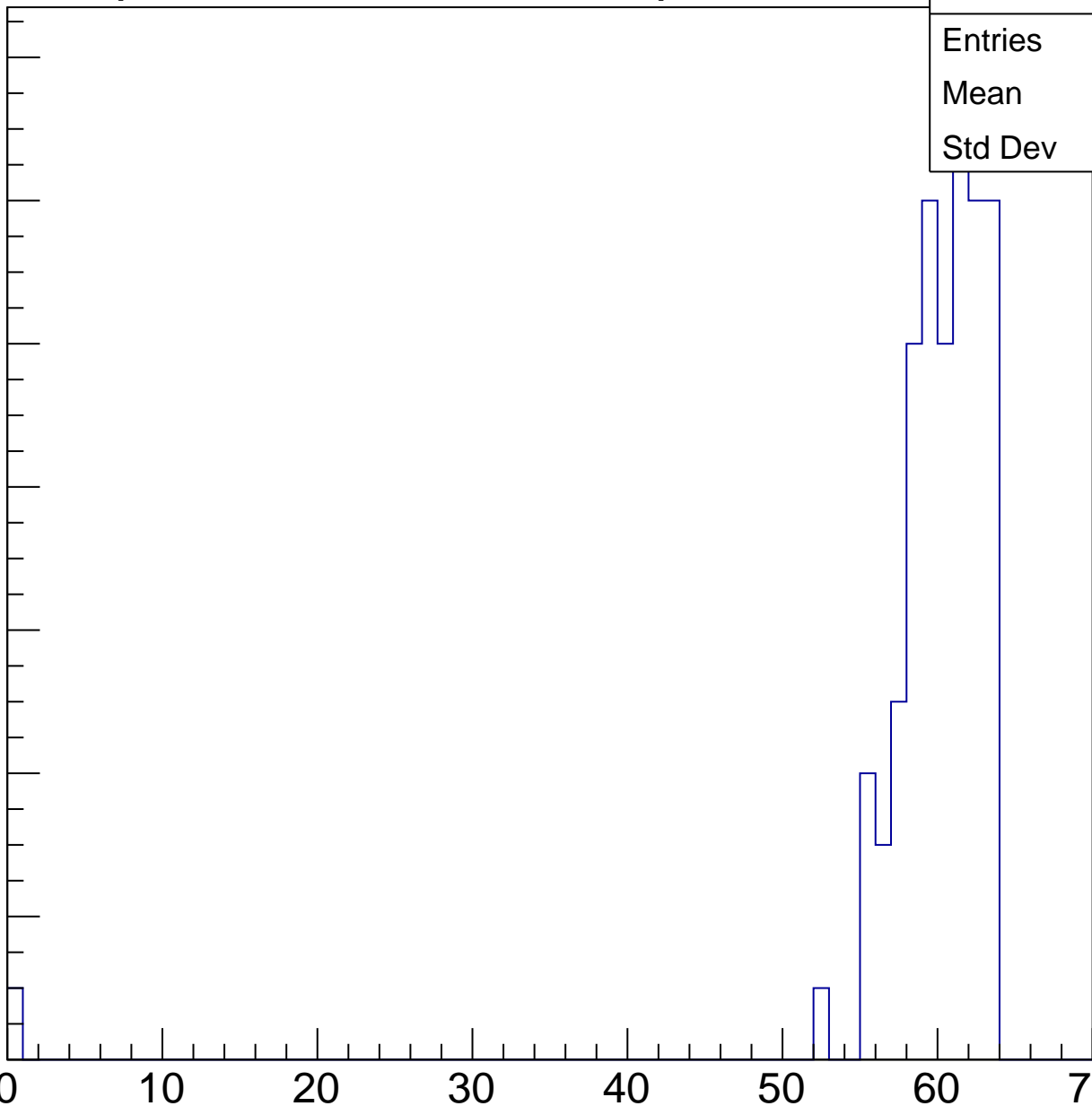
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 84    |
| Mean    | 59.13 |
| Std Dev | 6.914 |

ampl

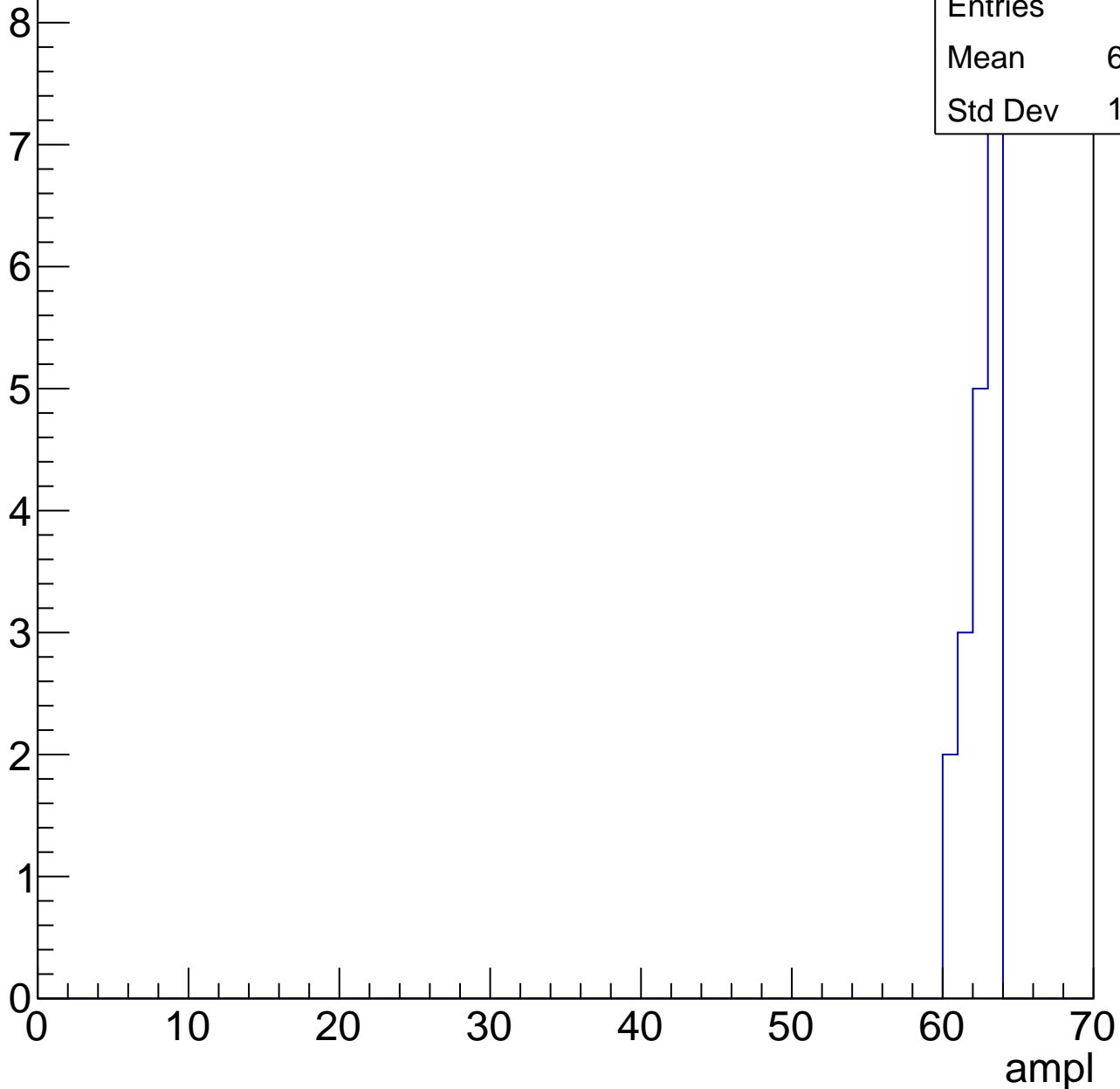


# B0L101S, U16-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 18    |
| Mean    | 62.06 |
| Std Dev | 1.026 |





# B0L101S, U16-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

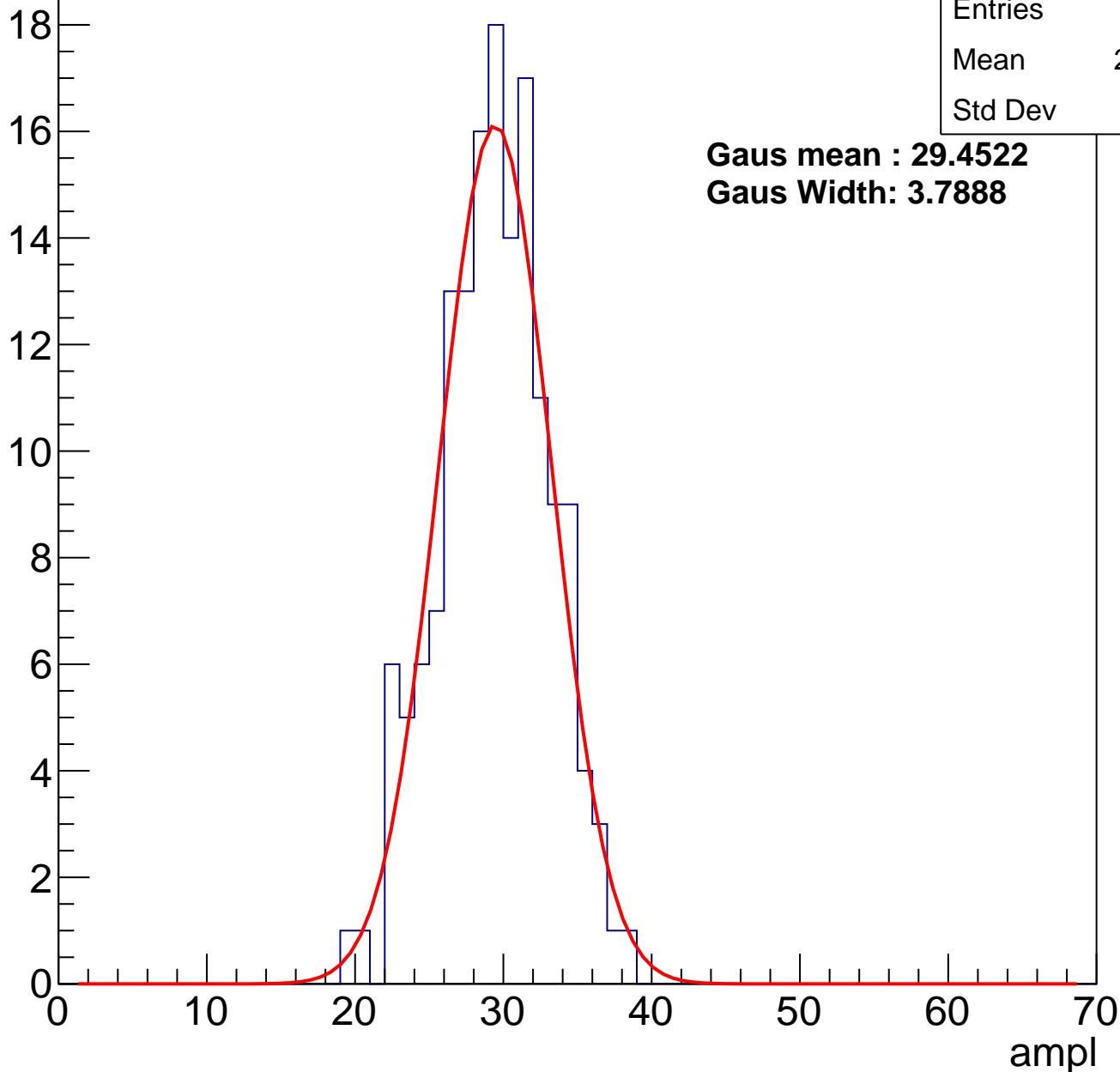


|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch35, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



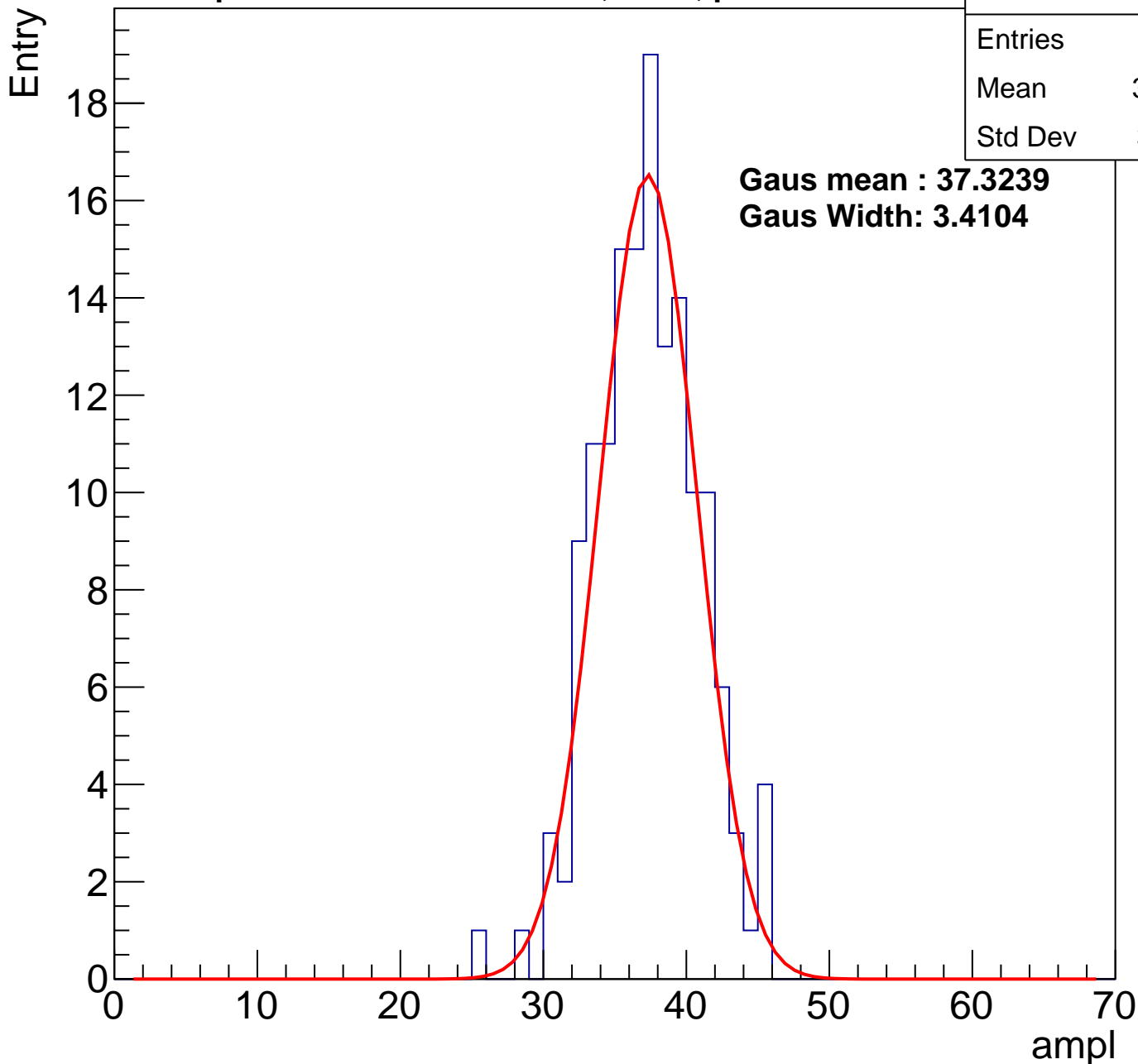
# B0L101S, U16-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 36.84 |
| Std Dev | 3.581 |

**Gaus mean : 37.3239**

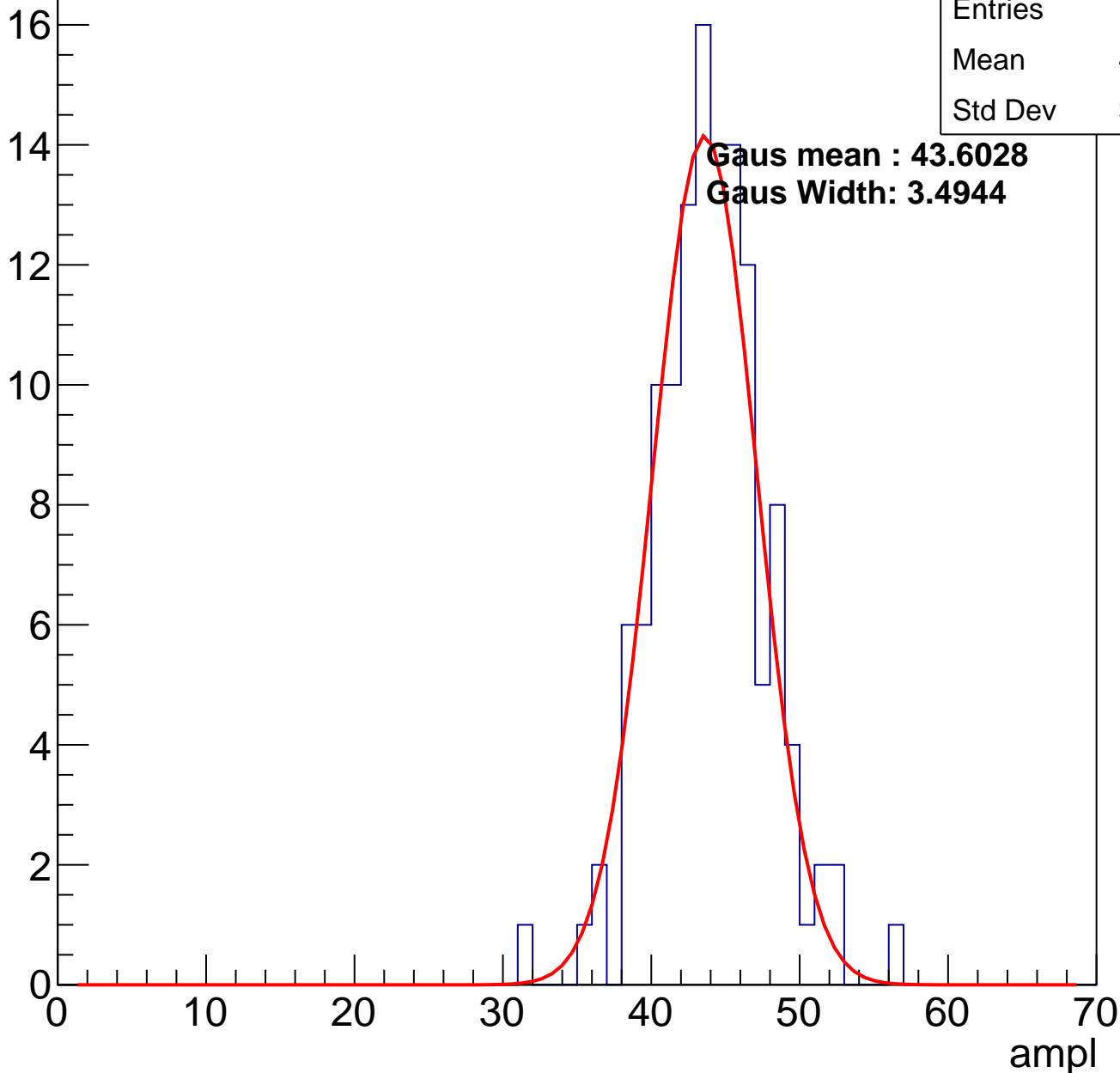
**Gaus Width: 3.4104**



# B0L101S, U16-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

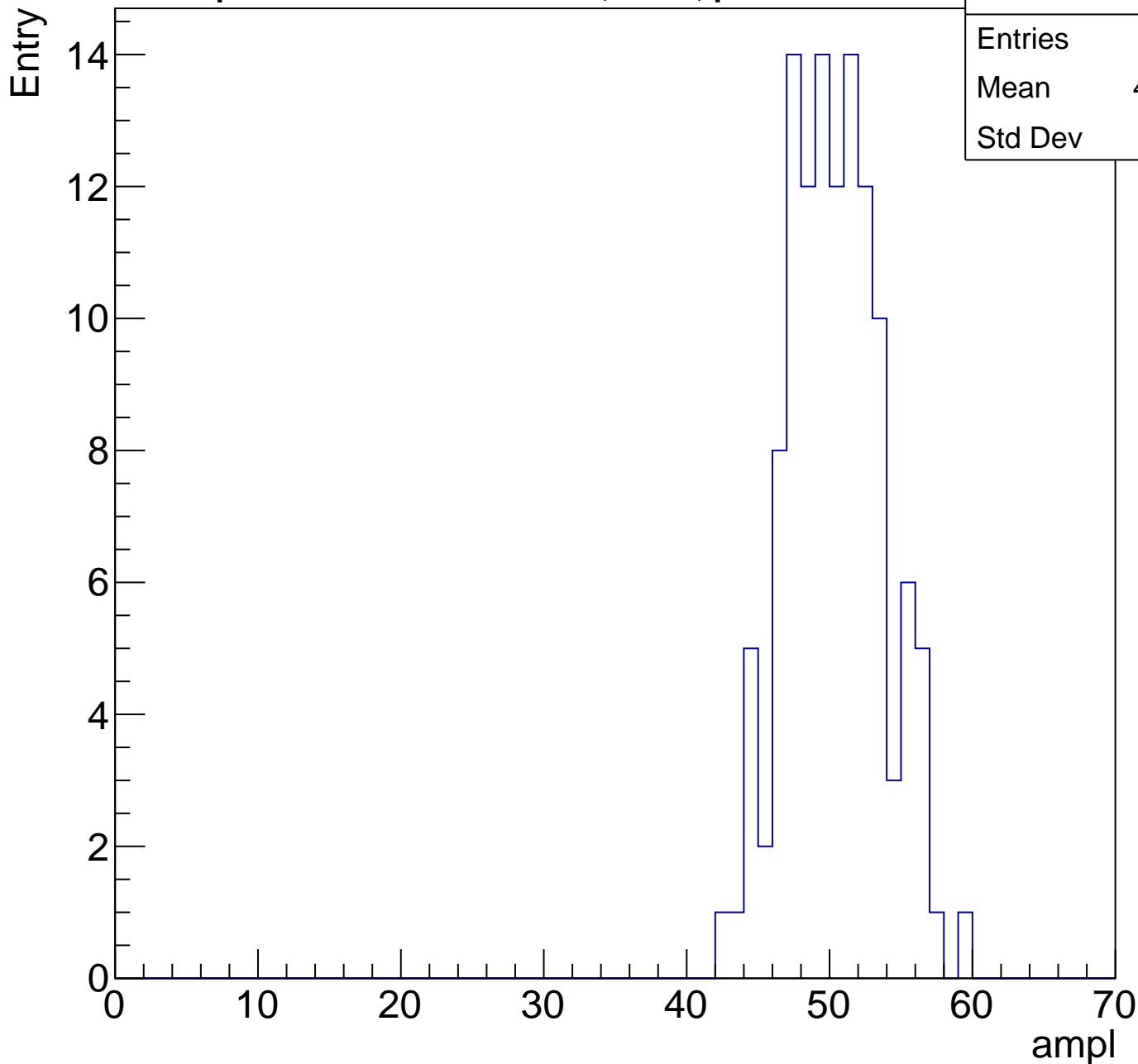
Entry



# B0L101S, U16-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

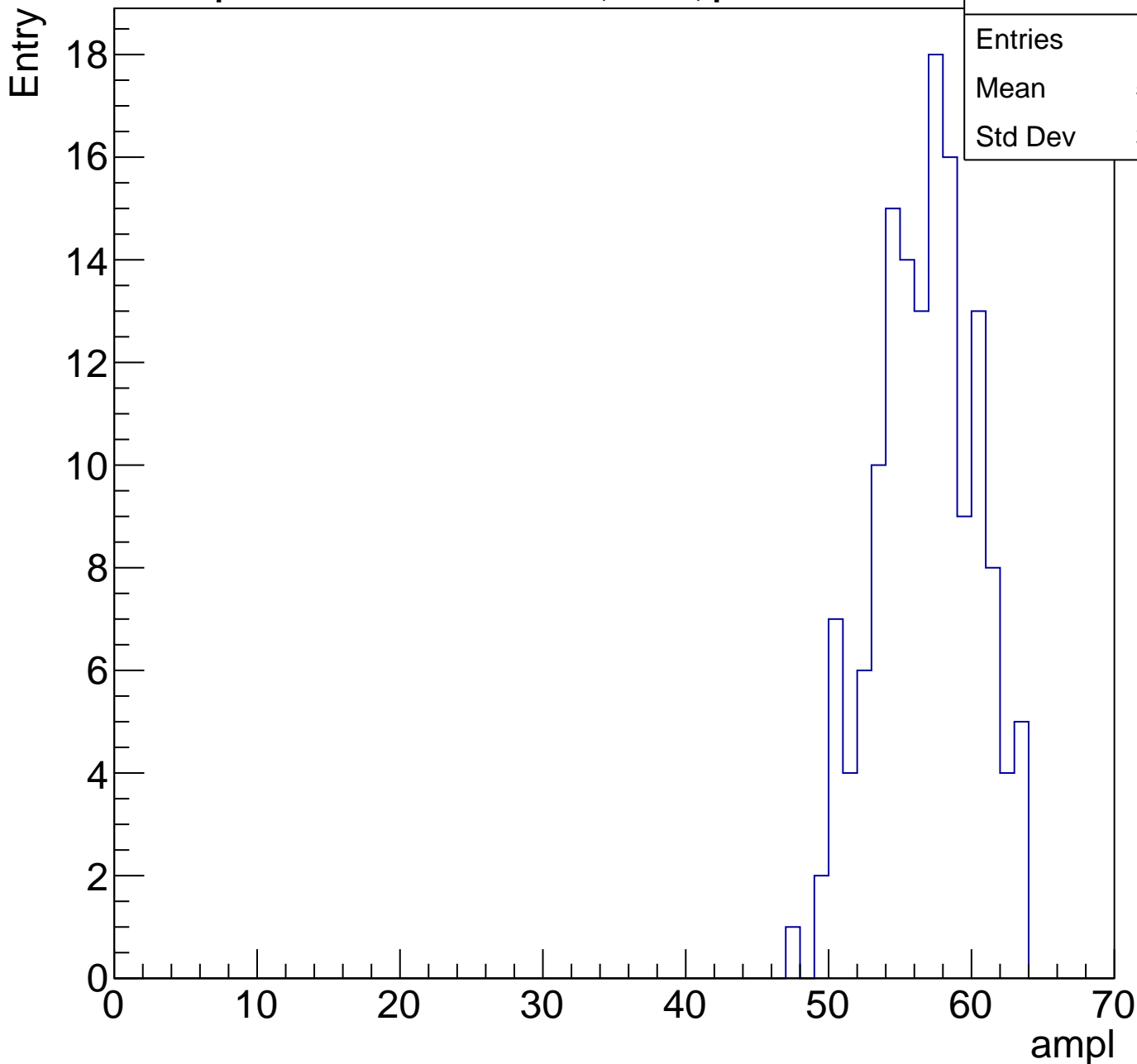
|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 49.91 |
| Std Dev | 3.3   |



# B0L101S, U16-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 145   |
| Mean    | 56.31 |
| Std Dev | 3.461 |



# B0L101S, U16-ch35, adc5

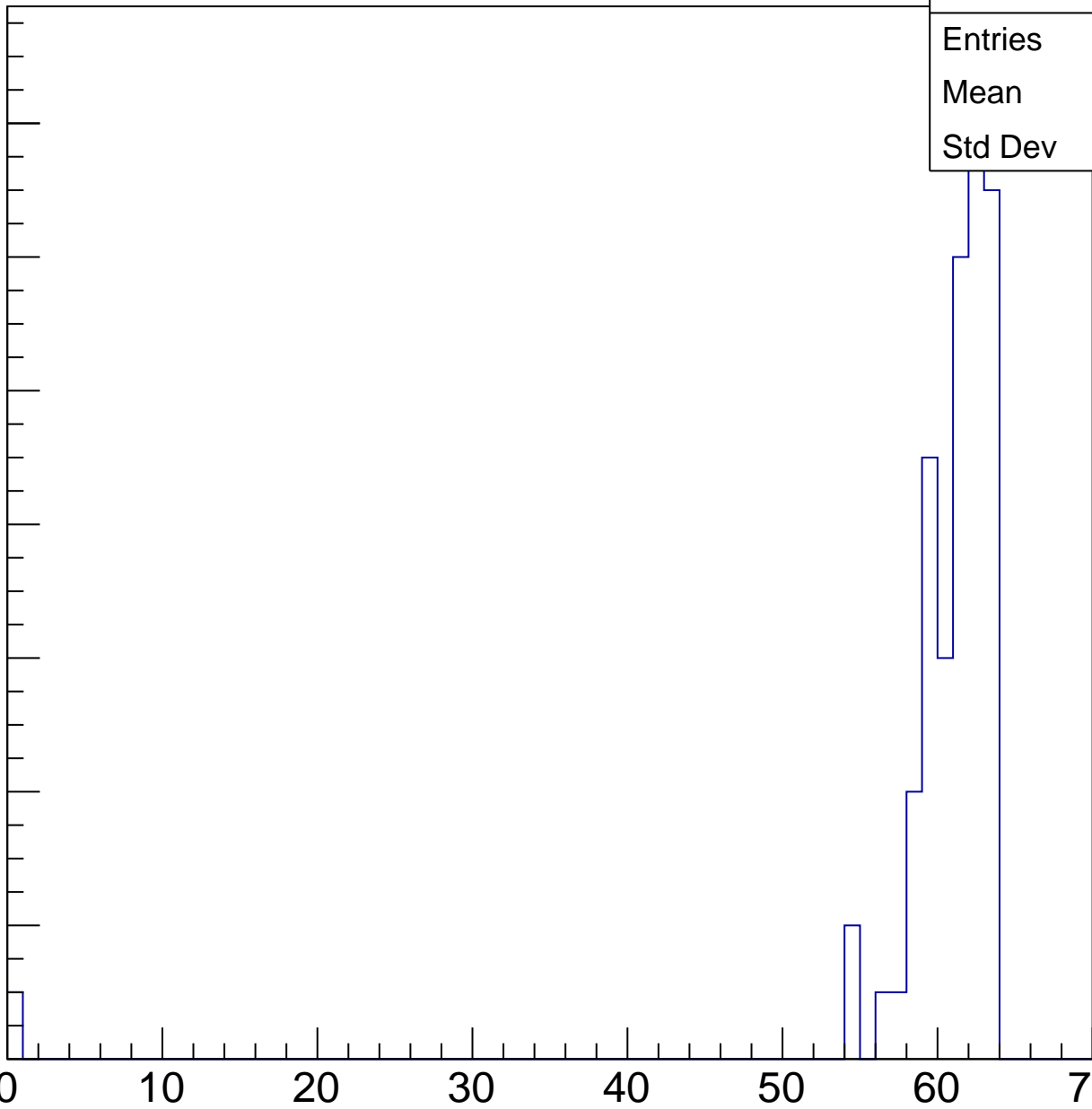
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 59.77 |
| Std Dev | 7.814 |

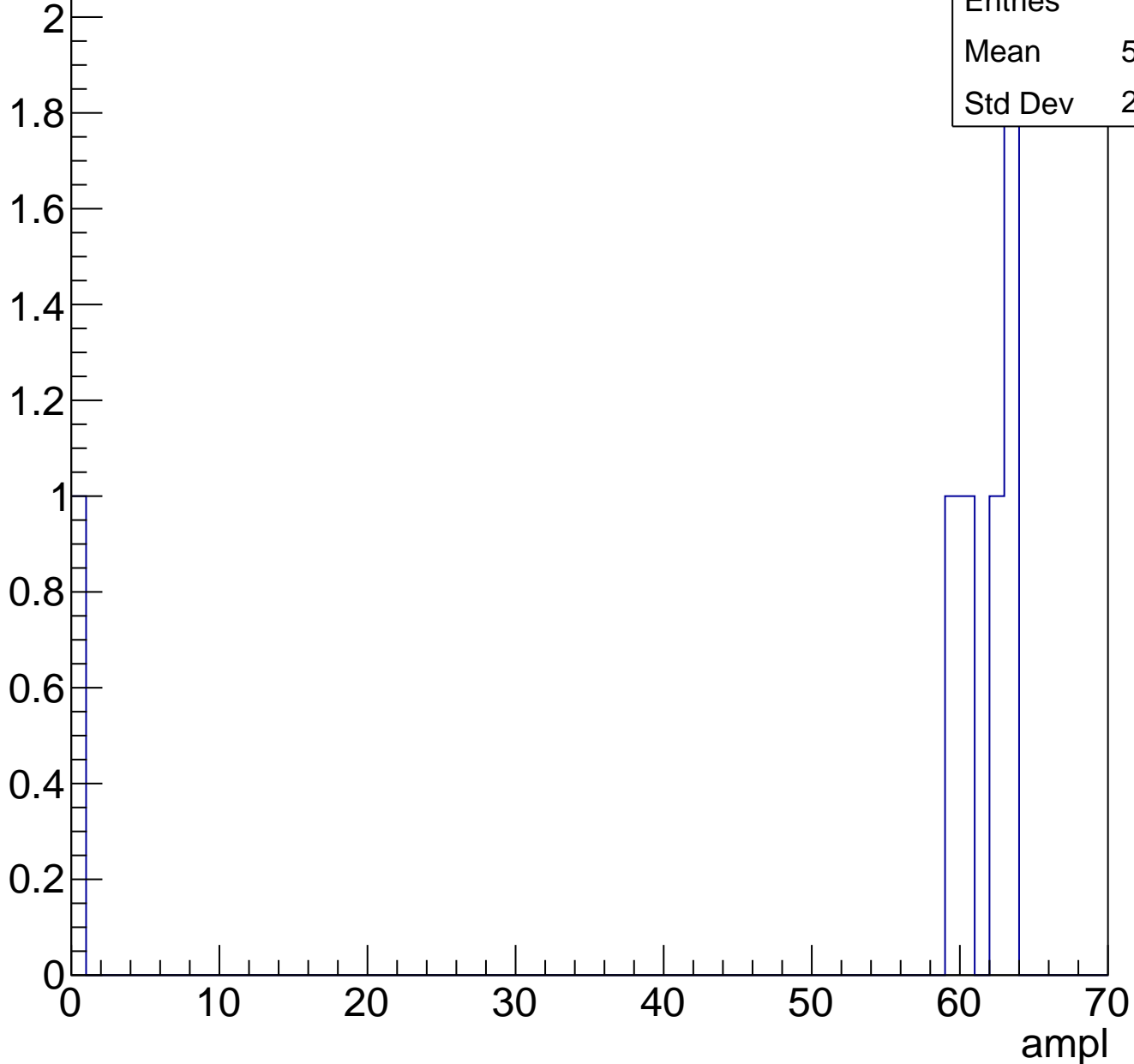
ampl



# B0L101S, U16-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch36, adc0

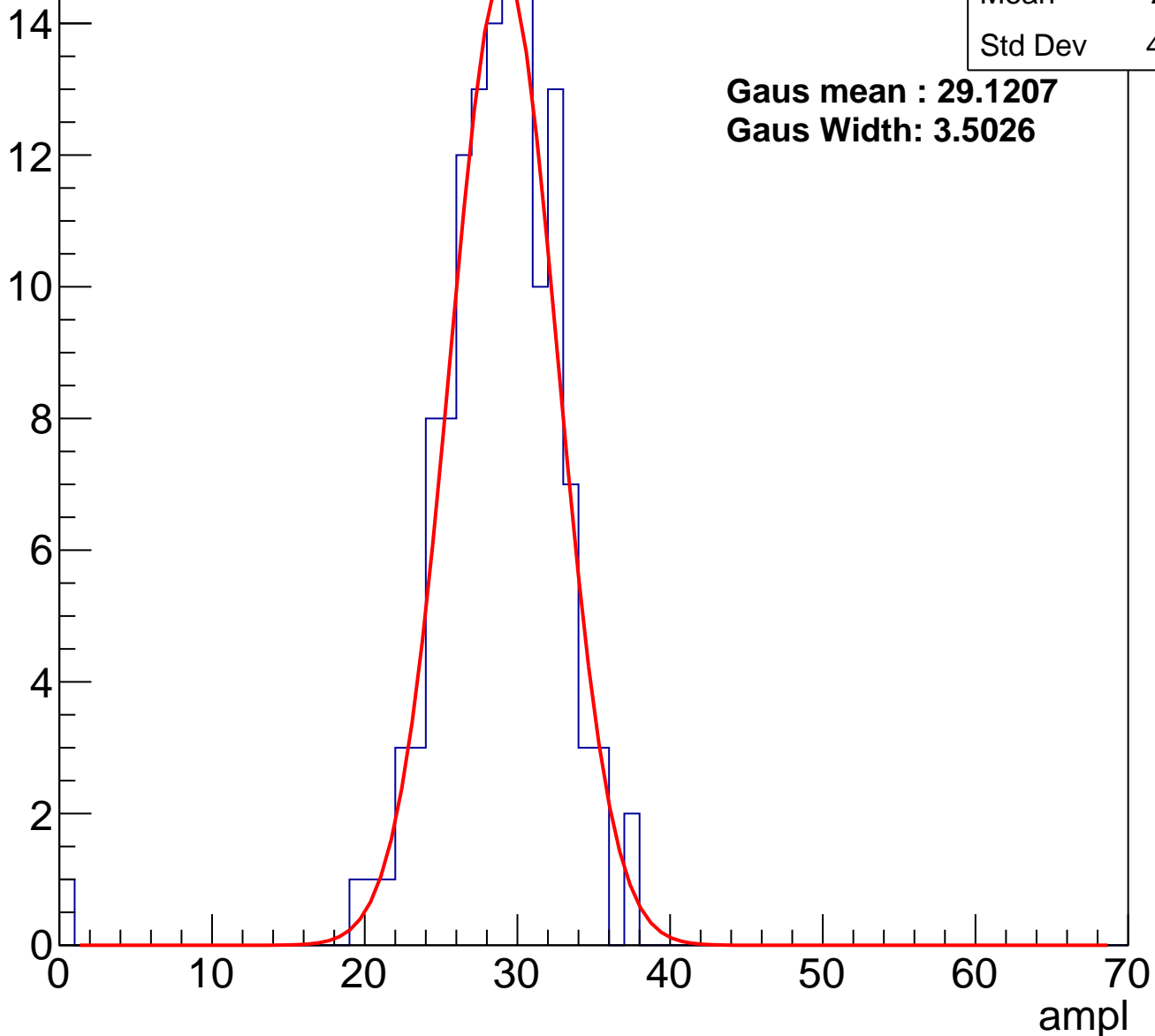
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 28.31 |
| Std Dev | 4.225 |

**Gaus mean : 29.1207**

**Gaus Width: 3.5026**

Entry



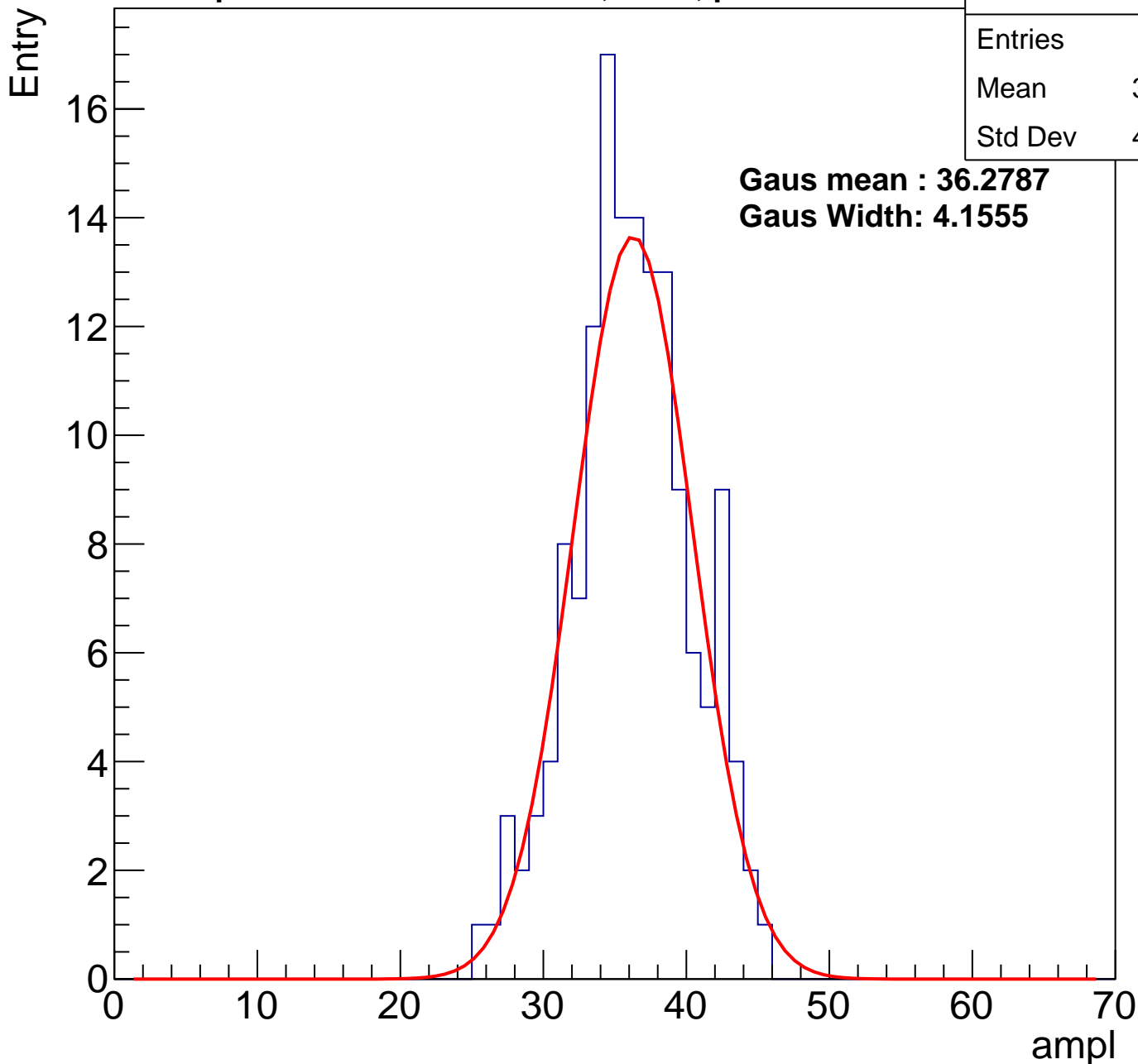
# B0L101S, U16-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 35.74 |
| Std Dev | 4.086 |

**Gaus mean : 36.2787**

**Gaus Width: 4.1555**



# B0L101S, U16-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

113

Mean

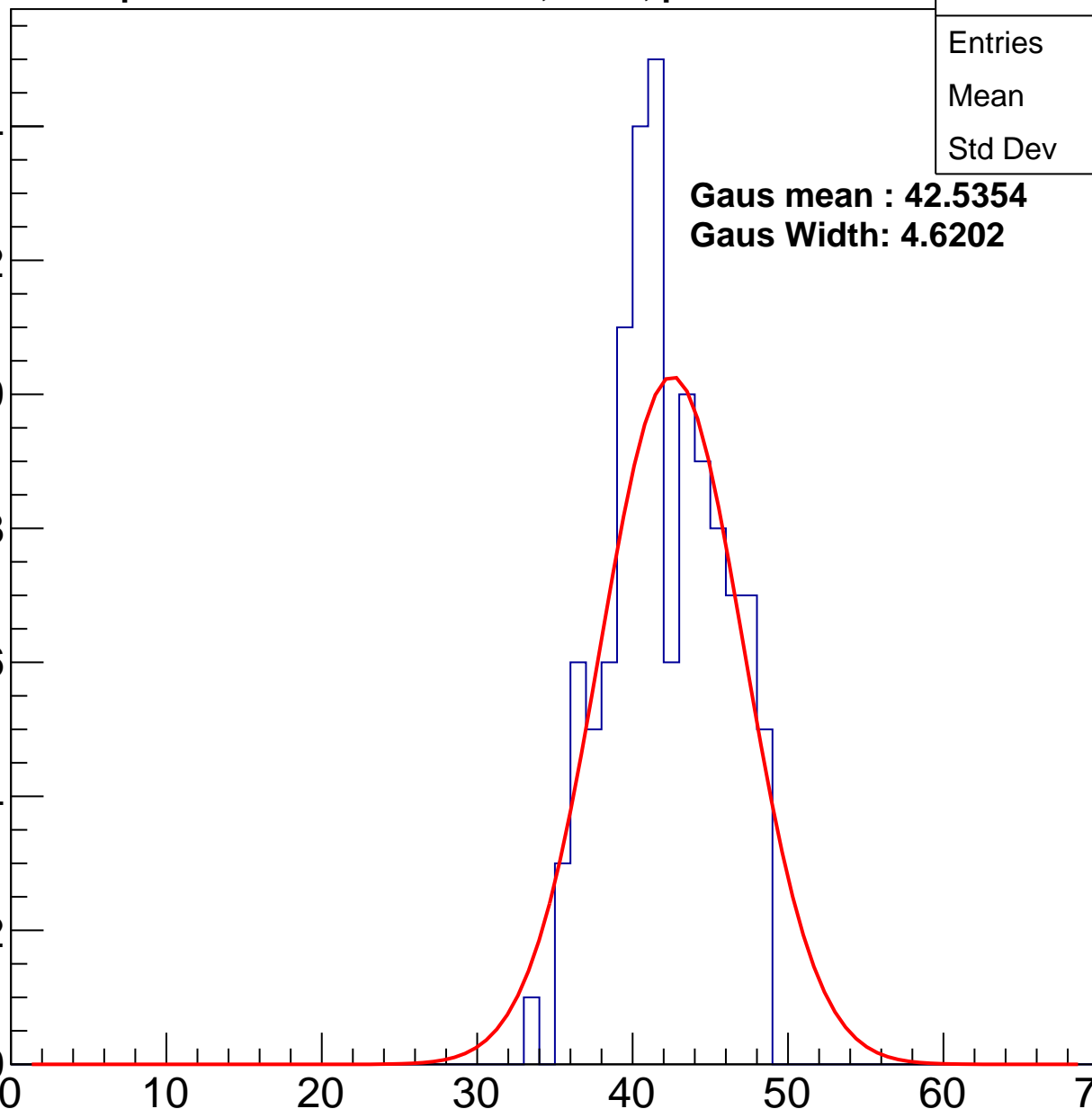
41.59

Std Dev

3.521

**Gaus mean : 42.5354**

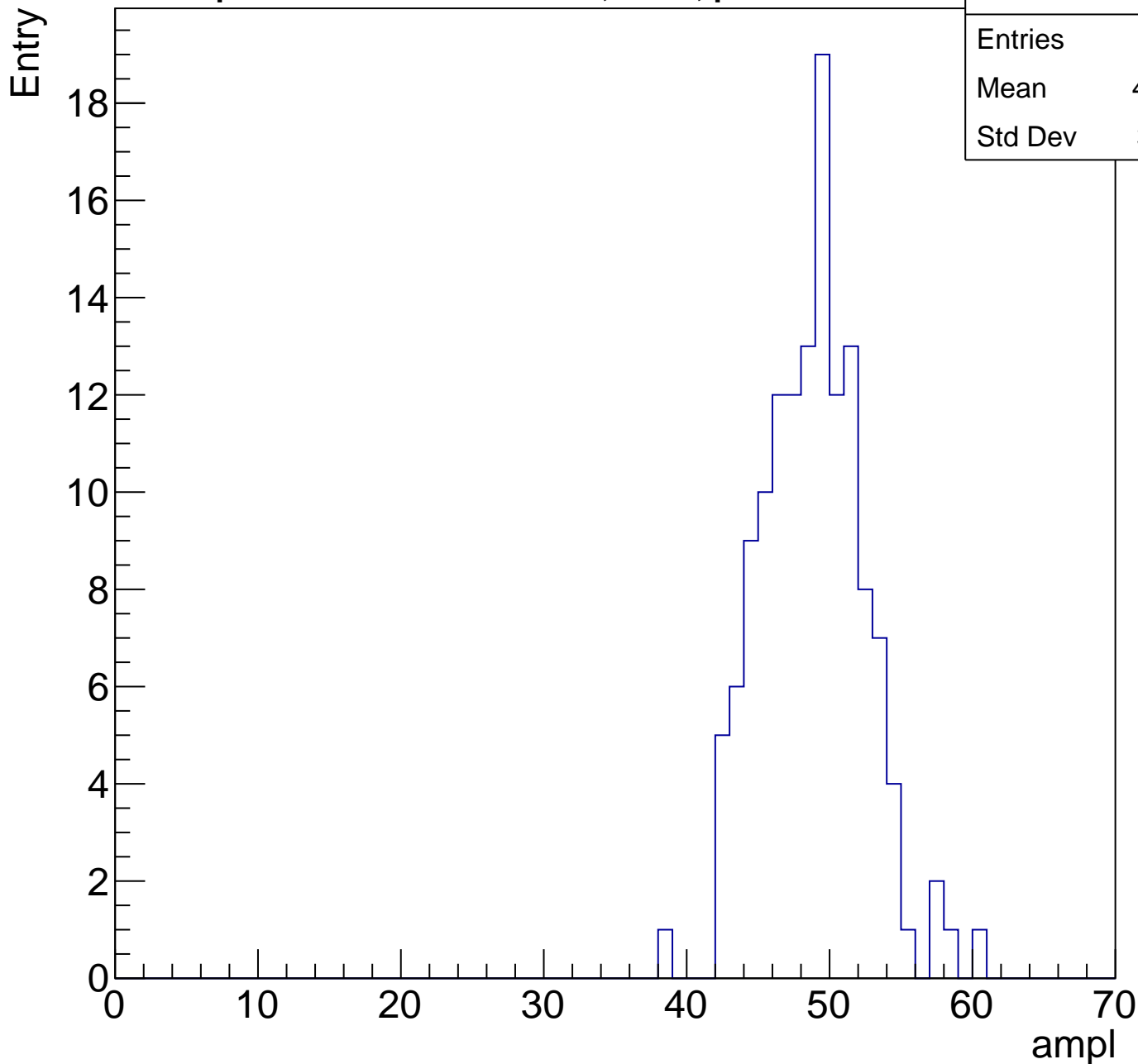
**Gaus Width: 4.6202**



# B0L101S, U16-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

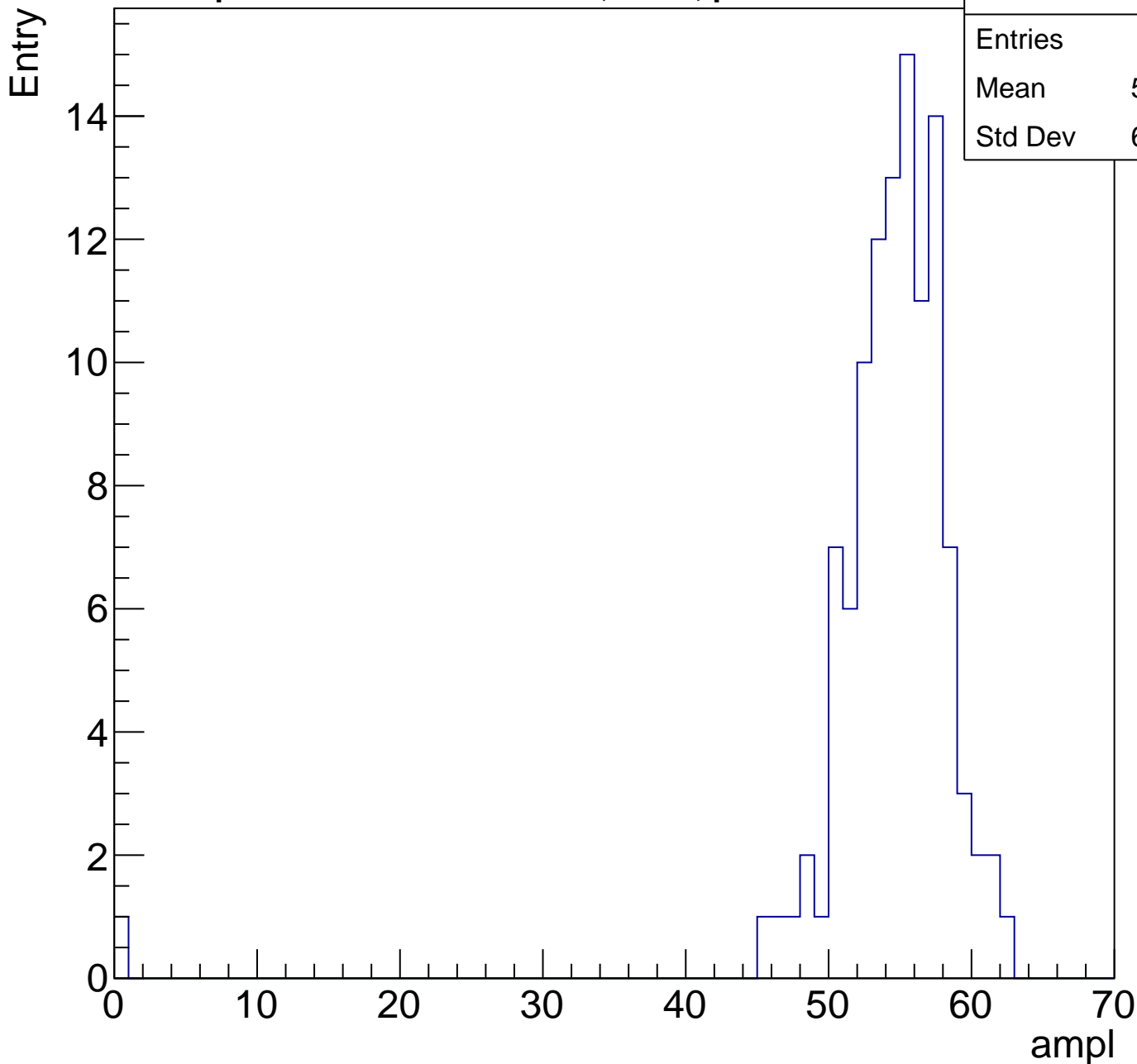
|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 48.35 |
| Std Dev | 3.631 |



# B0L101S, U16-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 53.85 |
| Std Dev | 6.052 |



# B0L101S, U16-ch36, adc5

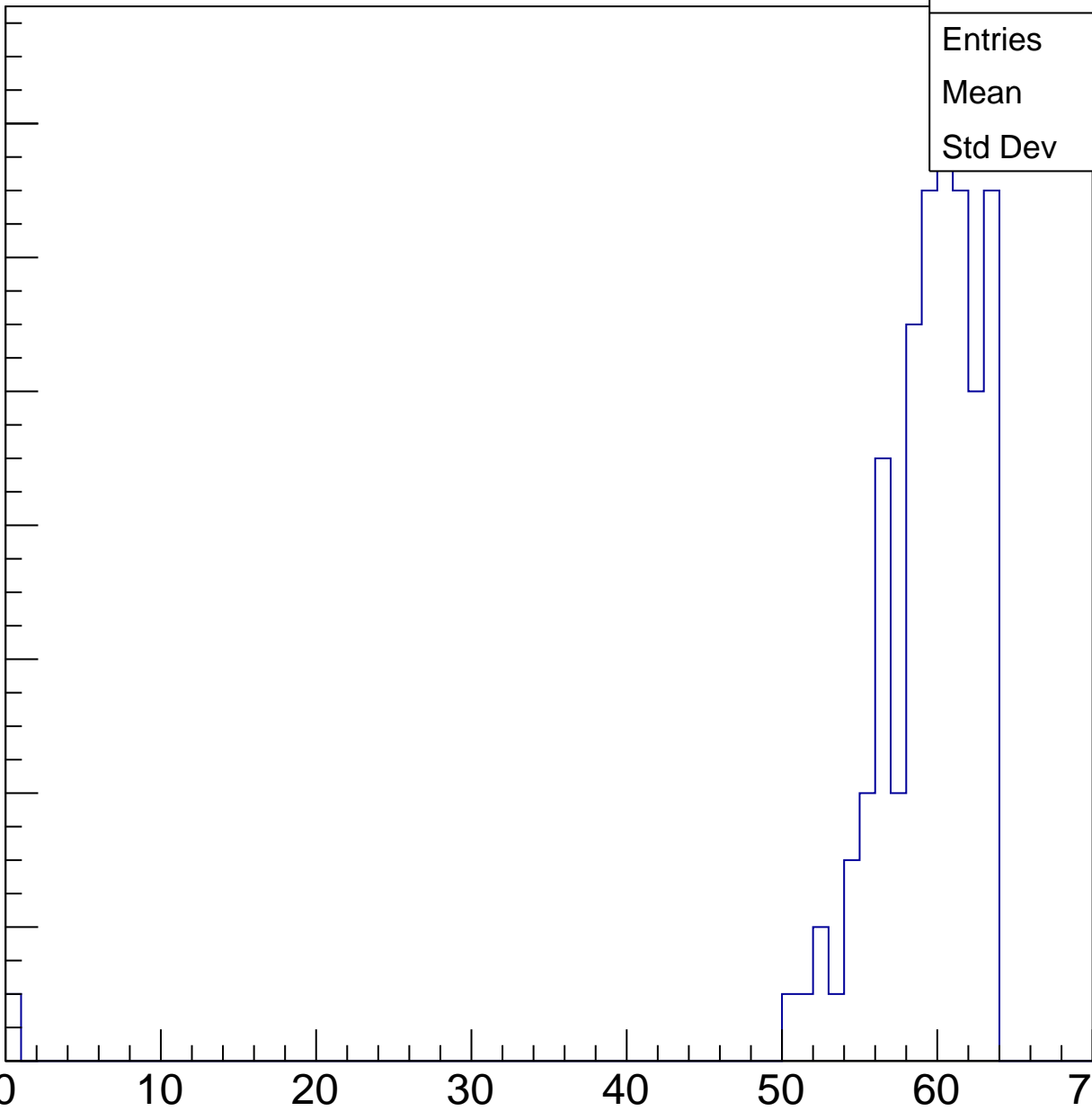
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 58.5  |
| Std Dev | 6.561 |

ampl

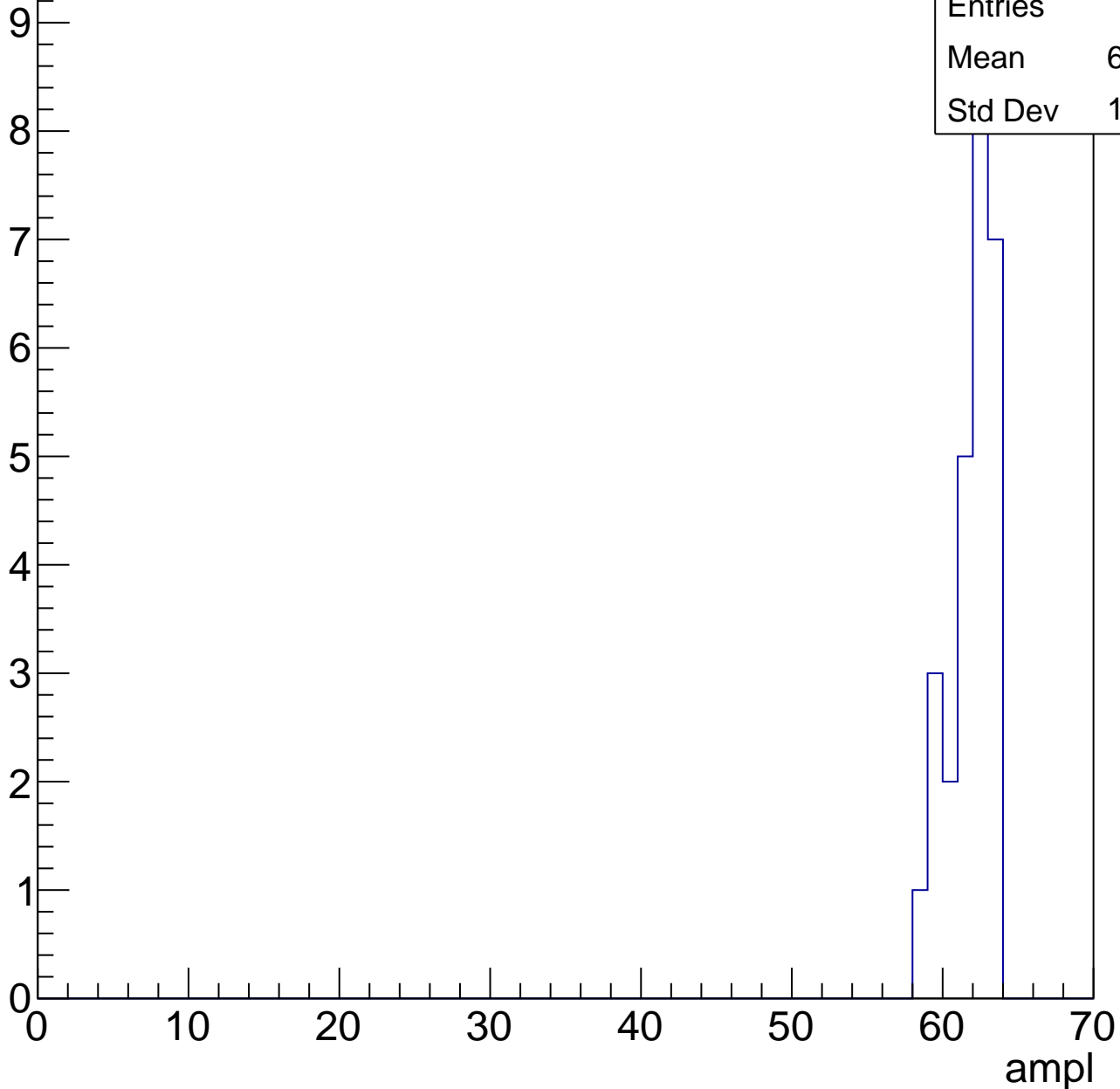


# B0L101S, U16-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 27    |
| Mean    | 61.44 |
| Std Dev | 1.423 |

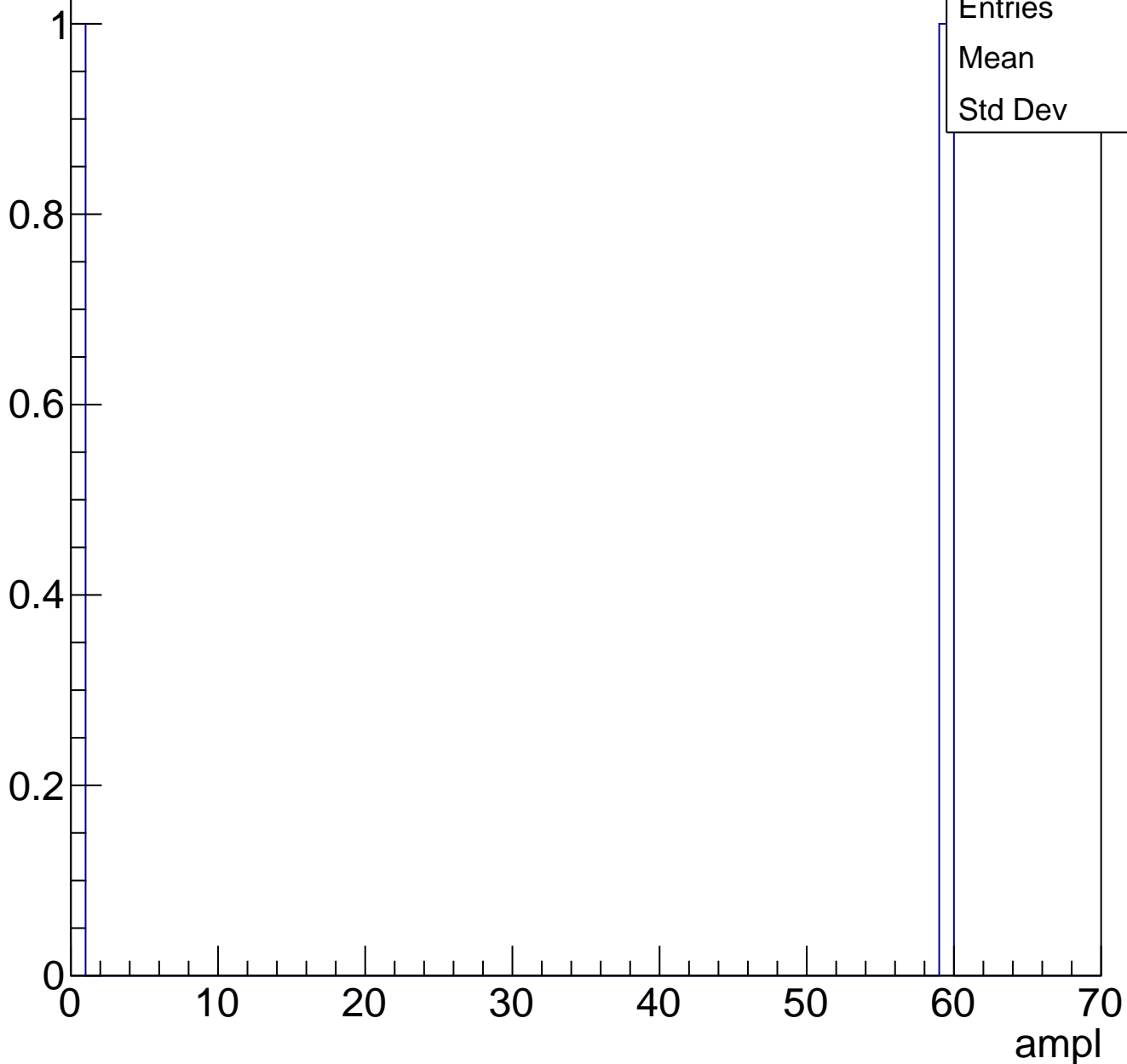




# B0L101S, U16-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

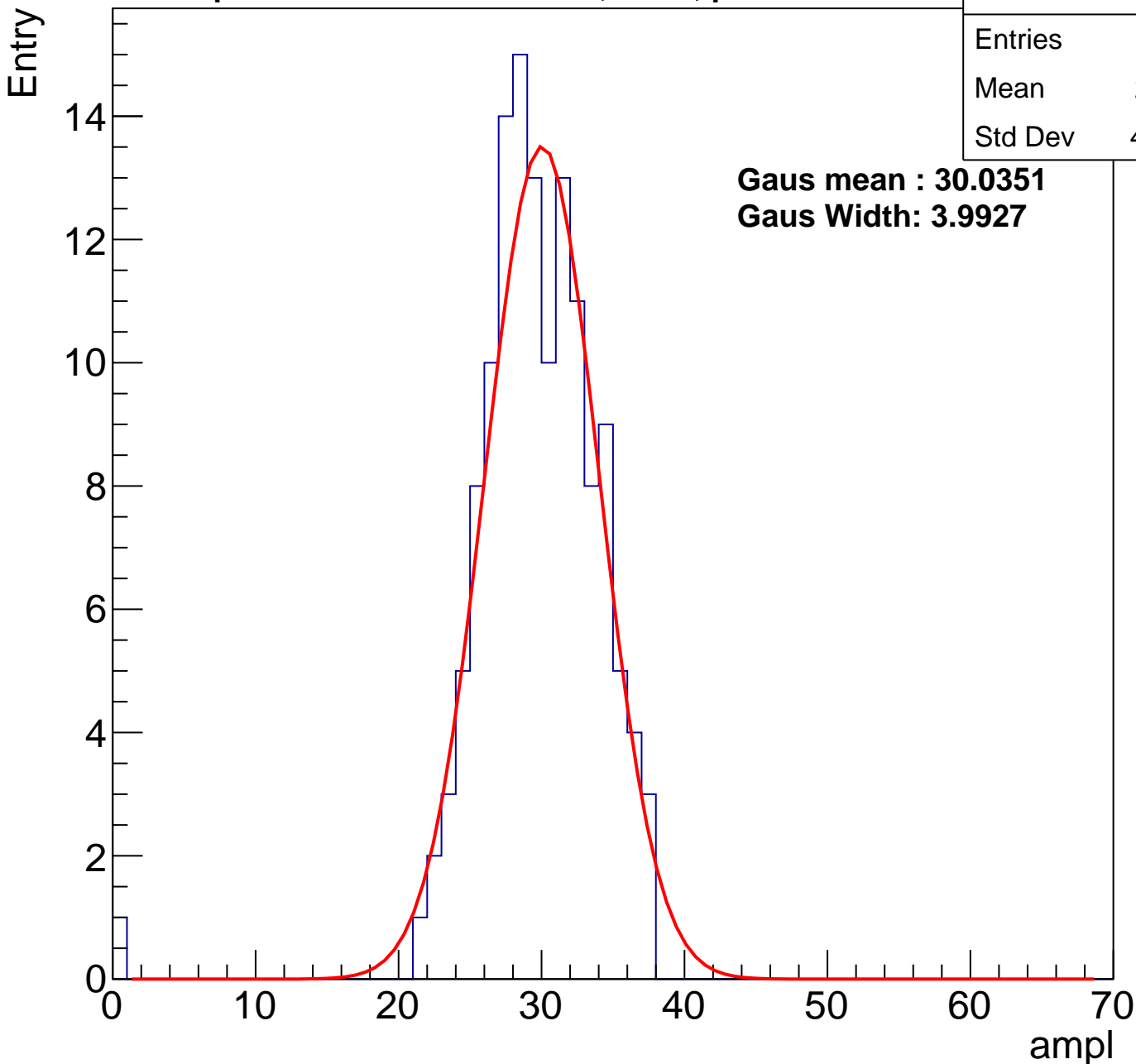


# B0L101S, U16-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 29.21 |
| Std Dev | 4.389 |

**Gaus mean : 30.0351**  
**Gaus Width: 3.9927**



# B0L101S, U16-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 36.62 |
| Std Dev | 3.848 |

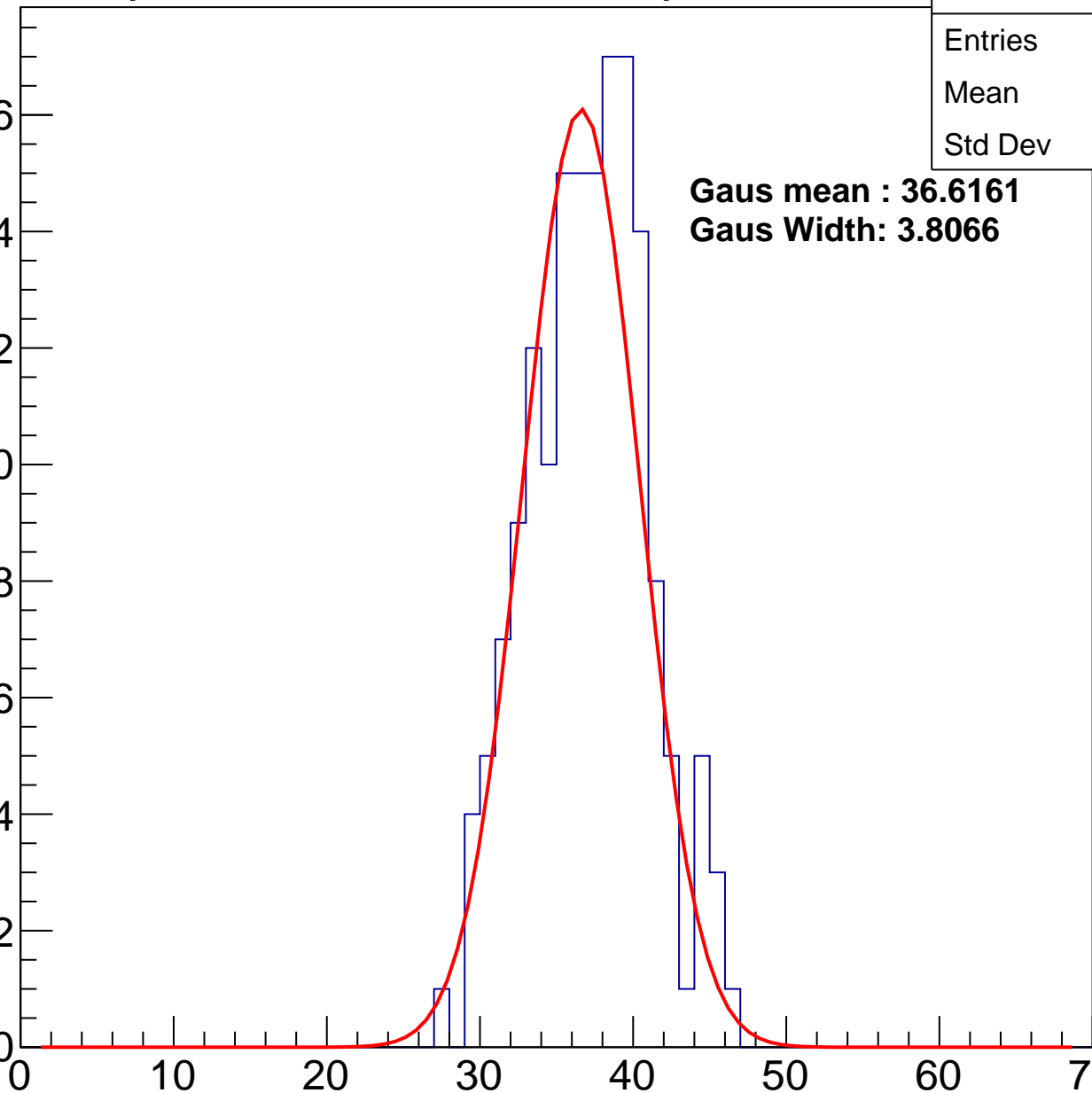
**Gaus mean : 36.6161**

**Gaus Width: 3.8066**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch37, adc2

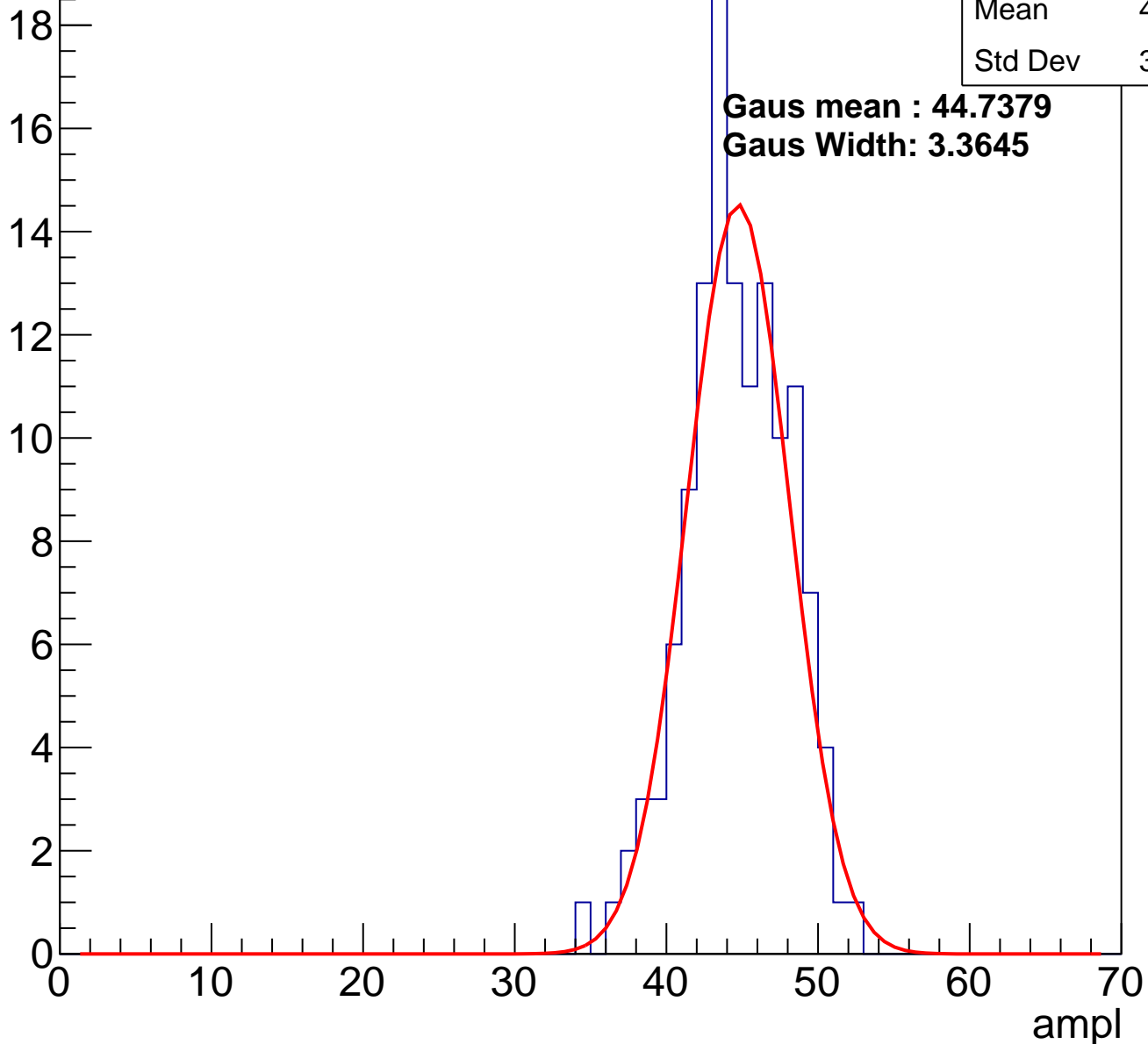
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 44.19 |
| Std Dev | 3.365 |

**Gaus mean : 44.7379**

**Gaus Width: 3.3645**

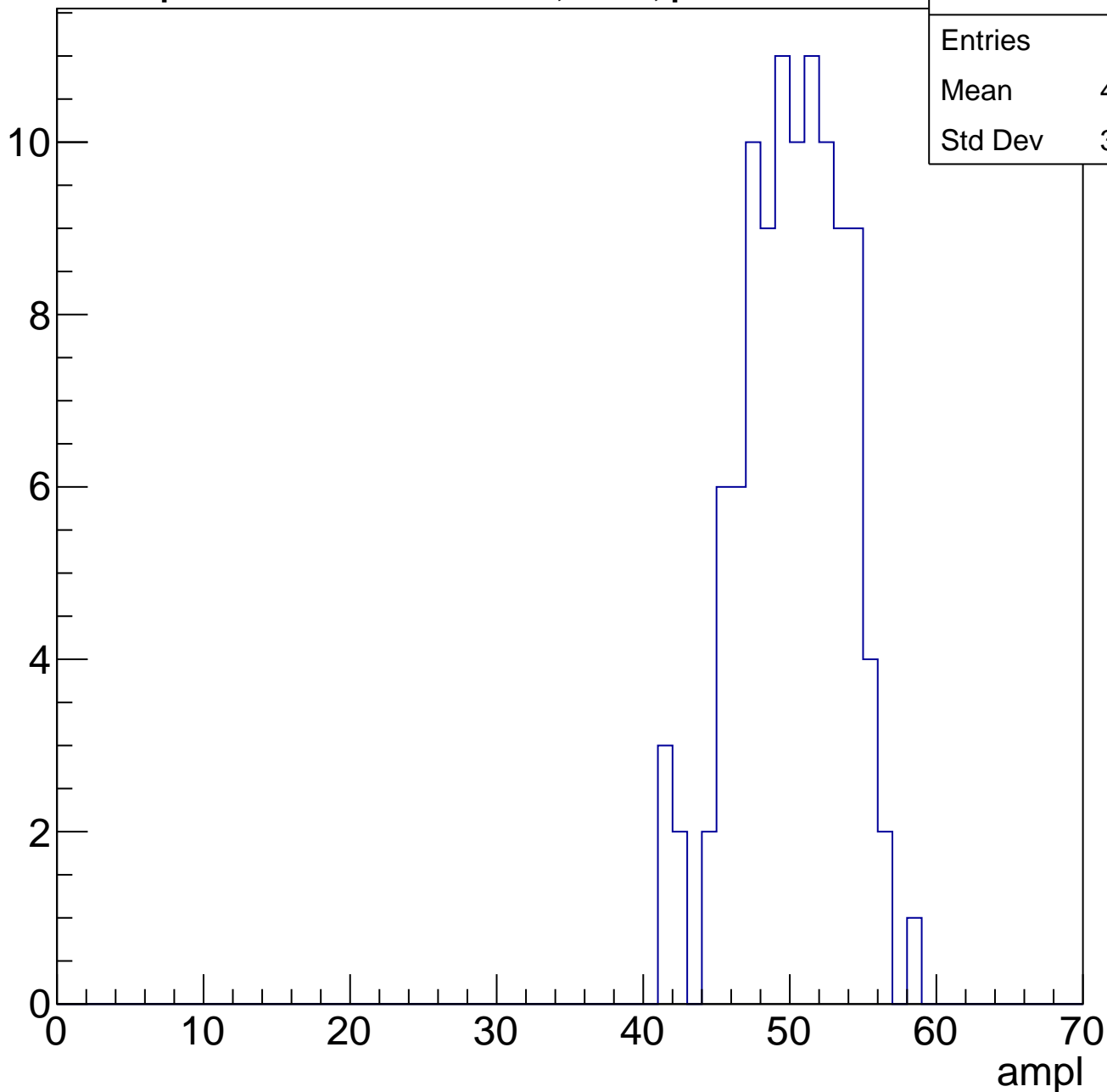
Entry



# B0L101S, U16-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

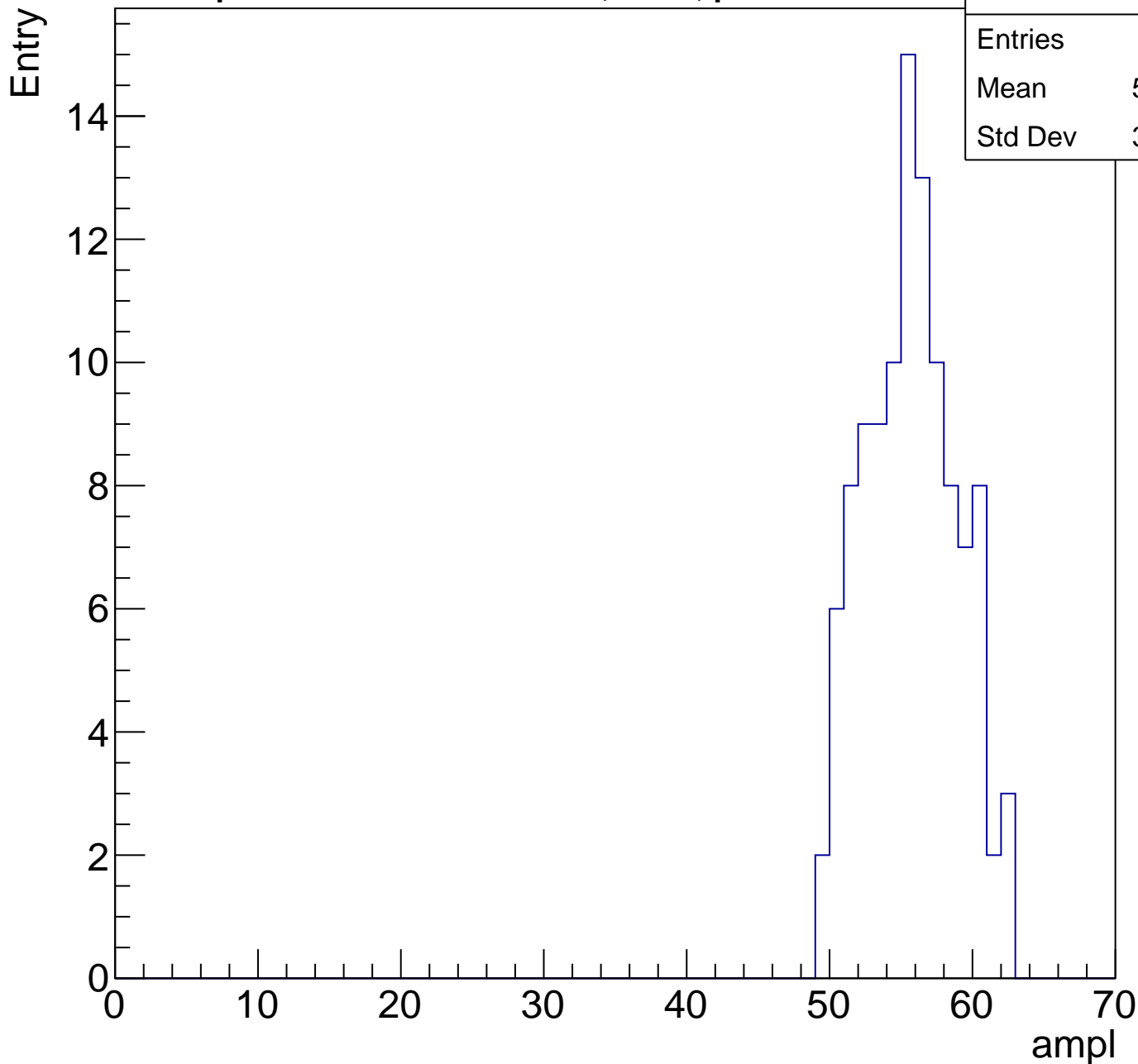
Entry



# B0L101S, U16-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 55.26 |
| Std Dev | 3.199 |



# B0L101S, U16-ch37, adc5

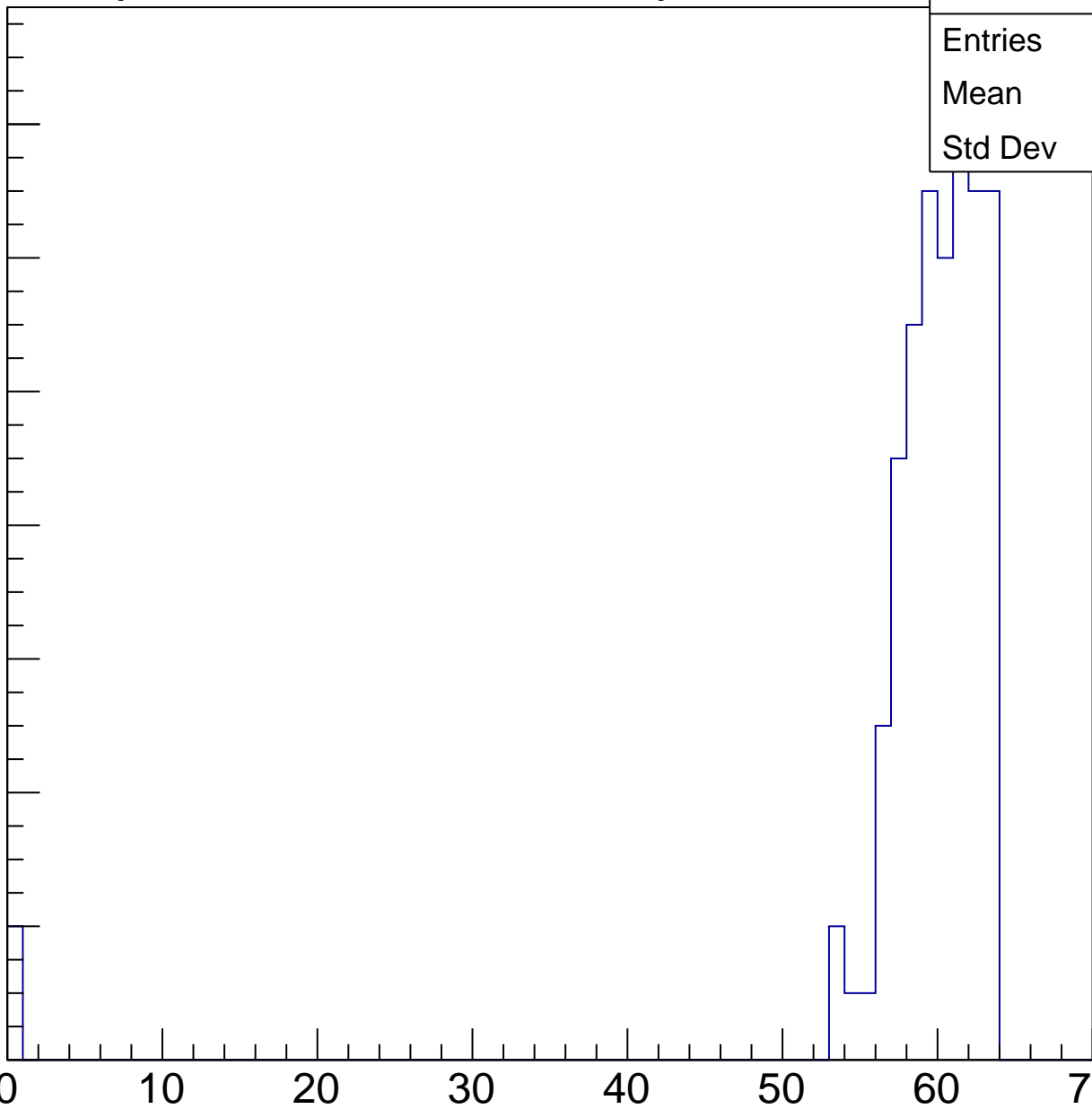
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 97    |
| Mean    | 58.48 |
| Std Dev | 8.816 |

ampl

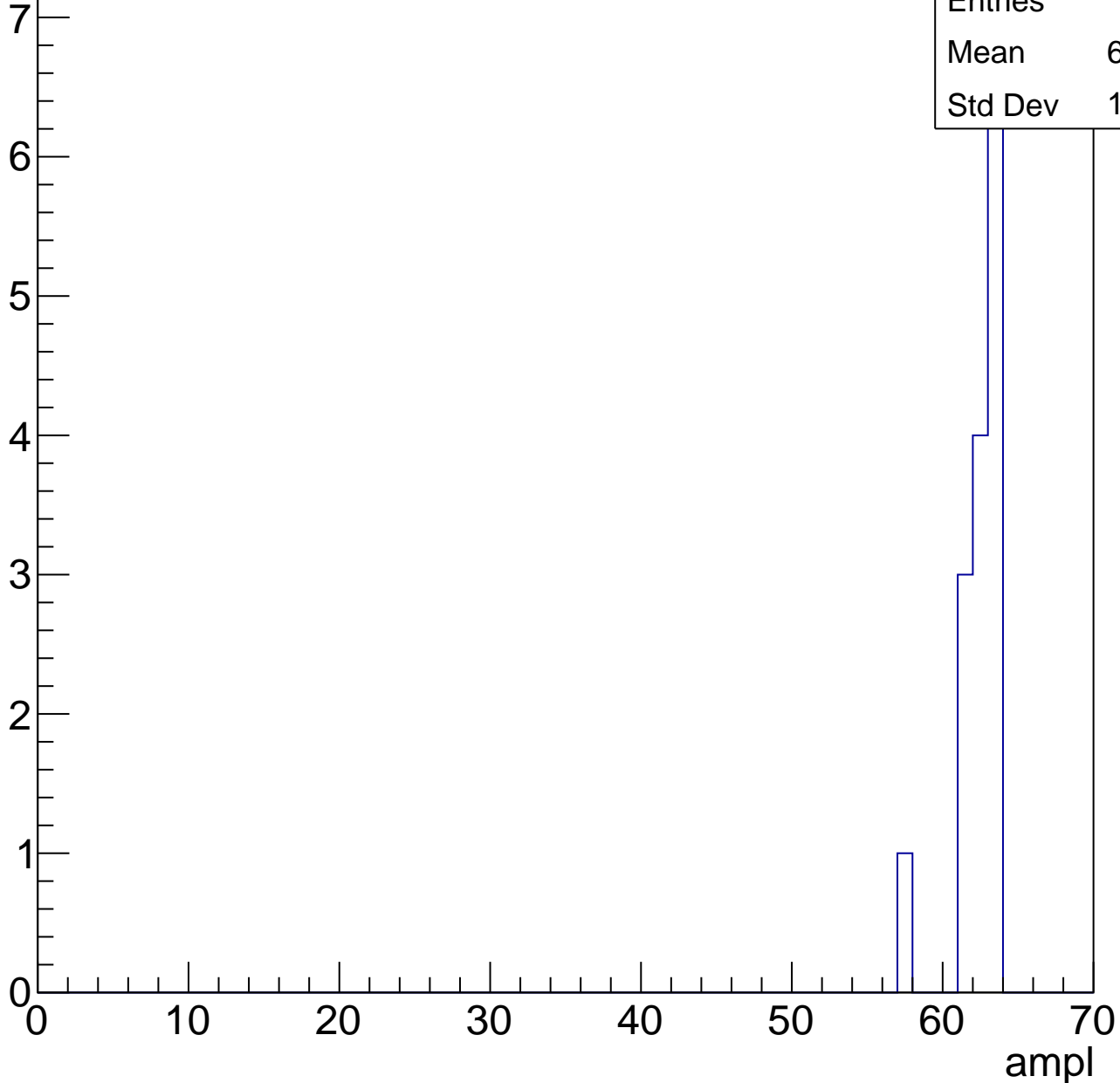


# B0L101S, U16-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 15    |
| Mean    | 61.93 |
| Std Dev | 1.526 |





# B0L101S, U16-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch38, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

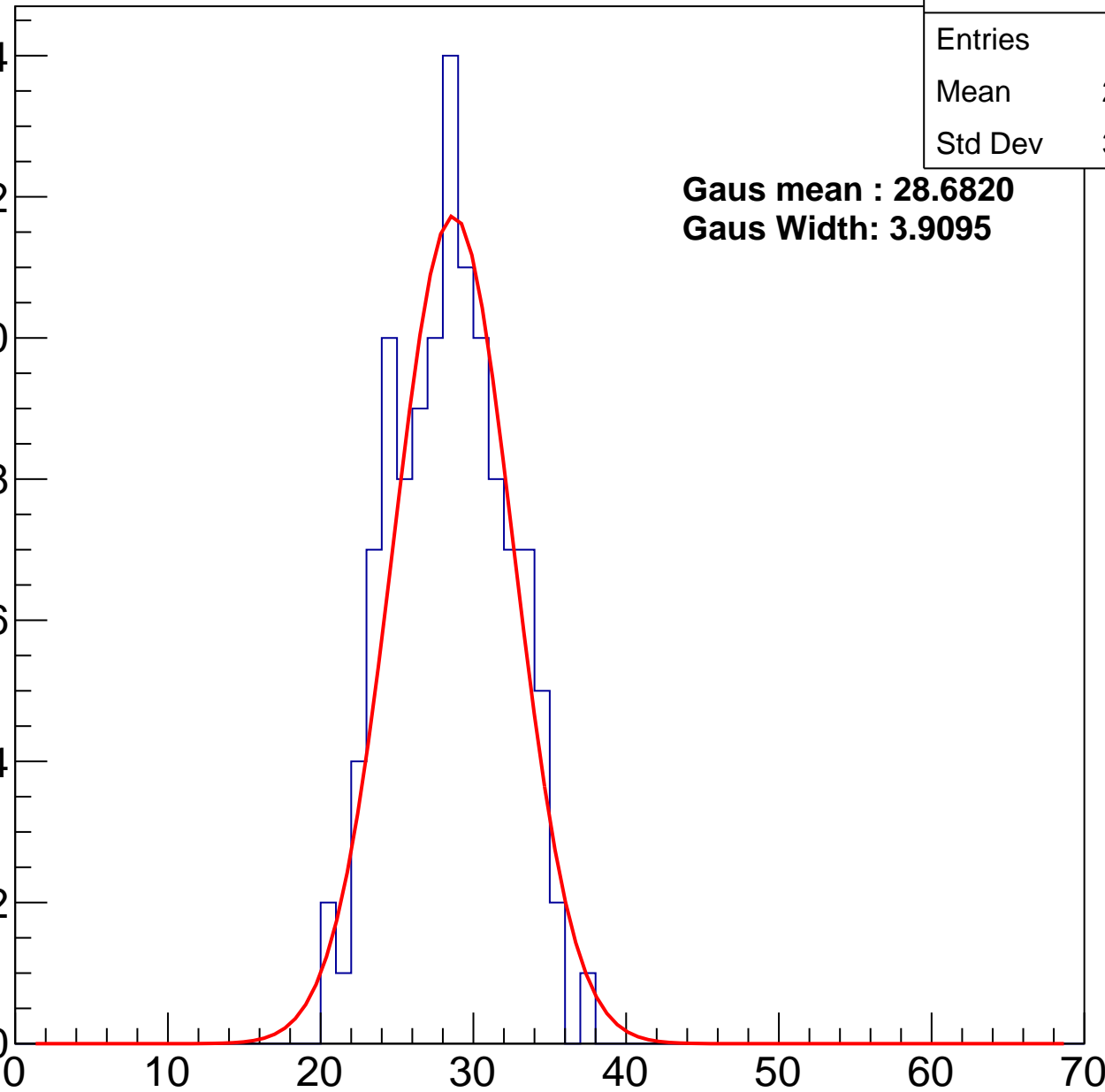
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 27.97 |
| Std Dev | 3.647 |

**Gaus mean : 28.6820**

**Gaus Width: 3.9095**

ampl

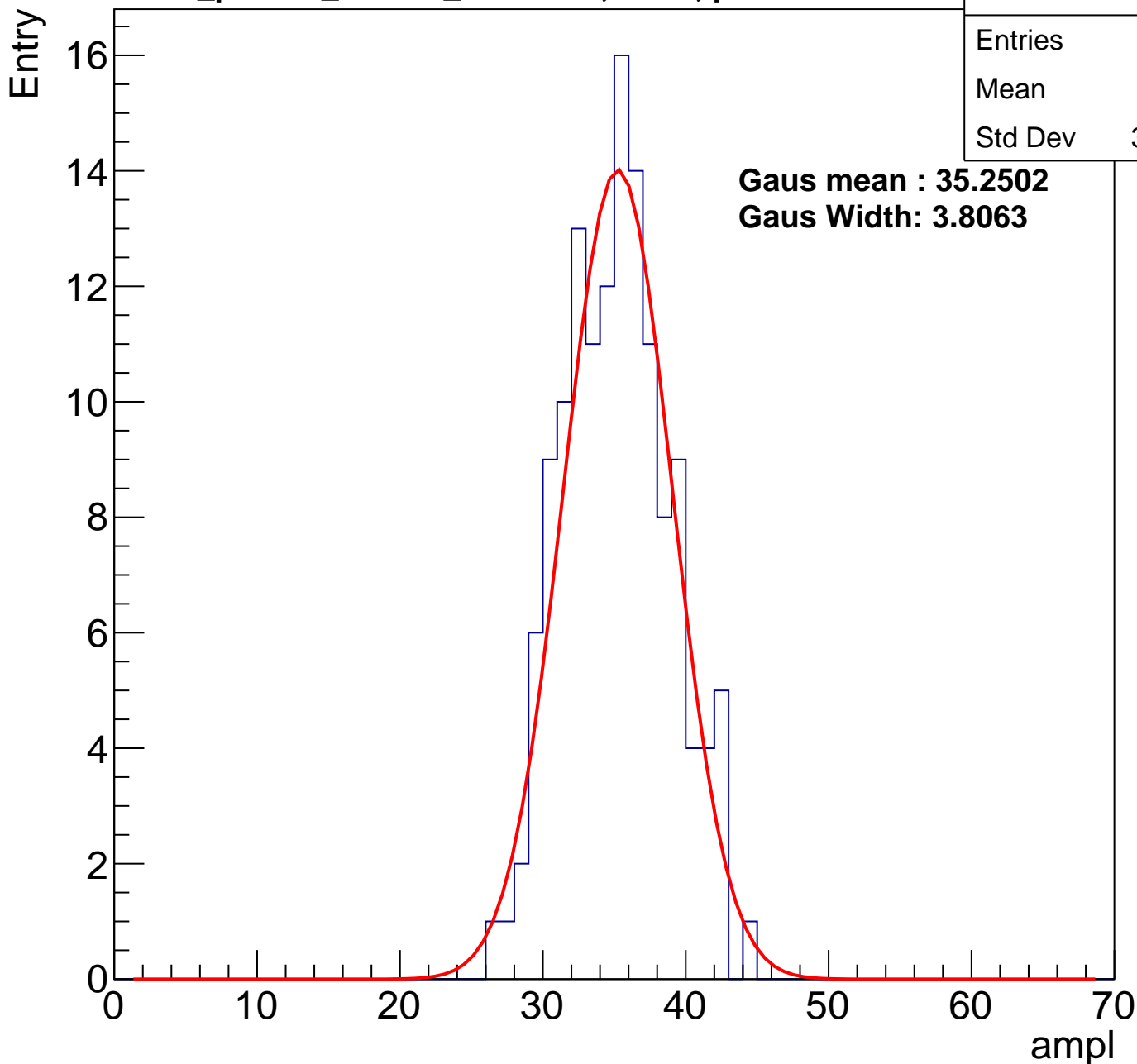


# B0L101S, U16-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 34.7  |
| Std Dev | 3.673 |

**Gaus mean : 35.2502**  
**Gaus Width: 3.8063**



# B0L101S, U16-ch38, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 41.64 |
| Std Dev | 3.909 |

**Gaus mean : 42.2195**

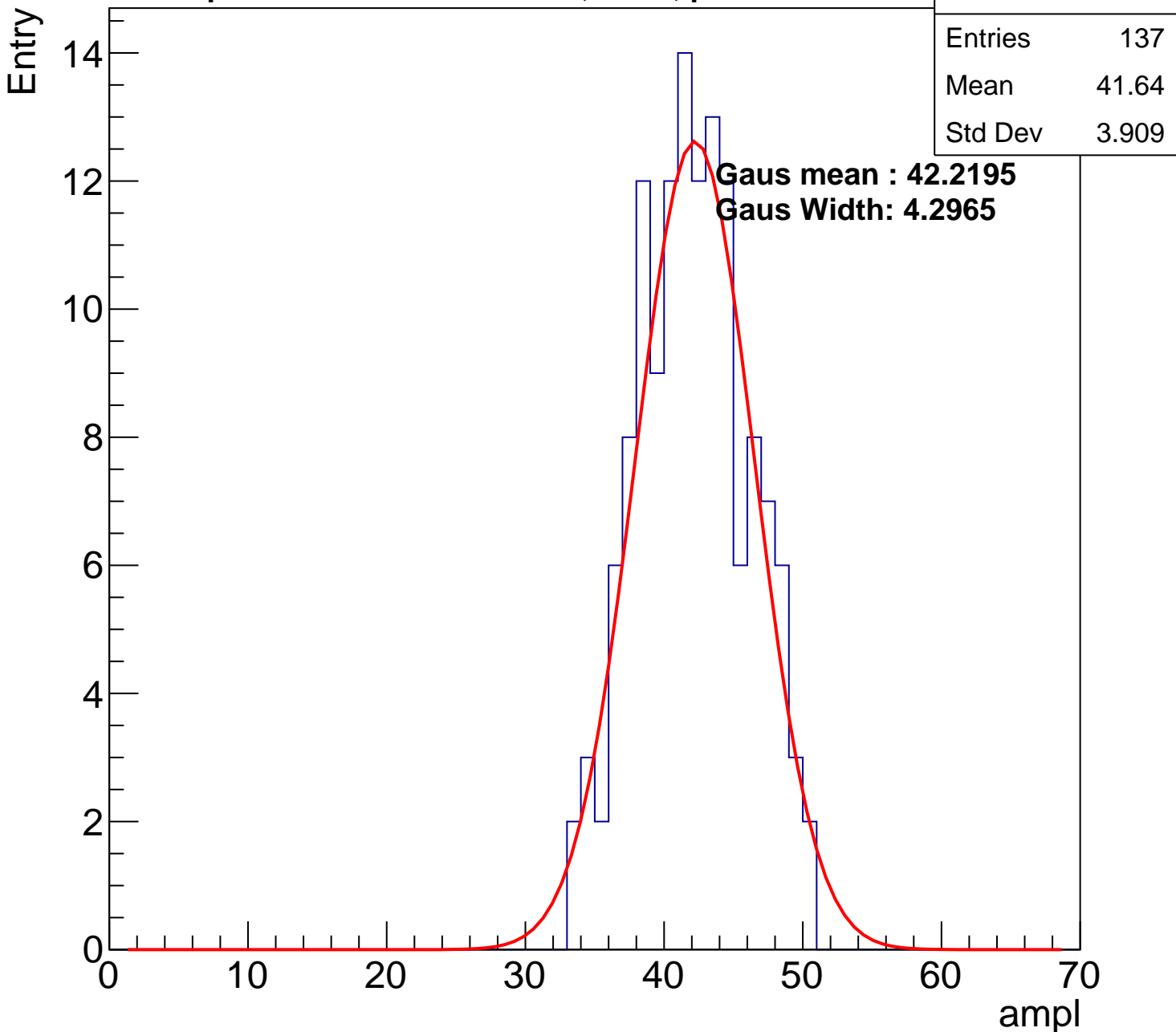
**Gaus Width: 4.2965**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

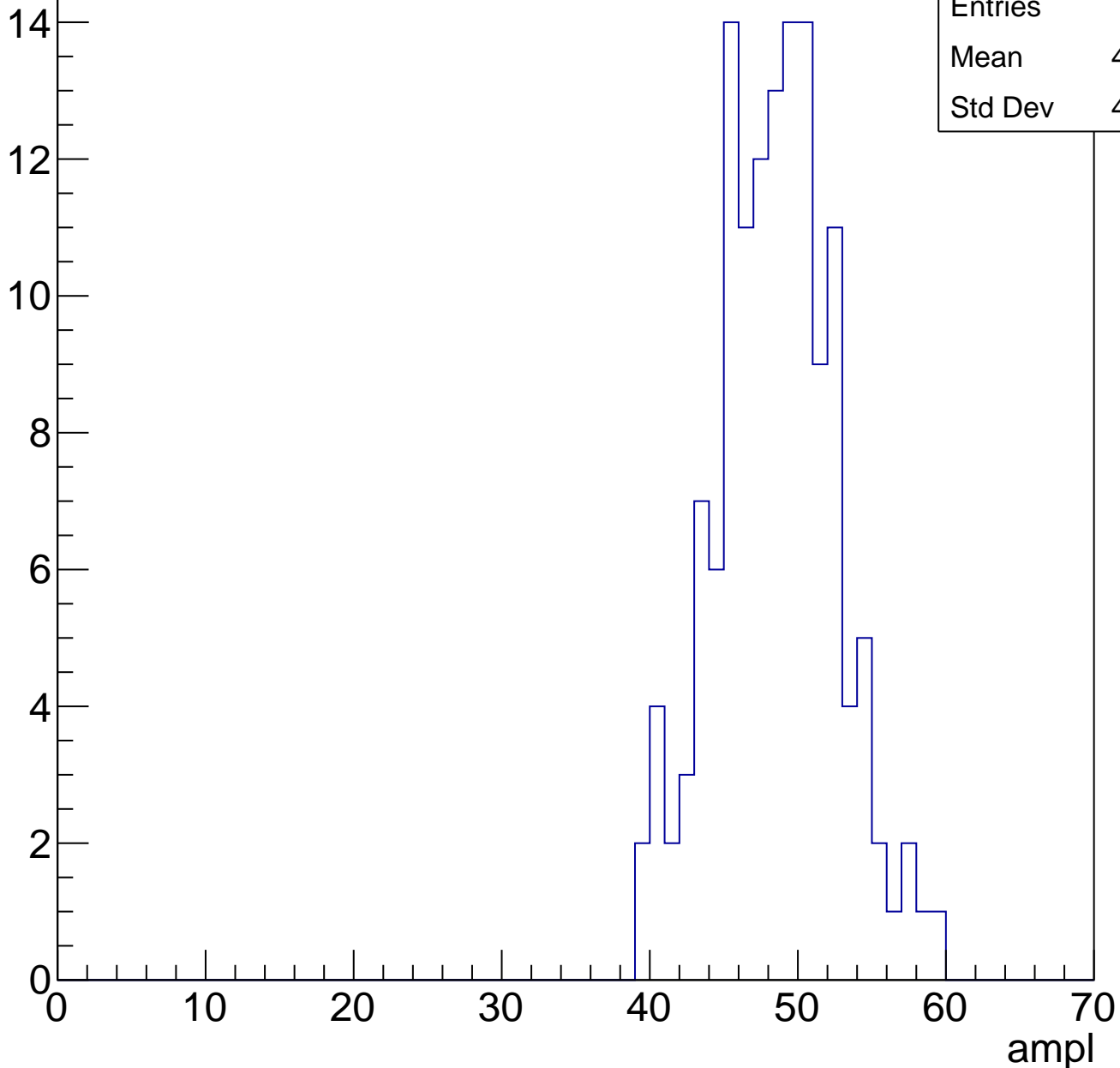


# B0L101S, U16-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 48.05 |
| Std Dev | 4.019 |

Entry



# B0L101S, U16-ch38, adc4

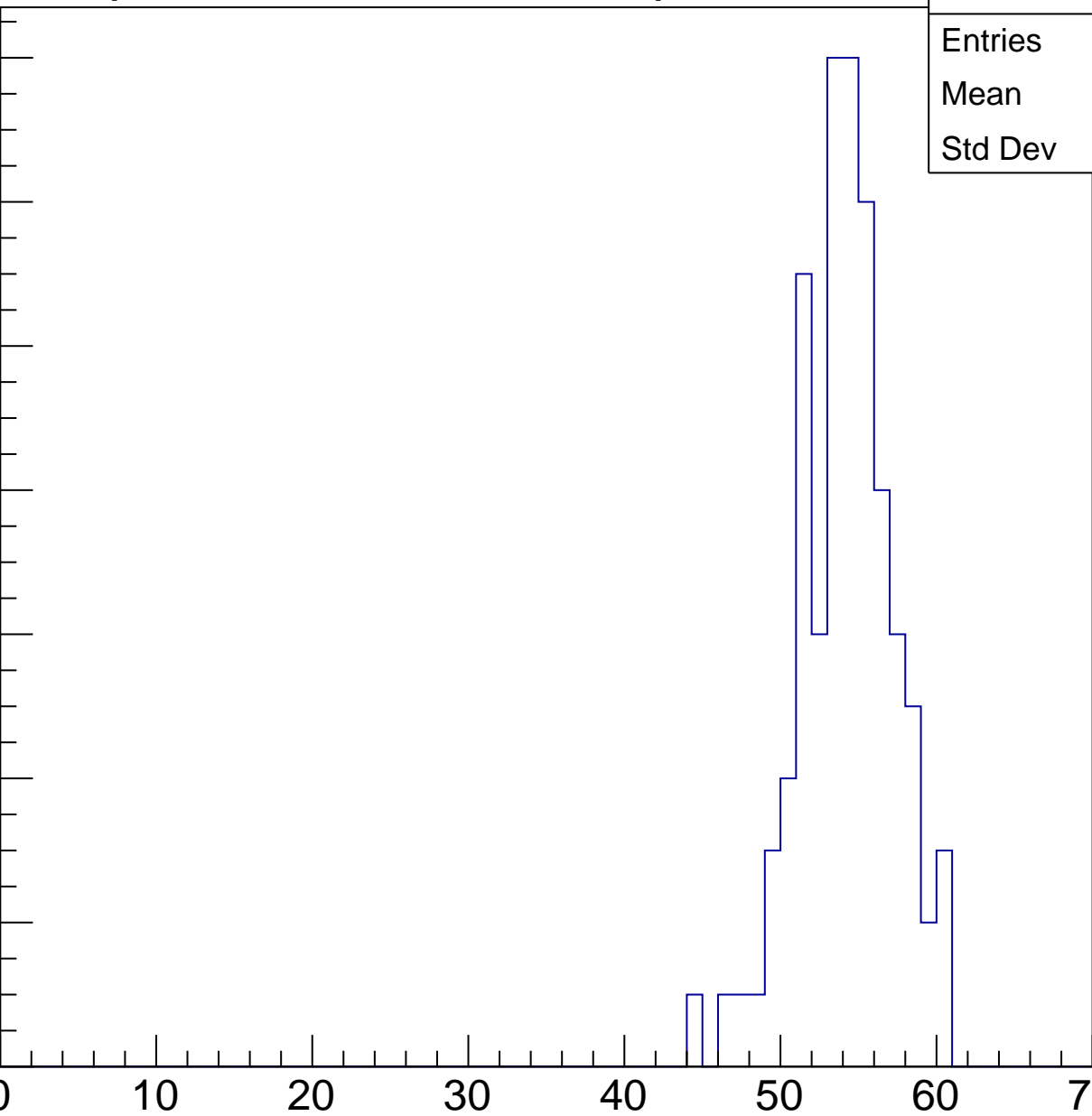
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 53.71 |
| Std Dev | 3.045 |

ampl



# B0L101S, U16-ch38, adc5

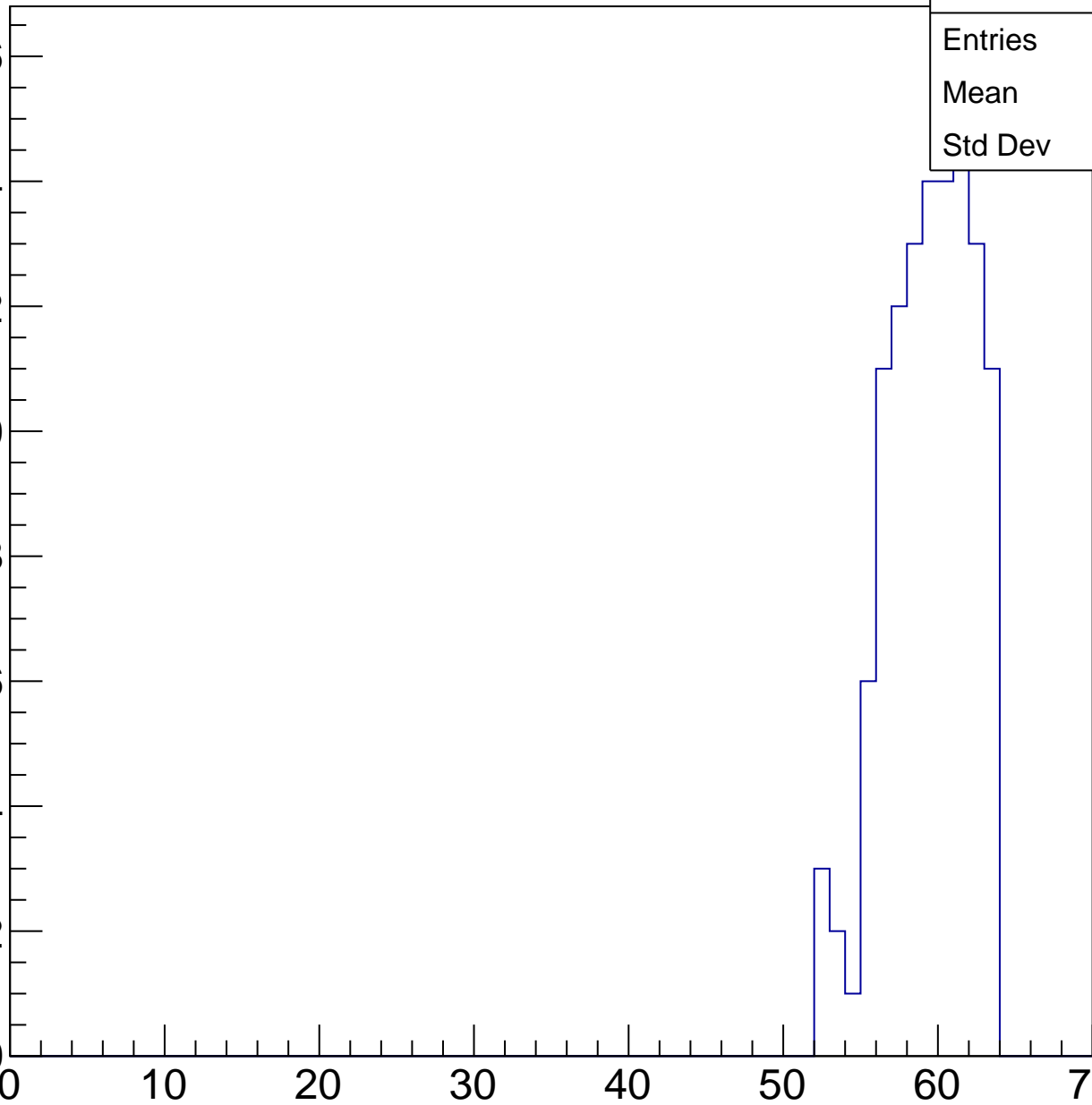
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 58.97 |
| Std Dev | 2.74  |

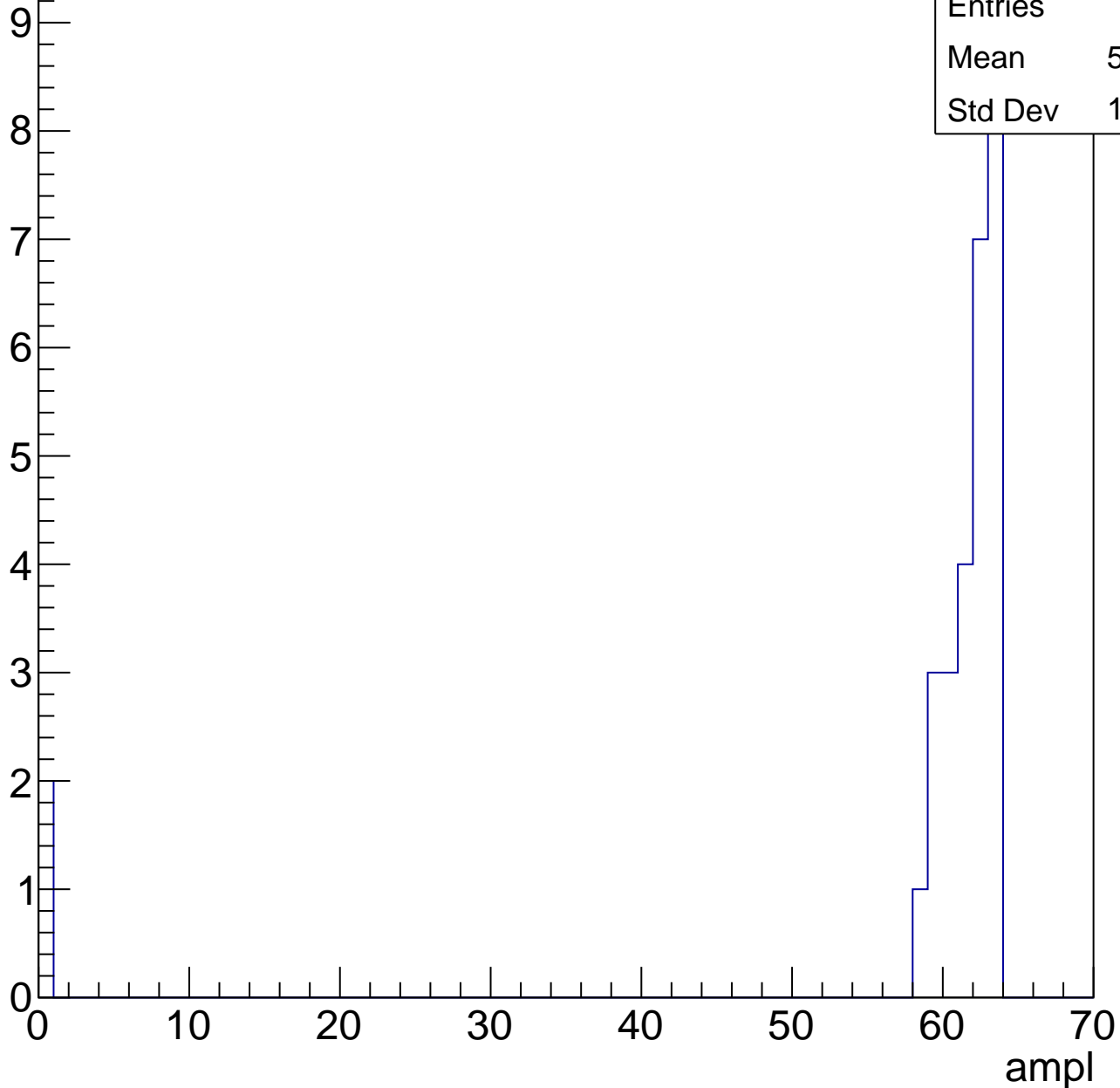
ampl



# B0L101S, U16-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 29.45 |
| Std Dev | 4.877 |

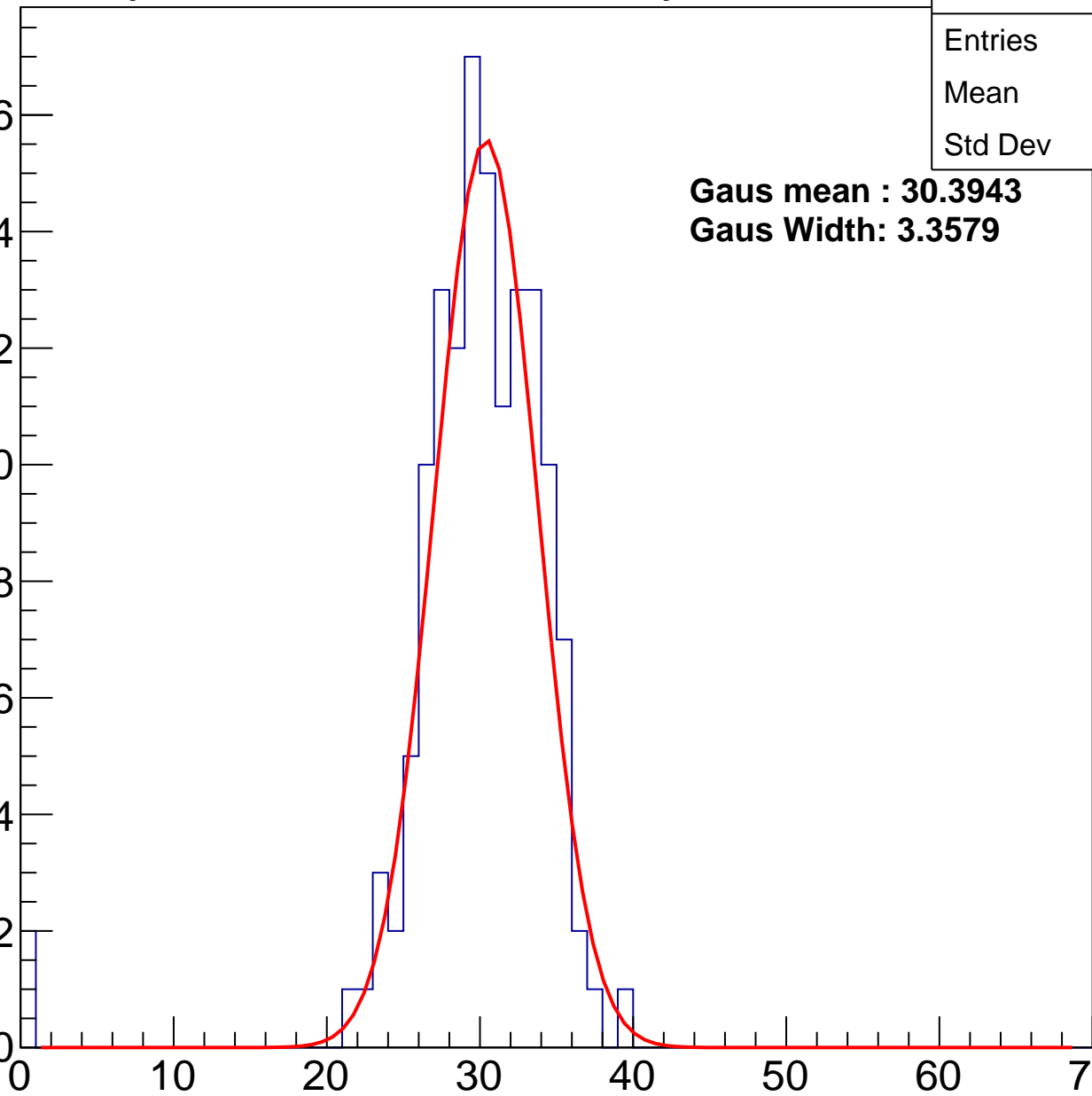
**Gaus mean : 30.3943**

**Gaus Width: 3.3579**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



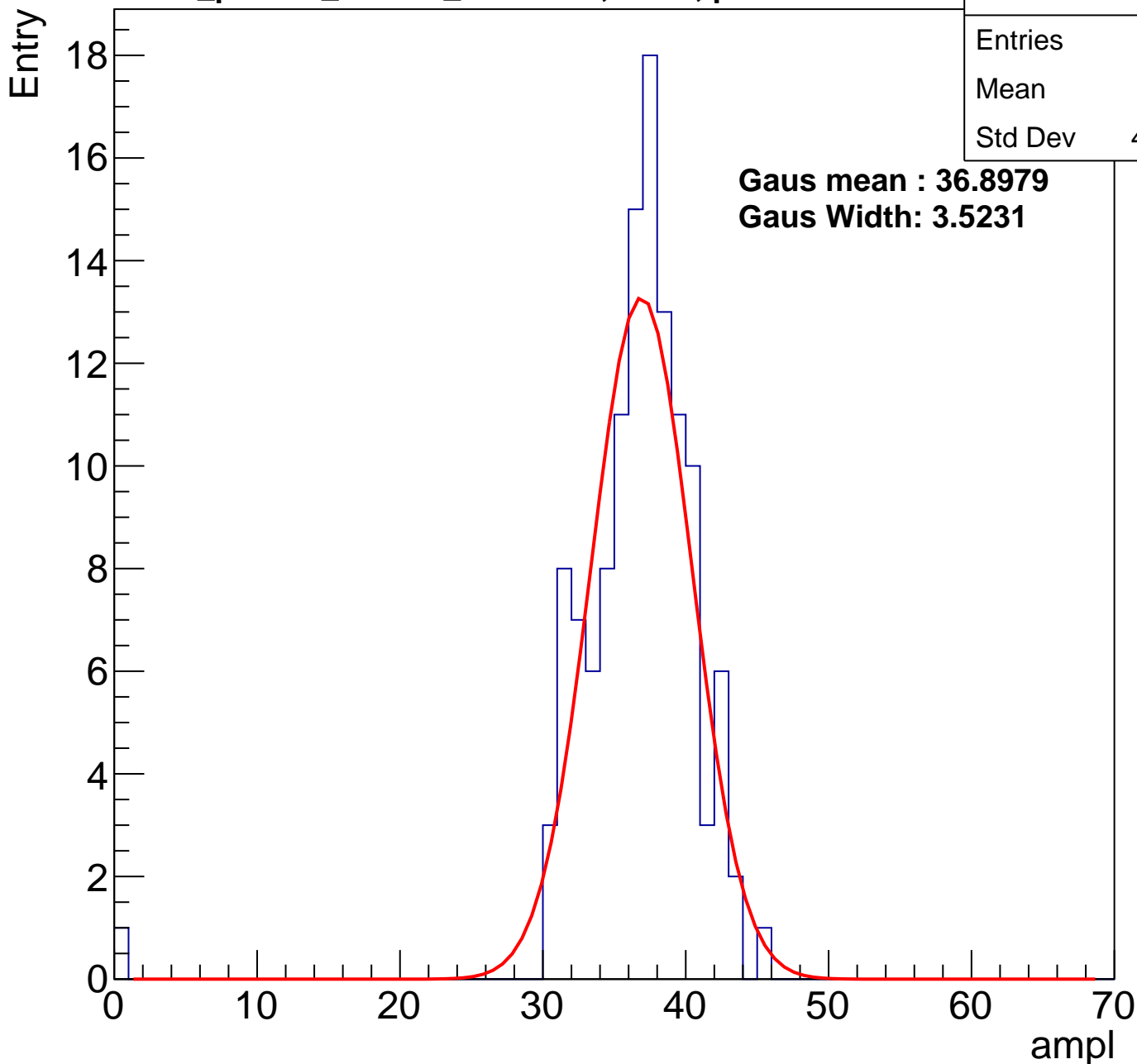
# B0L101S, U16-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 36.2  |
| Std Dev | 4.604 |

**Gaus mean : 36.8979**

**Gaus Width: 3.5231**



# B0L101S, U16-ch39, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

42.94

Std Dev

3.532

**Gaus mean : 43.7692**

**Gaus Width: 3.3665**

0

10

20

30

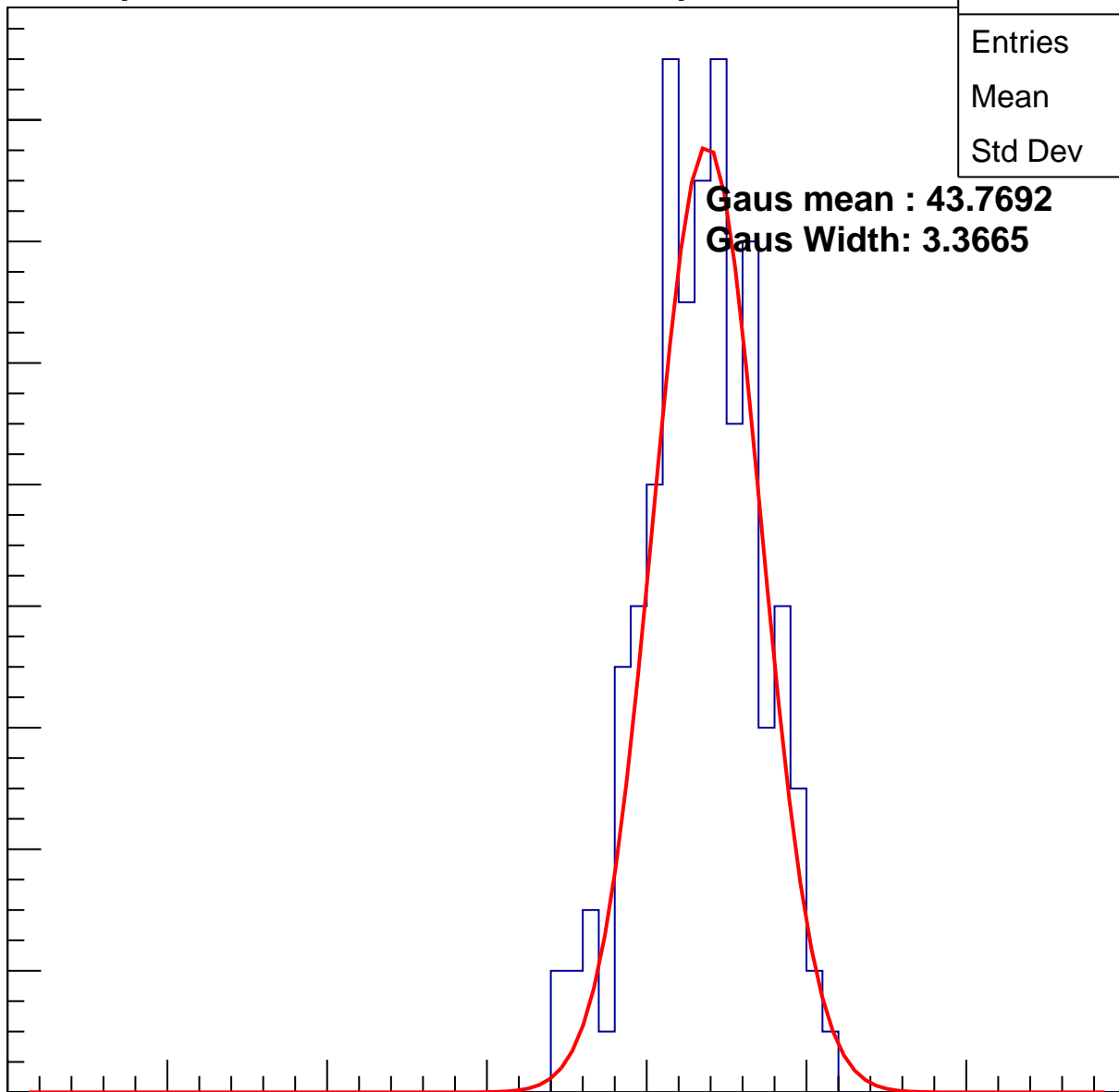
40

50

60

70

ampl



# B0L101S, U16-ch39, adc3

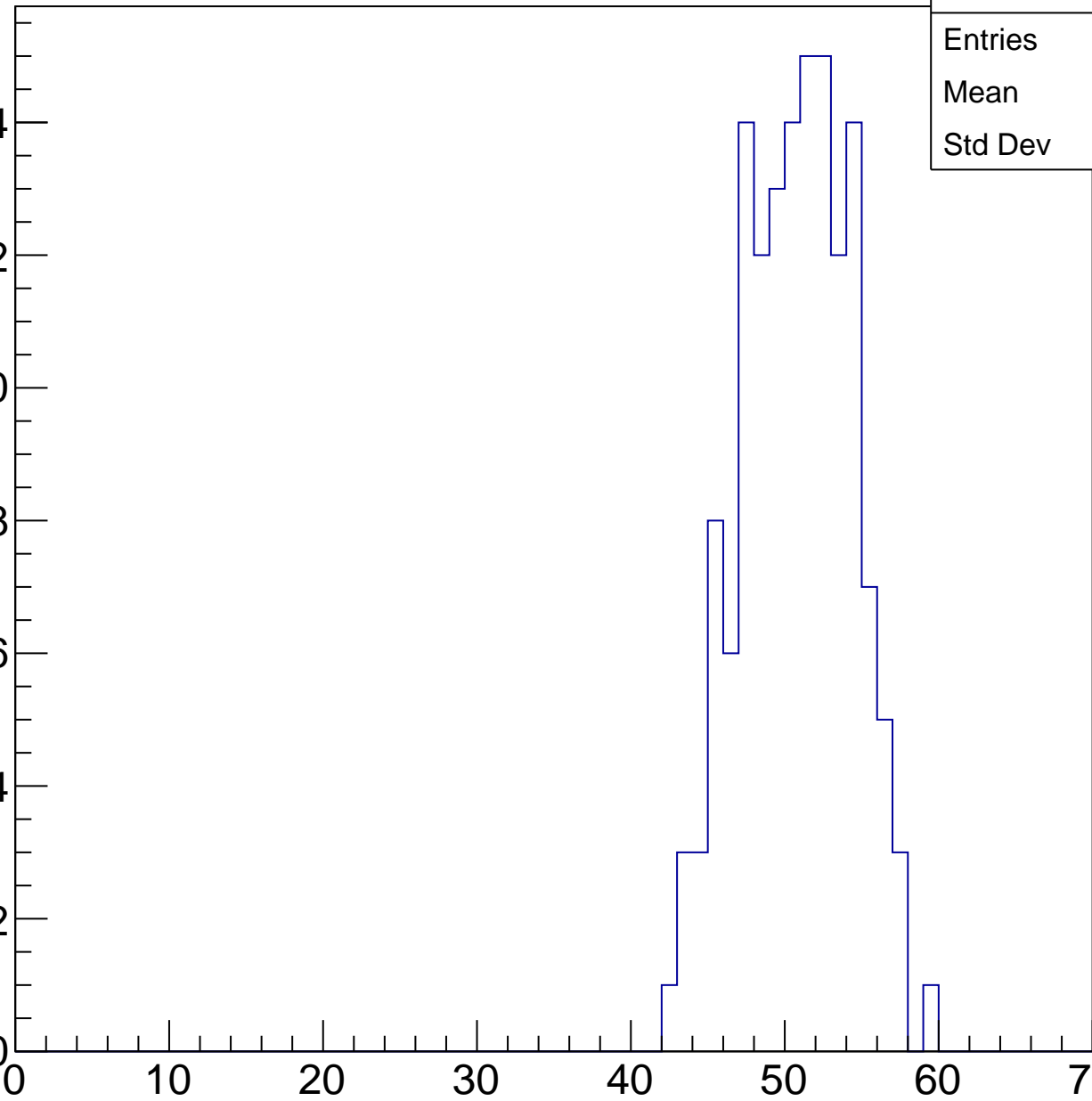
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 50.29 |
| Std Dev | 3.492 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

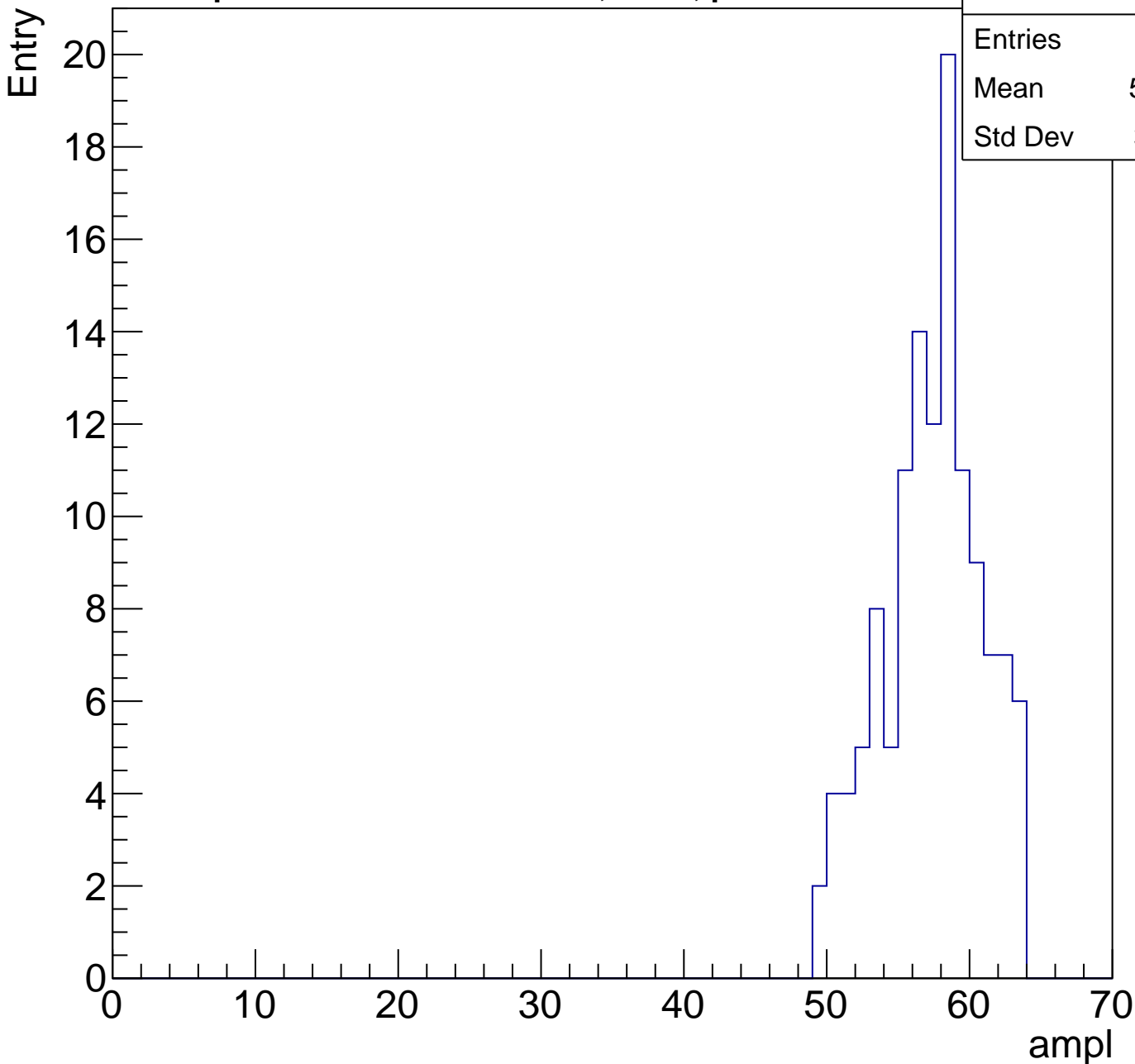
ampl



# B0L101S, U16-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 56.94 |
| Std Dev | 3.431 |



# B0L101S, U16-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 60.35 |
| Std Dev | 2.178 |

10

8

6

4

2

0

12

10

8

6

4

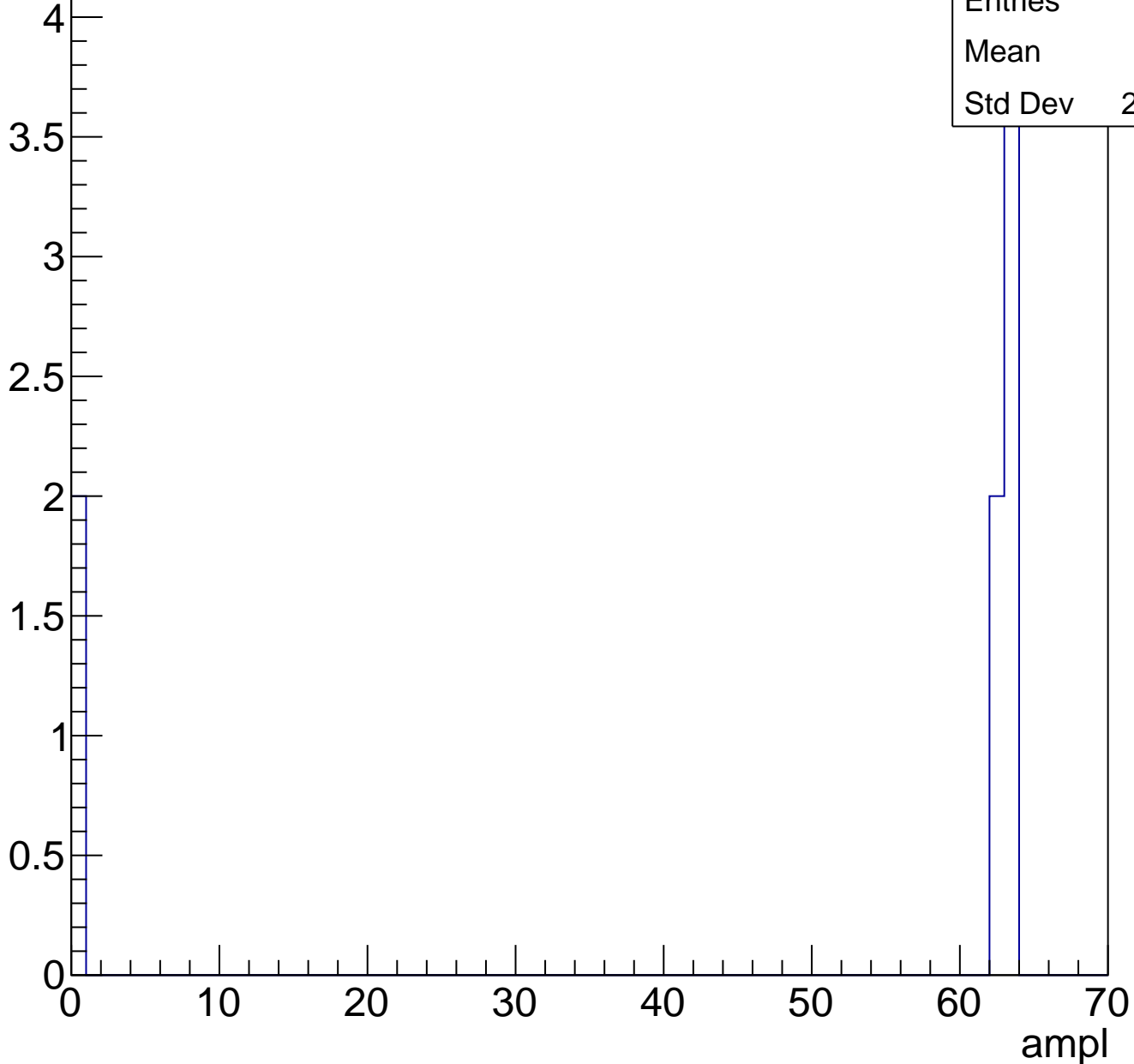
2

0

# B0L101S, U16-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 8     |
| Mean    | 47    |
| Std Dev | 27.14 |



# B0L101S, U16-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch40, adc0

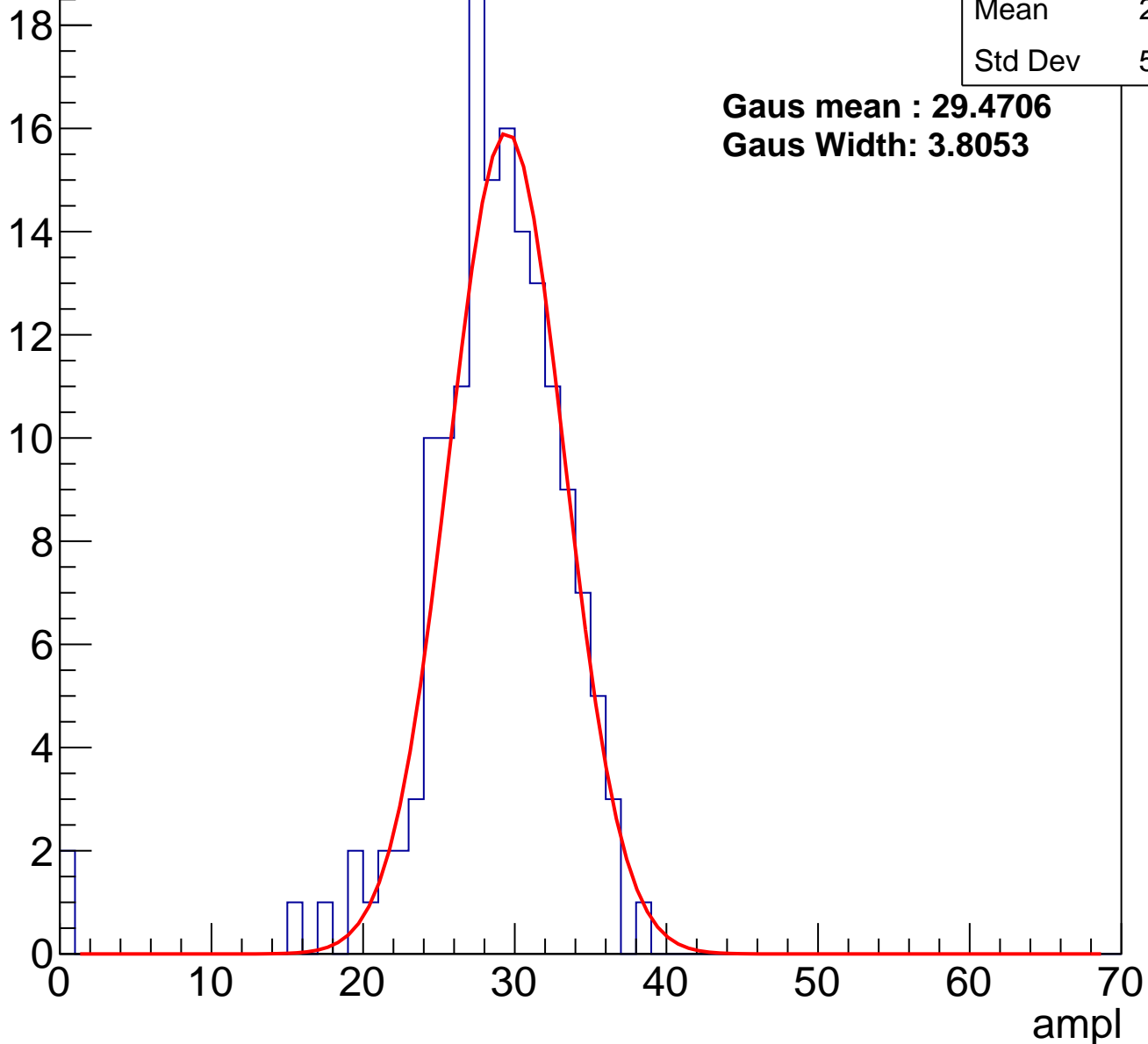
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 28.16 |
| Std Dev | 5.042 |

**Gaus mean : 29.4706**

**Gaus Width: 3.8053**

Entry

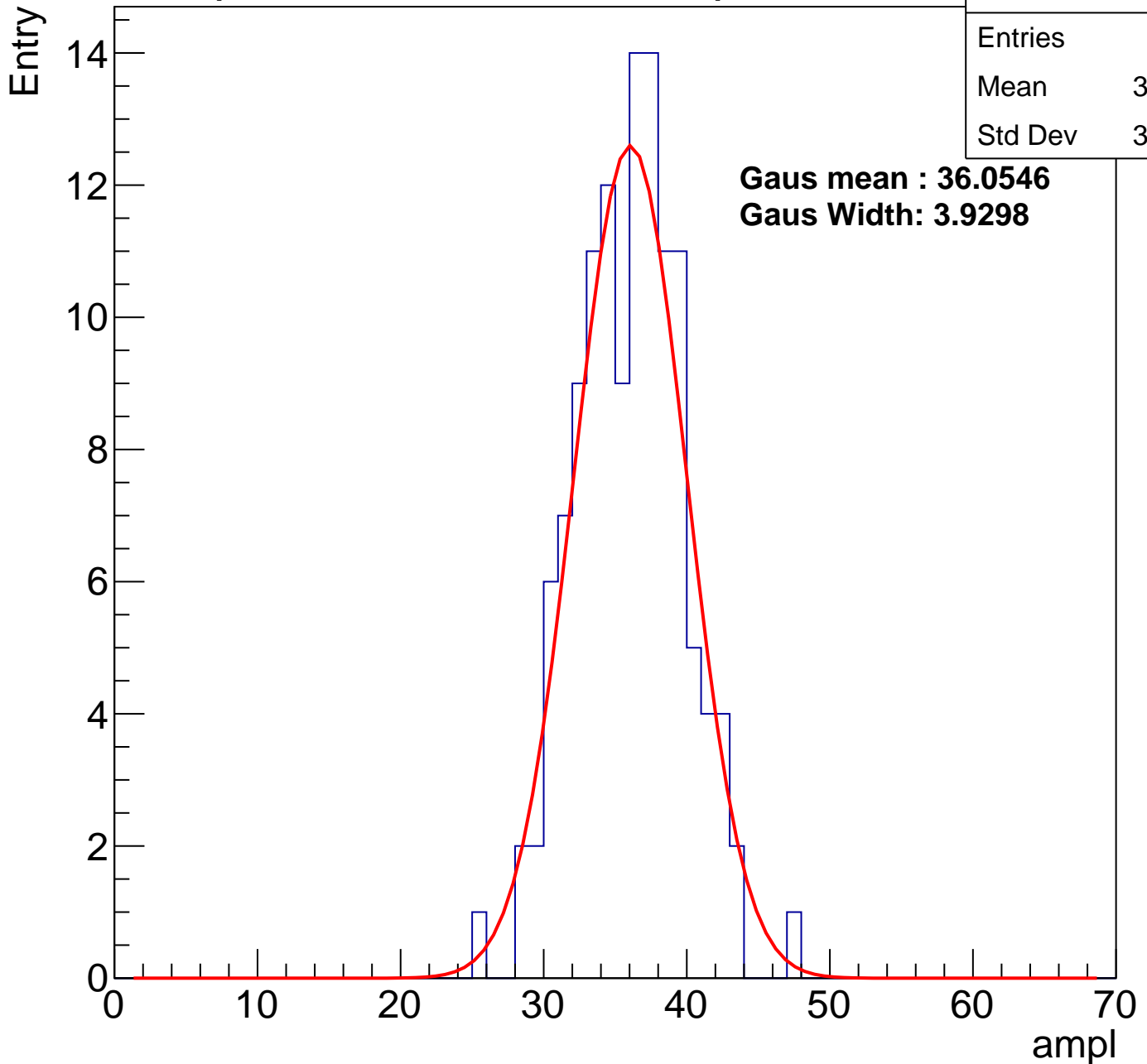


# B0L101S, U16-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 35.55 |
| Std Dev | 3.702 |

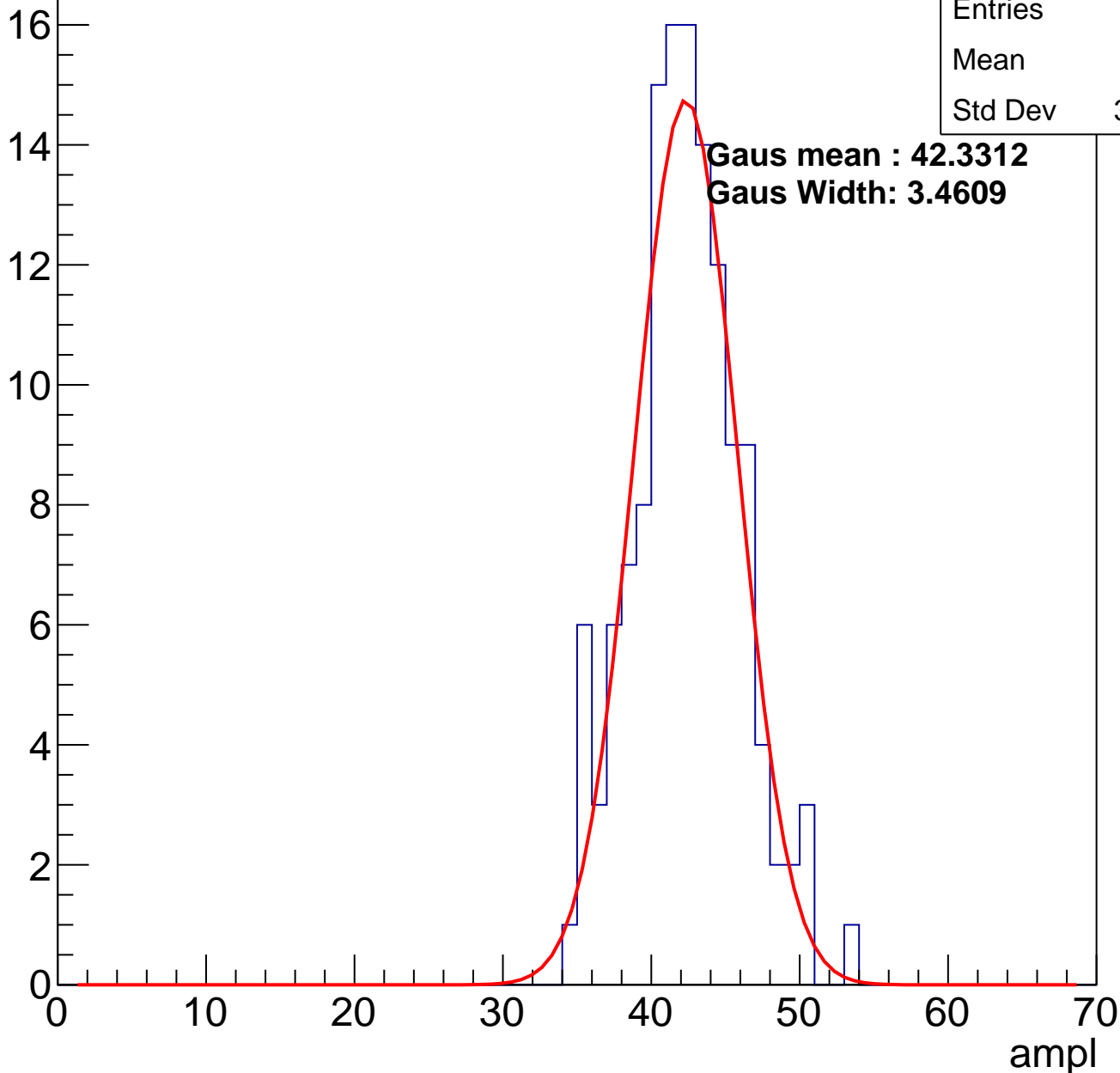
**Gaus mean : 36.0546**  
**Gaus Width: 3.9298**



# B0L101S, U16-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

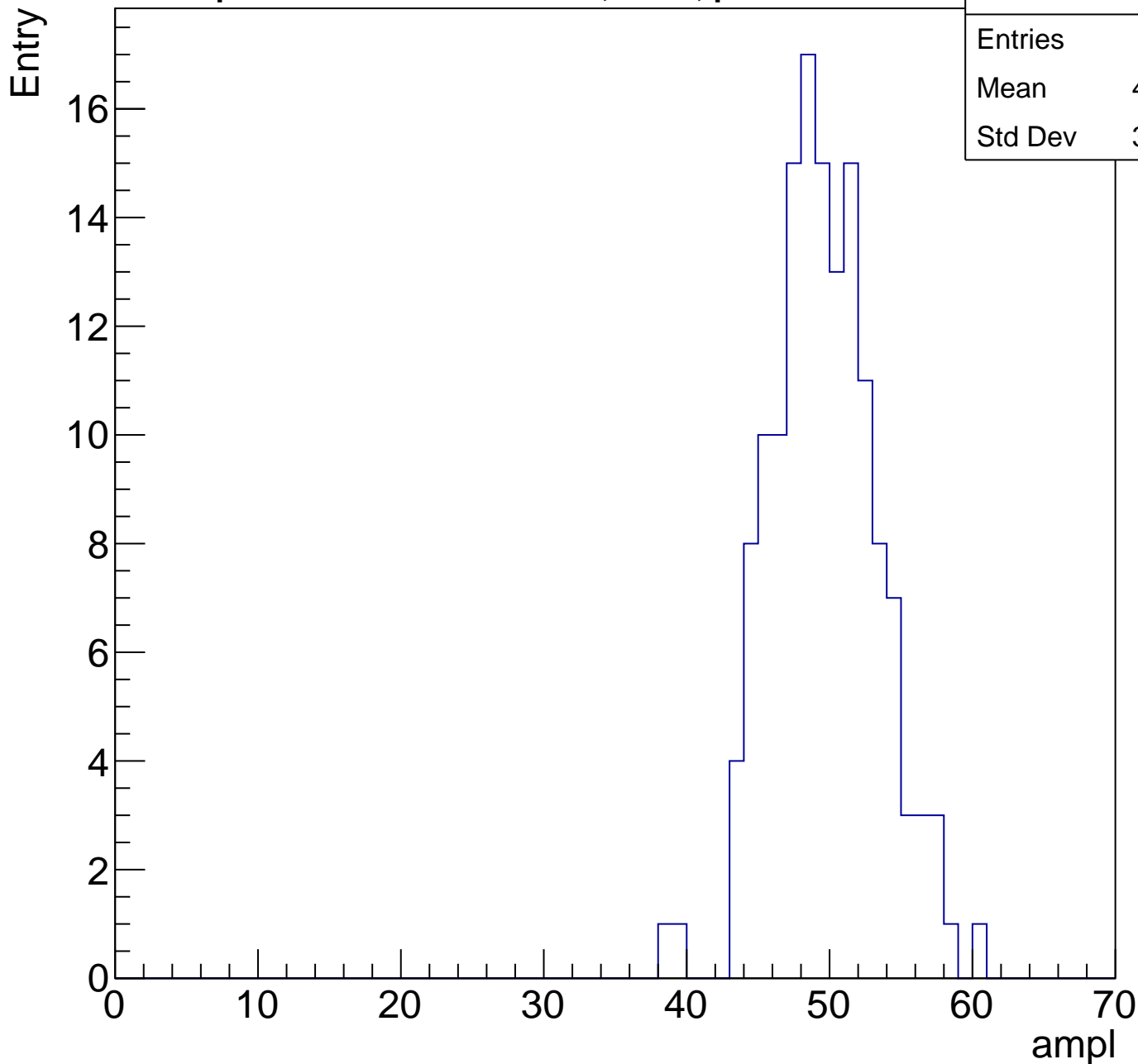
Entry



# B0L101S, U16-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

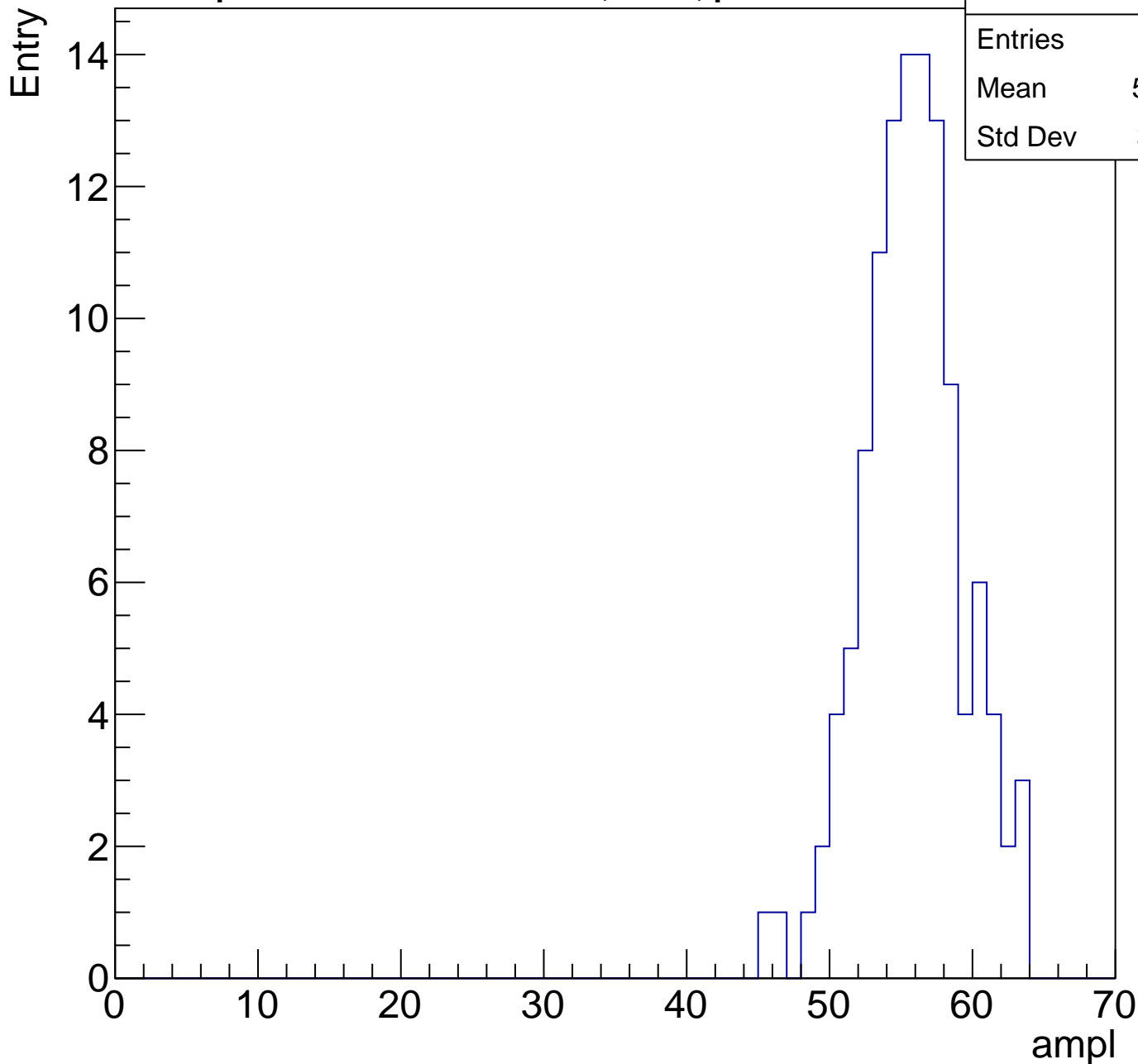
|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 49.16 |
| Std Dev | 3.712 |



# B0L101S, U16-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 55.33 |
| Std Dev | 3.481 |



# B0L101S, U16-ch40, adc5

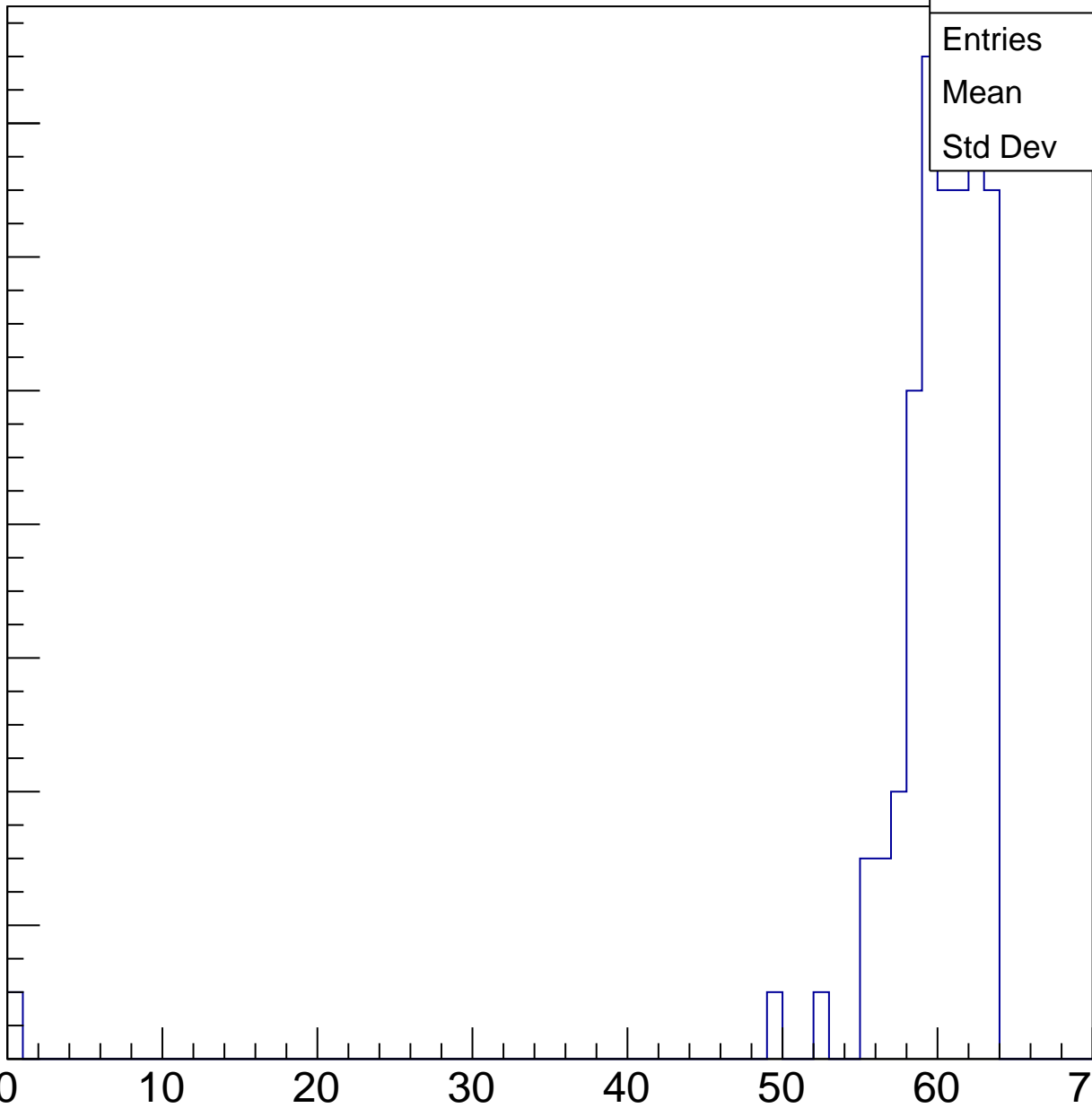
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 59.23 |
| Std Dev | 6.704 |

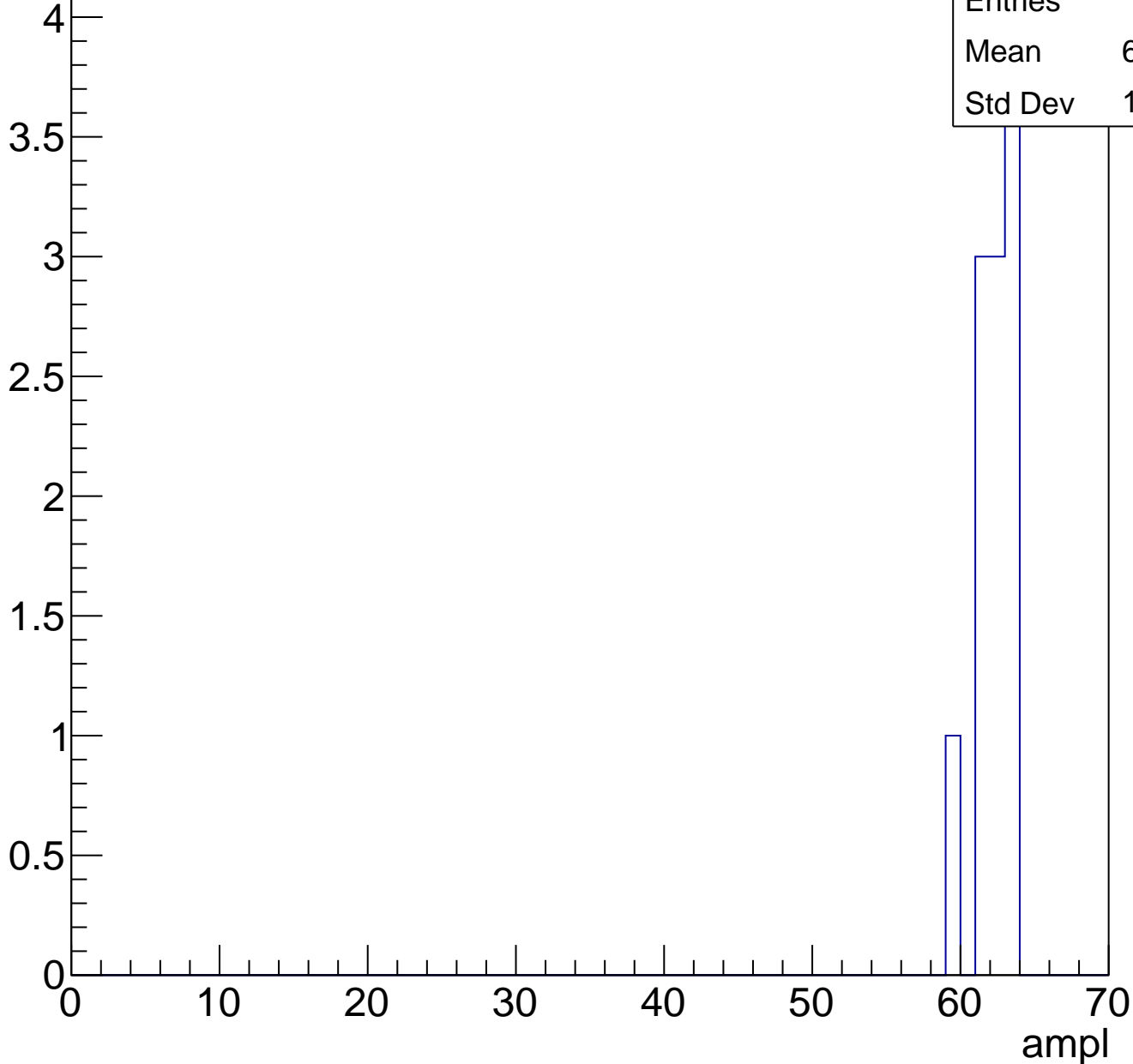
ampl



# B0L101S, U16-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



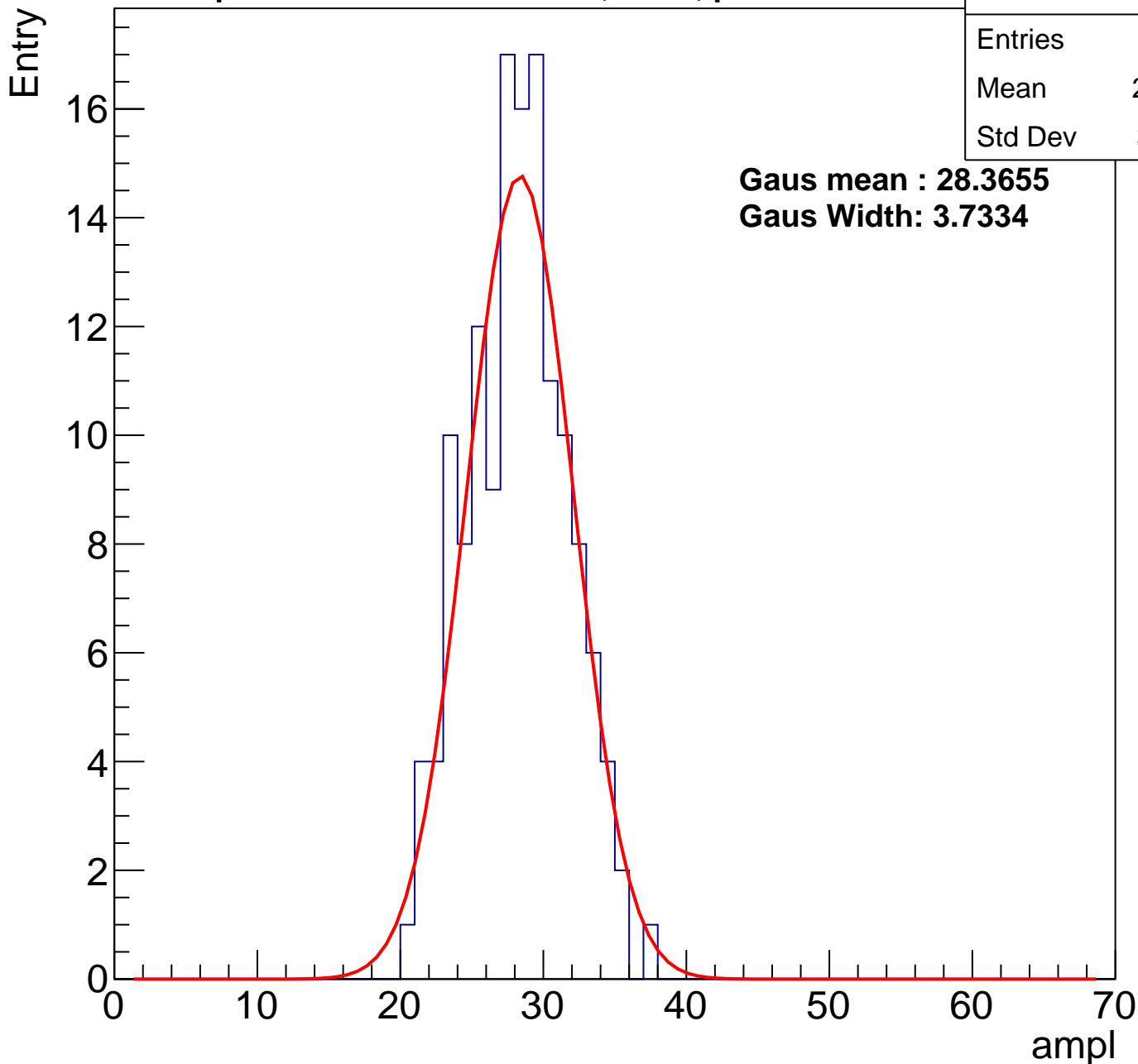
# B0L101S, U16-ch41, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 27.75 |
| Std Dev | 3.471 |

**Gaus mean : 28.3655**

**Gaus Width: 3.7334**



# B0L101S, U16-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 35.3  |
| Std Dev | 3.608 |

**Gaus mean : 35.8959**

**Gaus Width: 3.6914**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

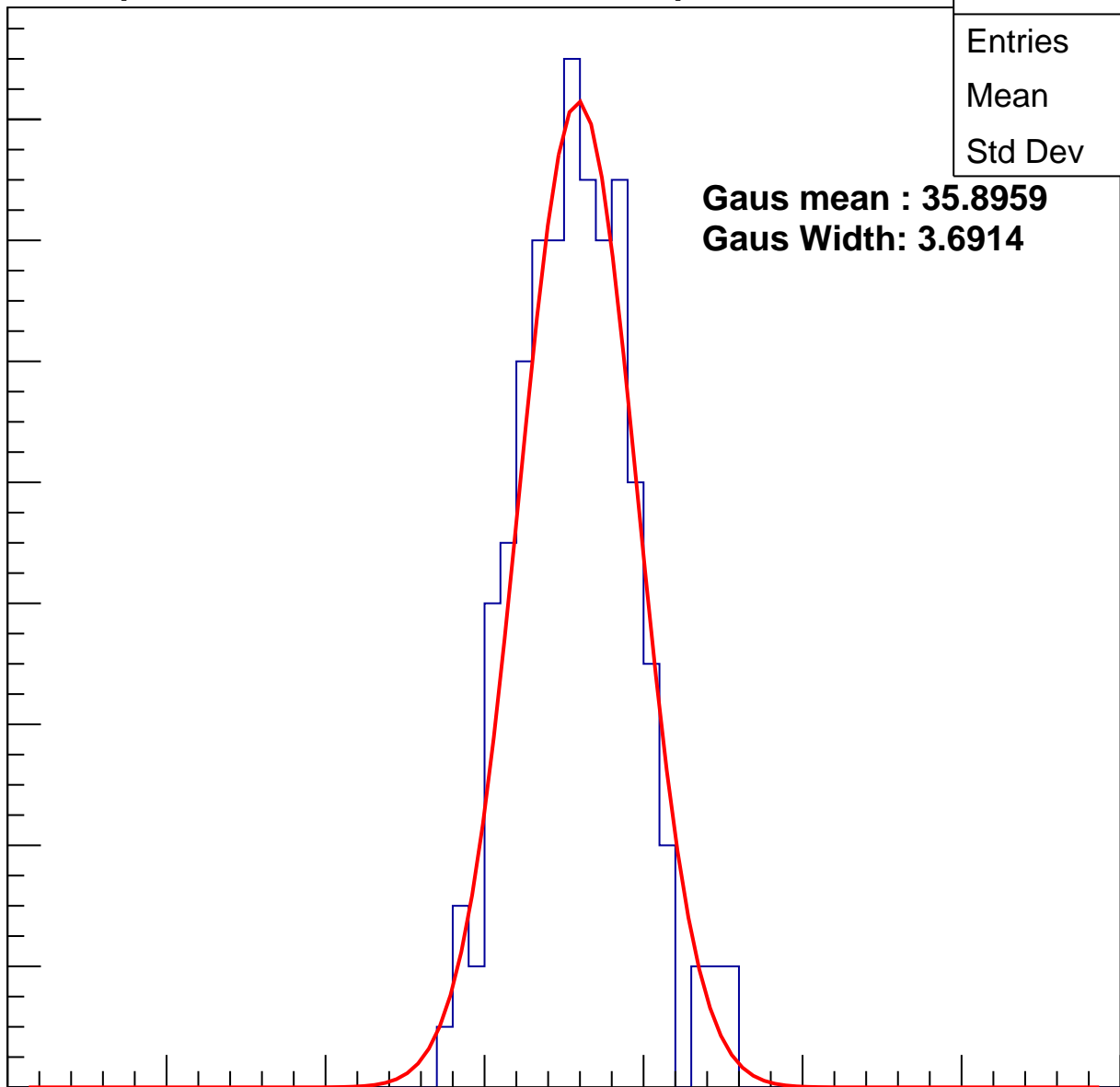
40

50

60

70

ampl



# B0L101S, U16-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

124

Mean

42.02

Std Dev

3.297

**Gaus mean : 42.4128**

**Gaus Width: 3.1510**

0

10

20

30

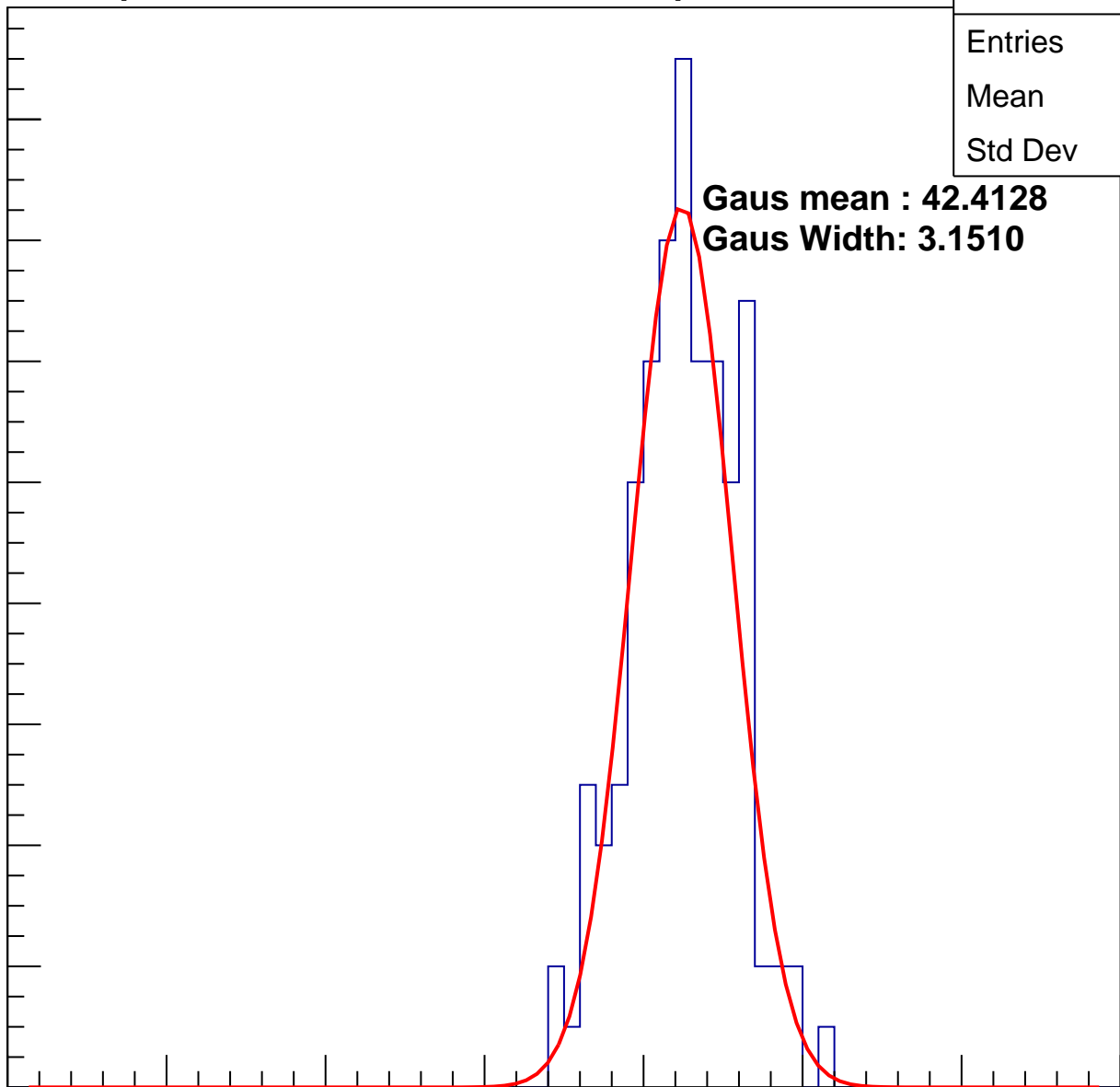
40

50

60

70

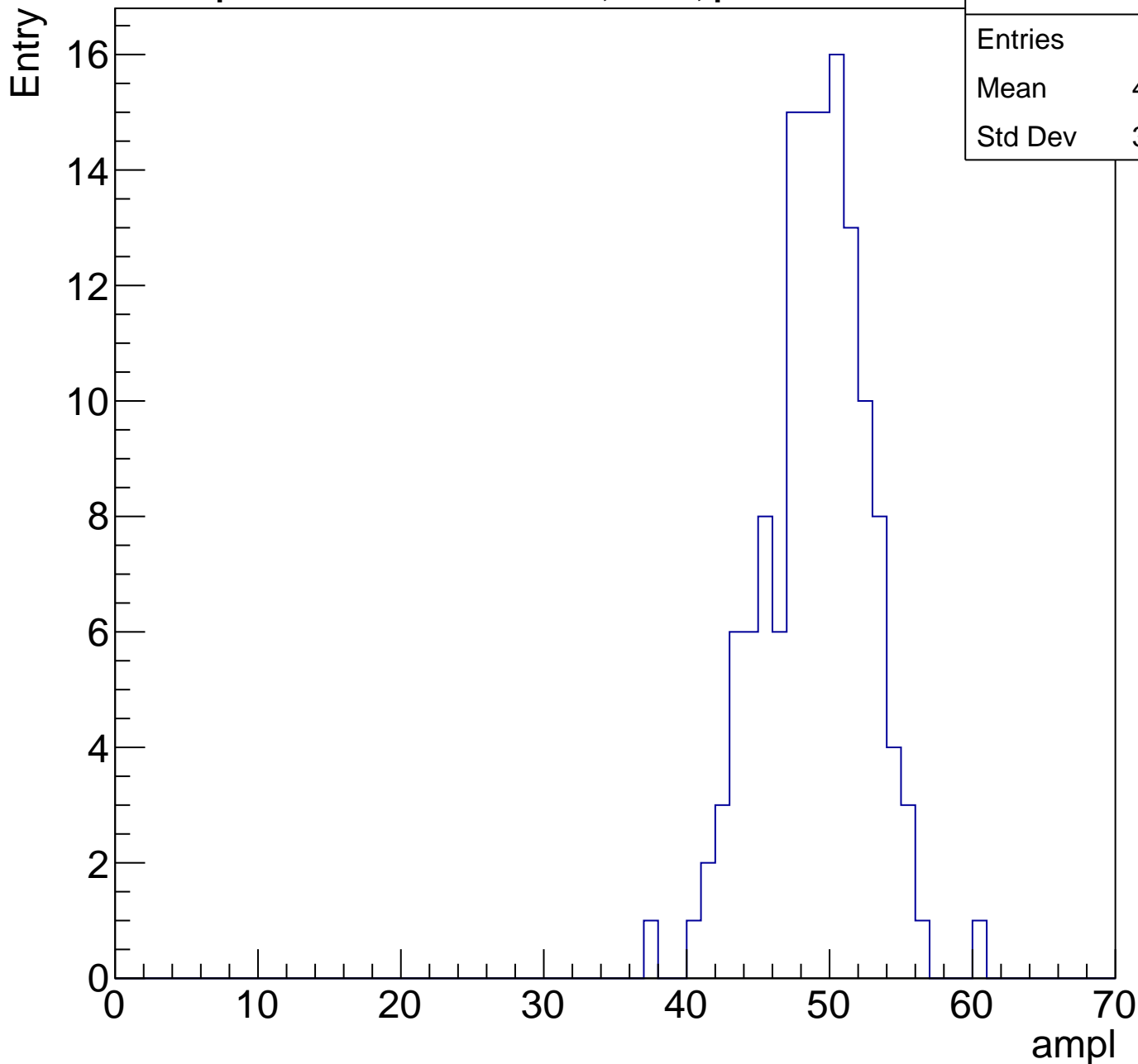
ampl



# B0L101S, U16-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 48.56 |
| Std Dev | 3.625 |



# B0L101S, U16-ch41, adc4

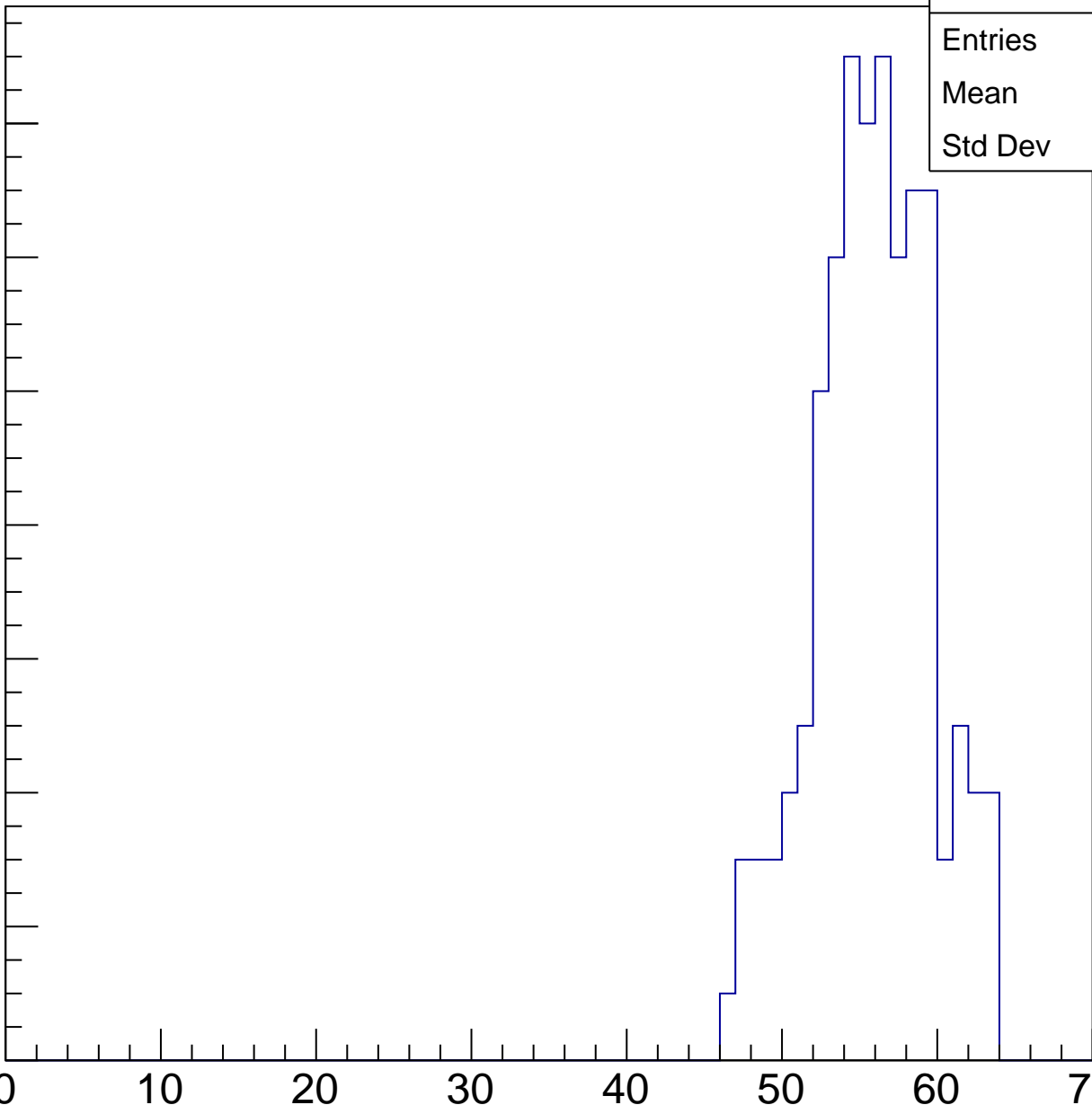
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 55.39 |
| Std Dev | 3.731 |

ampl



# B0L101S, U16-ch41, adc5

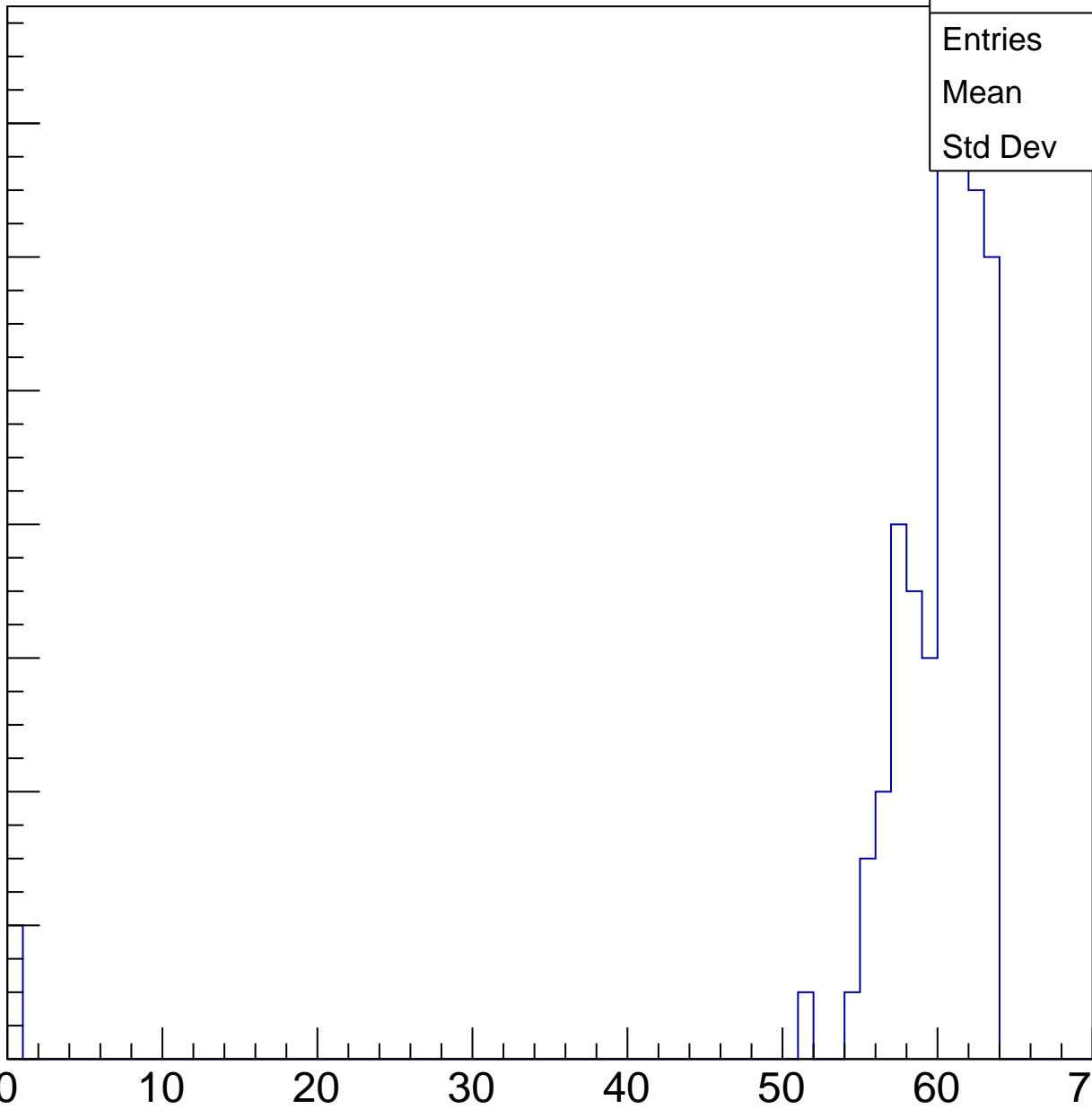
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 58.44 |
| Std Dev | 9.356 |

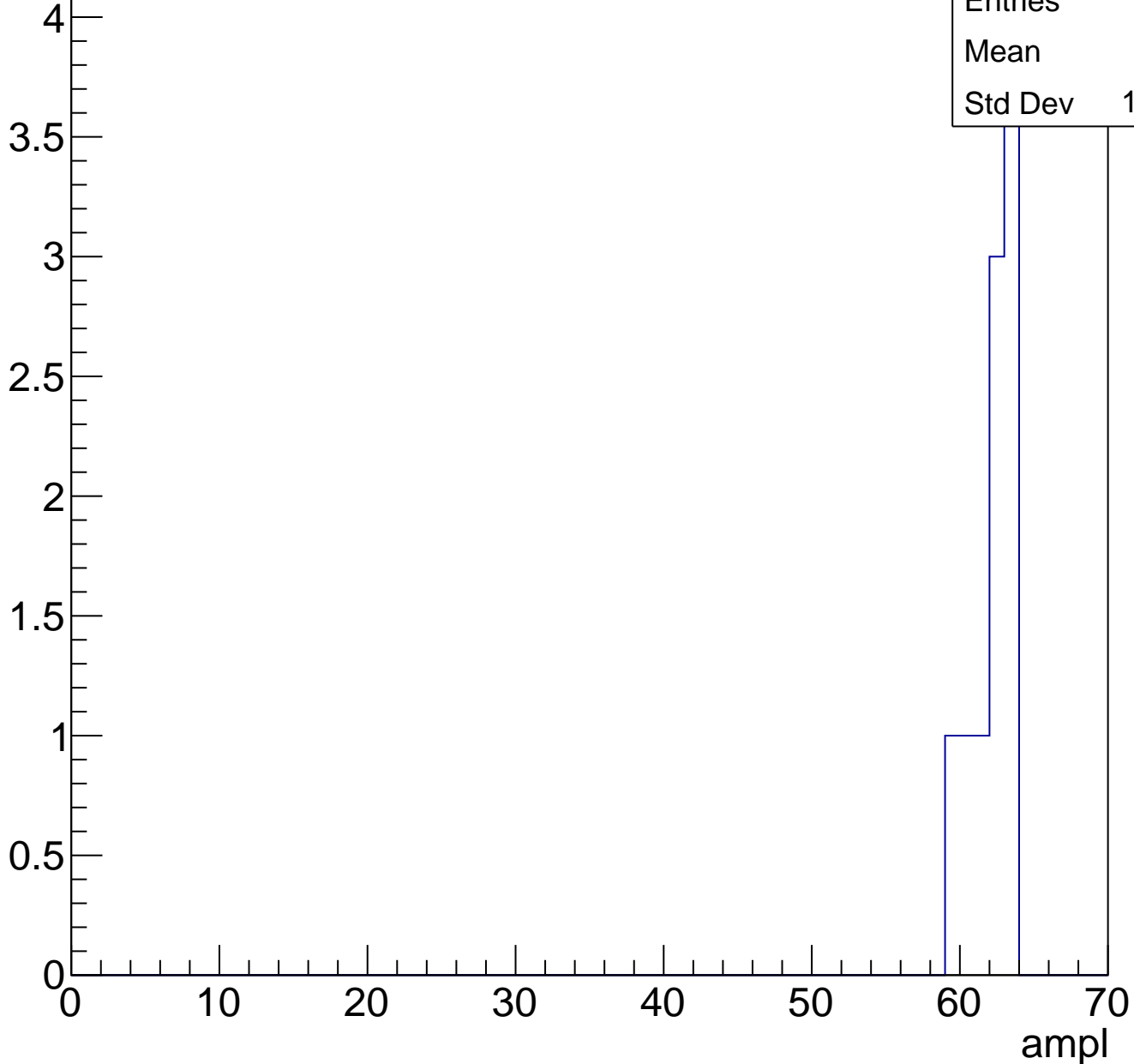
ampl



# B0L101S, U16-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 27.79 |
| Std Dev | 6.088 |

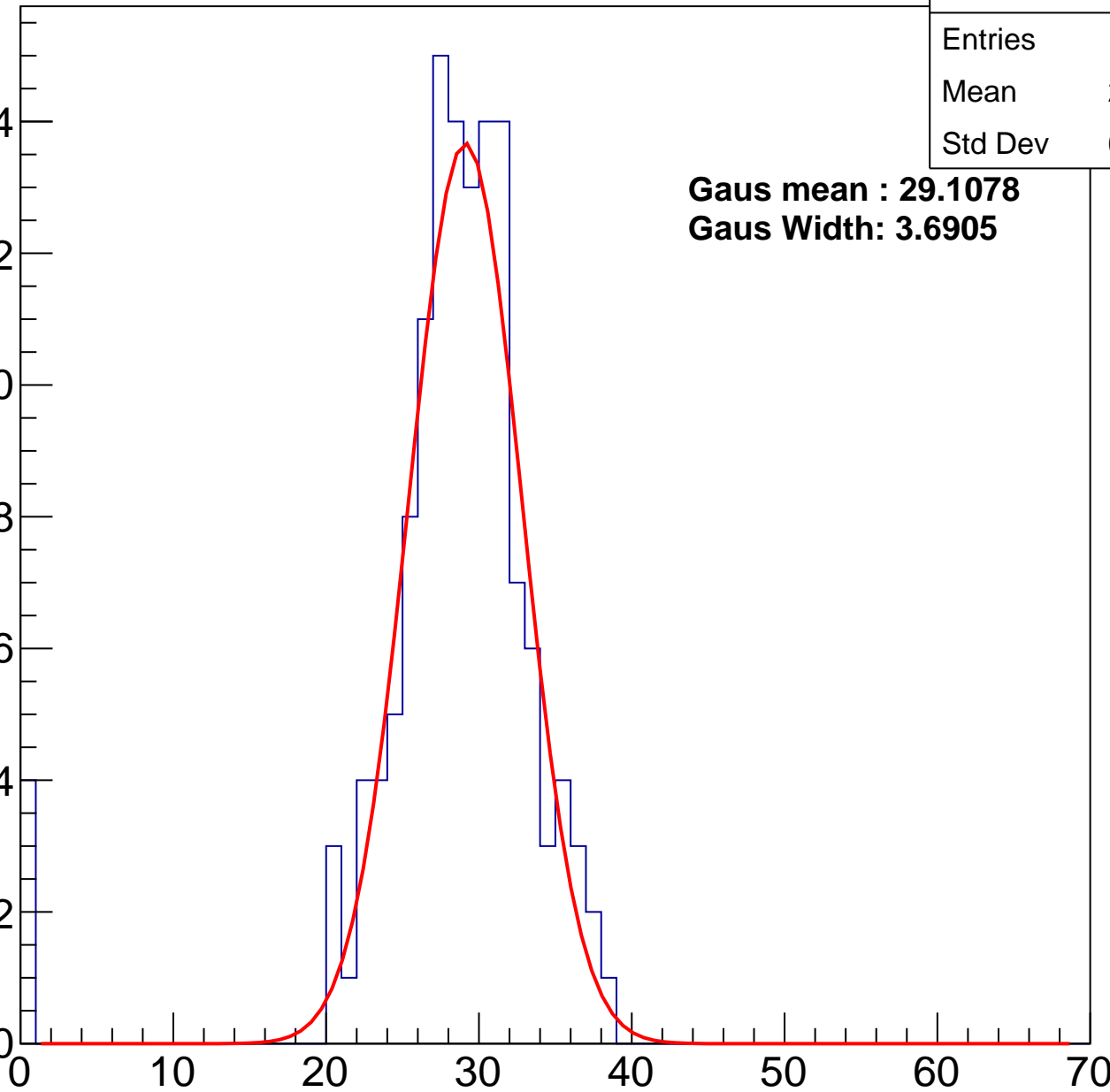
**Gaus mean : 29.1078**

**Gaus Width: 3.6905**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch42, adc1

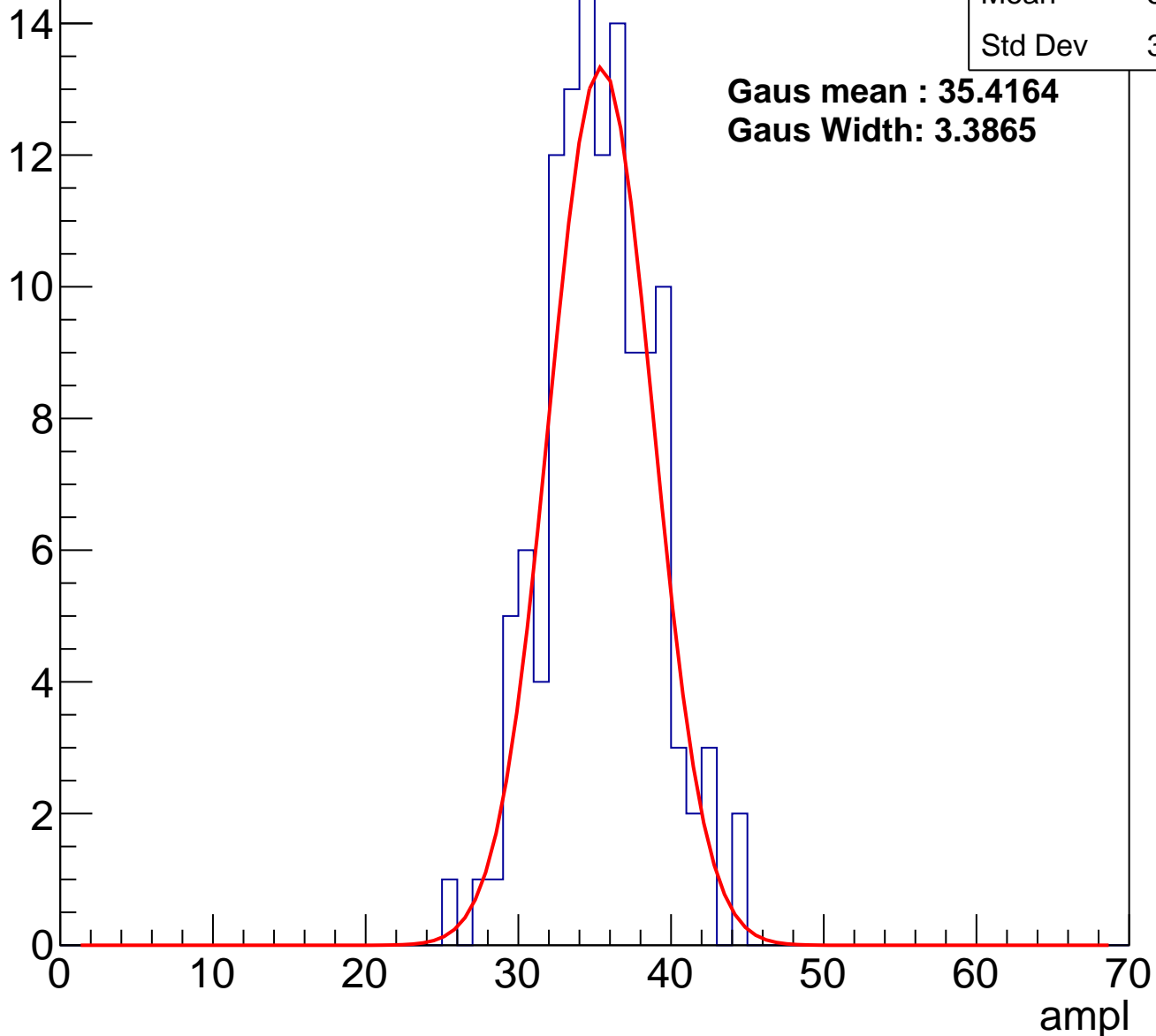
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 34.89 |
| Std Dev | 3.539 |

**Gaus mean : 35.4164**

**Gaus Width: 3.3865**

Entry



# B0L101S, U16-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

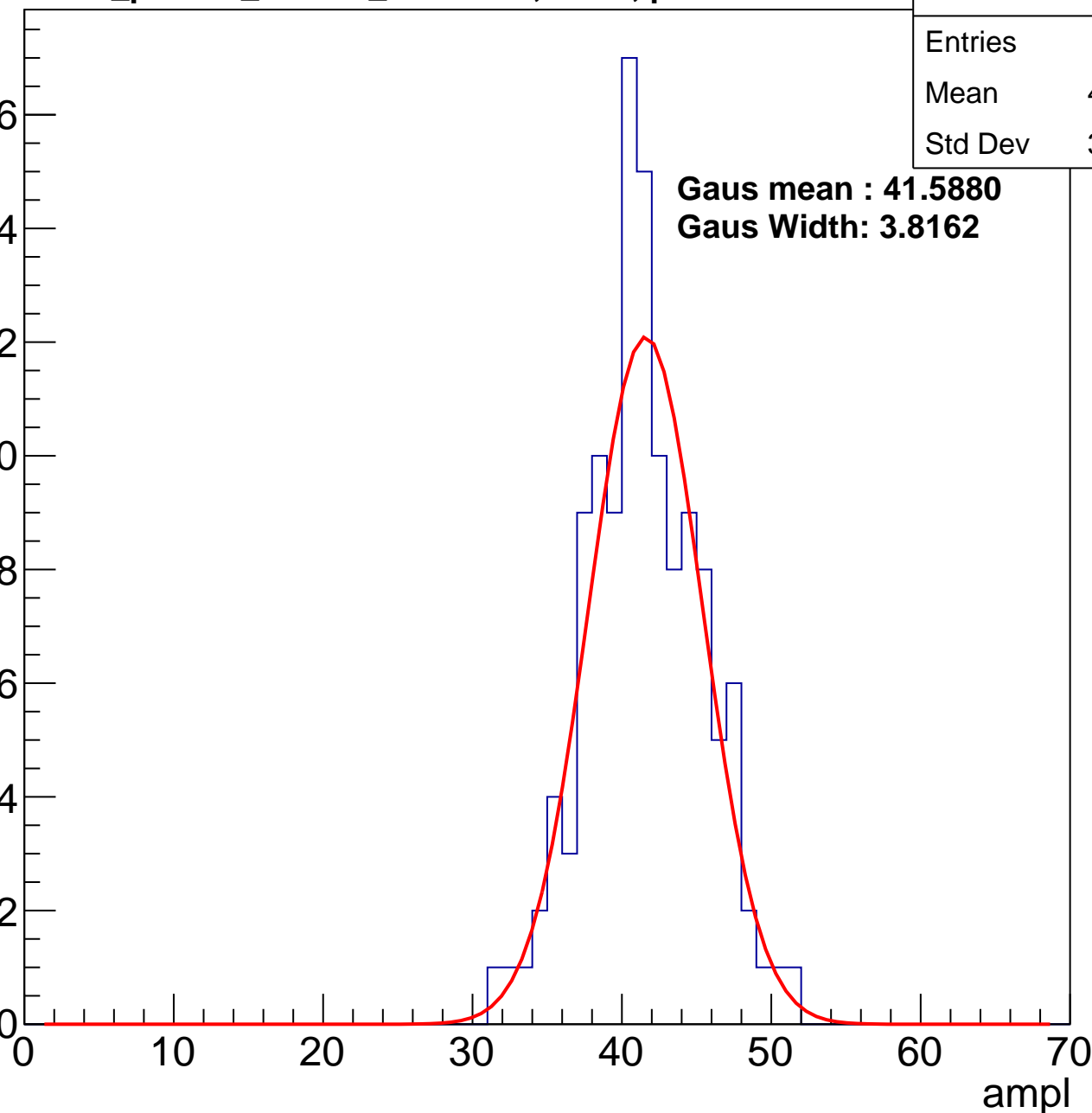
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 41.05 |
| Std Dev | 3.808 |

**Gaus mean : 41.5880**

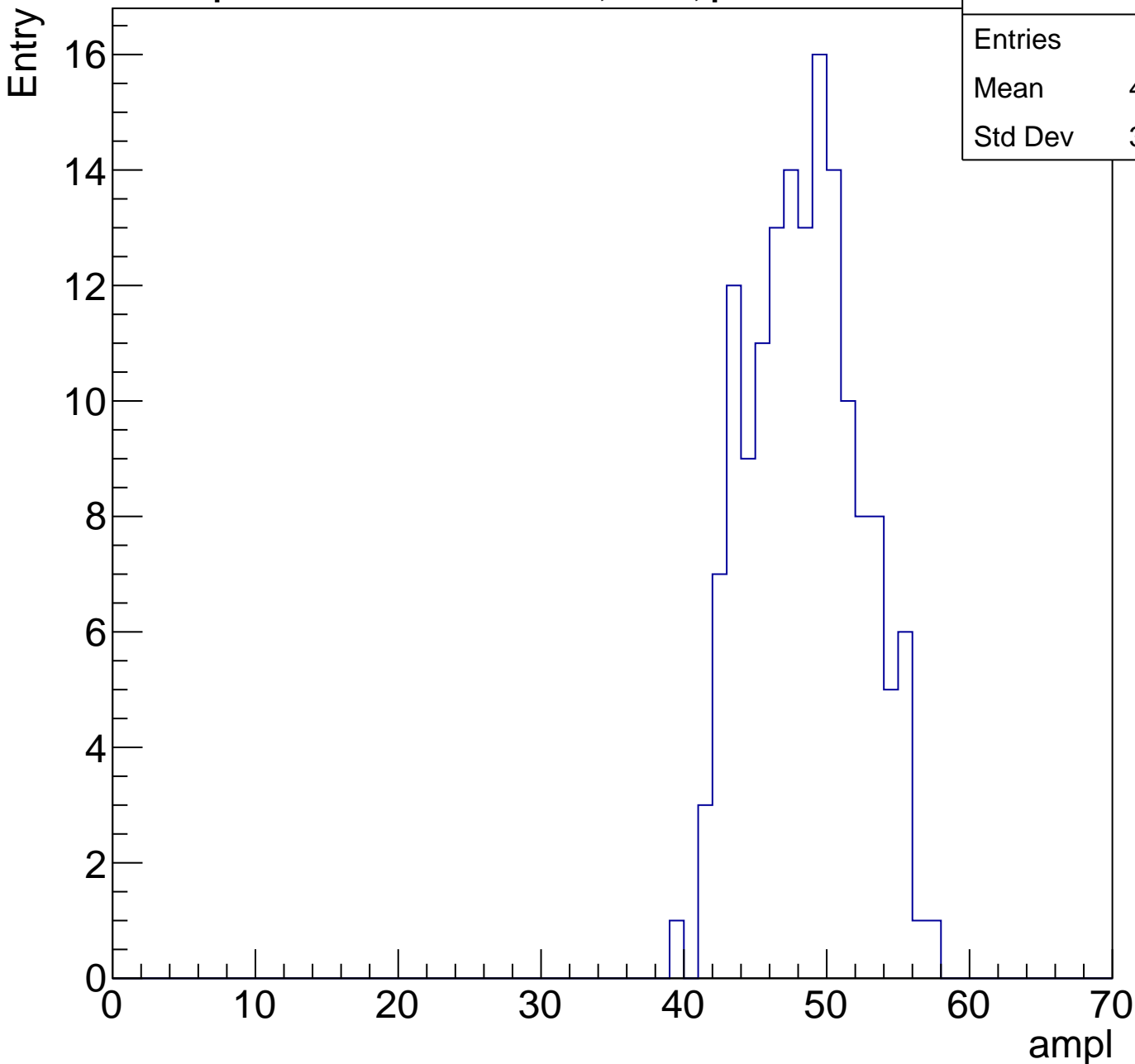
**Gaus Width: 3.8162**



# B0L101S, U16-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 152   |
| Mean    | 47.96 |
| Std Dev | 3.802 |



# B0L101S, U16-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

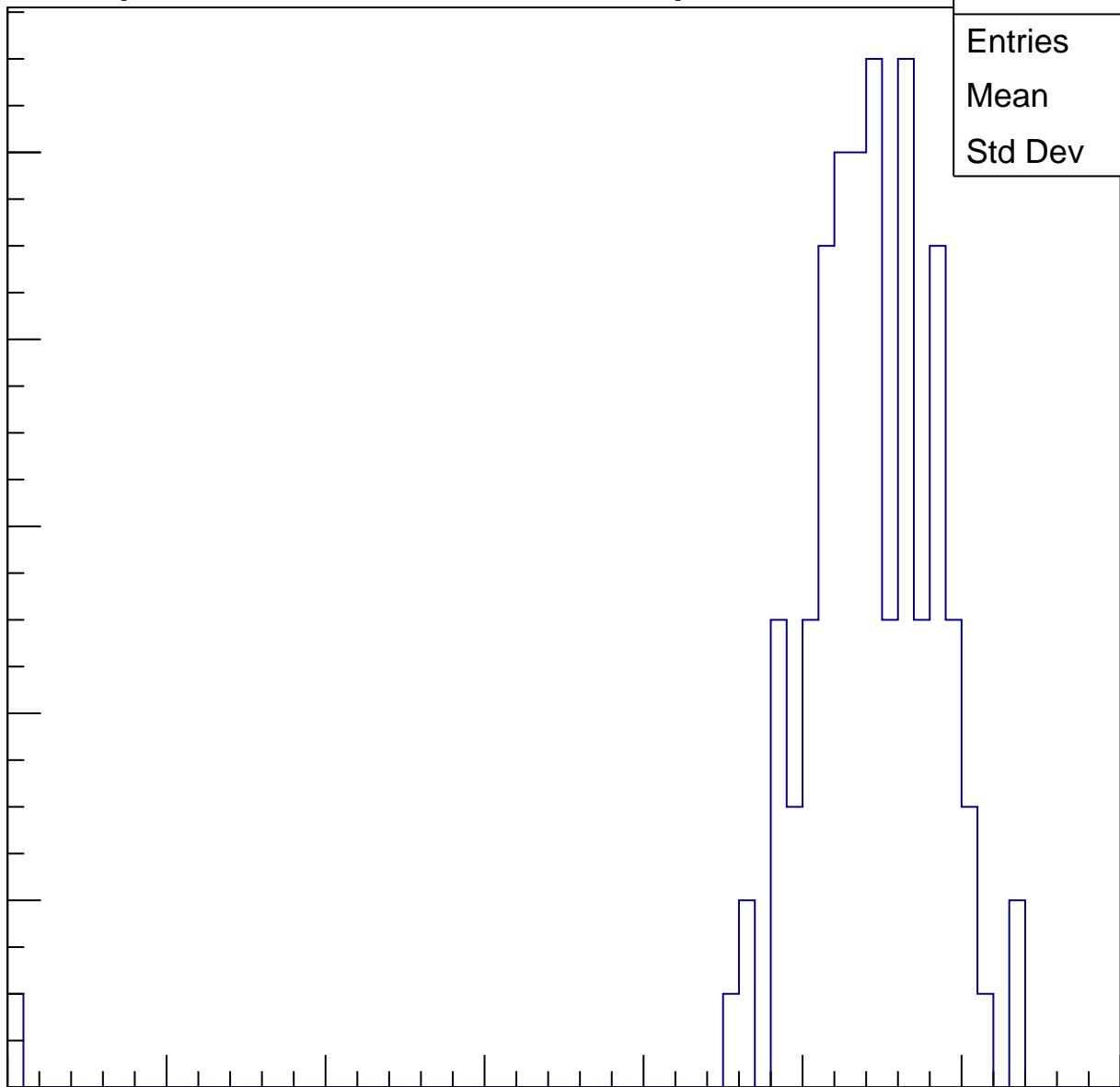
50

60

70

ampl

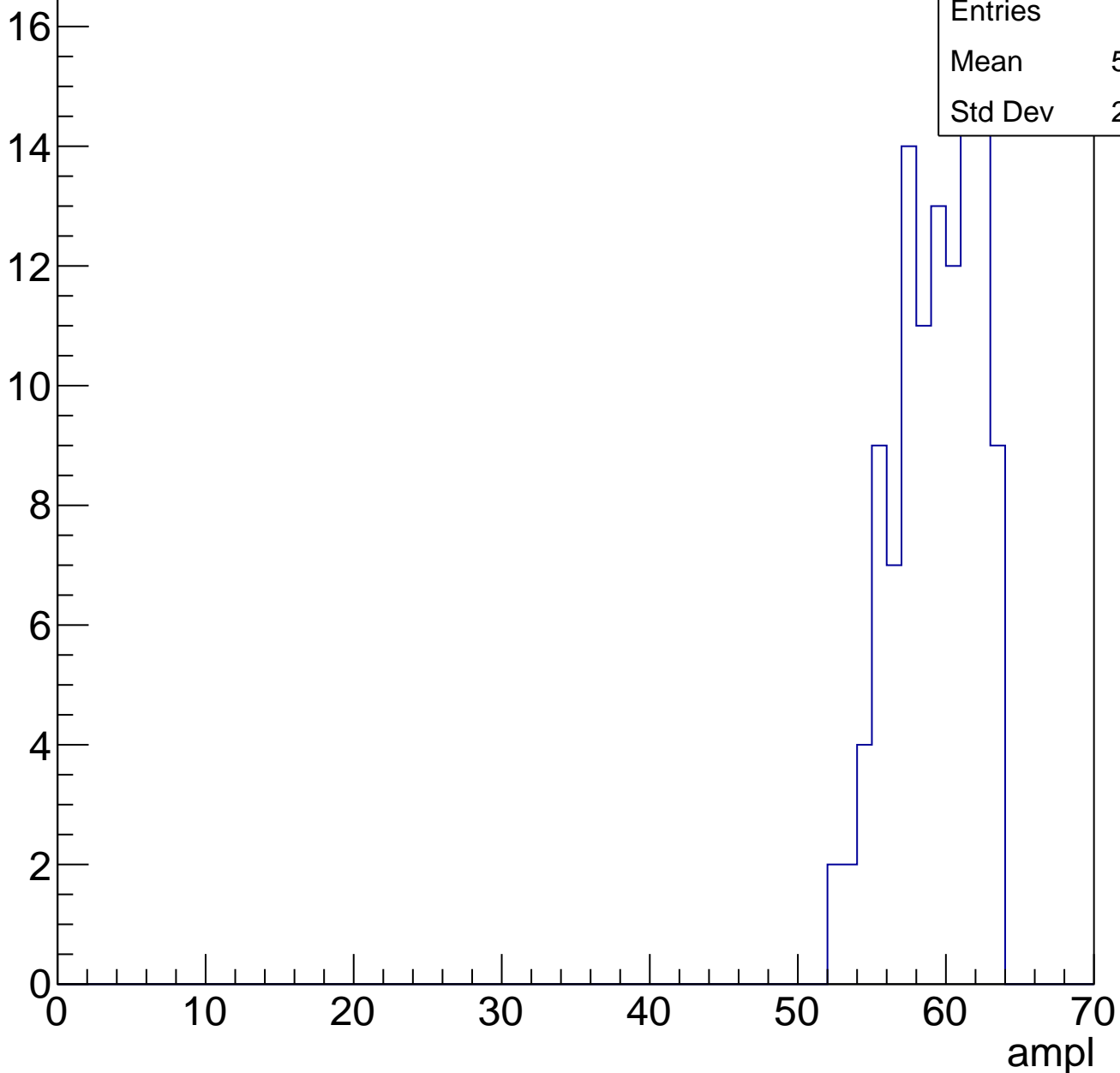
|         |       |
|---------|-------|
| Entries | 98    |
| Mean    | 53.44 |
| Std Dev | 6.579 |



# B0L101S, U16-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

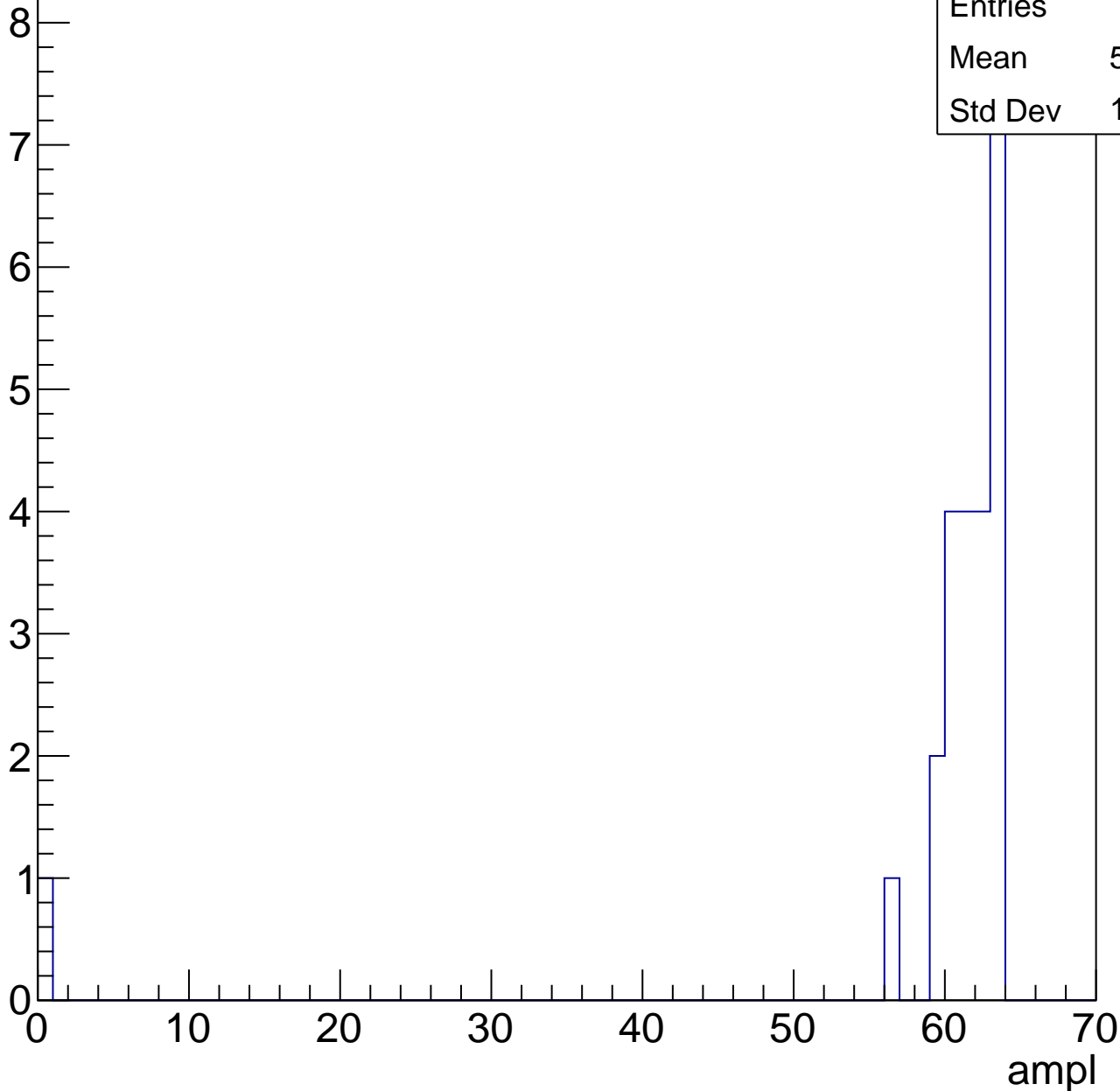


# B0L101S, U16-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 24    |
| Mean    | 58.75 |
| Std Dev | 12.37 |





# B0L101S, U16-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 48    |
| Std Dev | 19.13 |

ampl

# B0L101S, U16-ch43, adc0

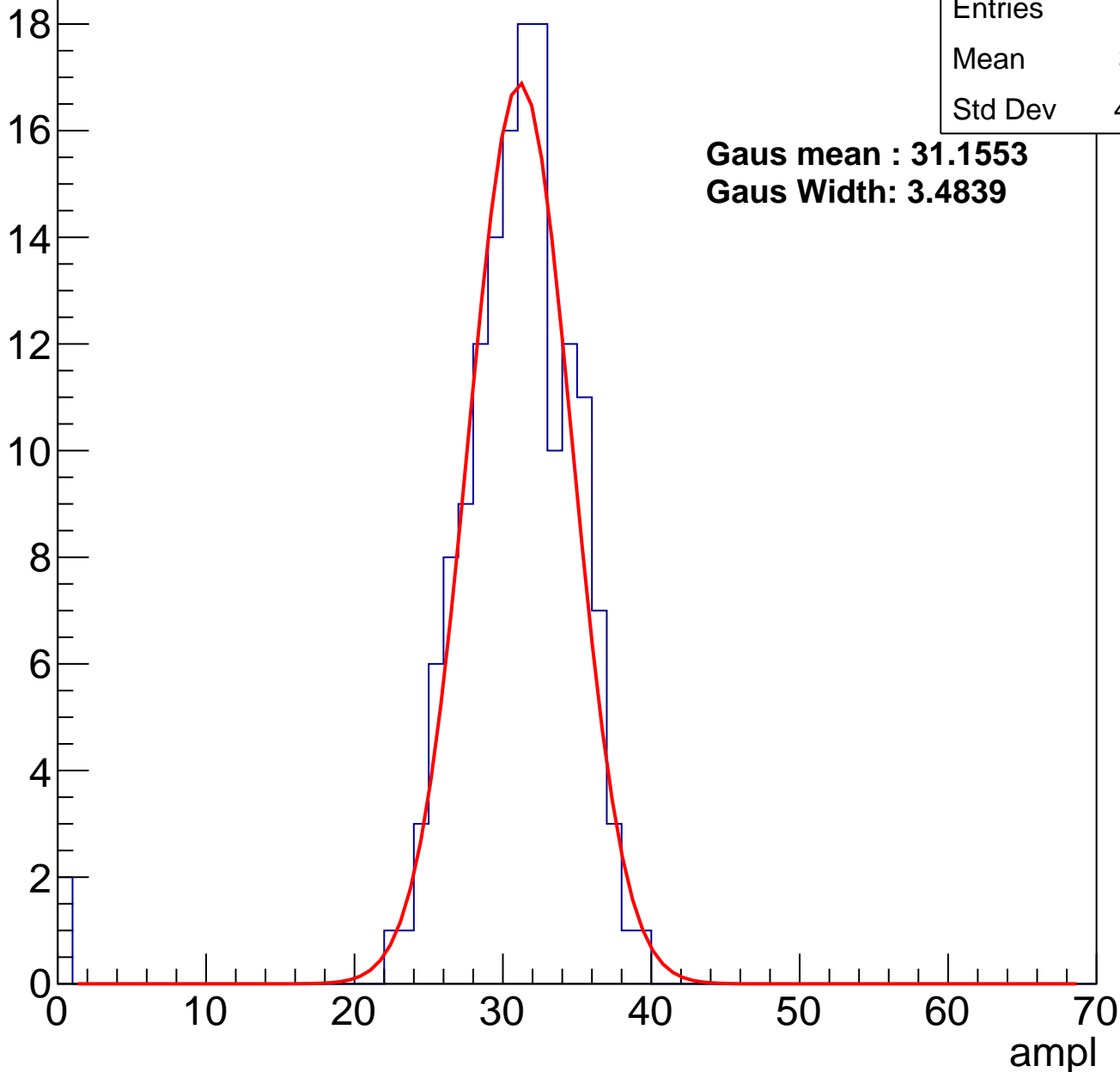
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 153   |
| Mean    | 30.31 |
| Std Dev | 4.863 |

**Gaus mean : 31.1553**

**Gaus Width: 3.4839**

Entry



# B0L101S, U16-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

145

Mean

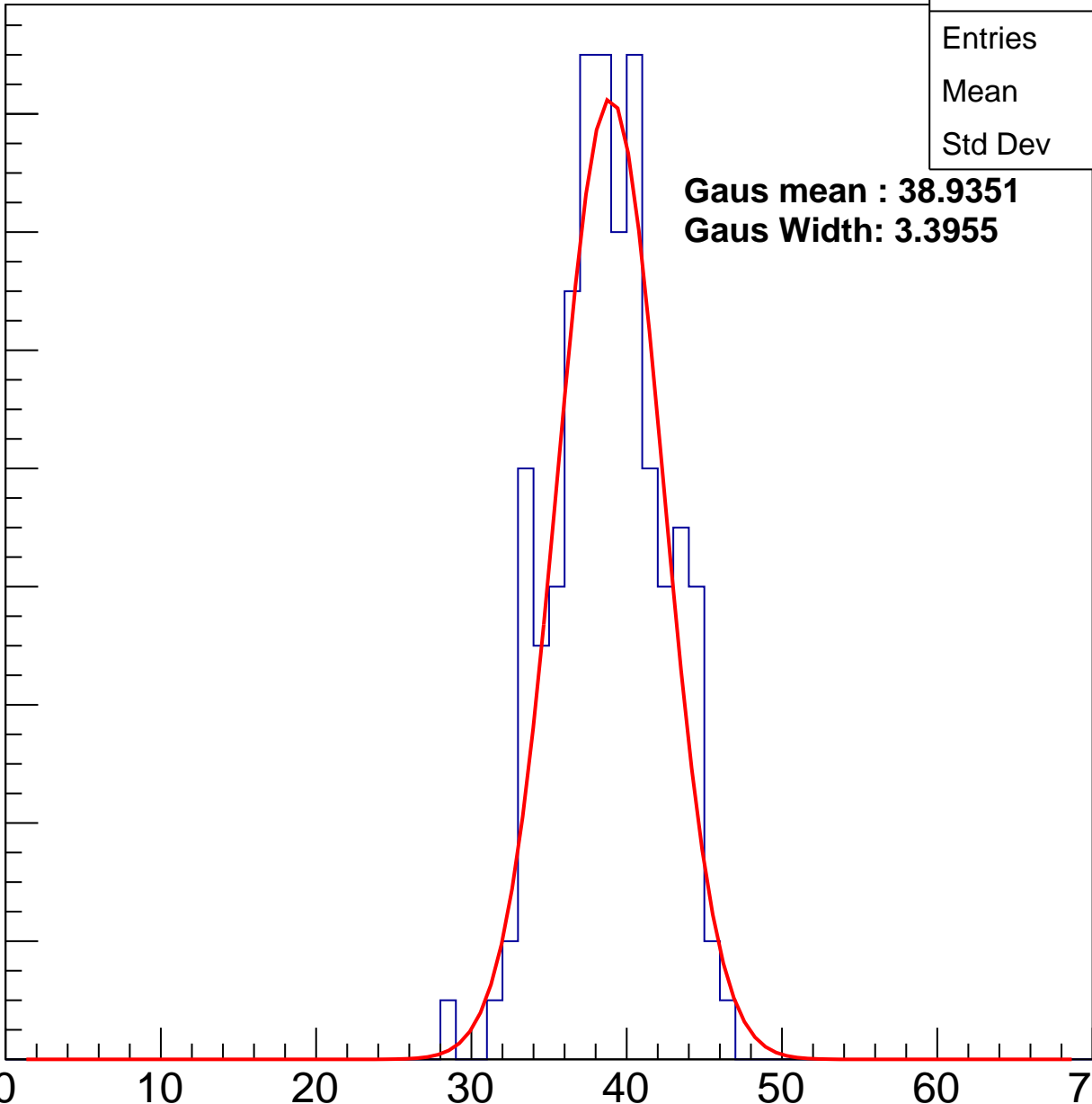
38.35

Std Dev

3.405

**Gaus mean : 38.9351**

**Gaus Width: 3.3955**



# B0L101S, U16-ch43, adc2

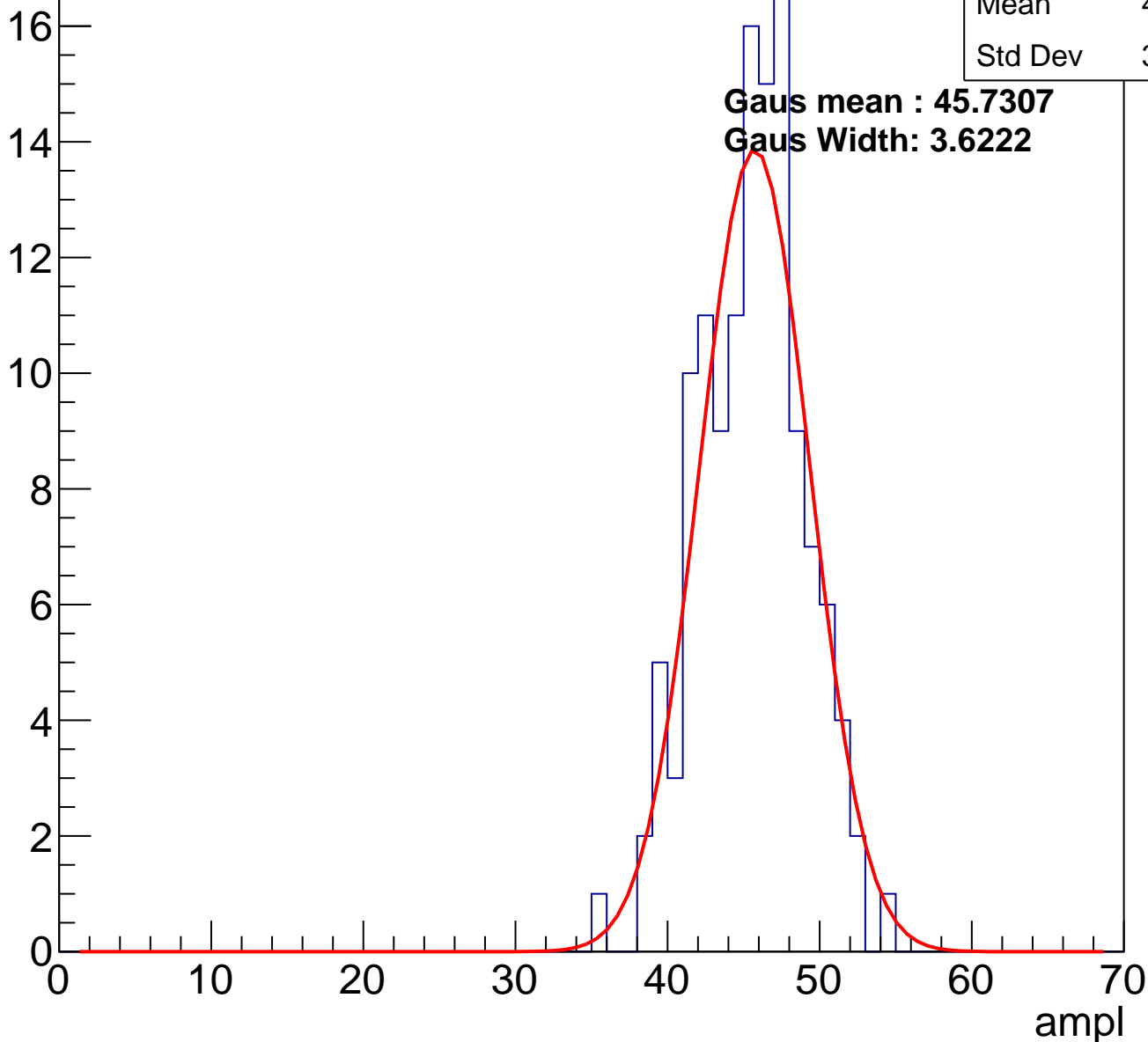
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 45.08 |
| Std Dev | 3.418 |

**Gaus mean : 45.7307**

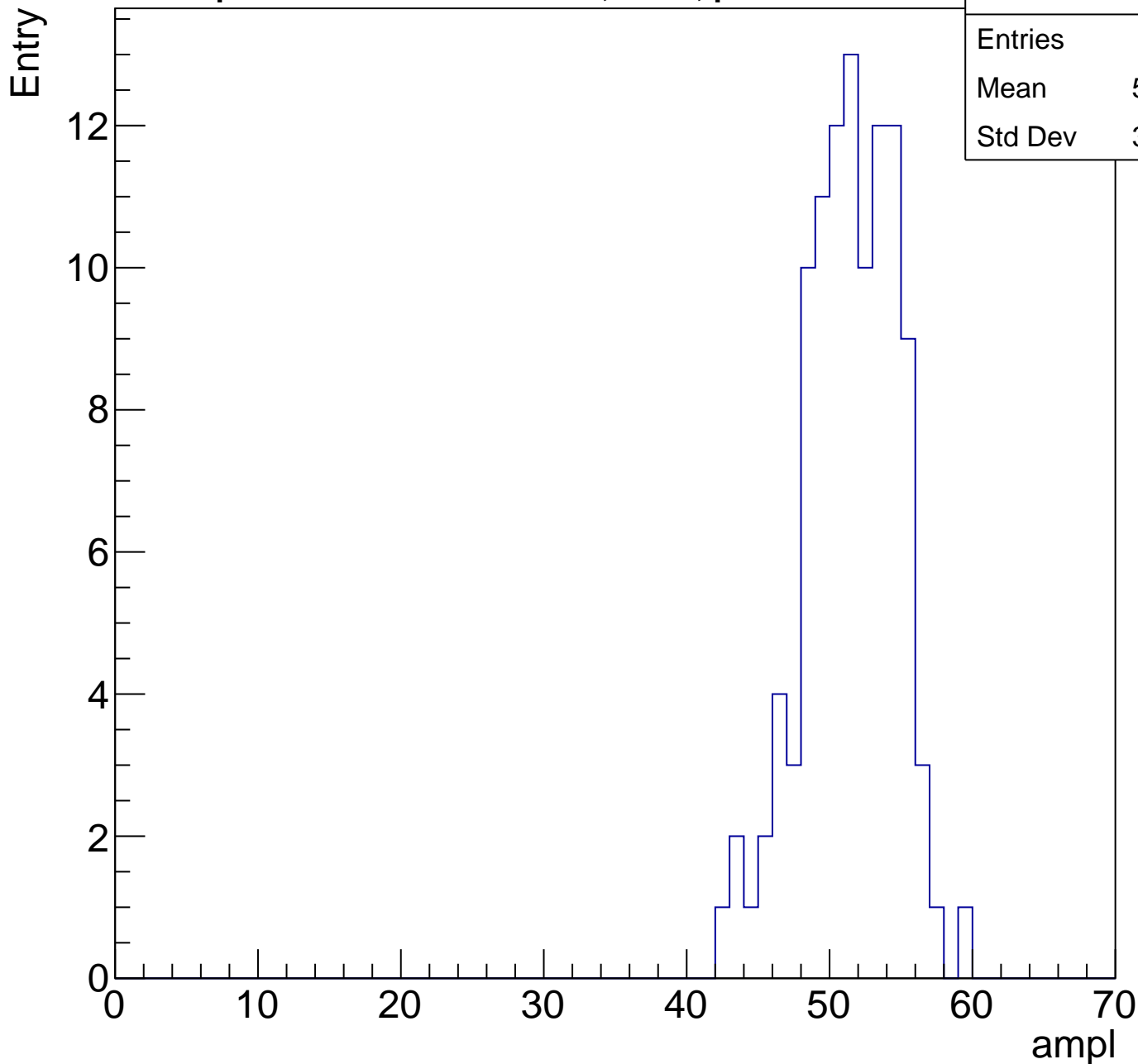
**Gaus Width: 3.6222**



# B0L101S, U16-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 50.95 |
| Std Dev | 3.219 |

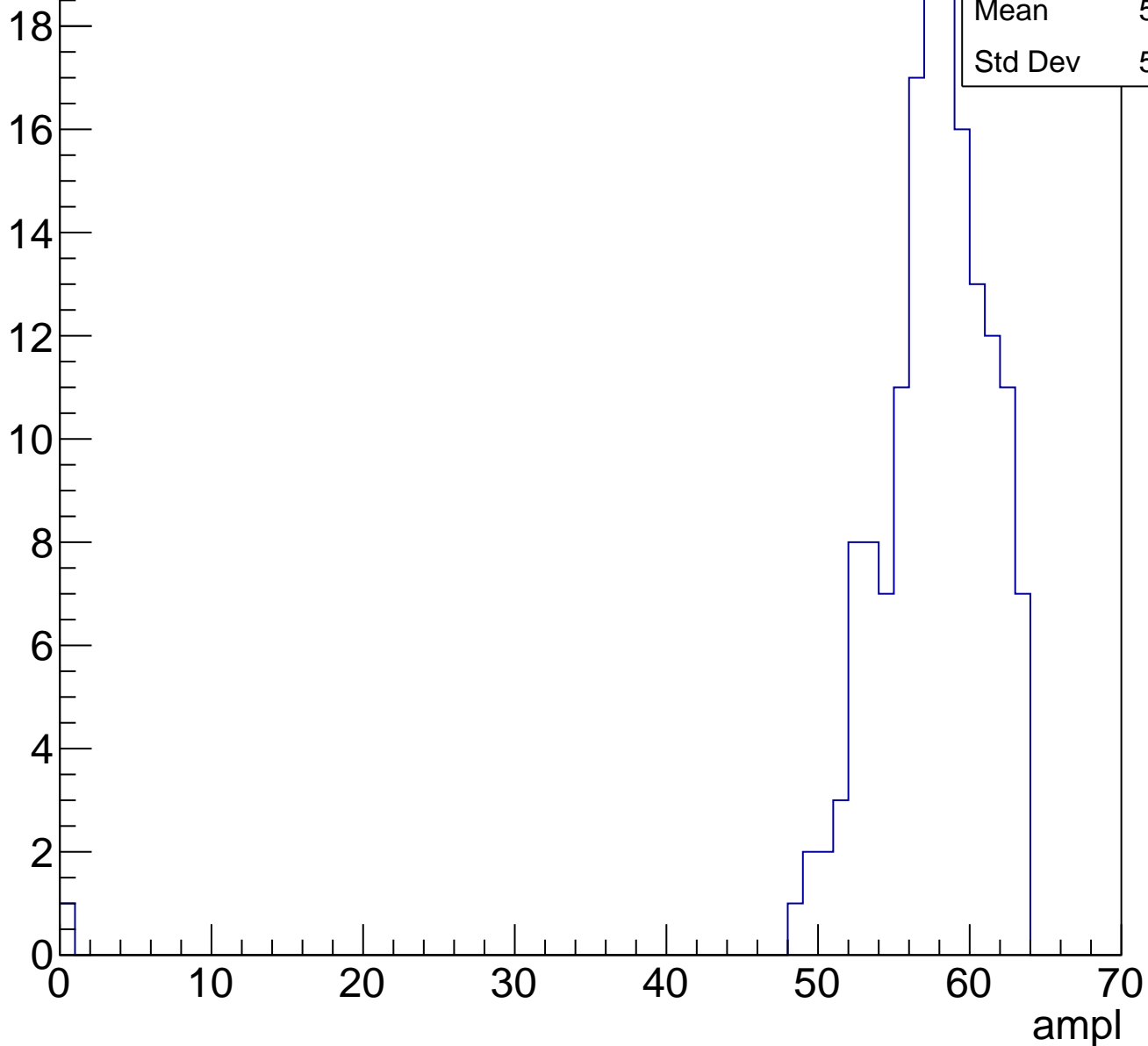


# B0L101S, U16-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 157   |
| Mean    | 56.93 |
| Std Dev | 5.663 |

Entry



# B0L101S, U16-ch43, adc5

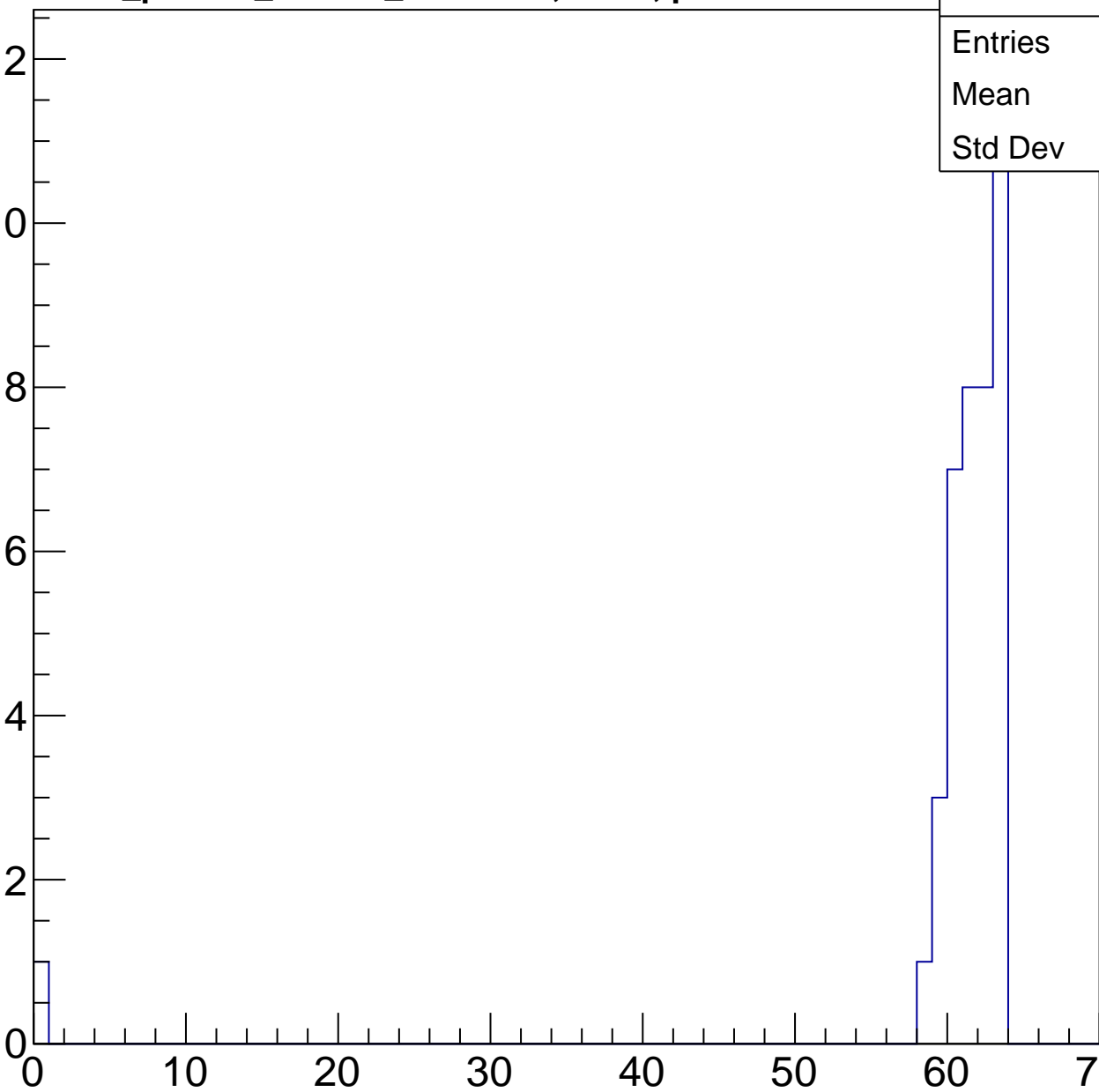
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 40    |
| Mean    | 59.88 |
| Std Dev | 9.688 |

ampl



# B0L101S, U16-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch44, adc0

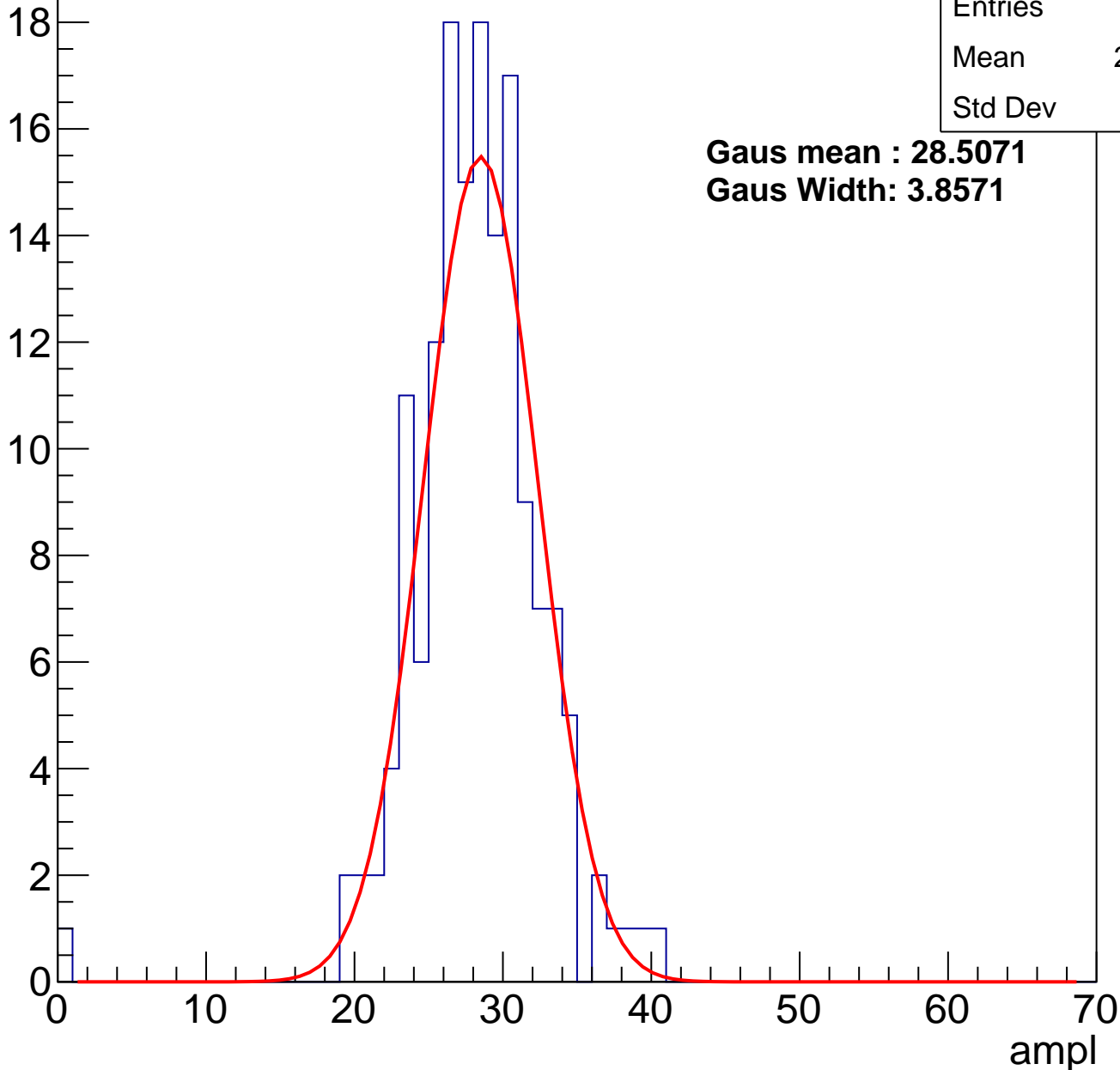
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 27.74 |
| Std Dev | 4.44  |

**Gaus mean : 28.5071**

**Gaus Width: 3.8571**

Entry



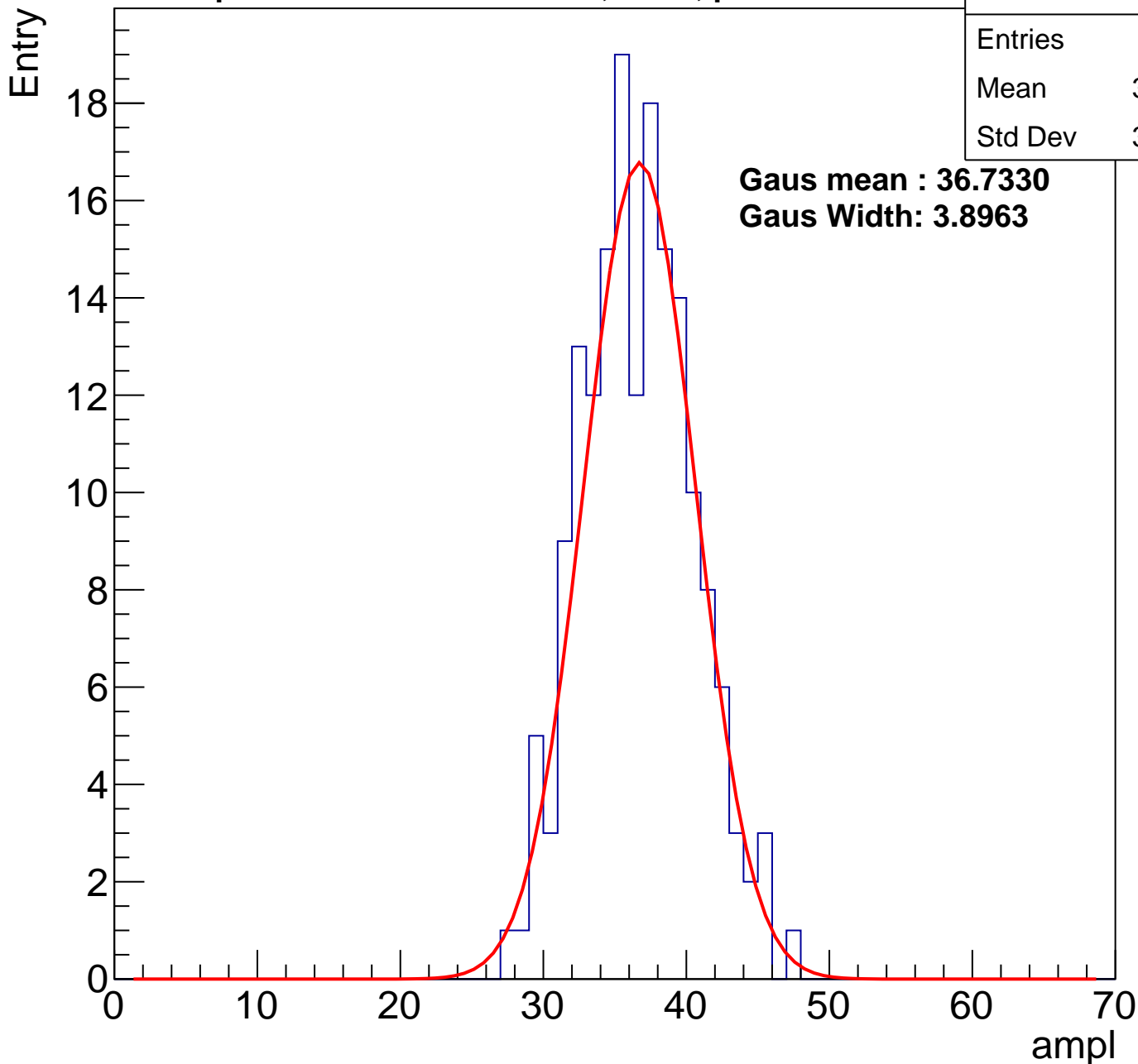
# B0L101S, U16-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 170   |
| Mean    | 36.17 |
| Std Dev | 3.859 |

**Gaus mean : 36.7330**

**Gaus Width: 3.8963**



# B0L101S, U16-ch44, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

|         |       |
|---------|-------|
| Entries | 97    |
| Mean    | 42.16 |
| Std Dev | 3.557 |

**Gaus mean : 43.0890**

**Gaus Width: 2.8464**

0

2

4

6

8

10

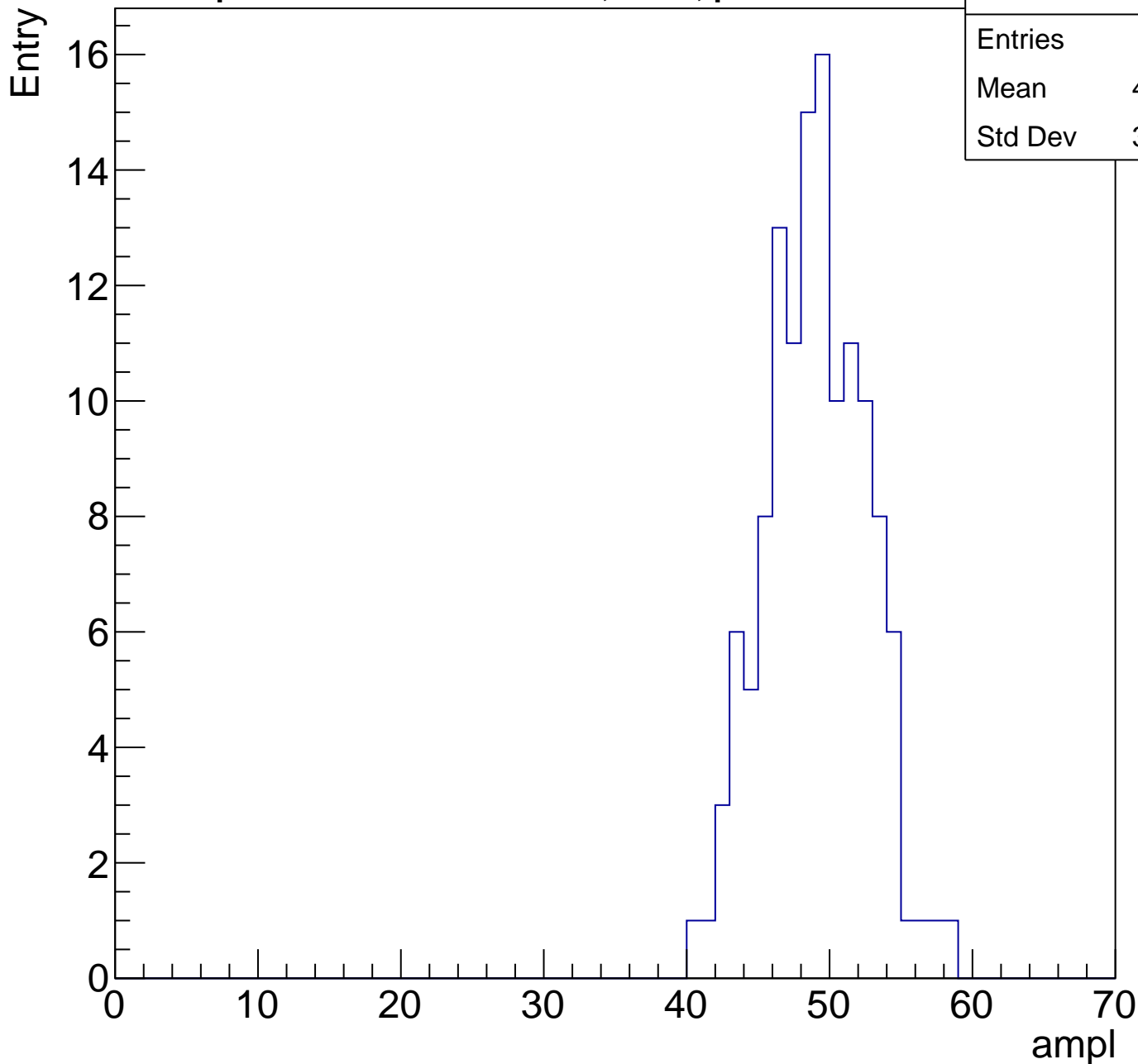
12

Entry

# B0L101S, U16-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 48.59 |
| Std Dev | 3.499 |



# B0L101S, U16-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 54.93 |
| Std Dev | 3.639 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

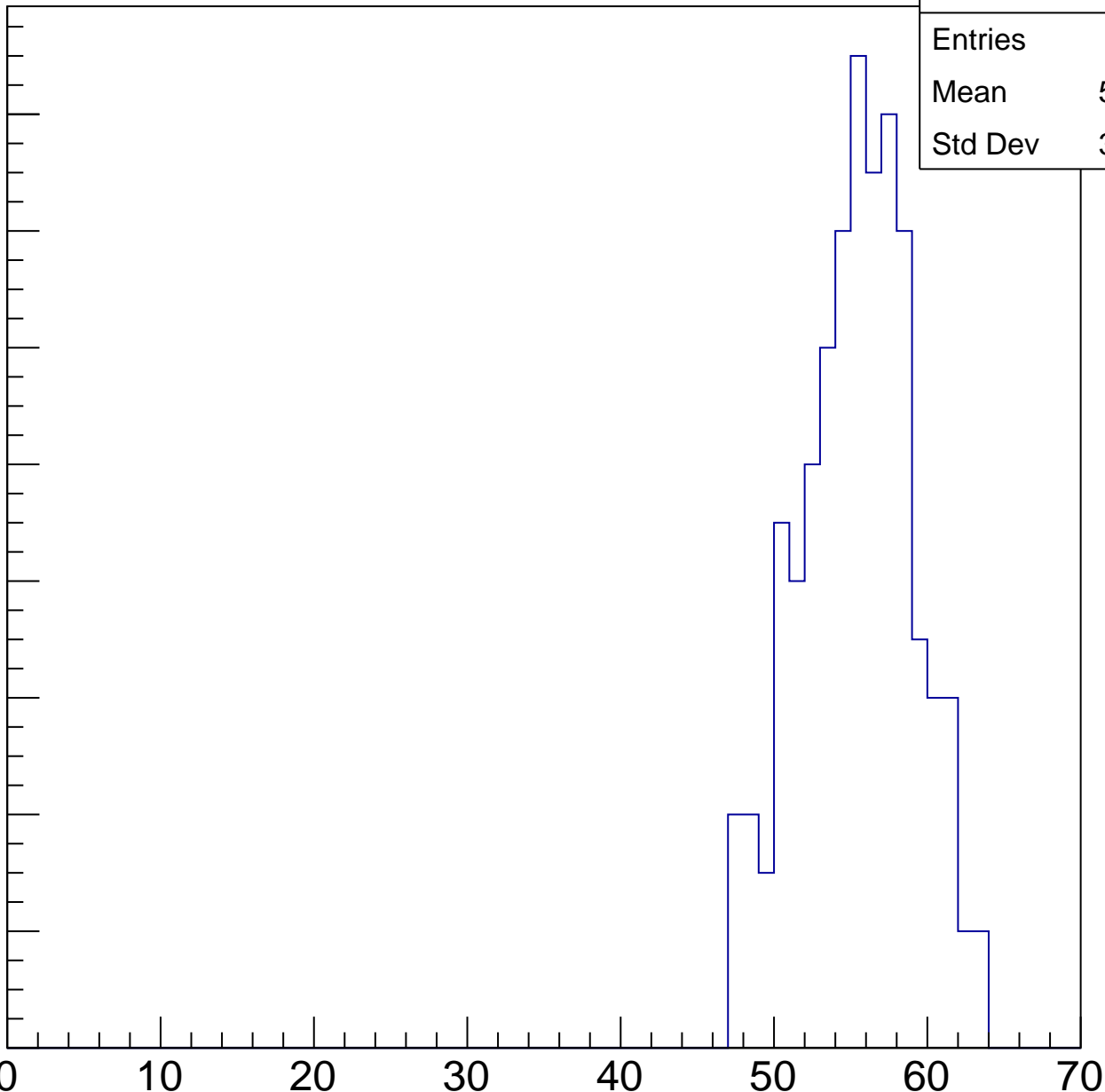
40

50

60

ampl

70



# B0L101S, U16-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

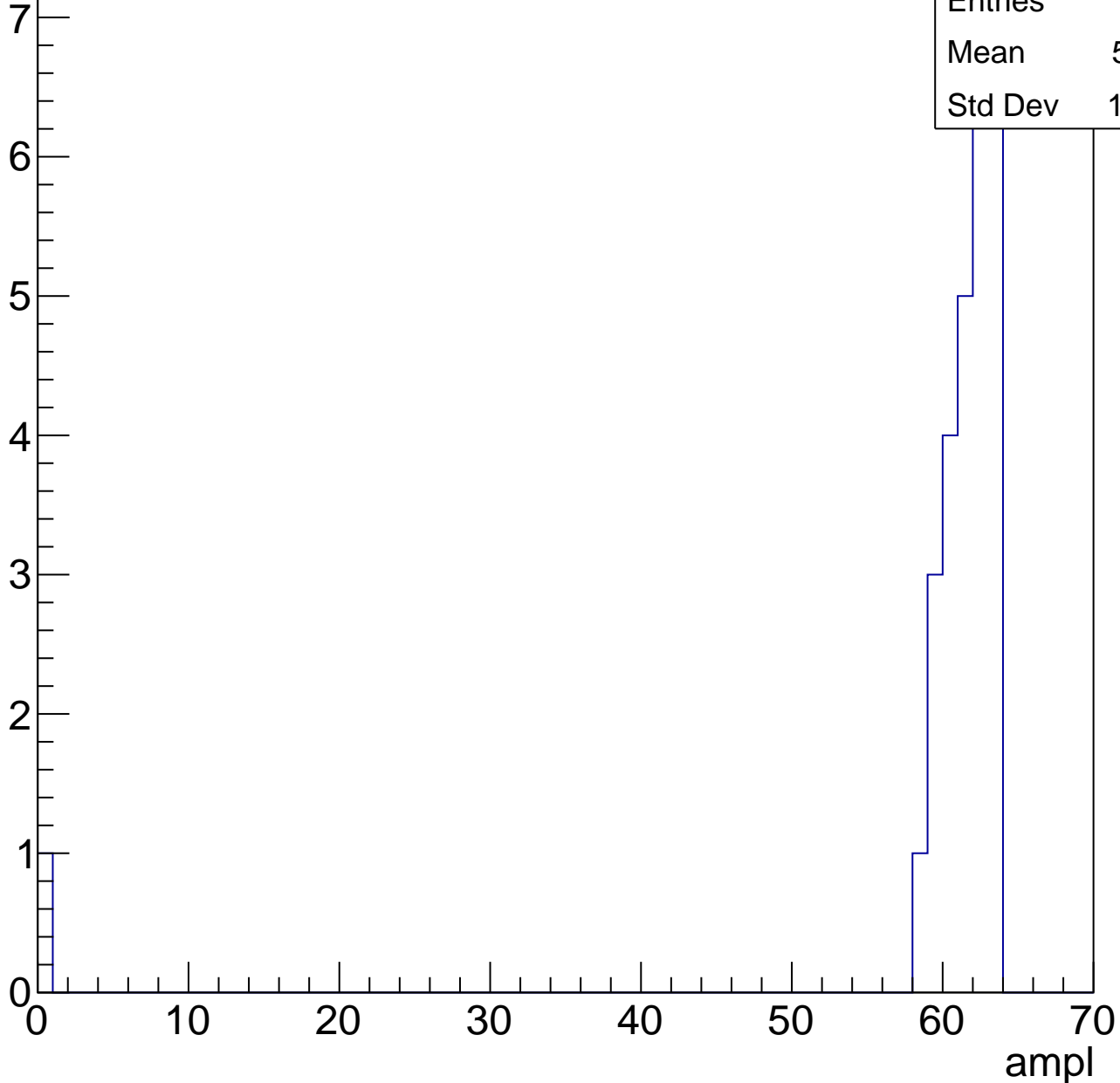
|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 59.19 |
| Std Dev | 7.774 |

# B0L101S, U16-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 28    |
| Mean    | 59.11 |
| Std Dev | 11.47 |





# B0L101S, U16-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 29.04 |
| Std Dev | 3.456 |

**Gaus mean : 29.2069**

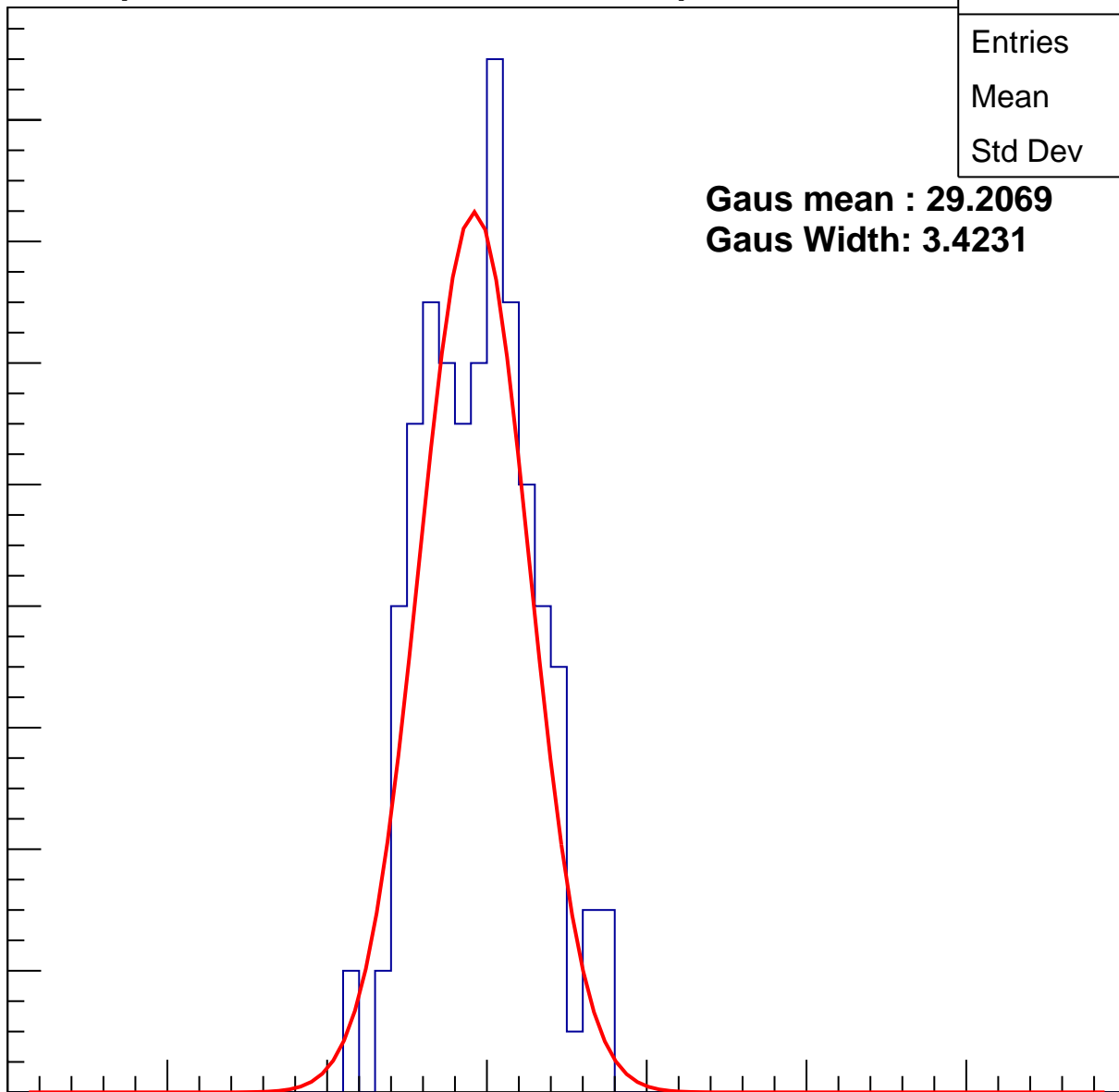
**Gaus Width: 3.4231**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



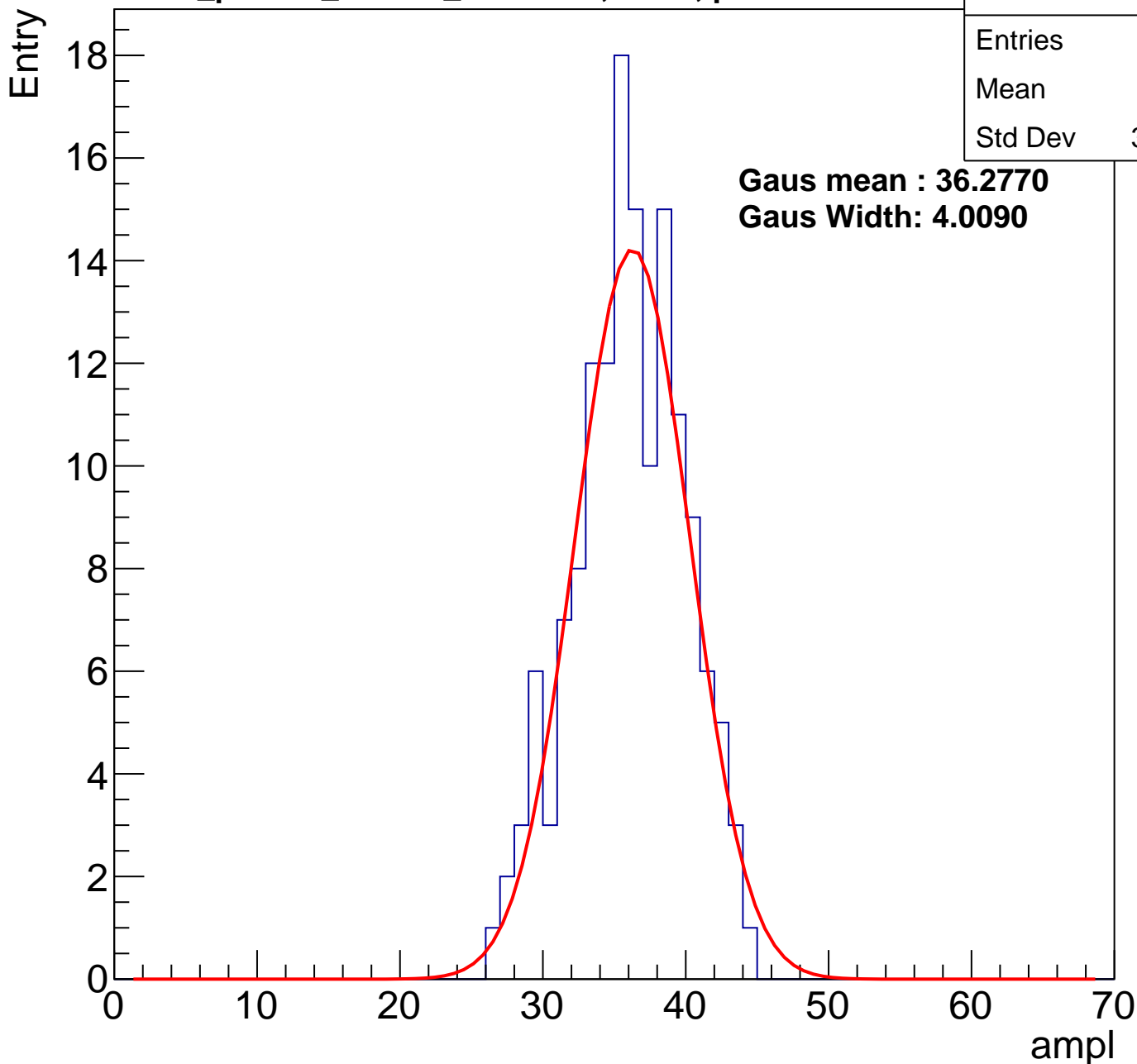
# B0L101S, U16-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 35.6  |
| Std Dev | 3.836 |

**Gaus mean : 36.2770**

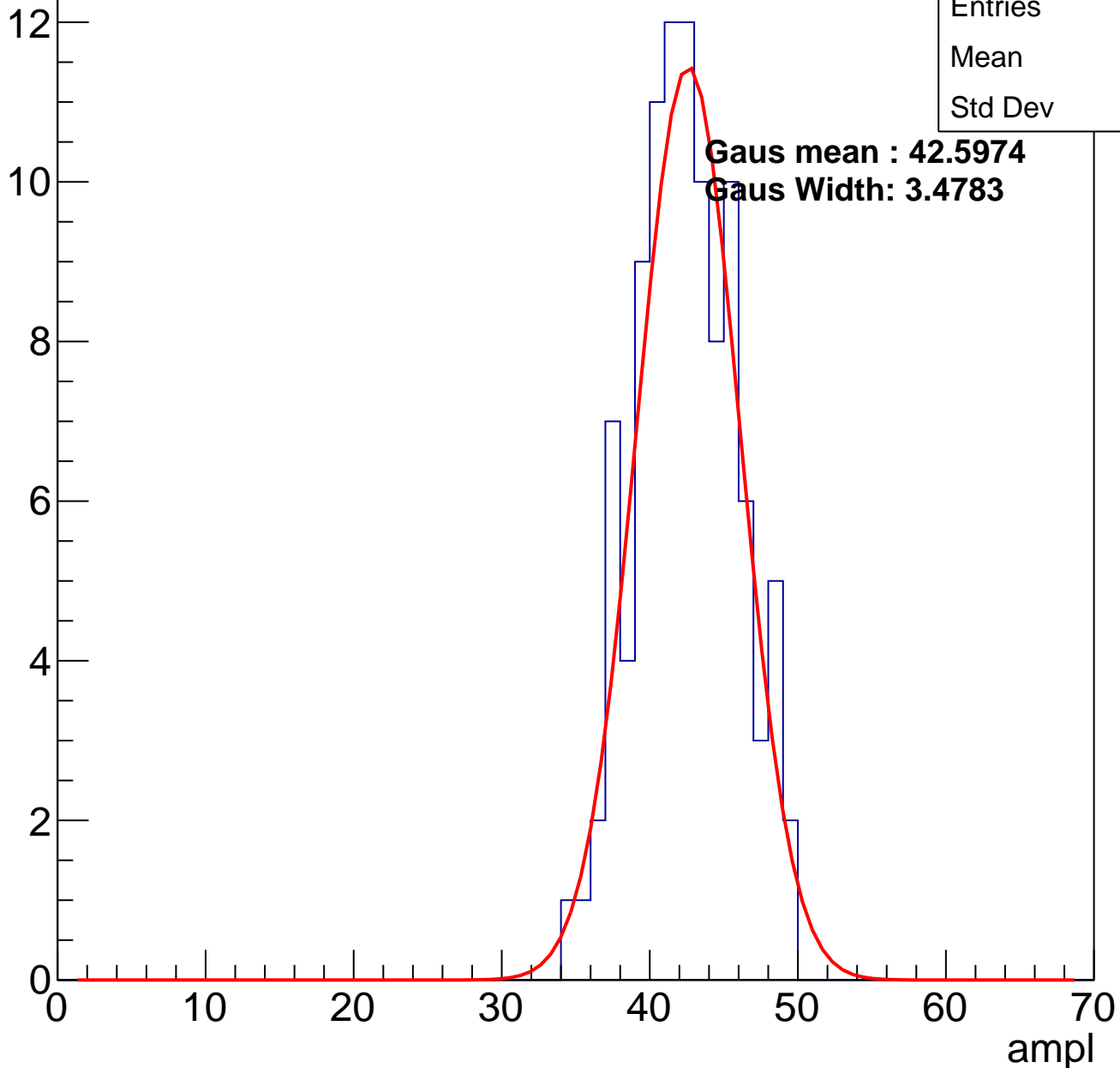
**Gaus Width: 4.0090**



# B0L101S, U16-ch45, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



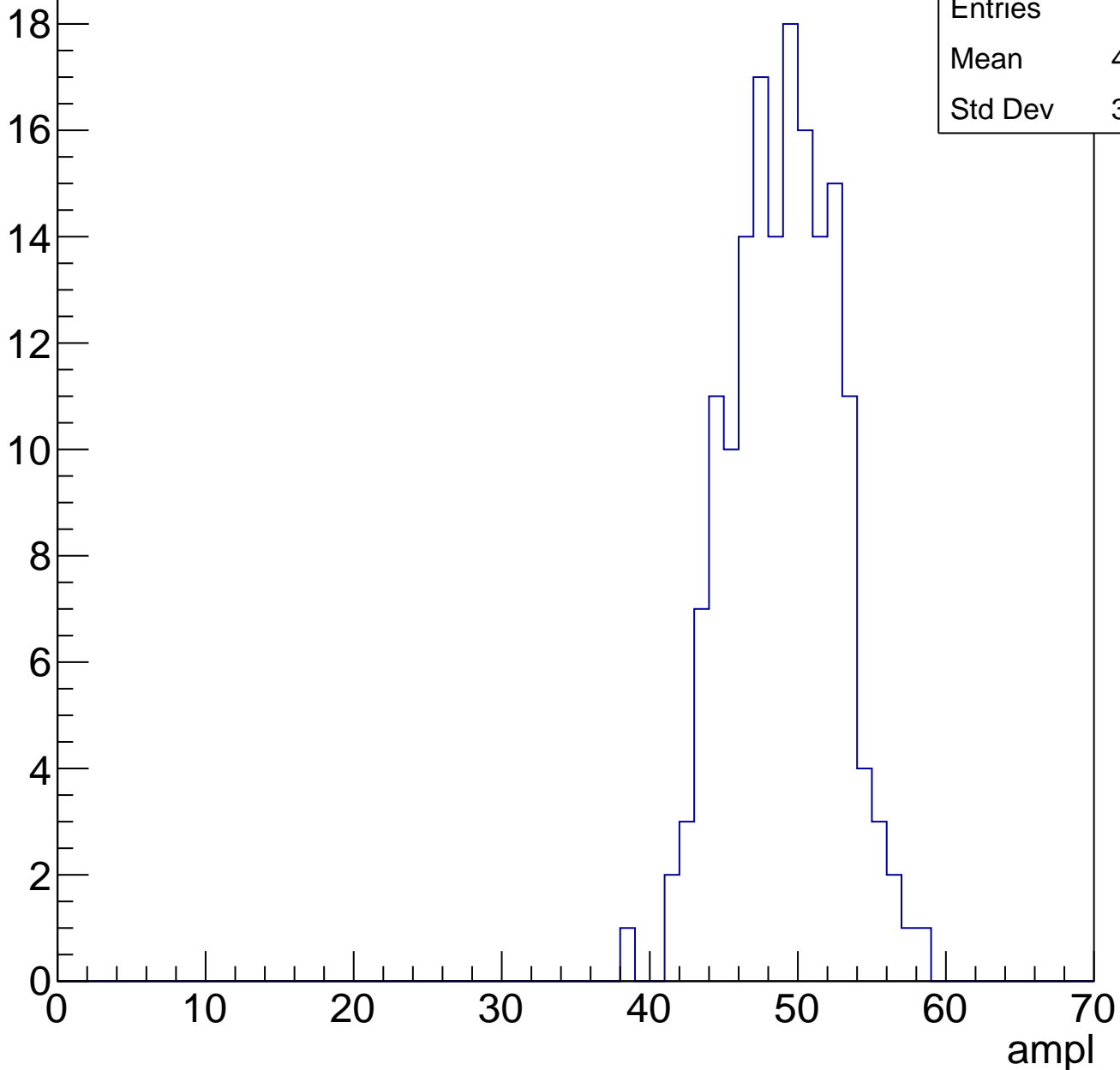
|         |      |
|---------|------|
| Entries | 103  |
| Mean    | 42   |
| Std Dev | 3.33 |

# B0L101S, U16-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 48.55 |
| Std Dev | 3.572 |

Entry

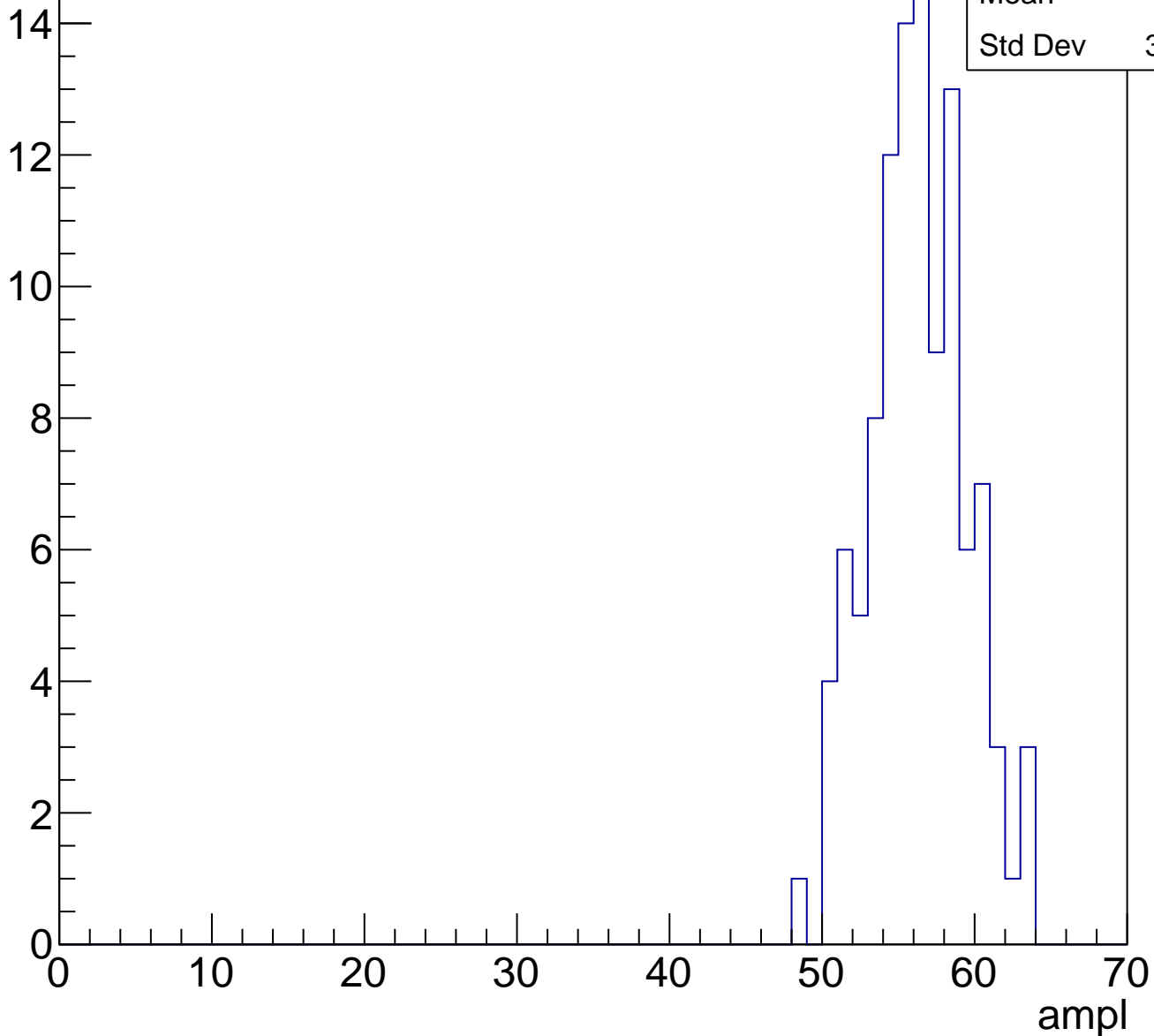


# B0L101S, U16-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 55.8  |
| Std Dev | 3.137 |

Entry



# B0L101S, U16-ch45, adc5

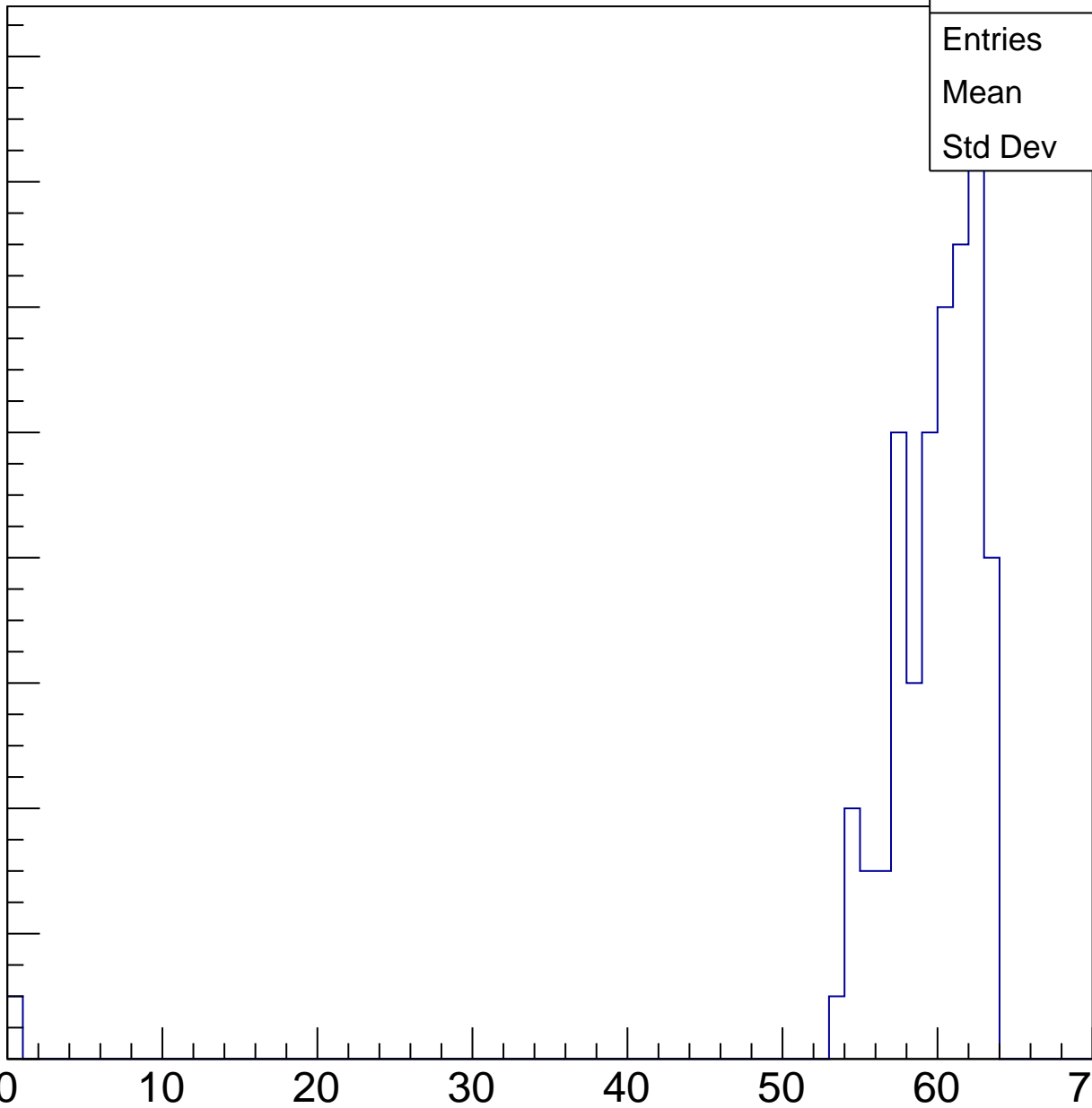
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 87    |
| Mean    | 58.84 |
| Std Dev | 6.841 |

ampl

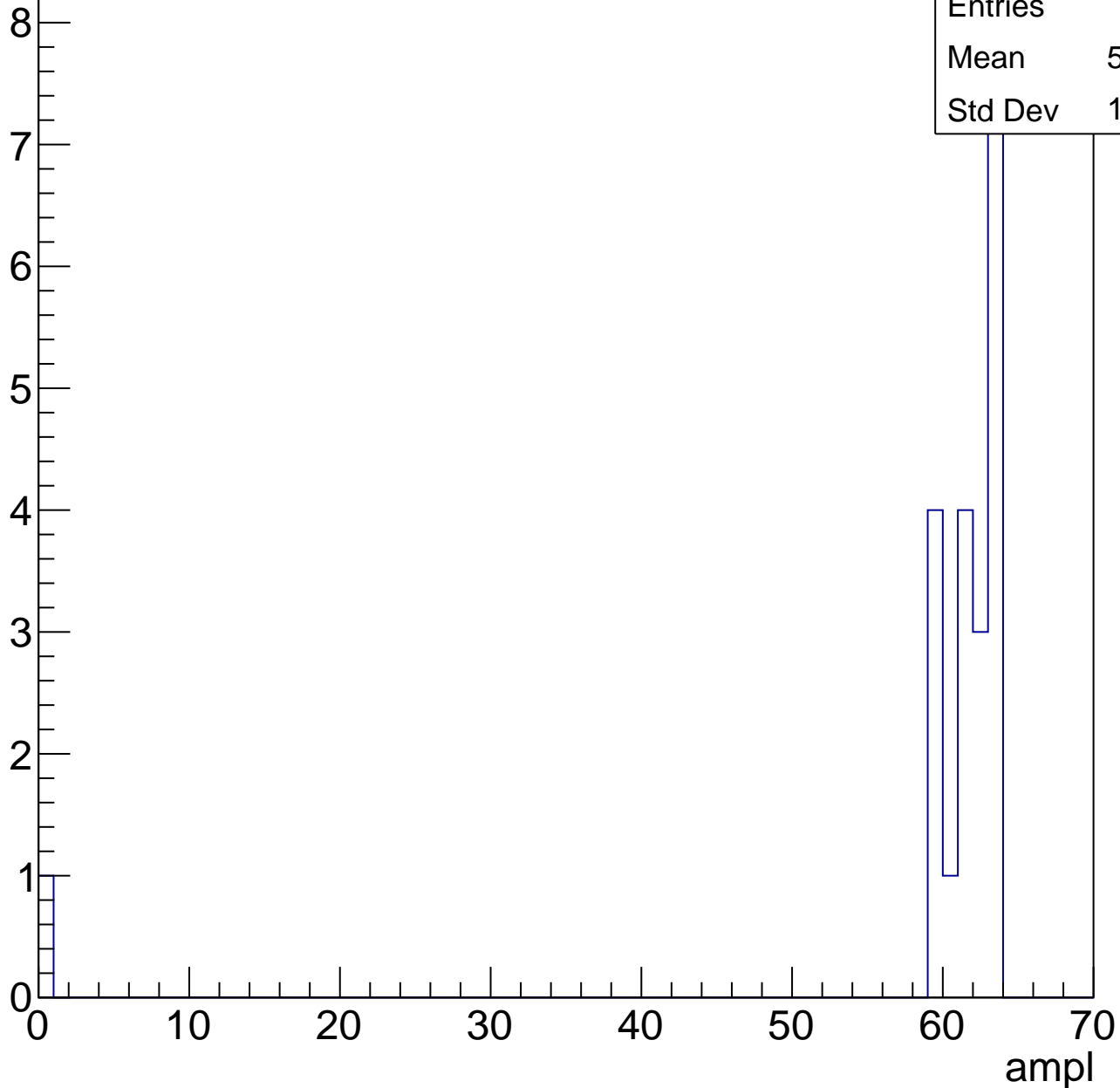


# B0L101S, U16-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 58.57 |
| Std Dev | 13.18 |





# B0L101S, U16-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 43 |
| Std Dev | 20 |

ampl

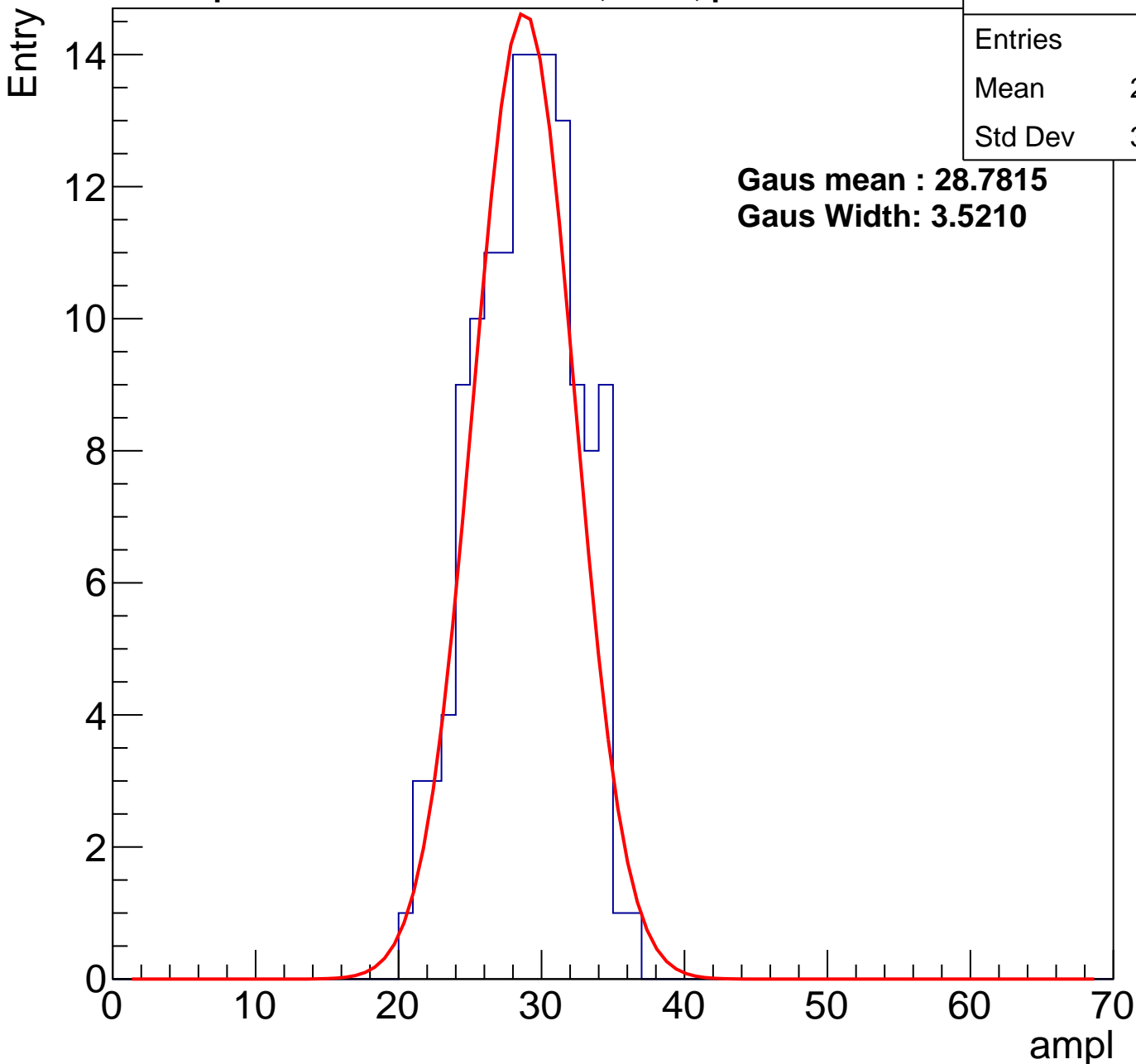
# B0L101S, U16-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 28.44 |
| Std Dev | 3.495 |

**Gaus mean : 28.7815**

**Gaus Width: 3.5210**



# B0L101S, U16-ch46, adc1

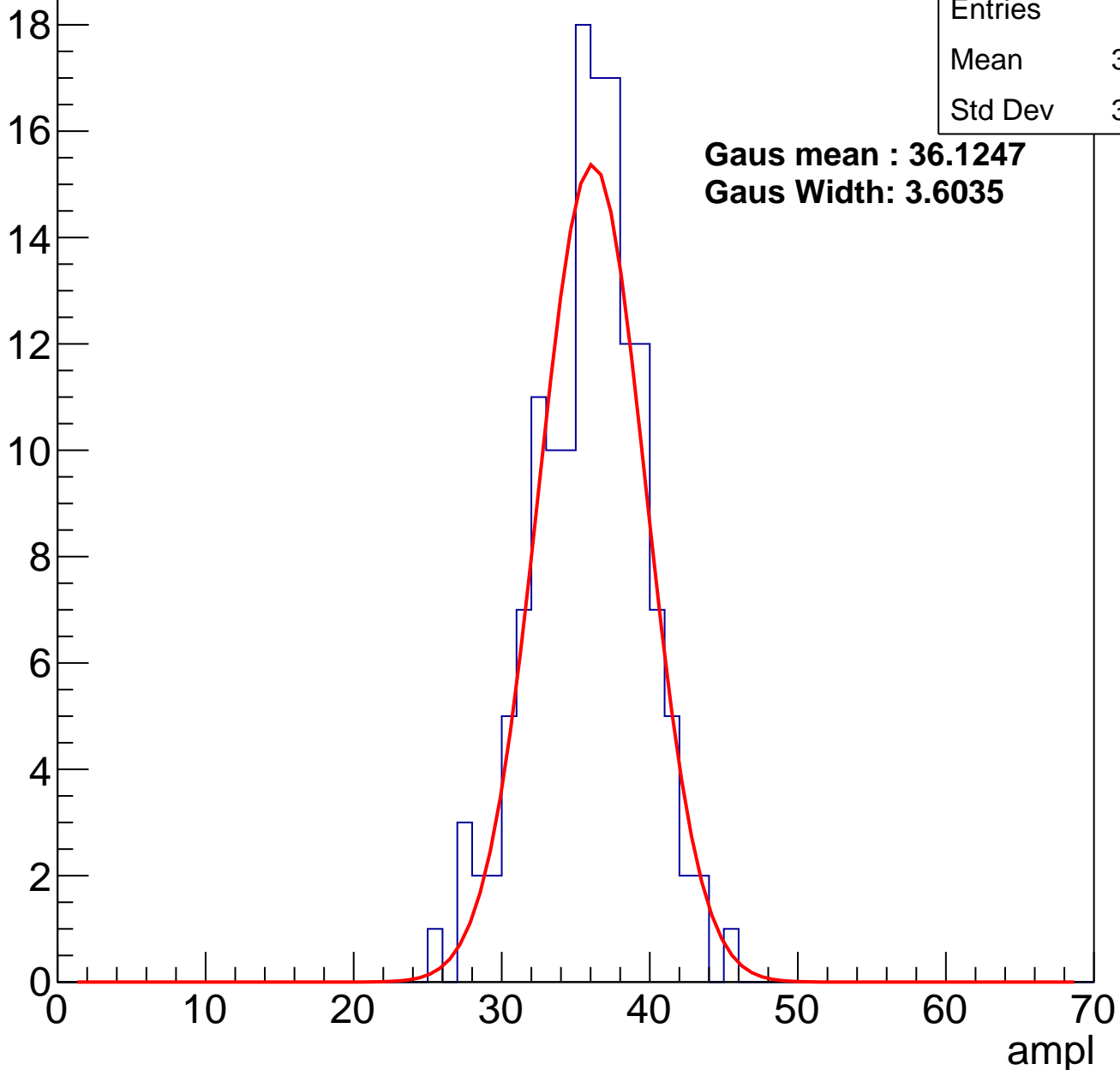
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 35.44 |
| Std Dev | 3.619 |

**Gaus mean : 36.1247**

**Gaus Width: 3.6035**

Entry



# B0L101S, U16-ch46, adc2

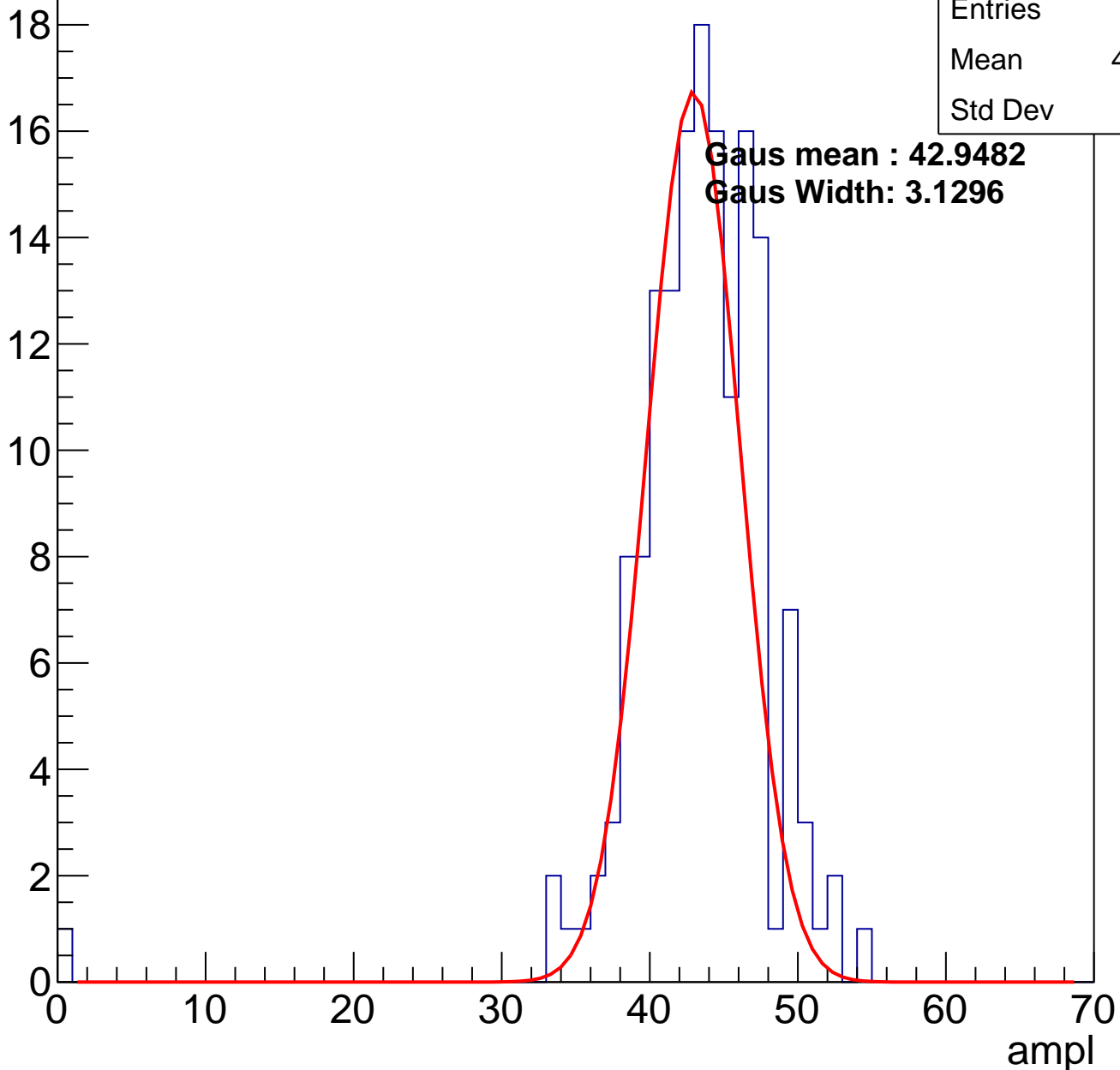
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 42.89 |
| Std Dev | 5.1   |

**Gaus mean : 42.9482**

**Gaus Width: 3.1296**

Entry



# B0L101S, U16-ch46, adc3

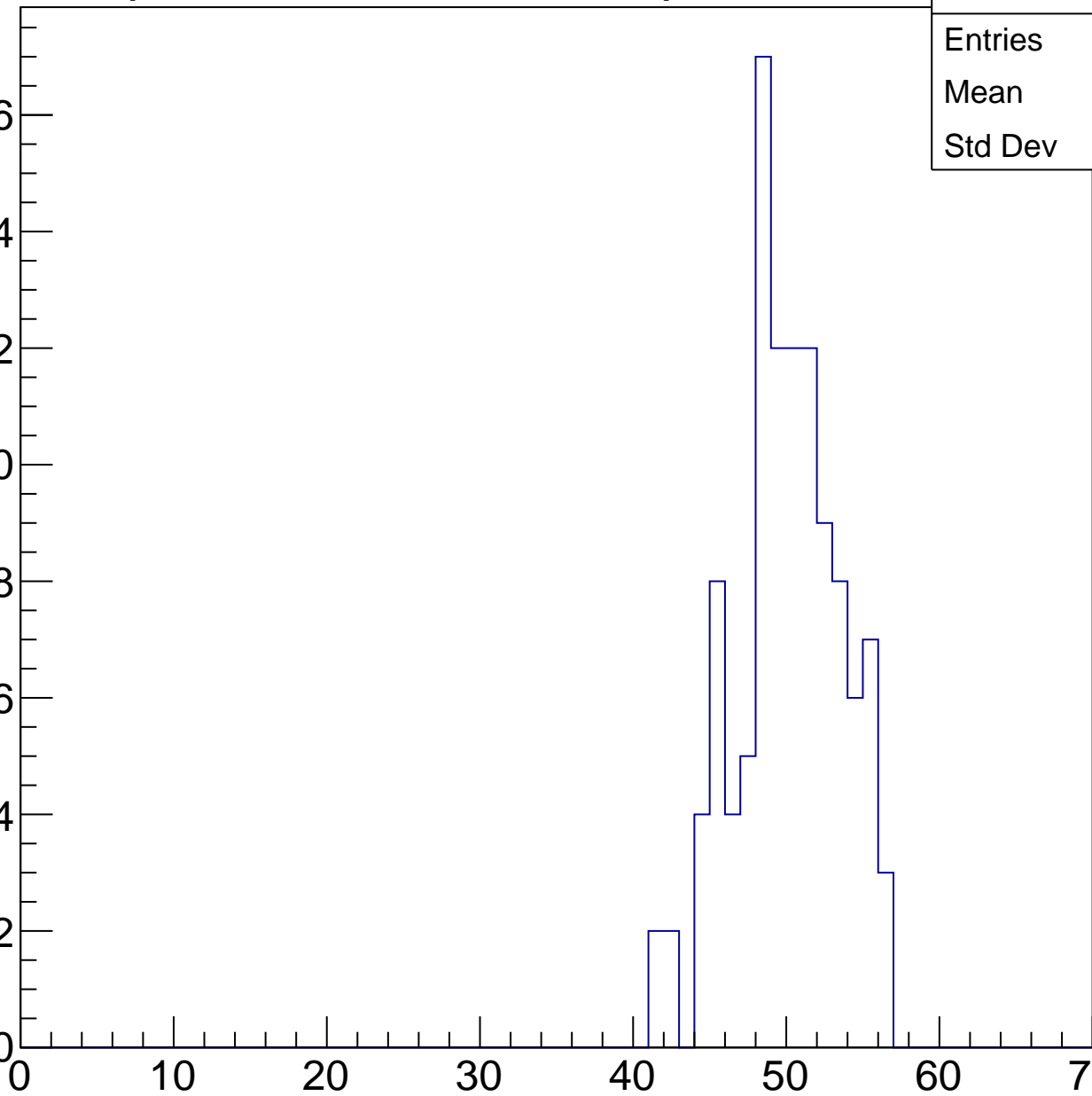
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 49.6  |
| Std Dev | 3.428 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

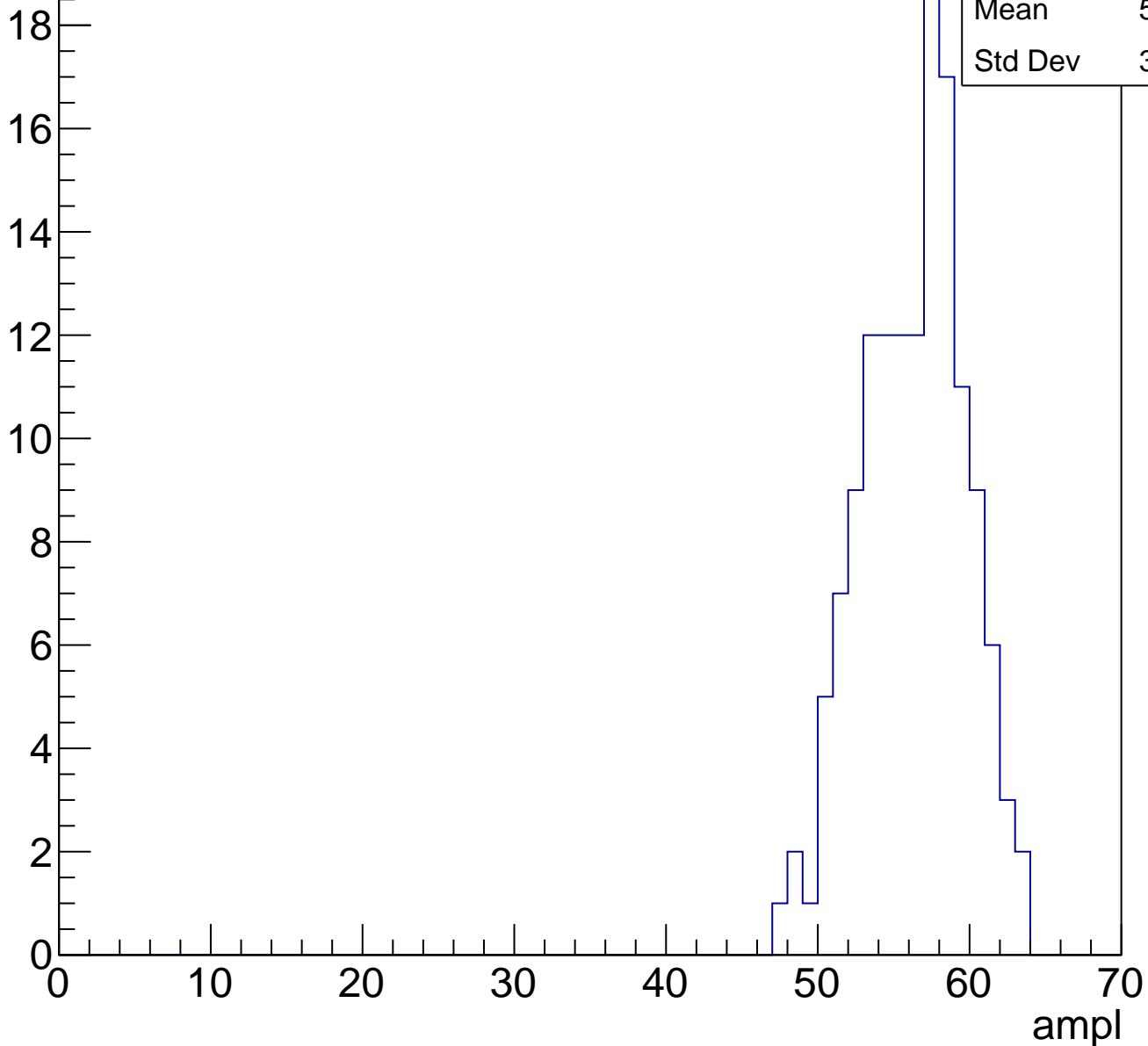


# B0L101S, U16-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 55.85 |
| Std Dev | 3.389 |

Entry



# B0L101S, U16-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 59.56 |
| Std Dev | 7.874 |

ampl

0

10

20

30

40

50

60

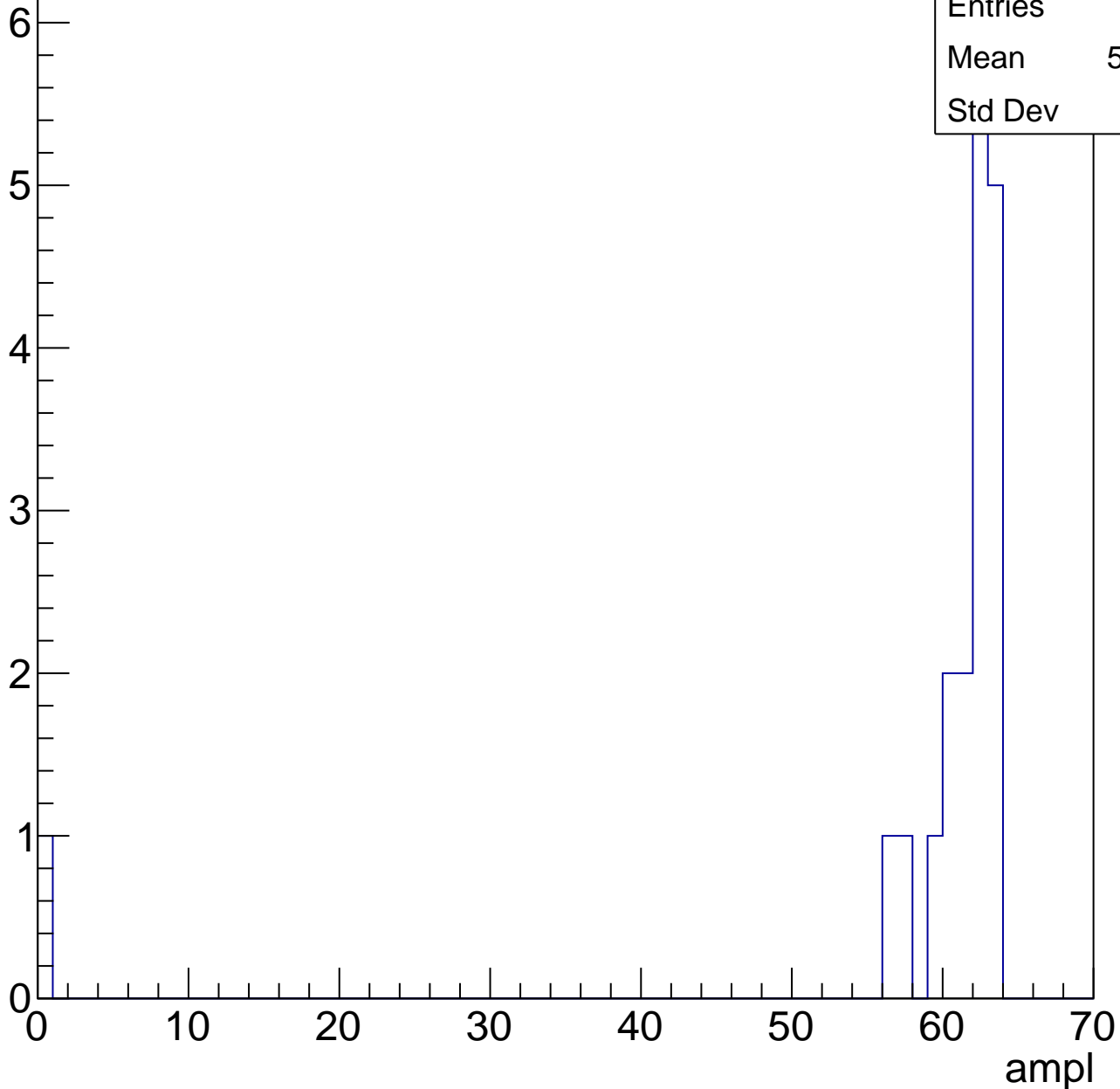
70

# B0L101S, U16-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 19    |
| Mean    | 57.95 |
| Std Dev | 13.8  |





# B0L101S, U16-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 10 |
| Std Dev | 10 |

# B0L101S, U16-ch47, adc0

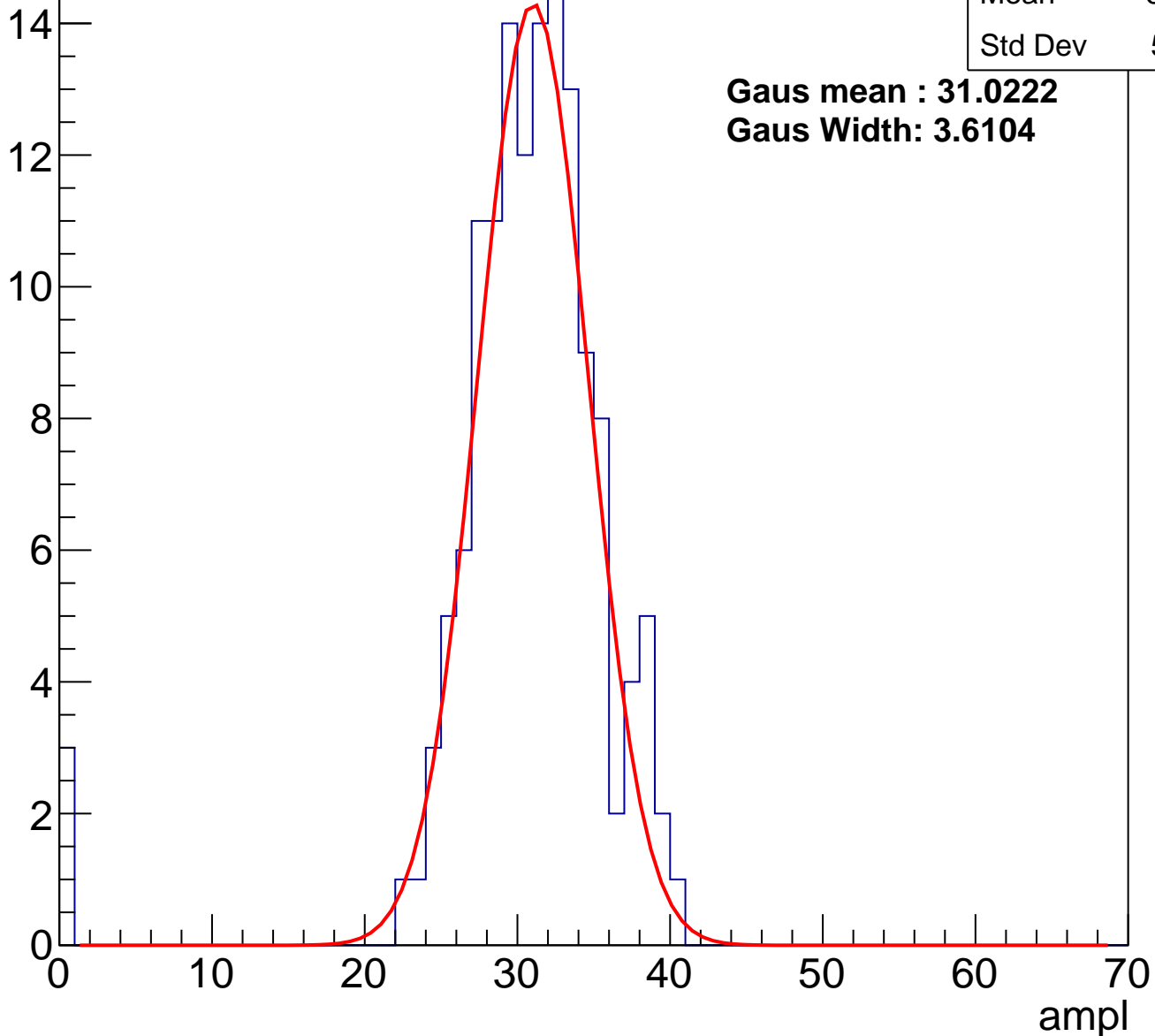
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 30.19 |
| Std Dev | 5.781 |

**Gaus mean : 31.0222**

**Gaus Width: 3.6104**

Entry



# B0L101S, U16-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

144

Mean

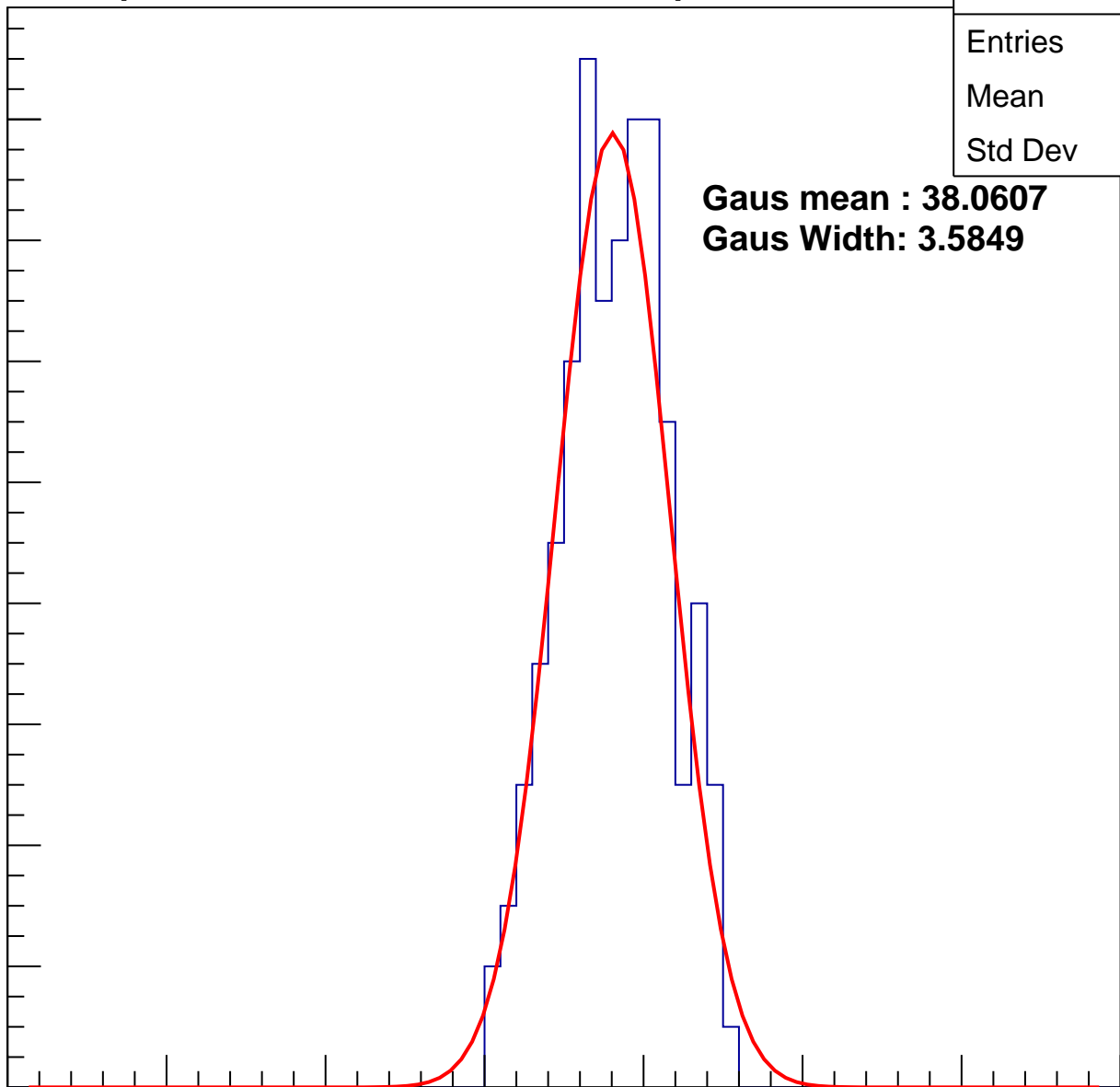
37.7

Std Dev

3.373

**Gaus mean : 38.0607**

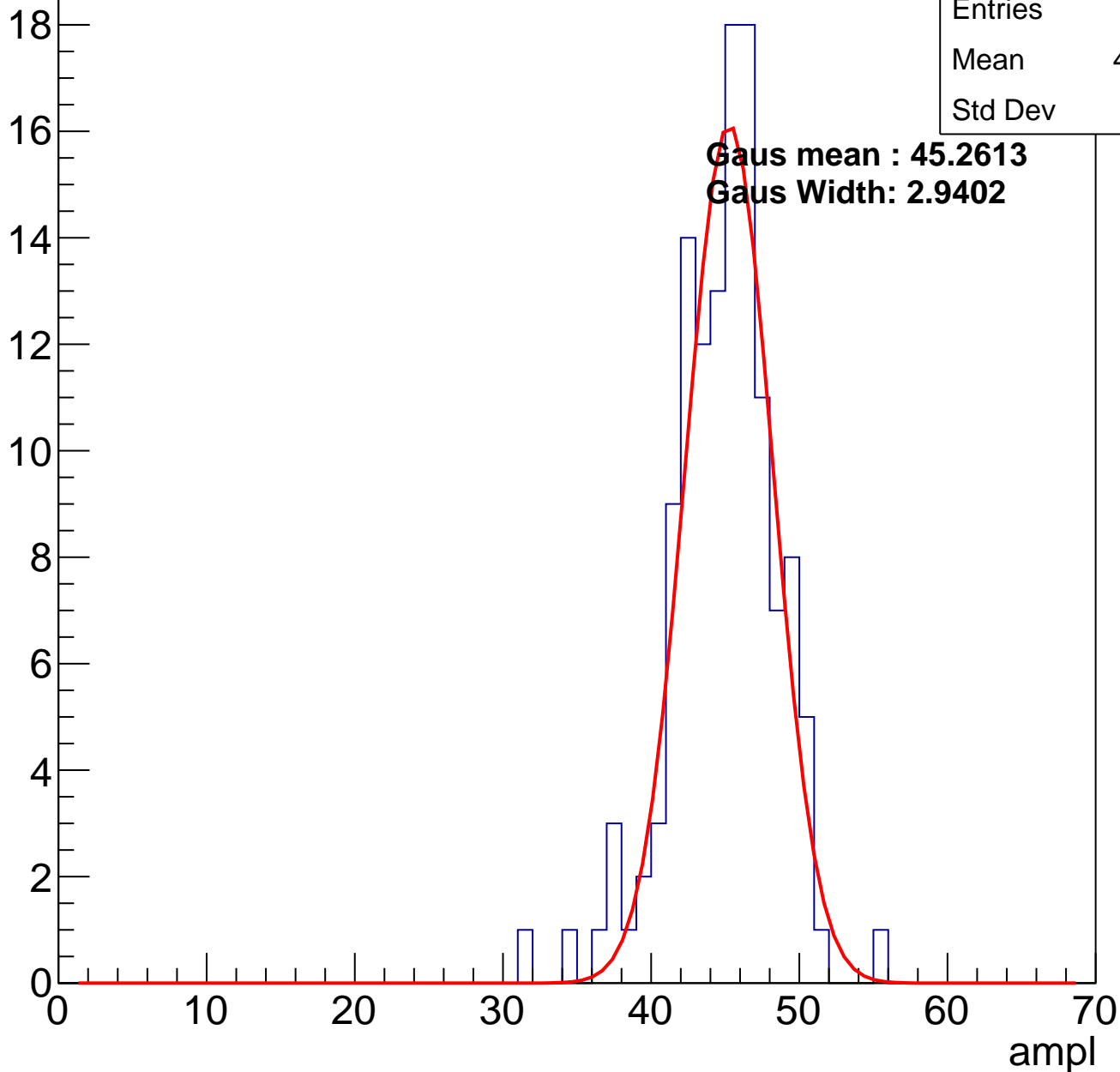
**Gaus Width: 3.5849**



# B0L101S, U16-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

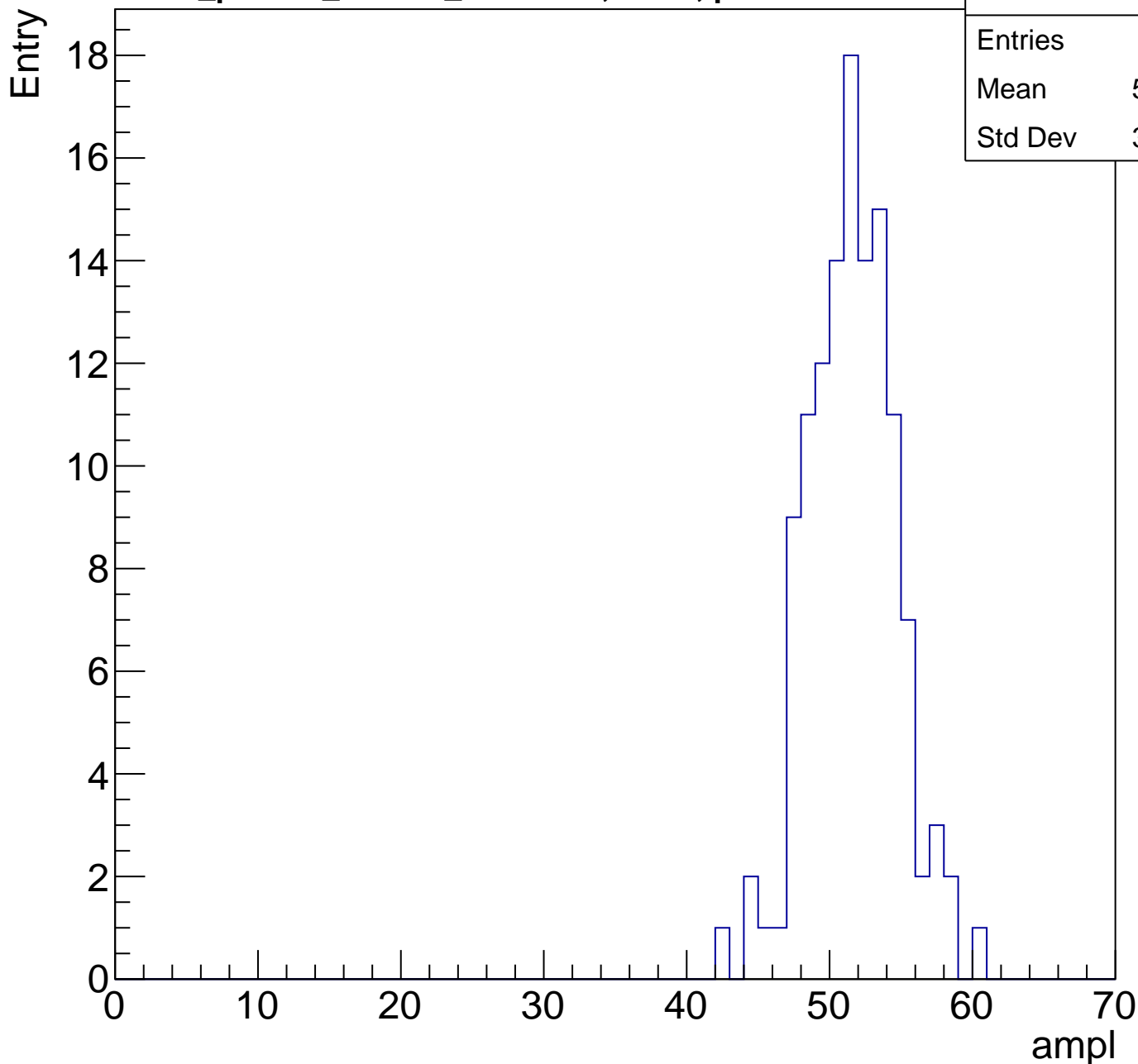
Entry



# B0L101S, U16-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 51.12 |
| Std Dev | 3.065 |



# B0L101S, U16-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

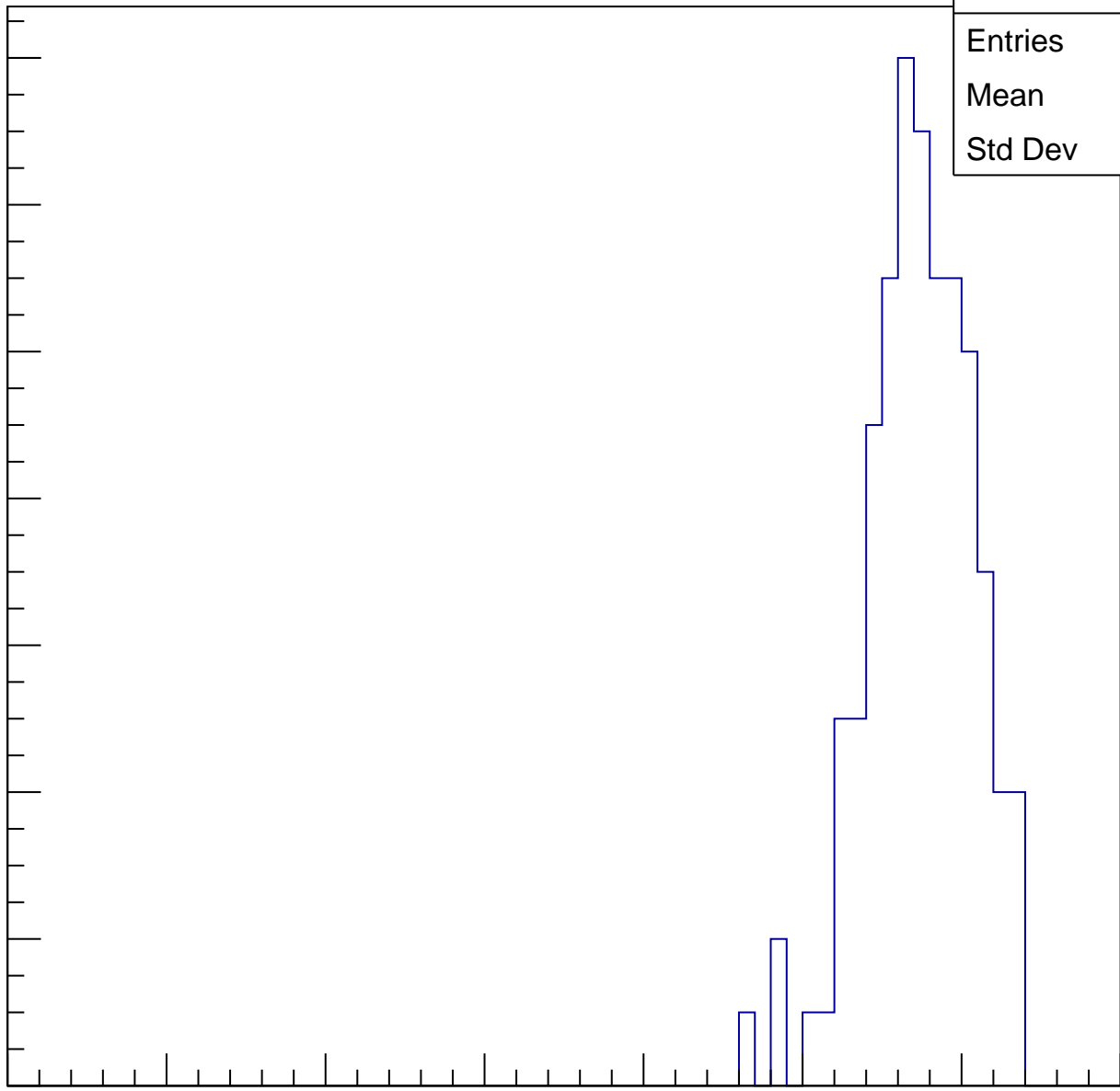
|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 56.86 |
| Std Dev | 3.333 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch47, adc5

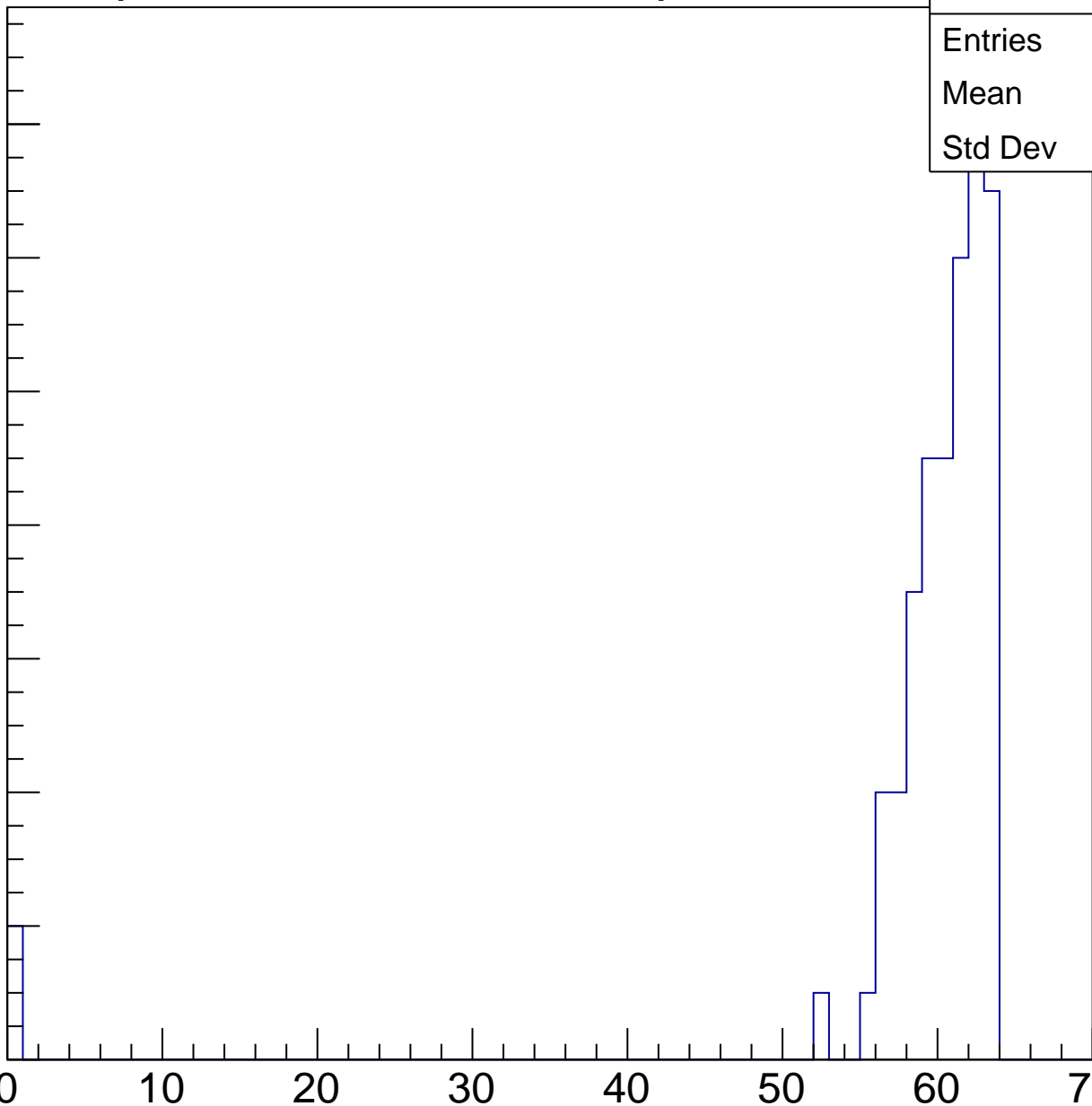
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 58.66 |
| Std Dev | 9.854 |

ampl



# B0L101S, U16-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch48, adc0

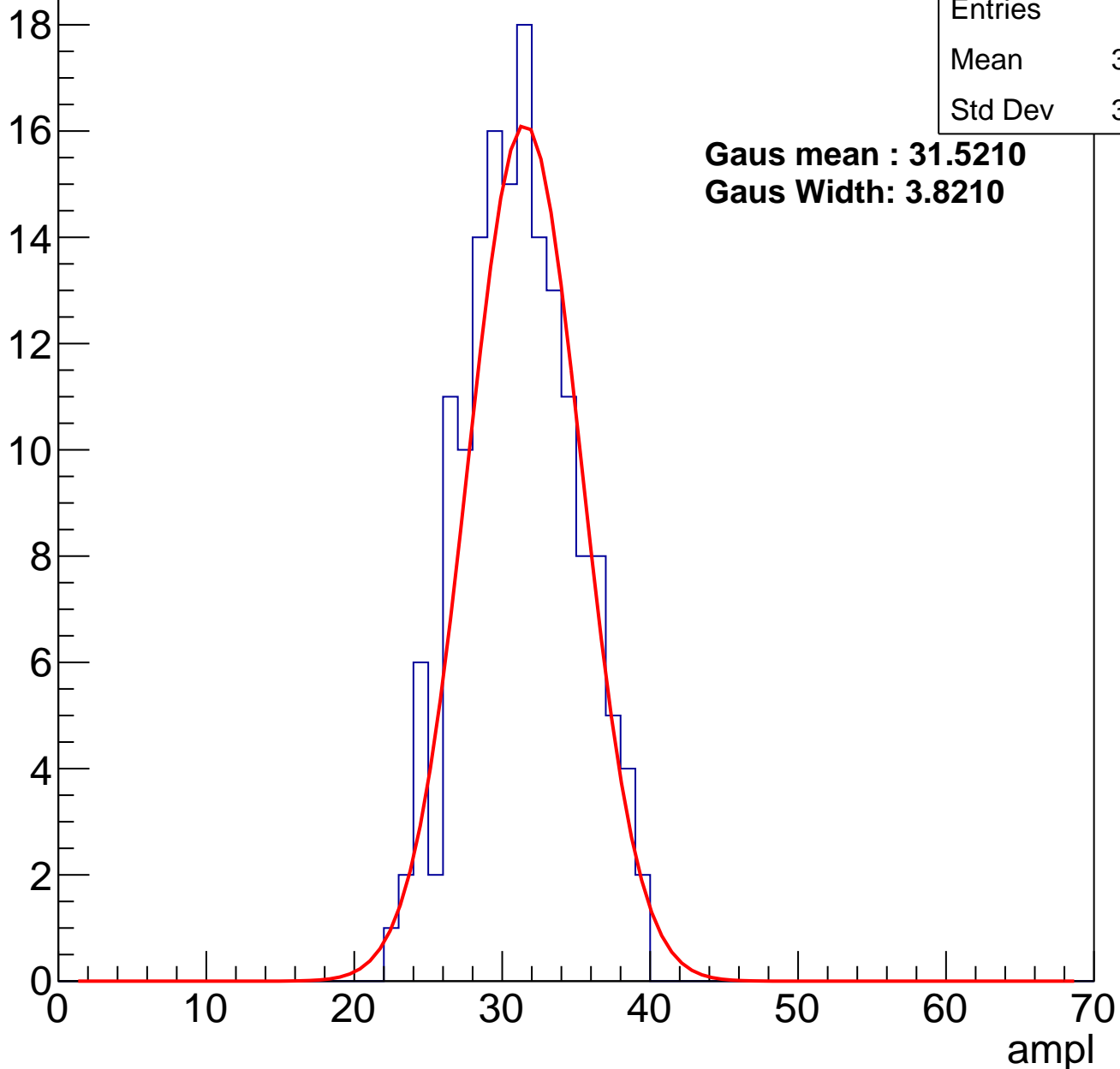
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 30.73 |
| Std Dev | 3.708 |

**Gaus mean : 31.5210**

**Gaus Width: 3.8210**

Entry

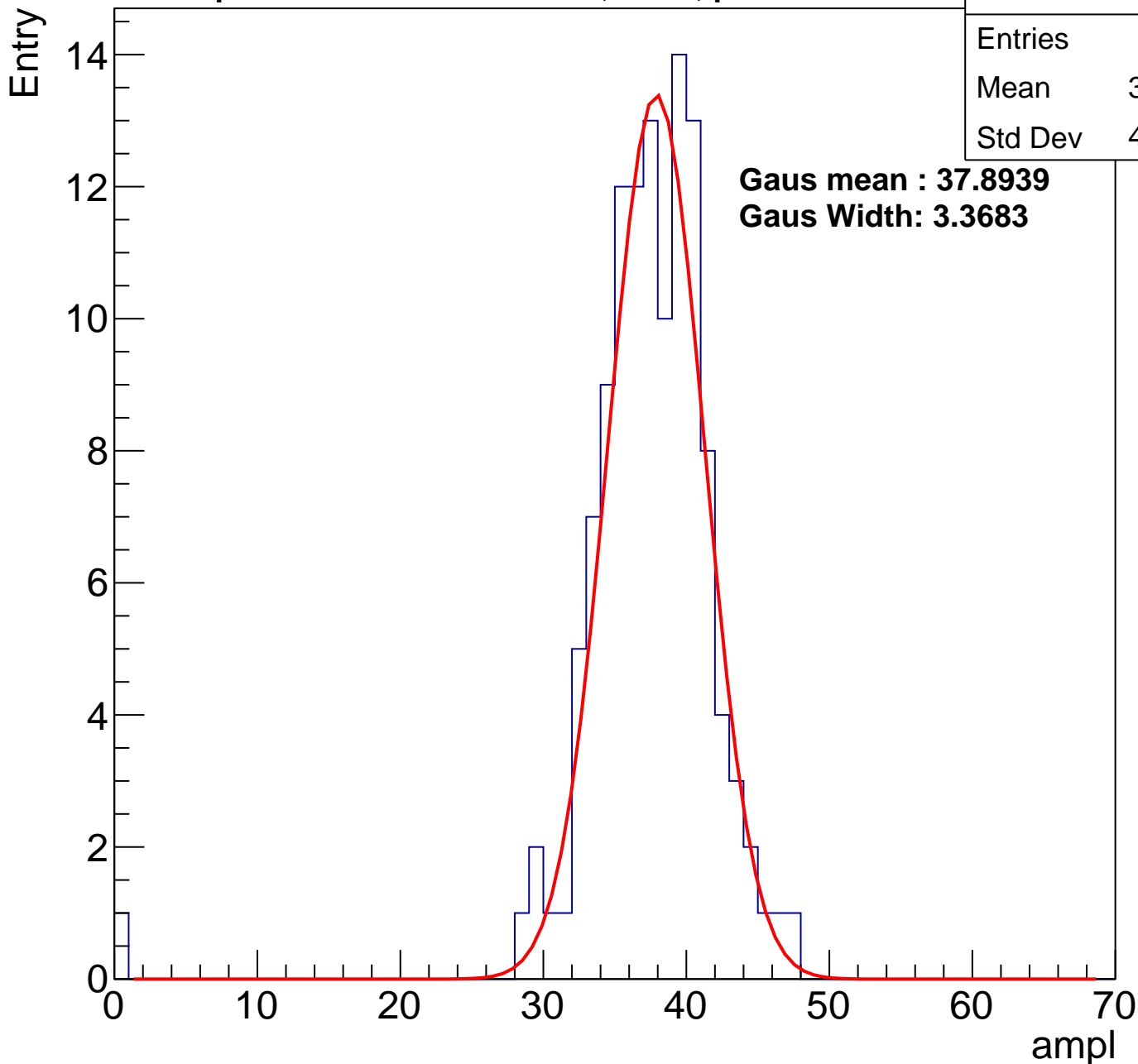


# B0L101S, U16-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 36.98 |
| Std Dev | 4.898 |

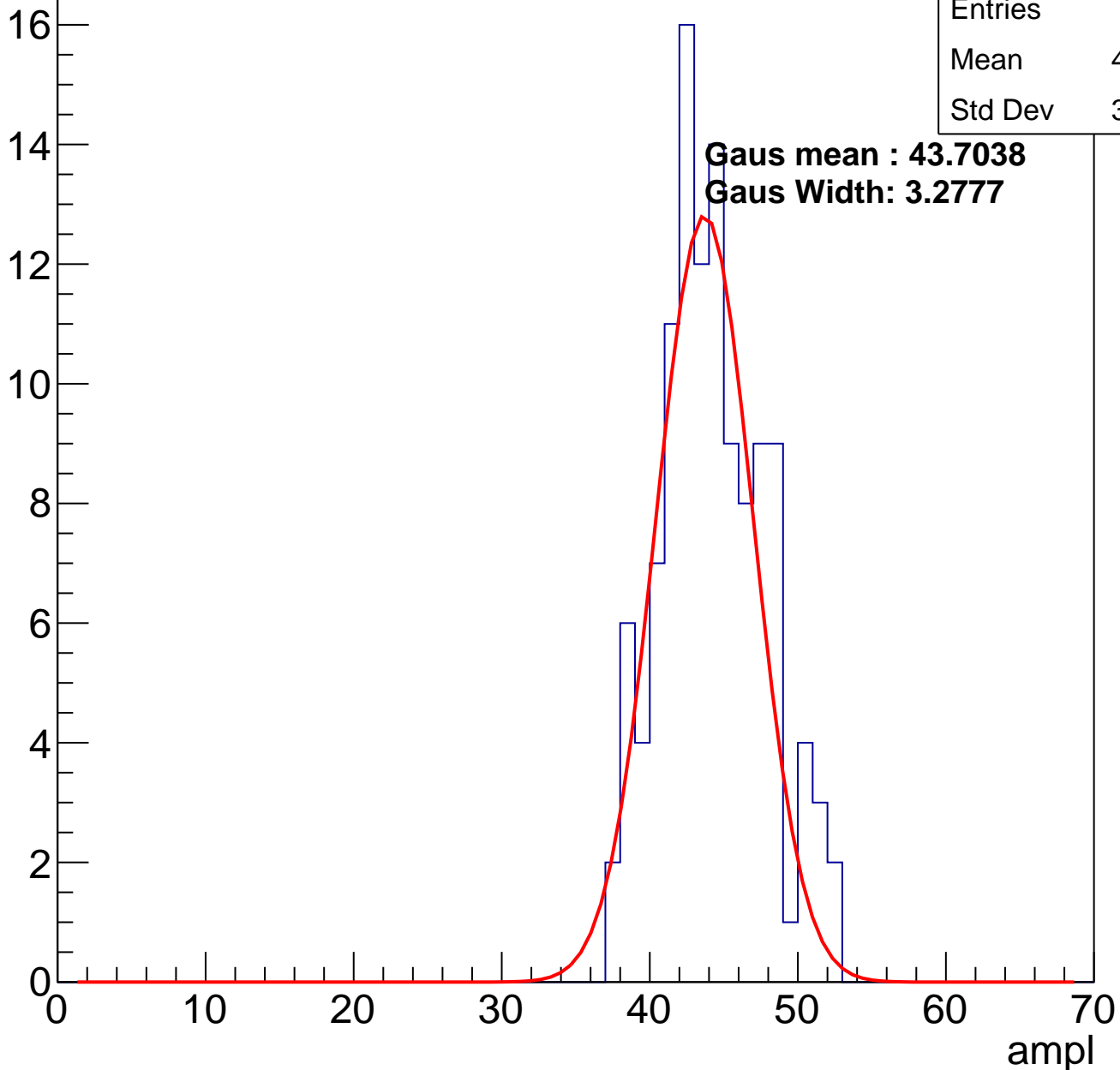
**Gaus mean : 37.8939**  
**Gaus Width: 3.3683**



# B0L101S, U16-ch48, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

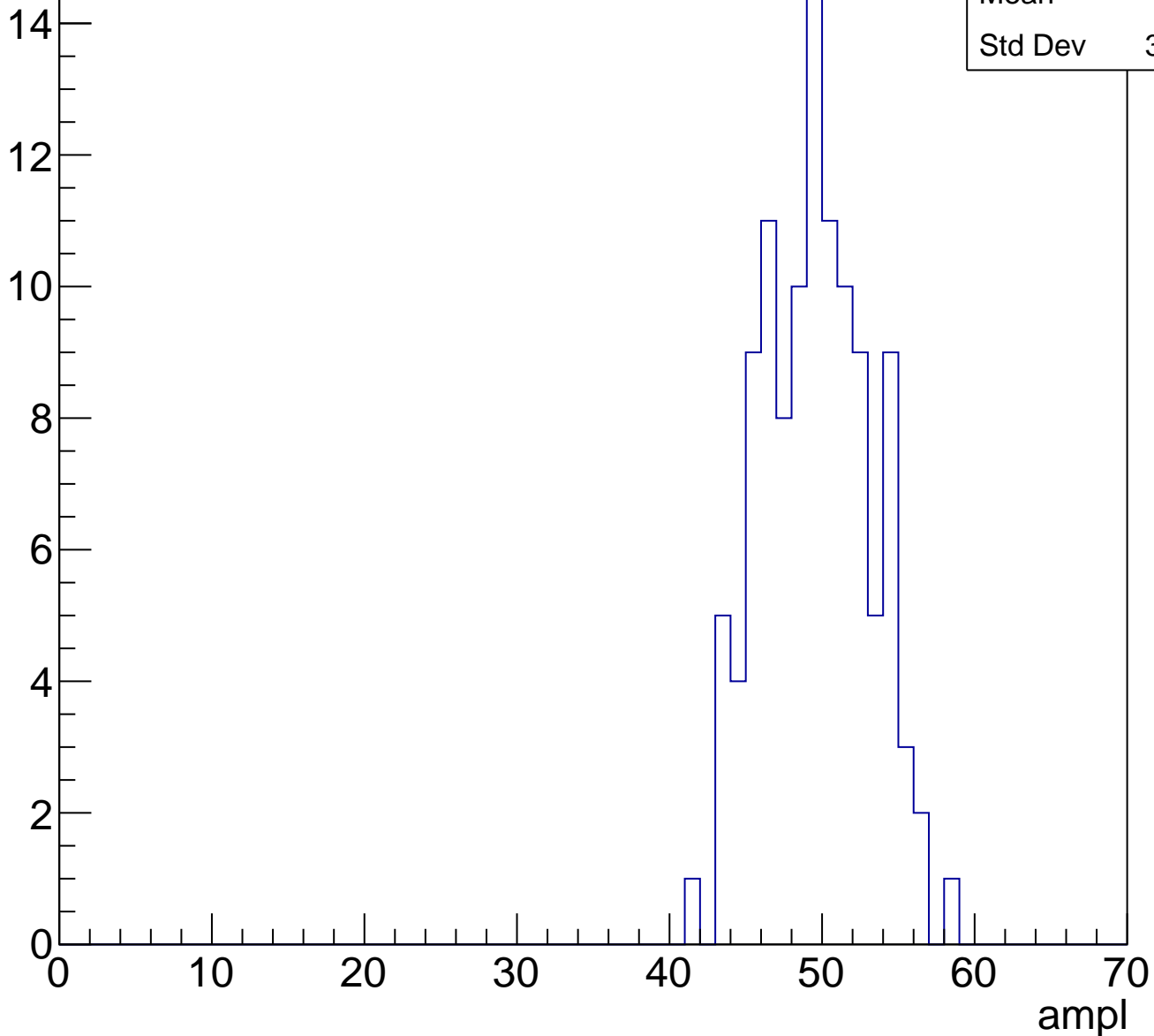


# B0L101S, U16-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 49.1  |
| Std Dev | 3.459 |

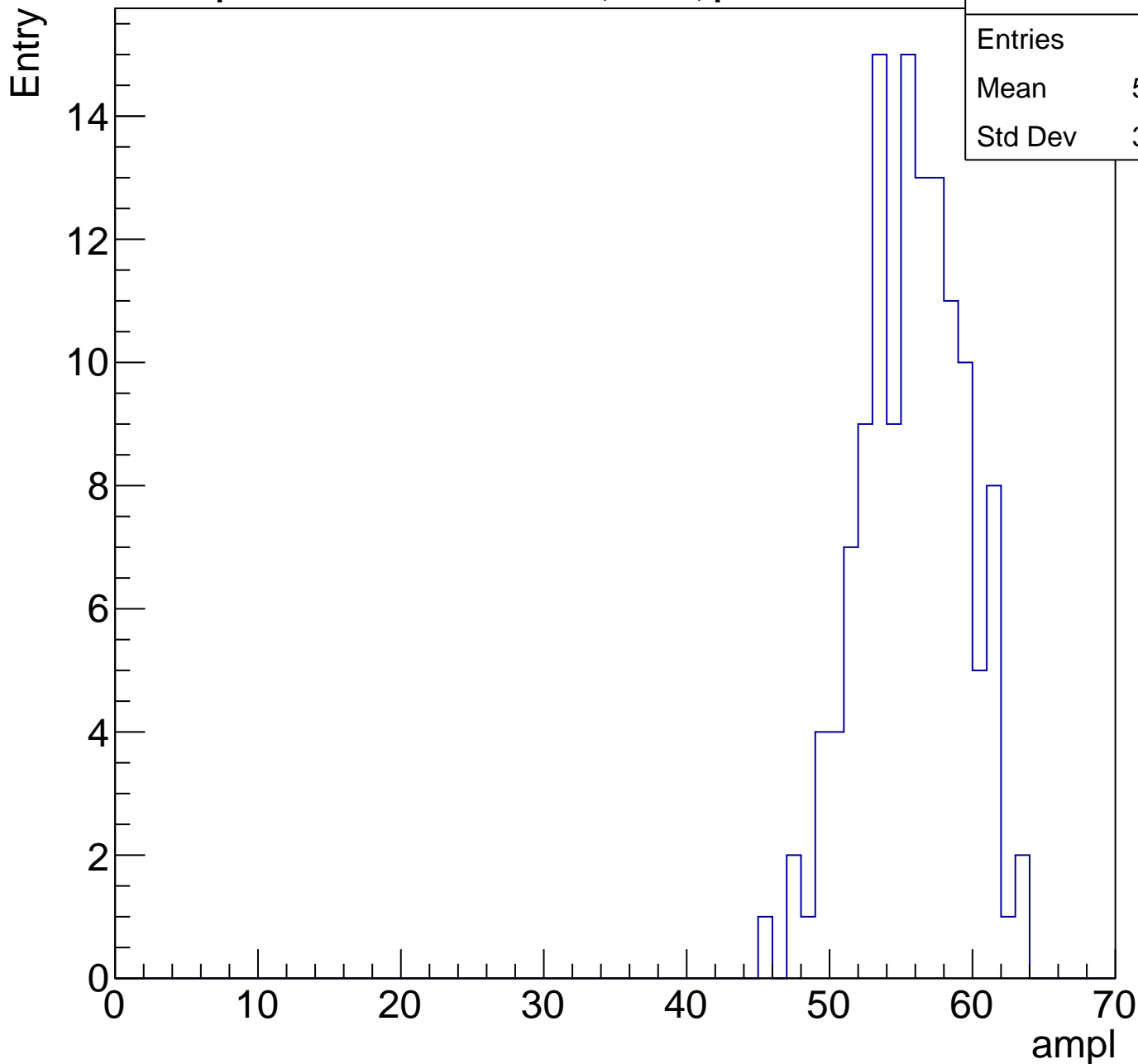
Entry



# B0L101S, U16-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

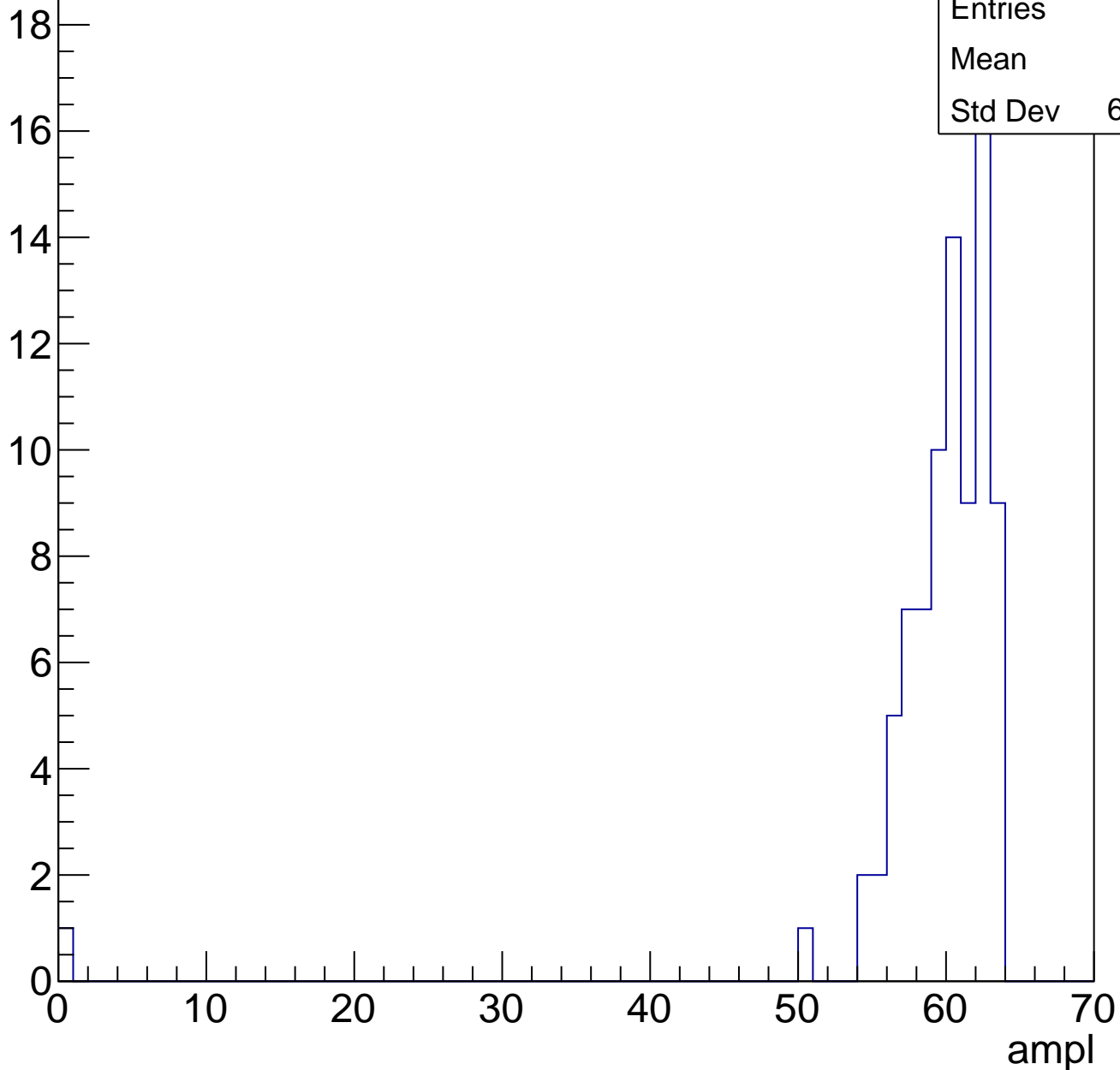
|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 55.28 |
| Std Dev | 3.602 |



# B0L101S, U16-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

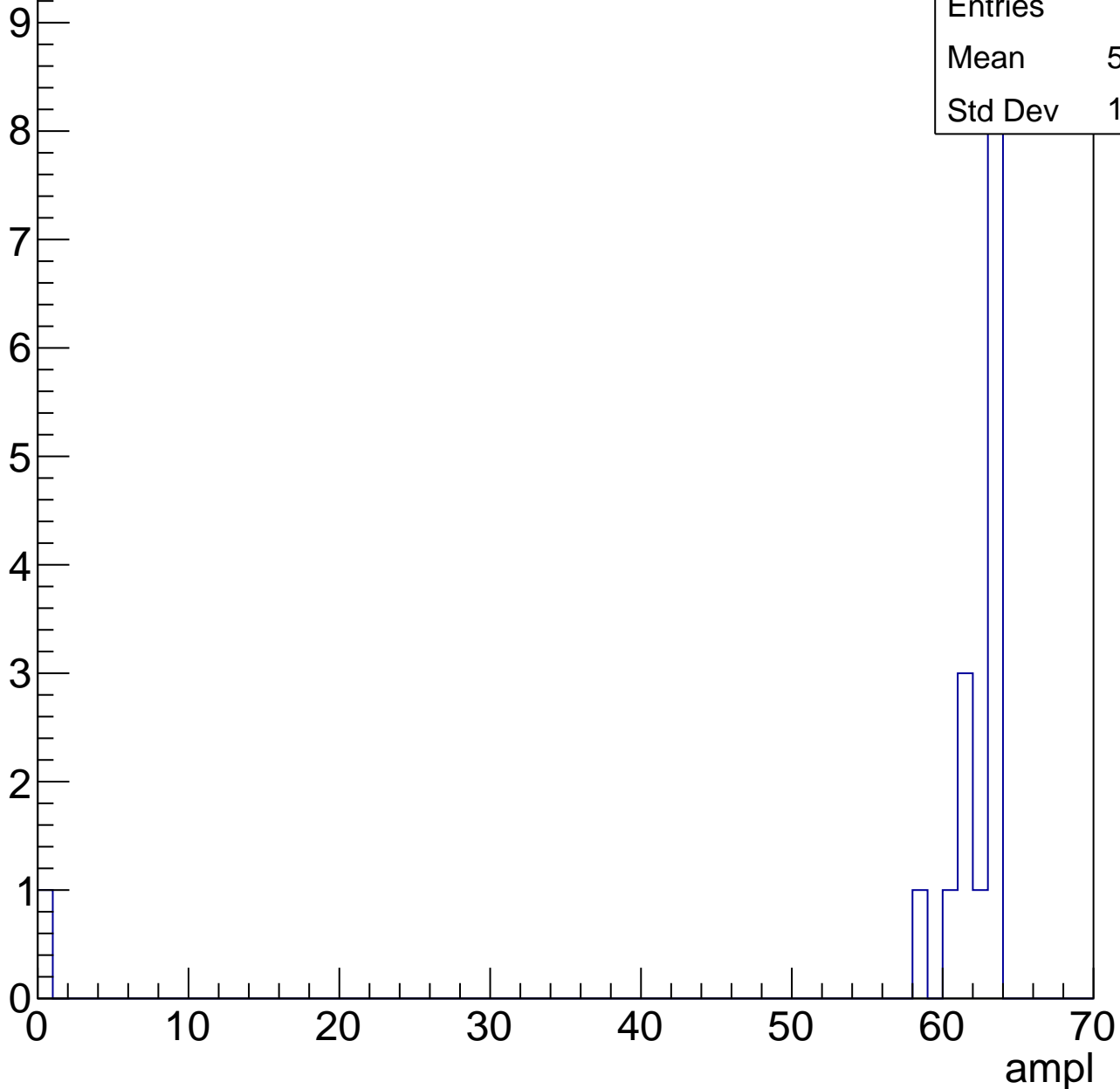


# B0L101S, U16-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 16    |
| Mean    | 58.12 |
| Std Dev | 15.07 |





# B0L101S, U16-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch49, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 30.59 |
| Std Dev | 3.39  |

**Gaus mean : 31.1919**

**Gaus Width: 3.4552**

Entry

12

10

8

6

4

2

0

0

10

20

30

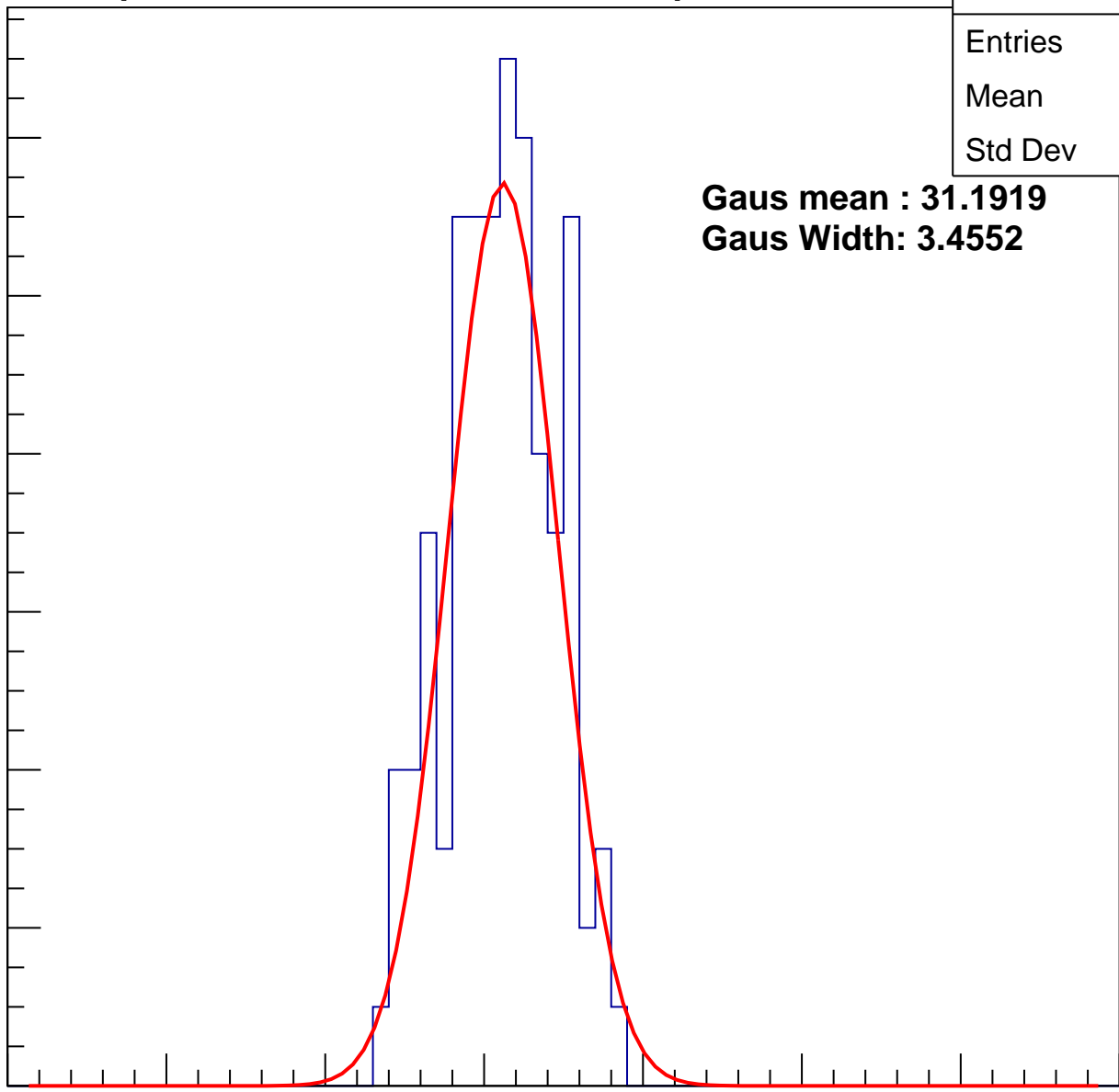
40

50

60

70

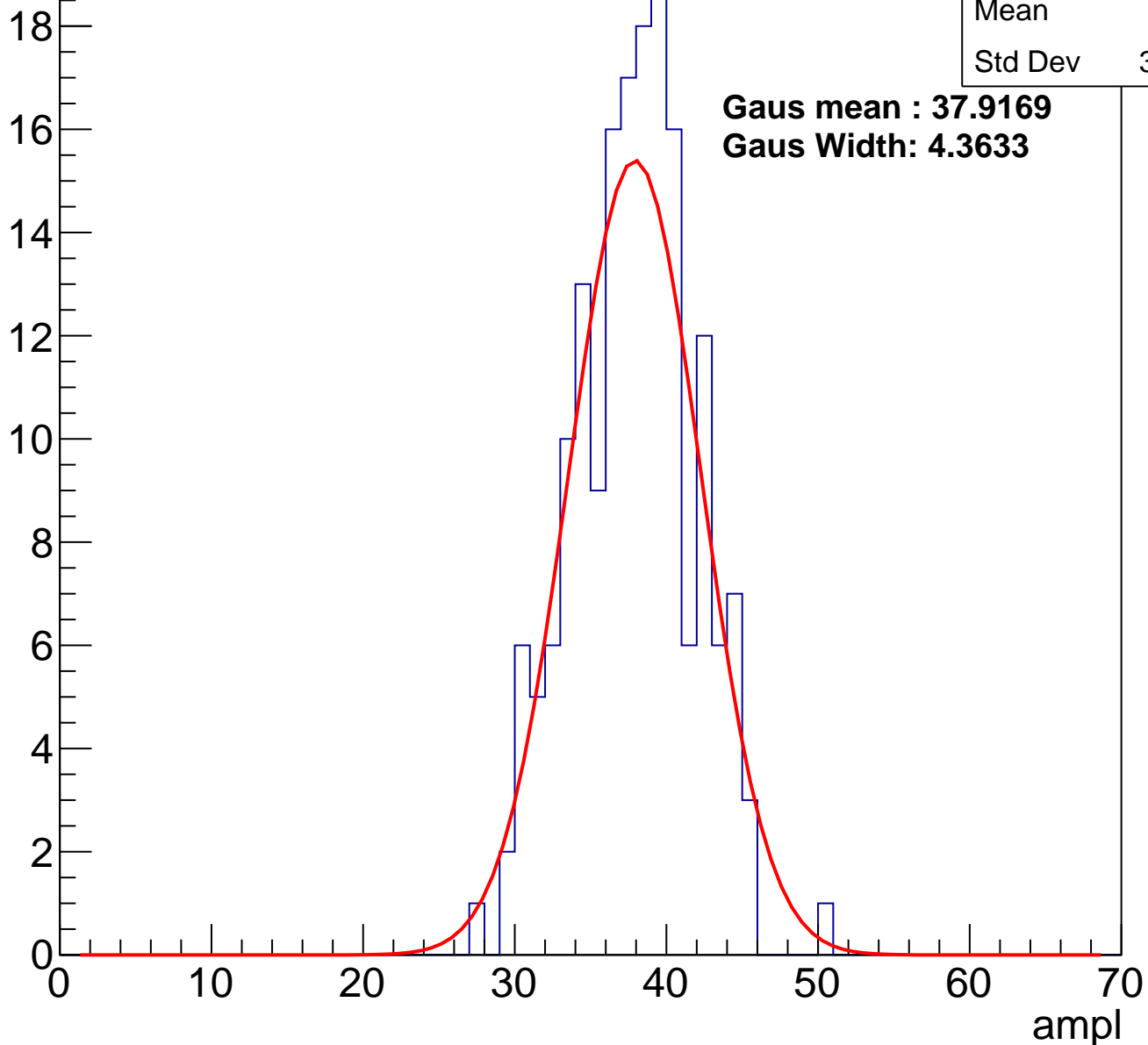
ampl



# B0L101S, U16-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



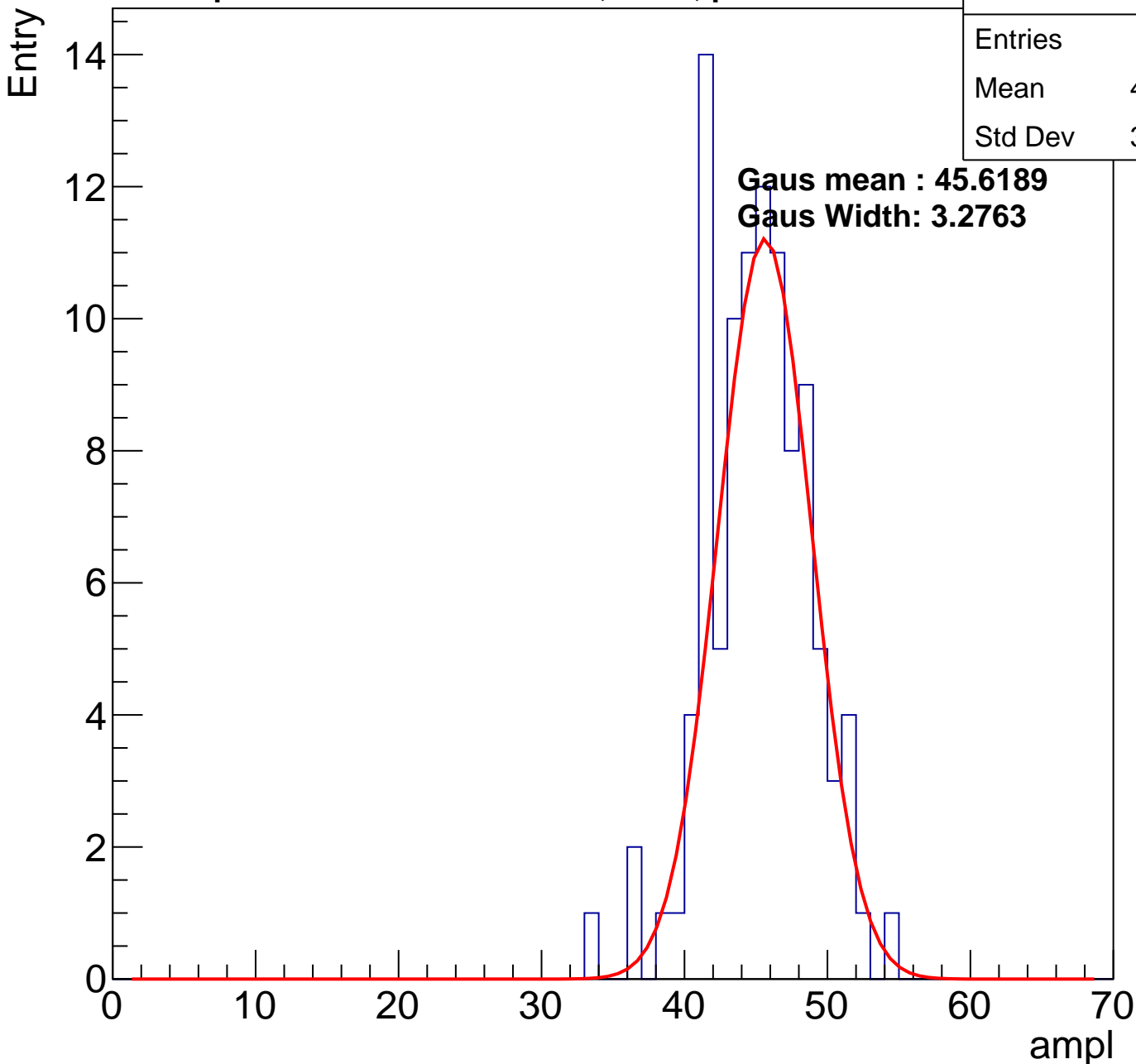
# B0L101S, U16-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 103   |
| Mean    | 44.65 |
| Std Dev | 3.622 |

**Gaus mean : 45.6189**

**Gaus Width: 3.2763**



# B0L101S, U16-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

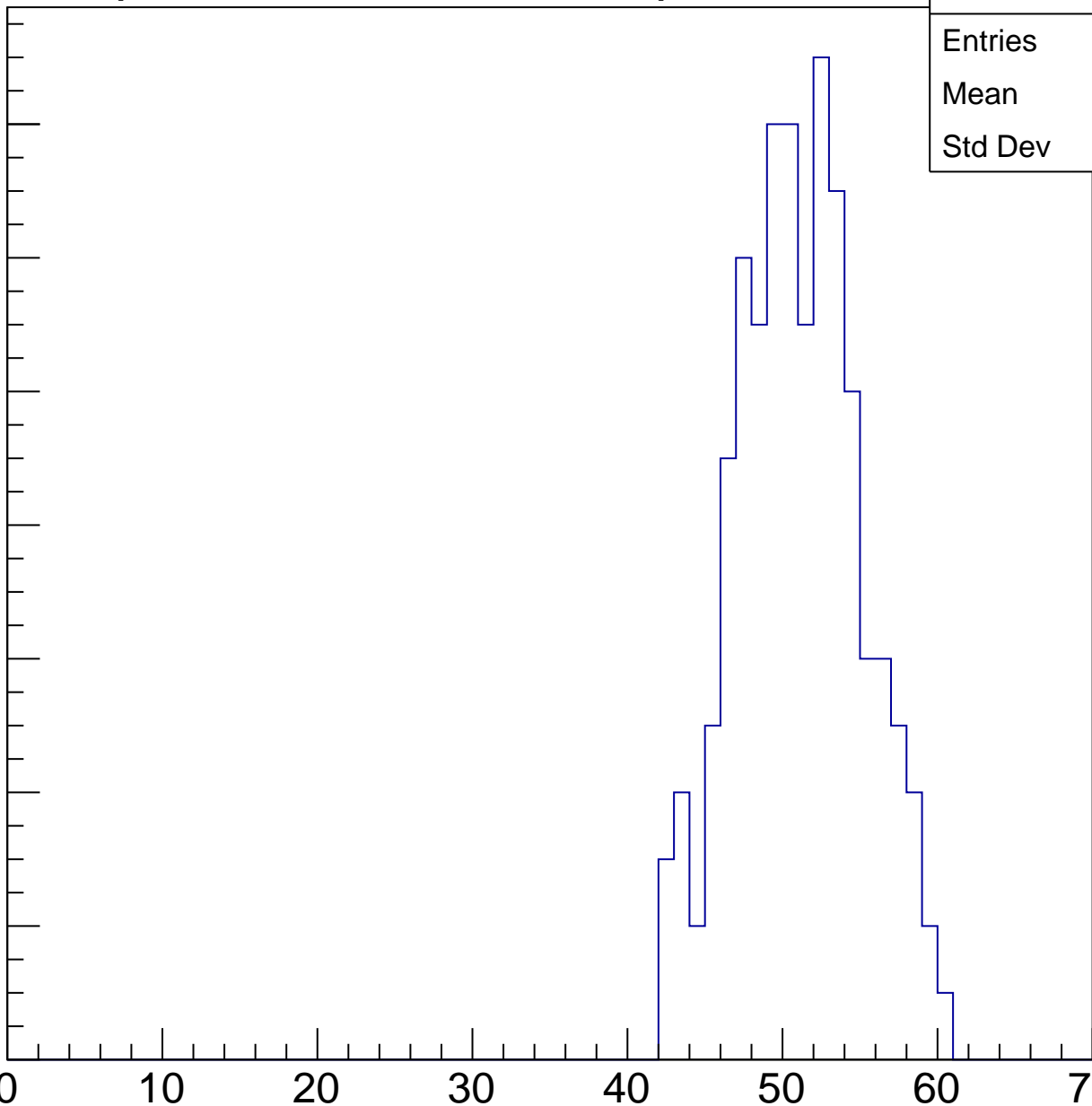
|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 50.57 |
| Std Dev | 3.976 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

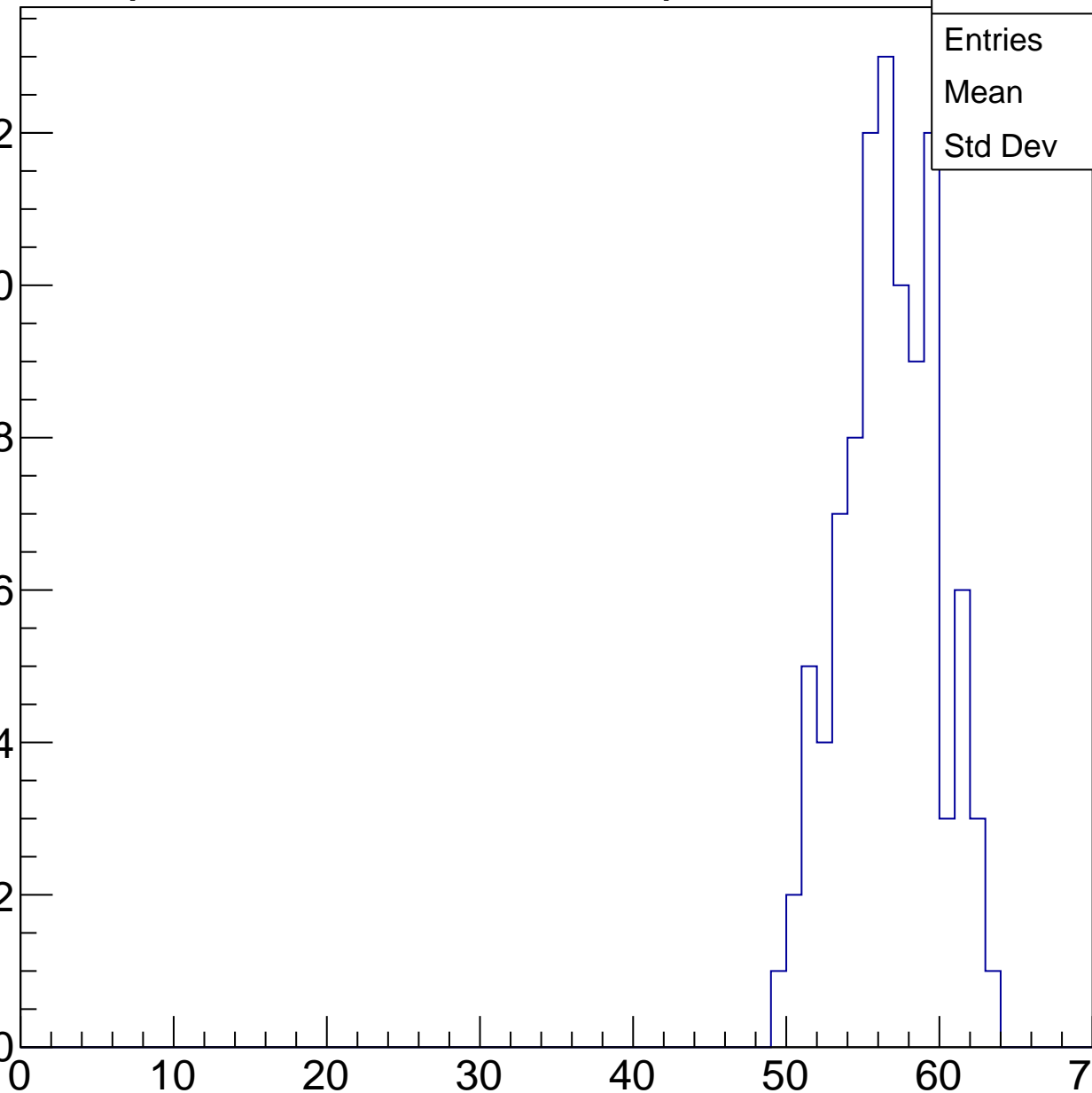
4

2

0

|         |       |
|---------|-------|
| Entries | 96    |
| Mean    | 56.23 |
| Std Dev | 3.097 |

ampl



# B0L101S, U16-ch49, adc5

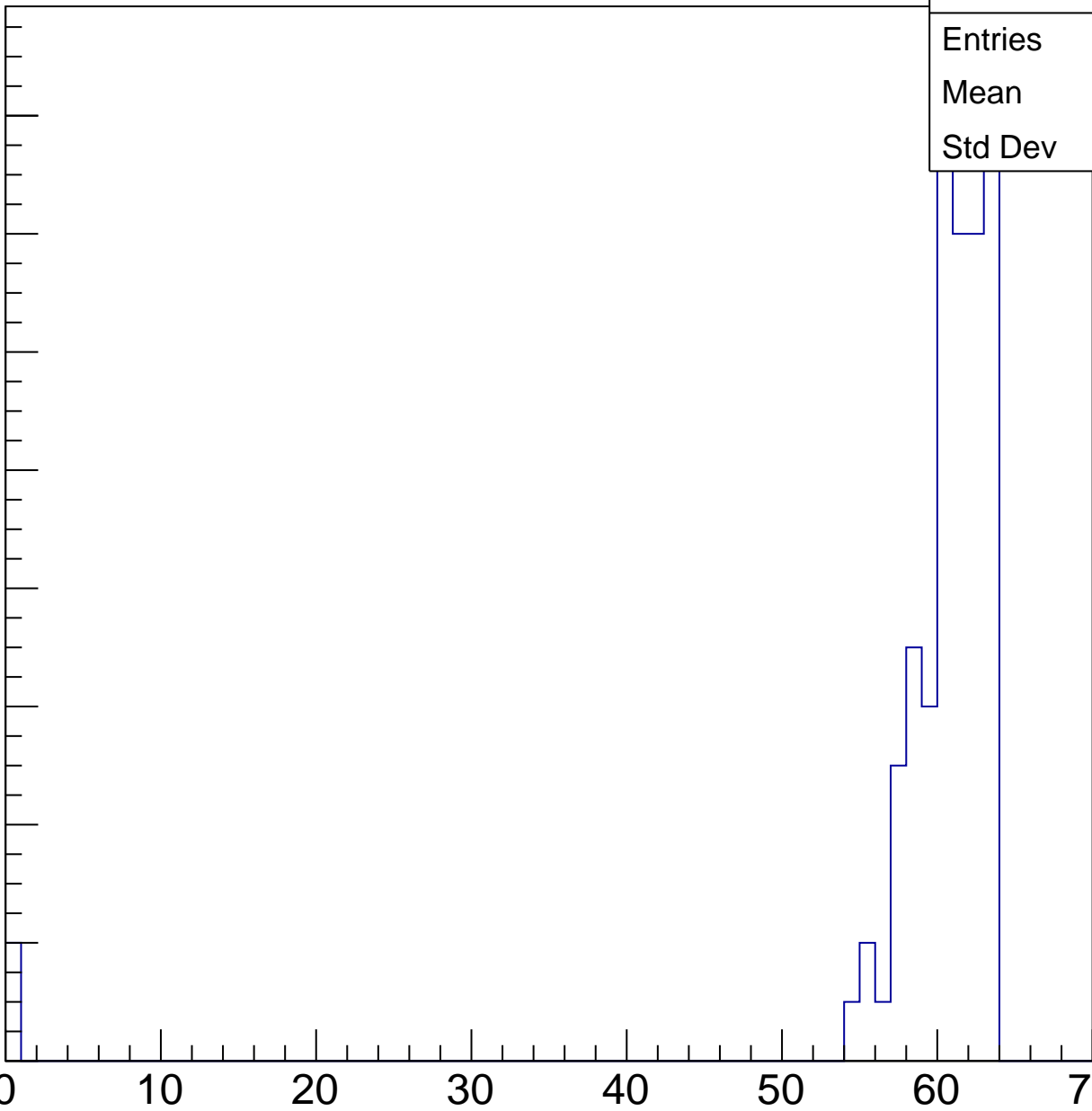
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 85    |
| Mean    | 59.04 |
| Std Dev | 9.409 |

ampl



# B0L101S, U16-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch50, adc0

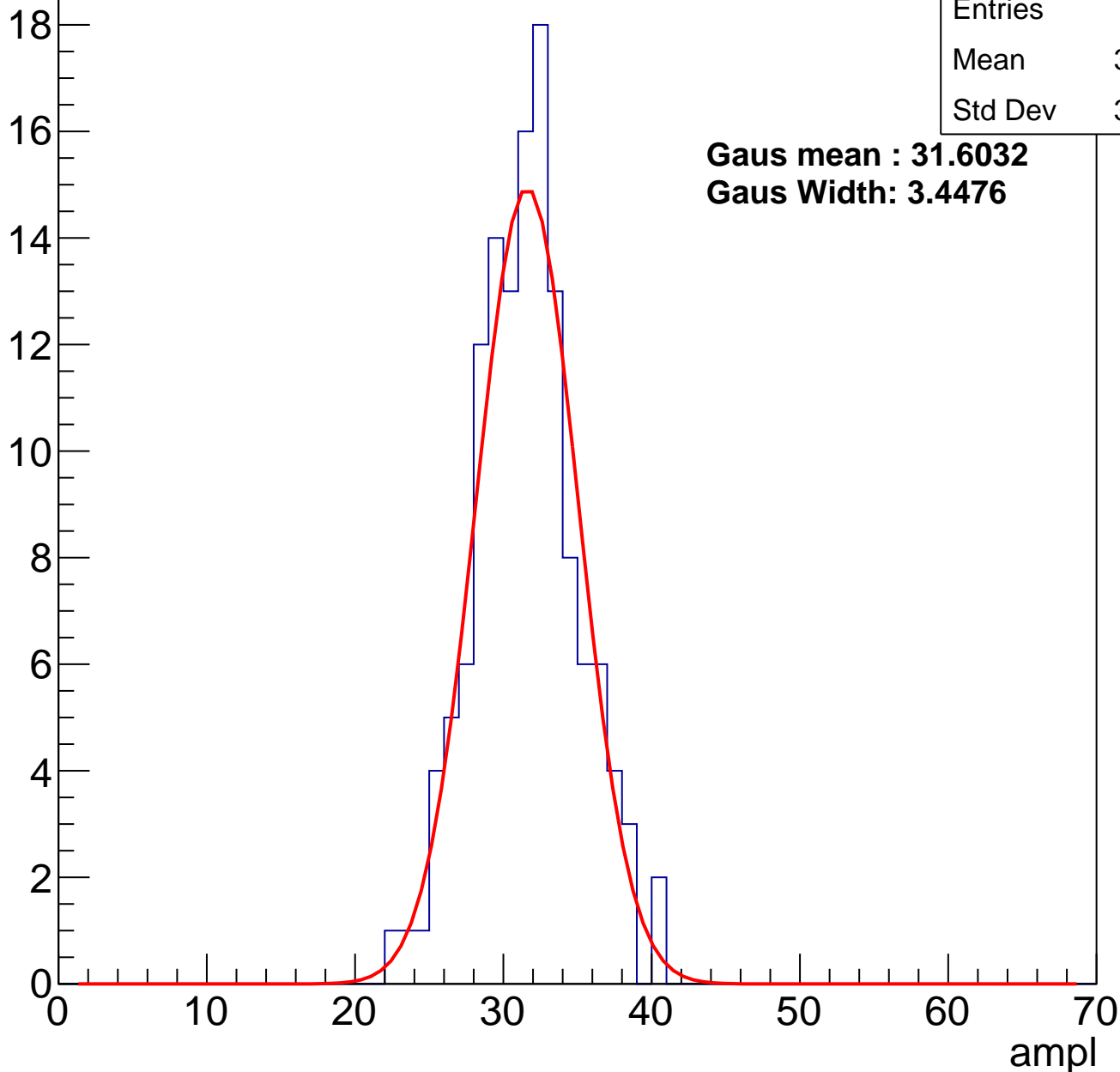
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 31.08 |
| Std Dev | 3.442 |

**Gaus mean : 31.6032**

**Gaus Width: 3.4476**

Entry



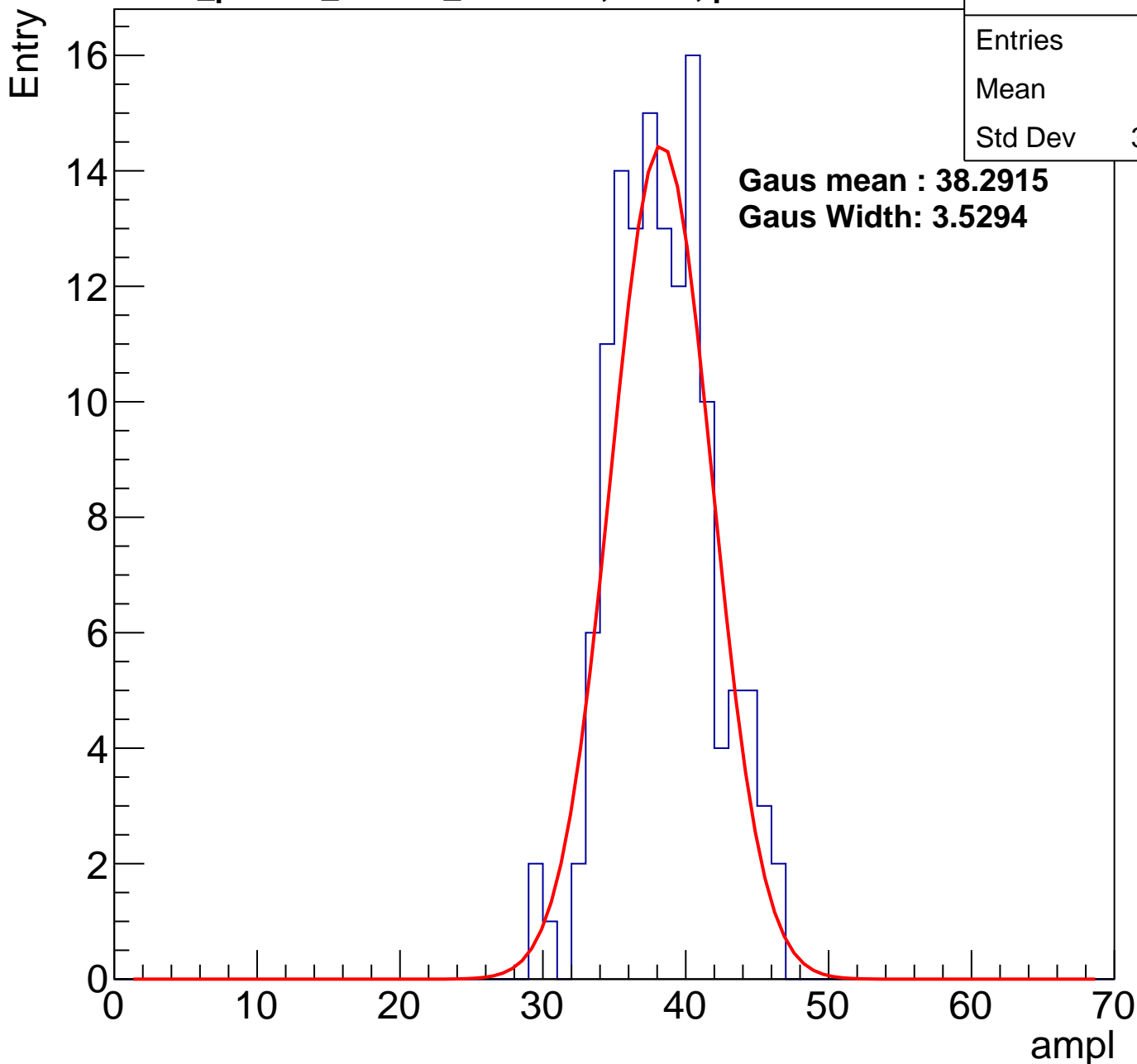
# B0L101S, U16-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 37.9  |
| Std Dev | 3.483 |

**Gaus mean : 38.2915**

**Gaus Width: 3.5294**



# B0L101S, U16-ch50, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

109

Mean

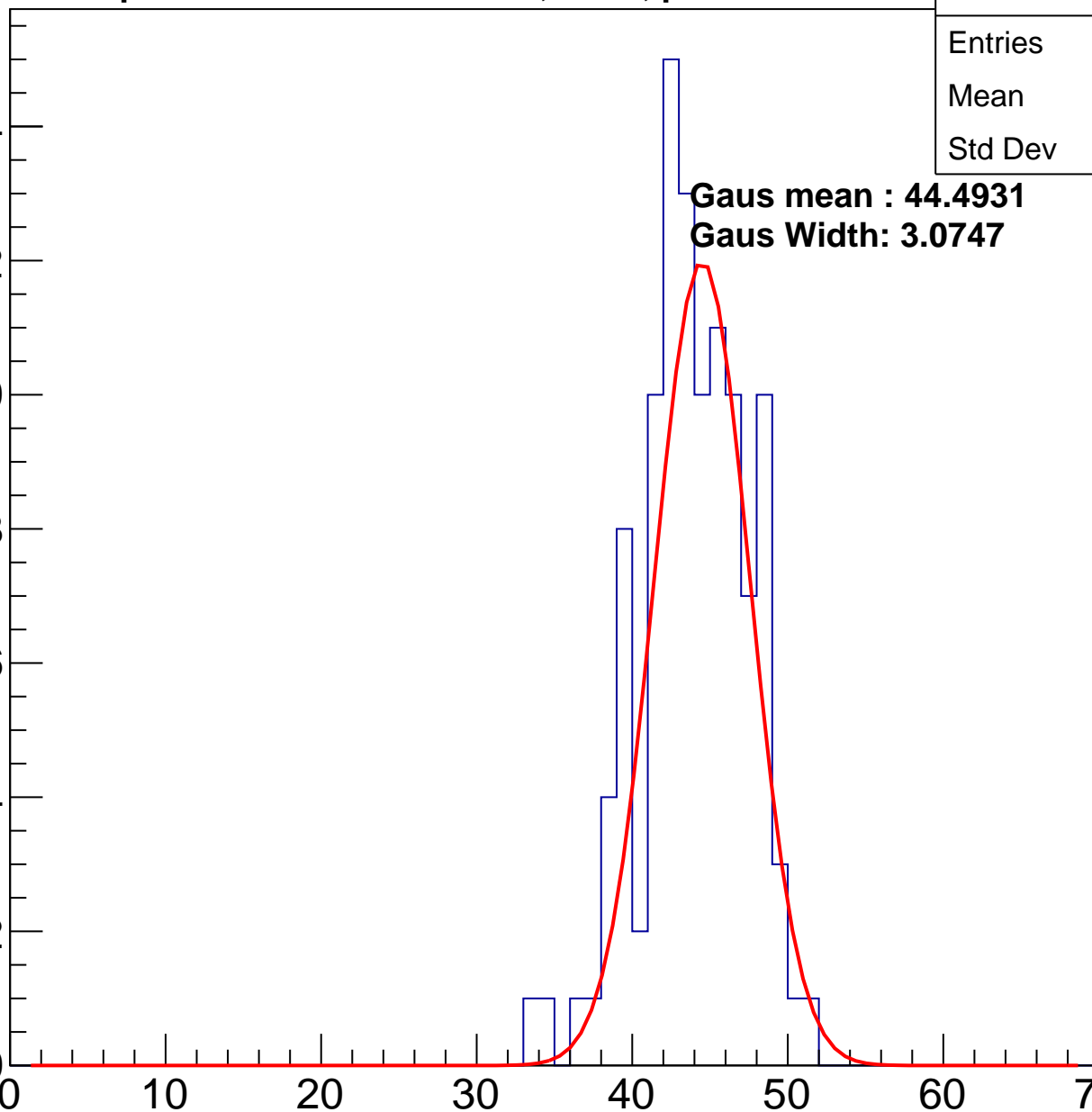
43.44

Std Dev

3.417

**Gaus mean : 44.4931**

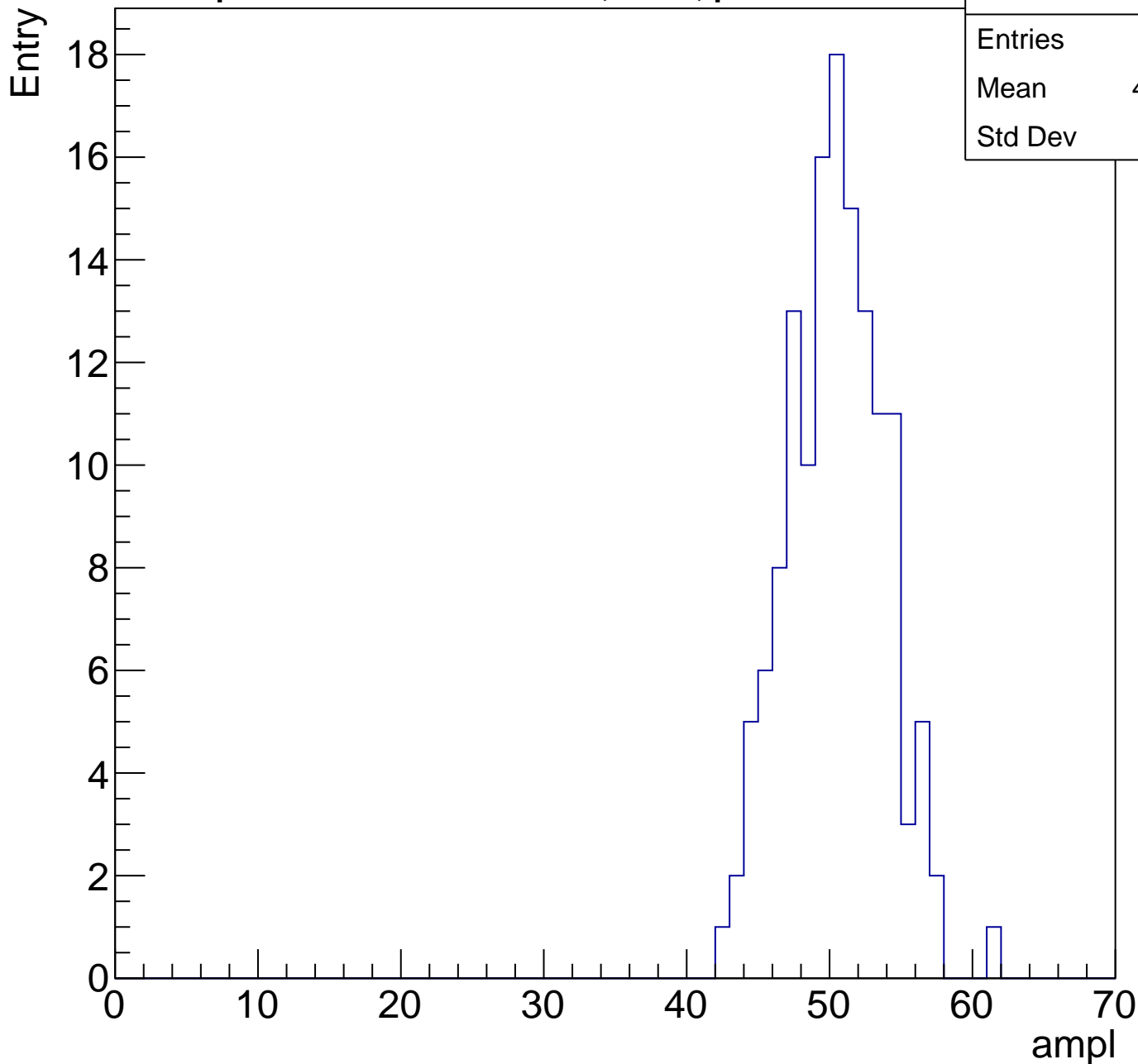
**Gaus Width: 3.0747**



# B0L101S, U16-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 49.99 |
| Std Dev | 3.39  |



# B0L101S, U16-ch50, adc4

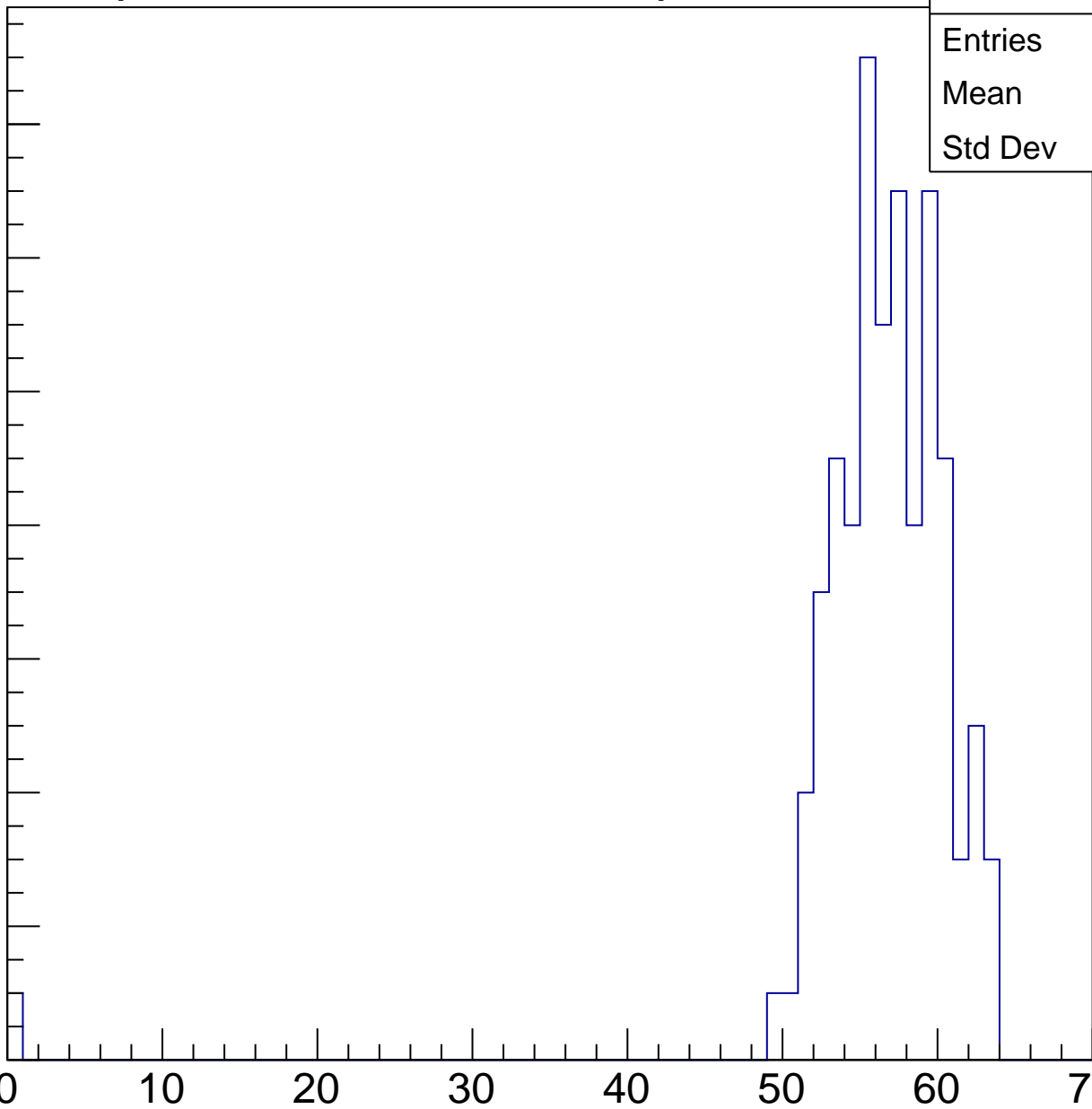
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 55.95 |
| Std Dev | 6.201 |

ampl



# B0L101S, U16-ch50, adc5

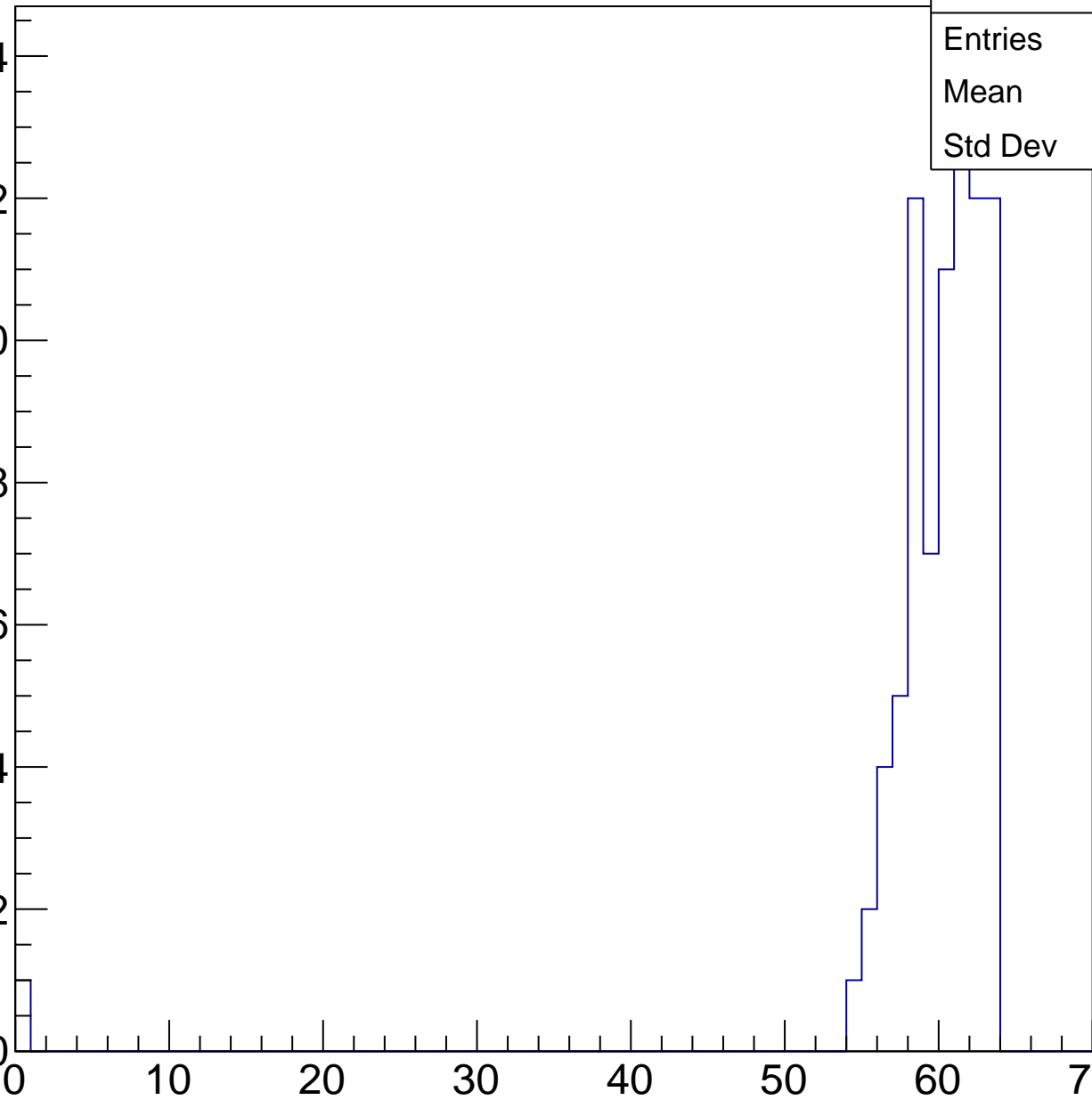
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 59.21 |
| Std Dev | 7     |

ampl

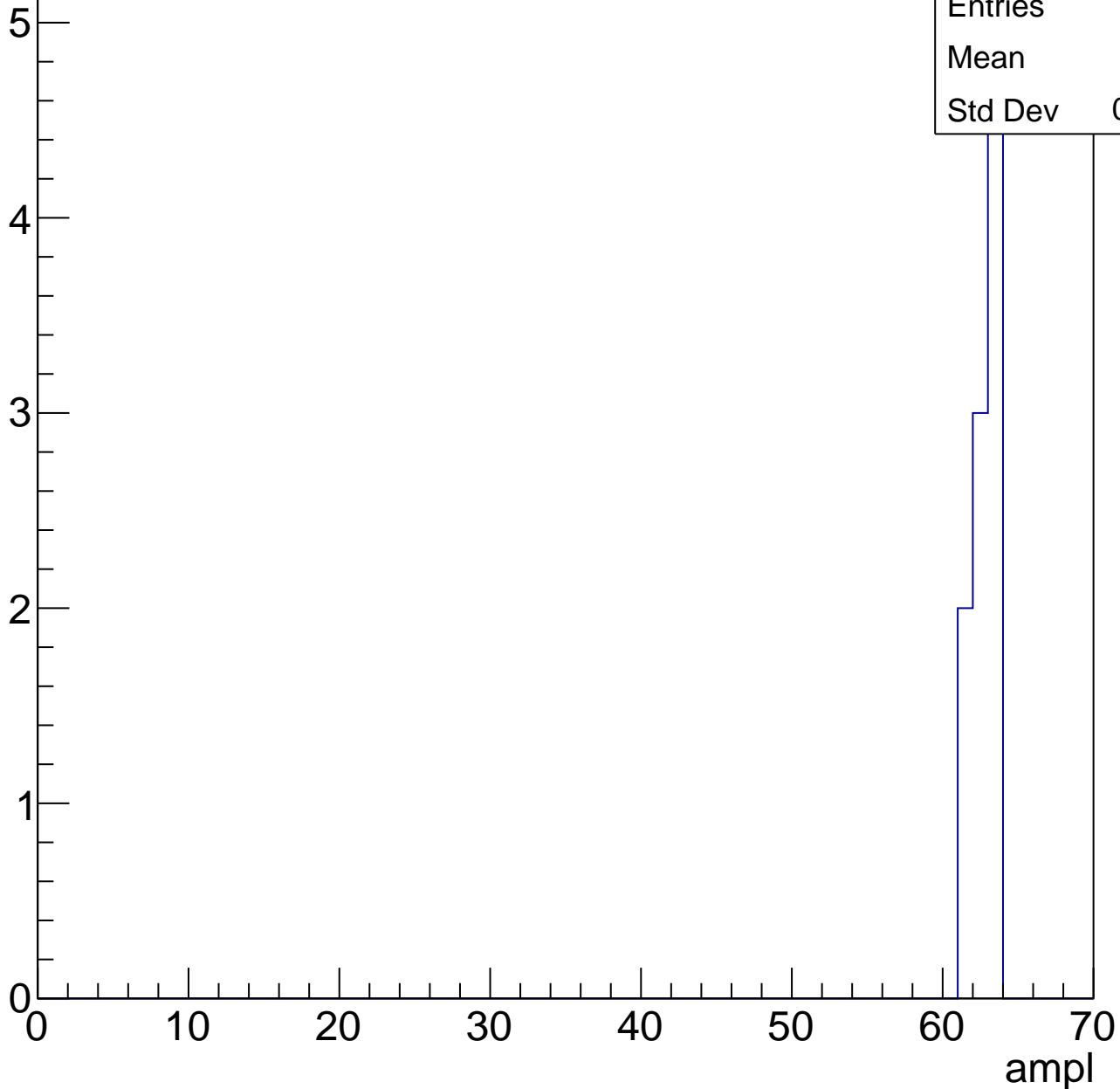


# B0L101S, U16-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 10    |
| Mean    | 62.3  |
| Std Dev | 0.781 |





# B0L101S, U16-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch51, adc0

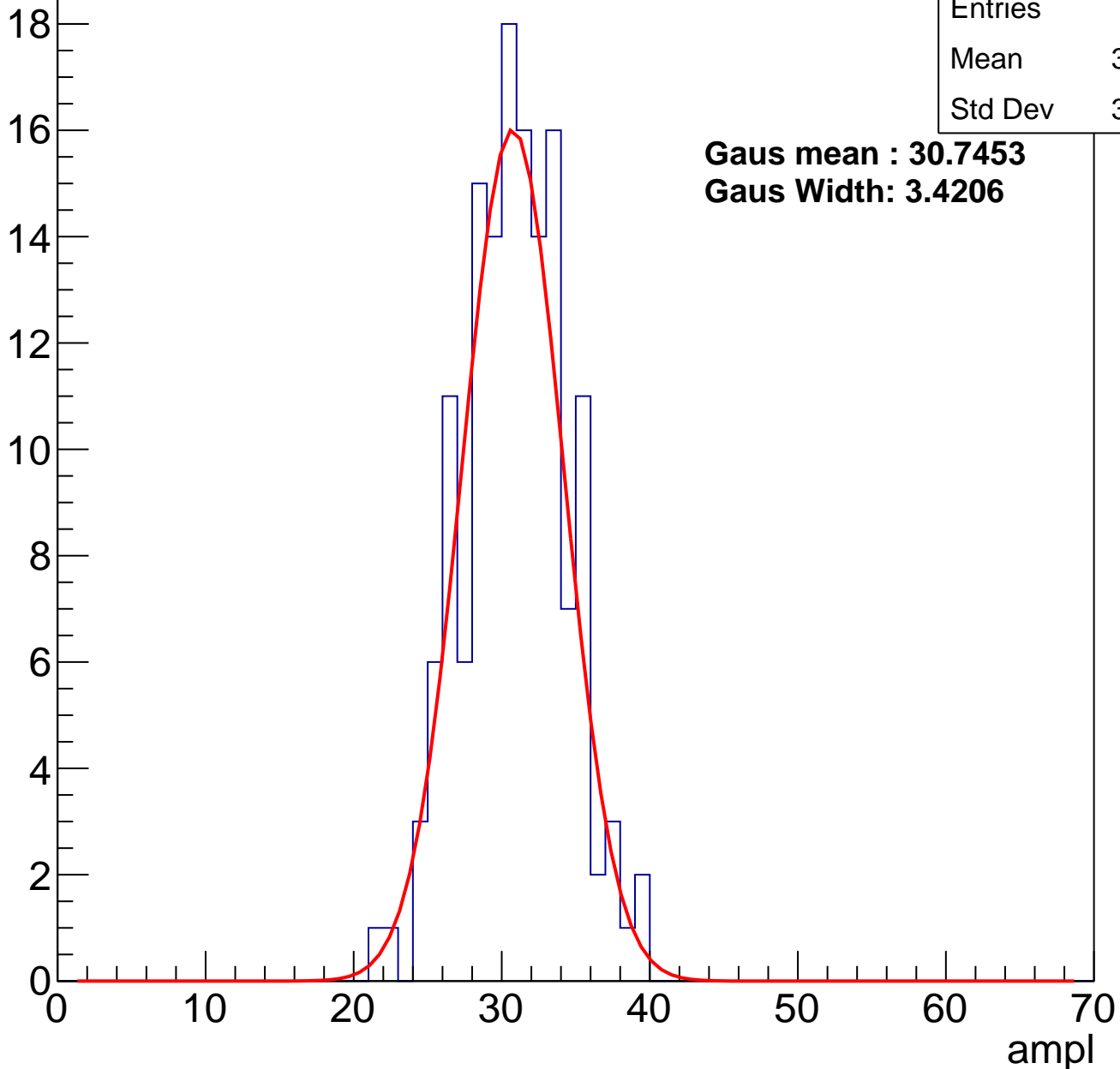
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 30.43 |
| Std Dev | 3.429 |

**Gaus mean : 30.7453**

**Gaus Width: 3.4206**

Entry

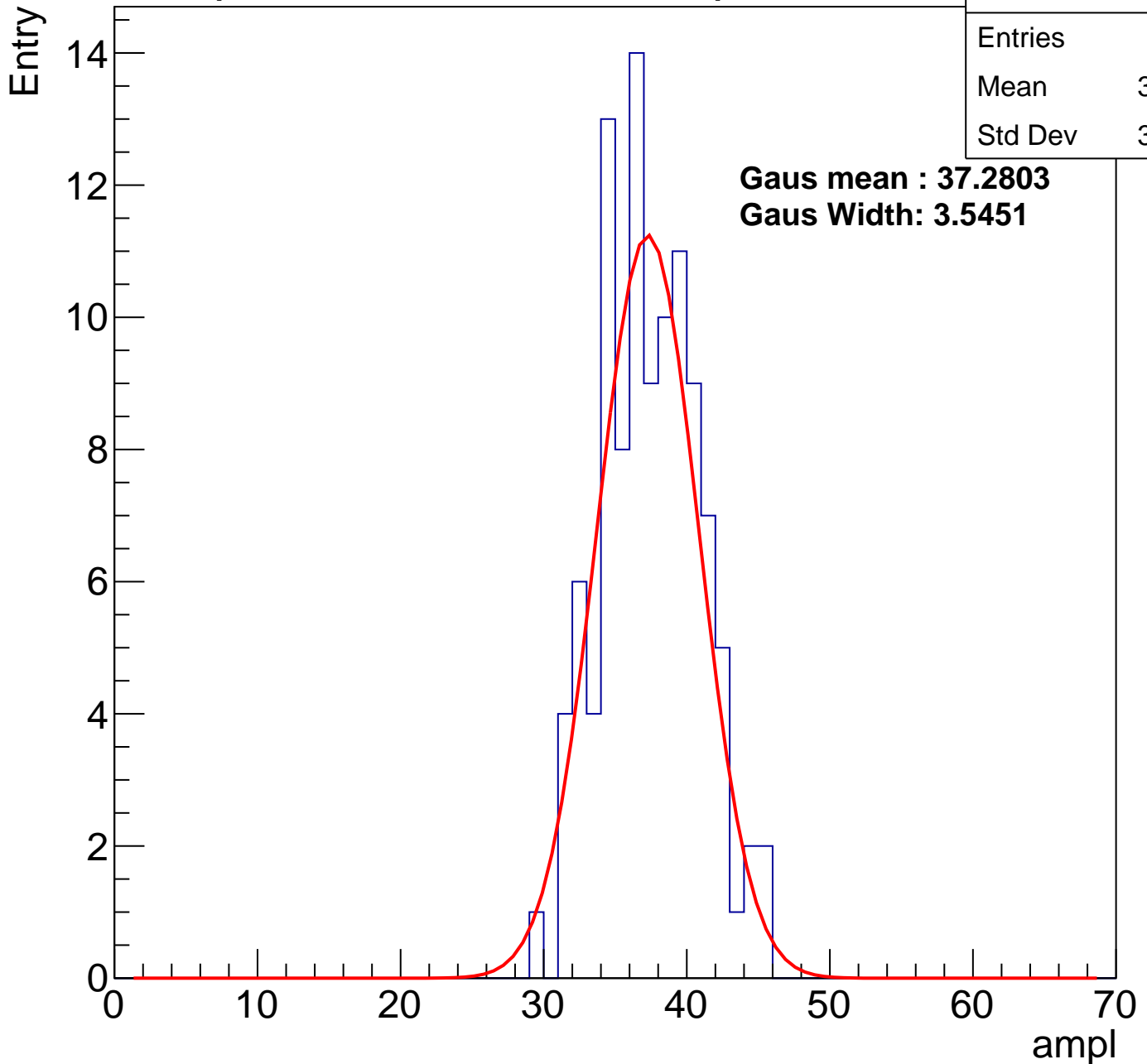


# B0L101S, U16-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 106   |
| Mean    | 37.01 |
| Std Dev | 3.391 |

**Gaus mean : 37.2803**  
**Gaus Width: 3.5451**



# B0L101S, U16-ch51, adc2

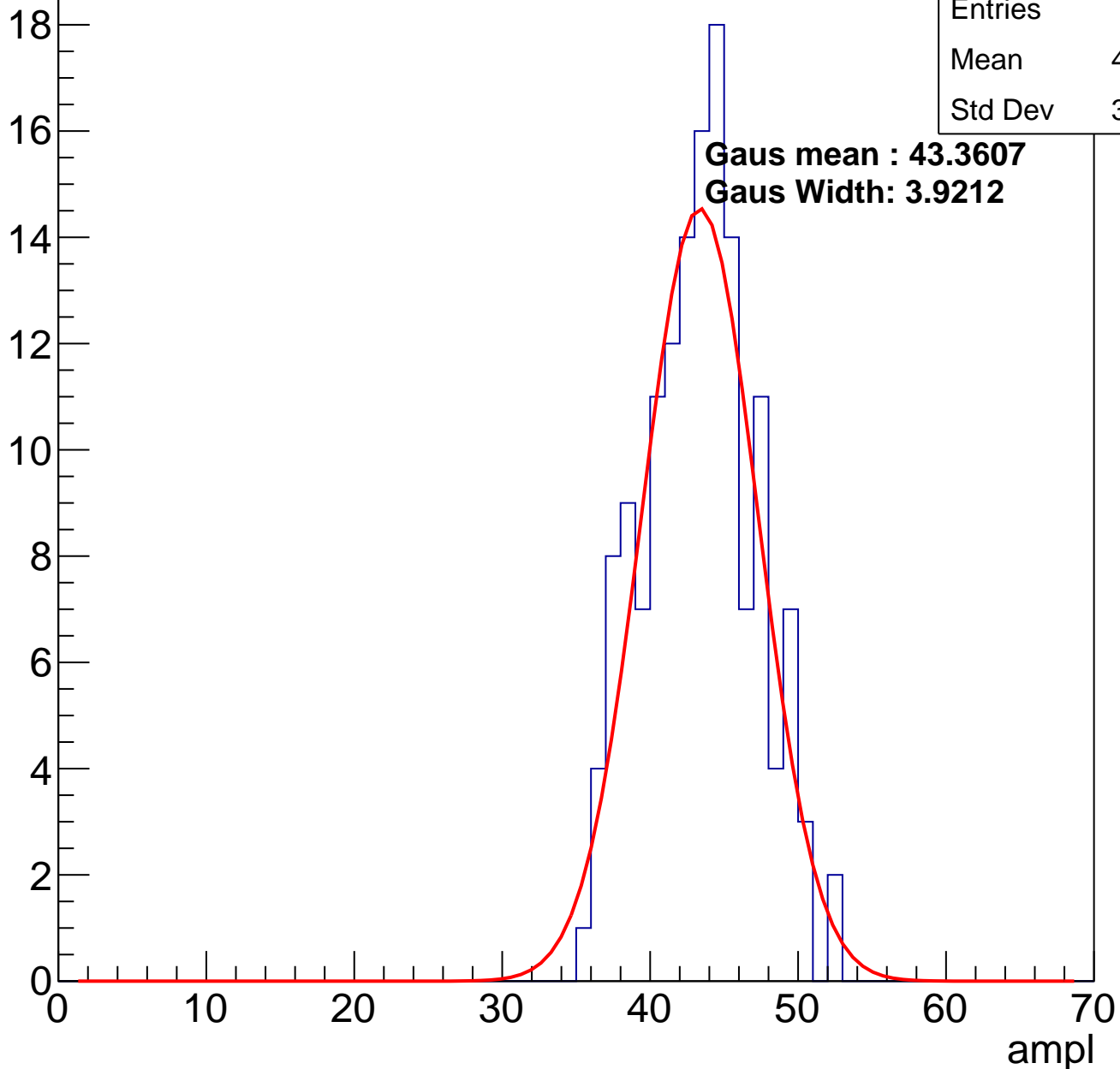
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 42.89 |
| Std Dev | 3.693 |

**Gaus mean : 43.3607**

**Gaus Width: 3.9212**

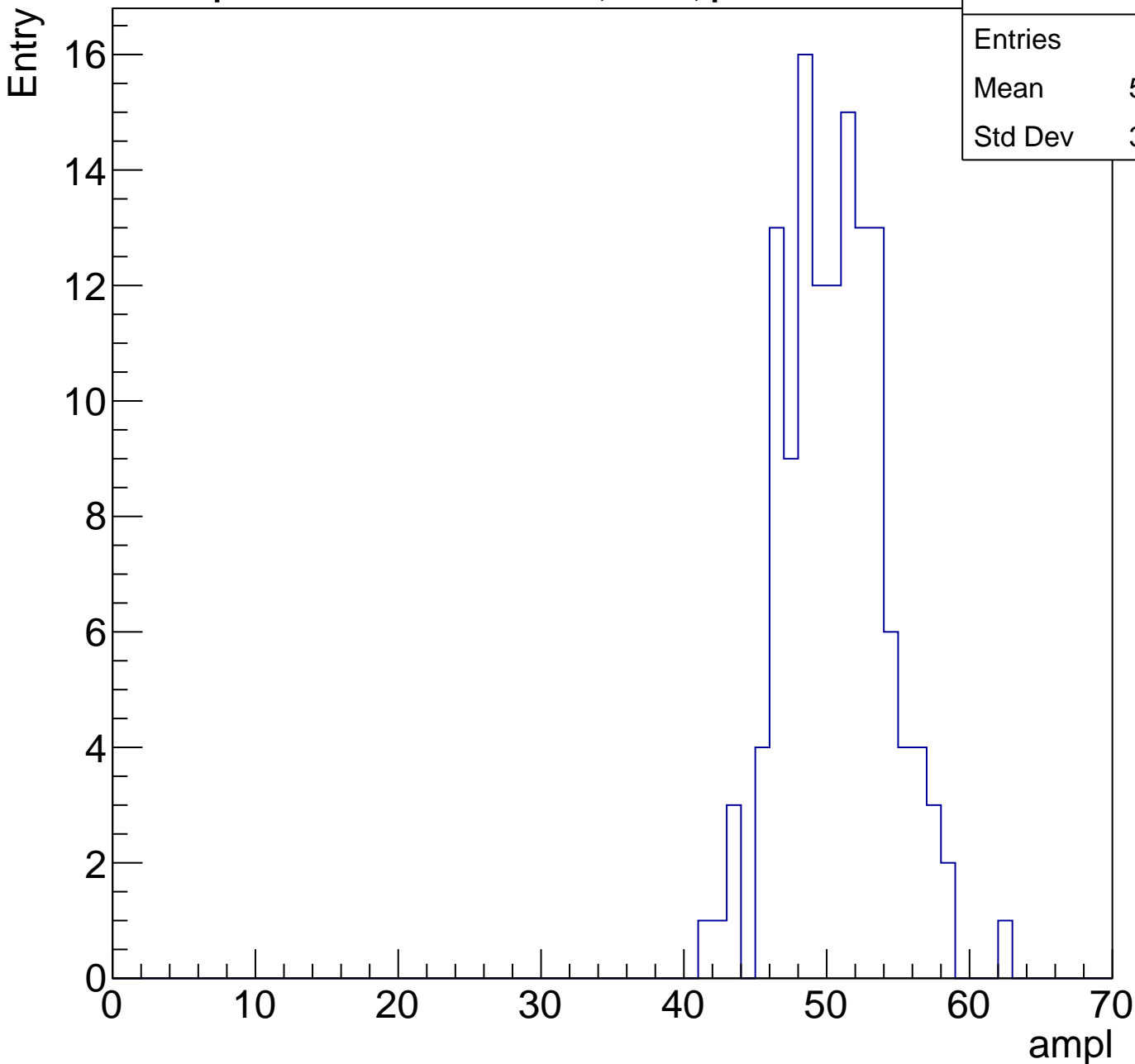
Entry



# B0L101S, U16-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 50.12 |
| Std Dev | 3.587 |



# B0L101S, U16-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

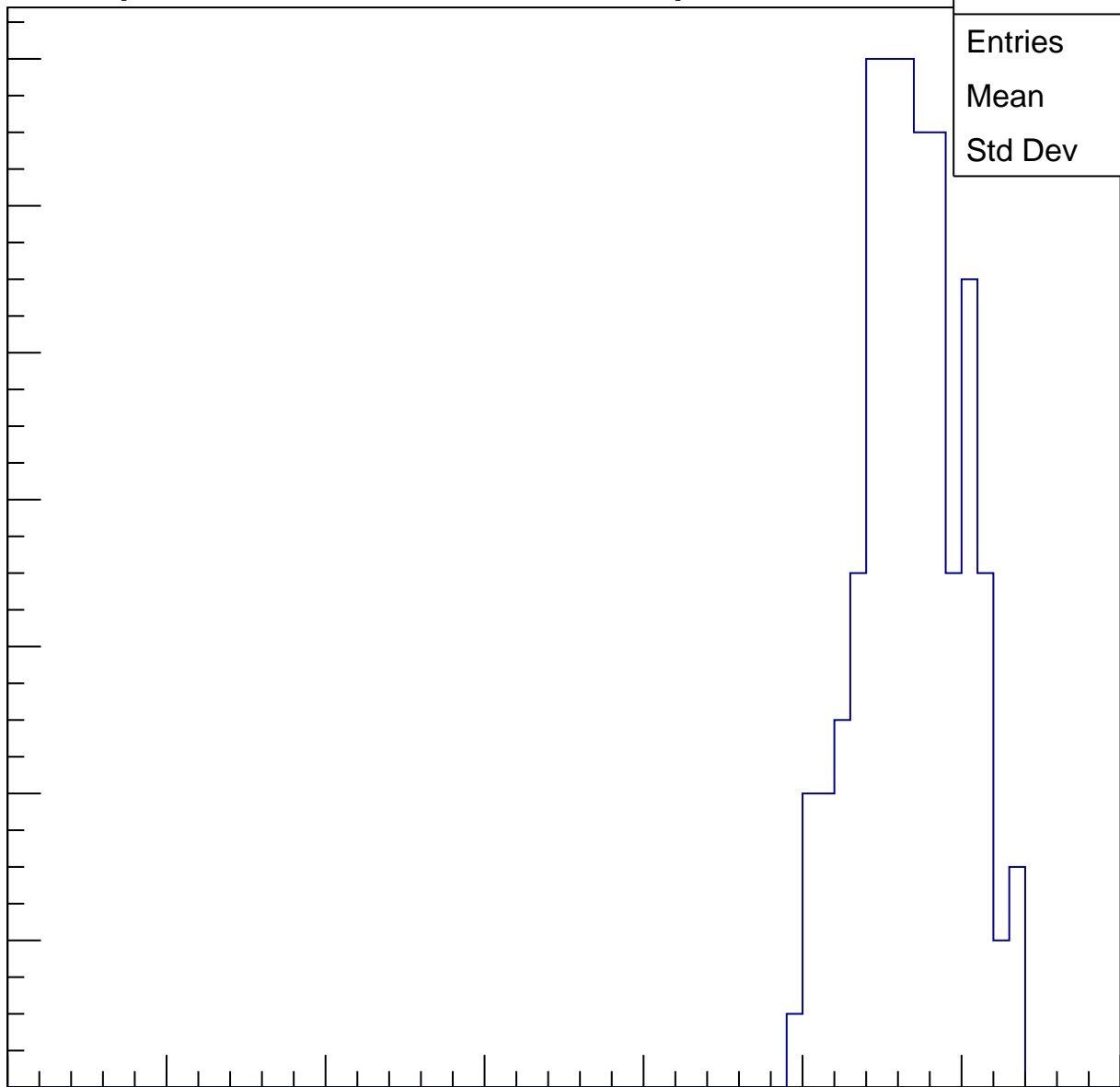
|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 56.32 |
| Std Dev | 3.165 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch51, adc5

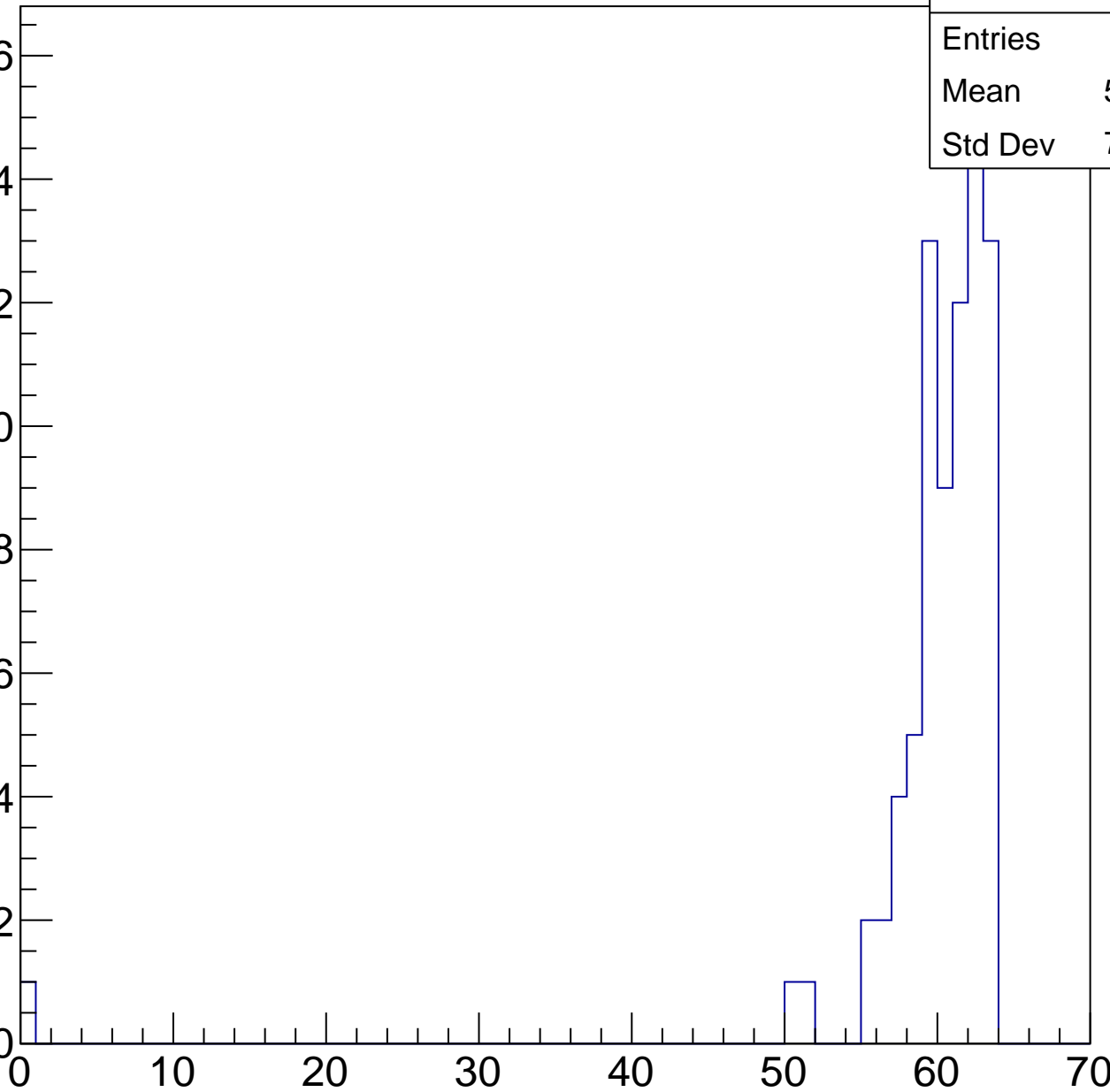
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 59.38 |
| Std Dev | 7.199 |

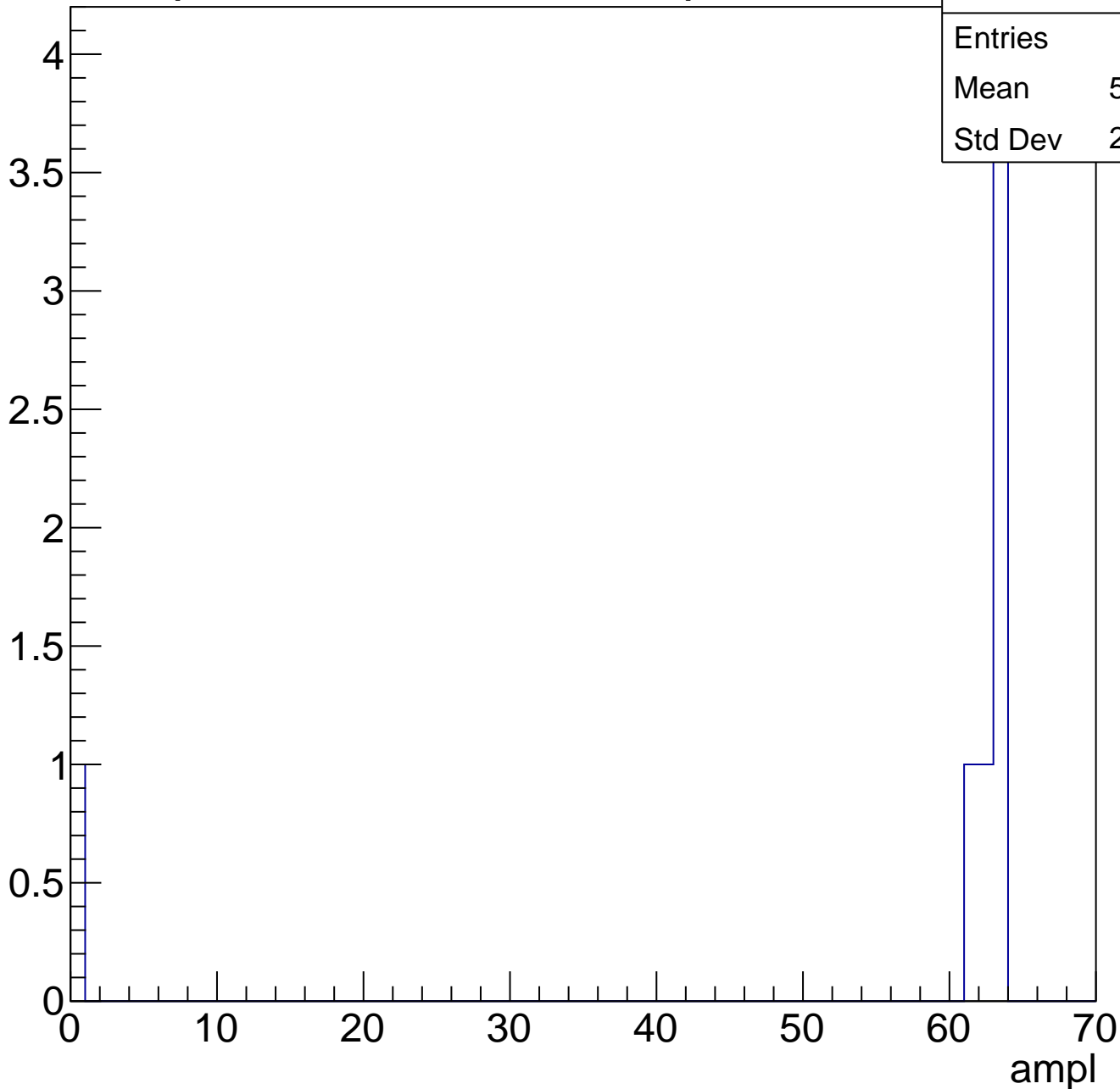
ampl



# B0L101S, U16-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch52, adc0

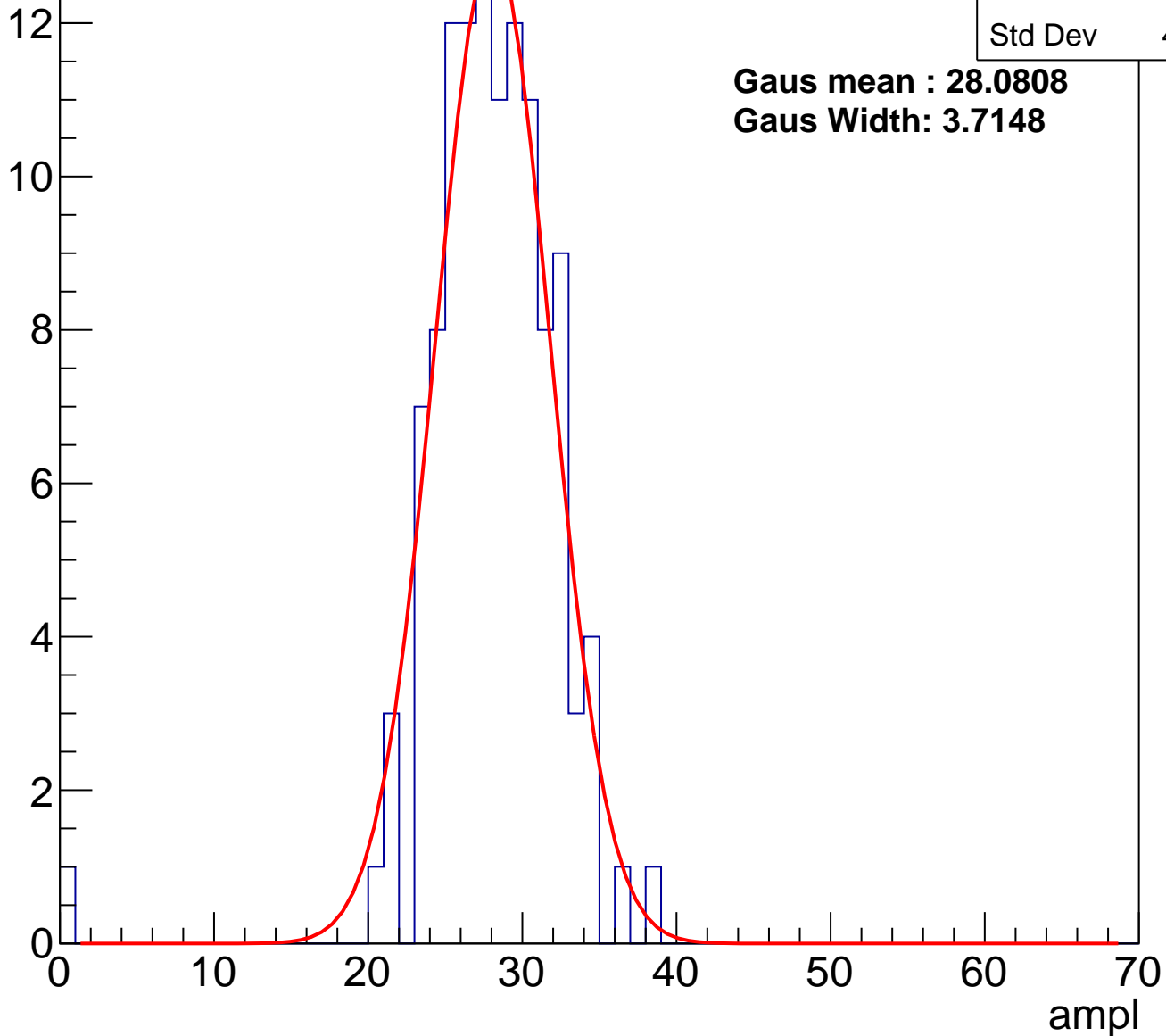
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 27.61 |
| Std Dev | 4.241 |

**Gaus mean : 28.0808**

**Gaus Width: 3.7148**

Entry



# B0L101S, U16-ch52, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 34.76 |
| Std Dev | 3.955 |

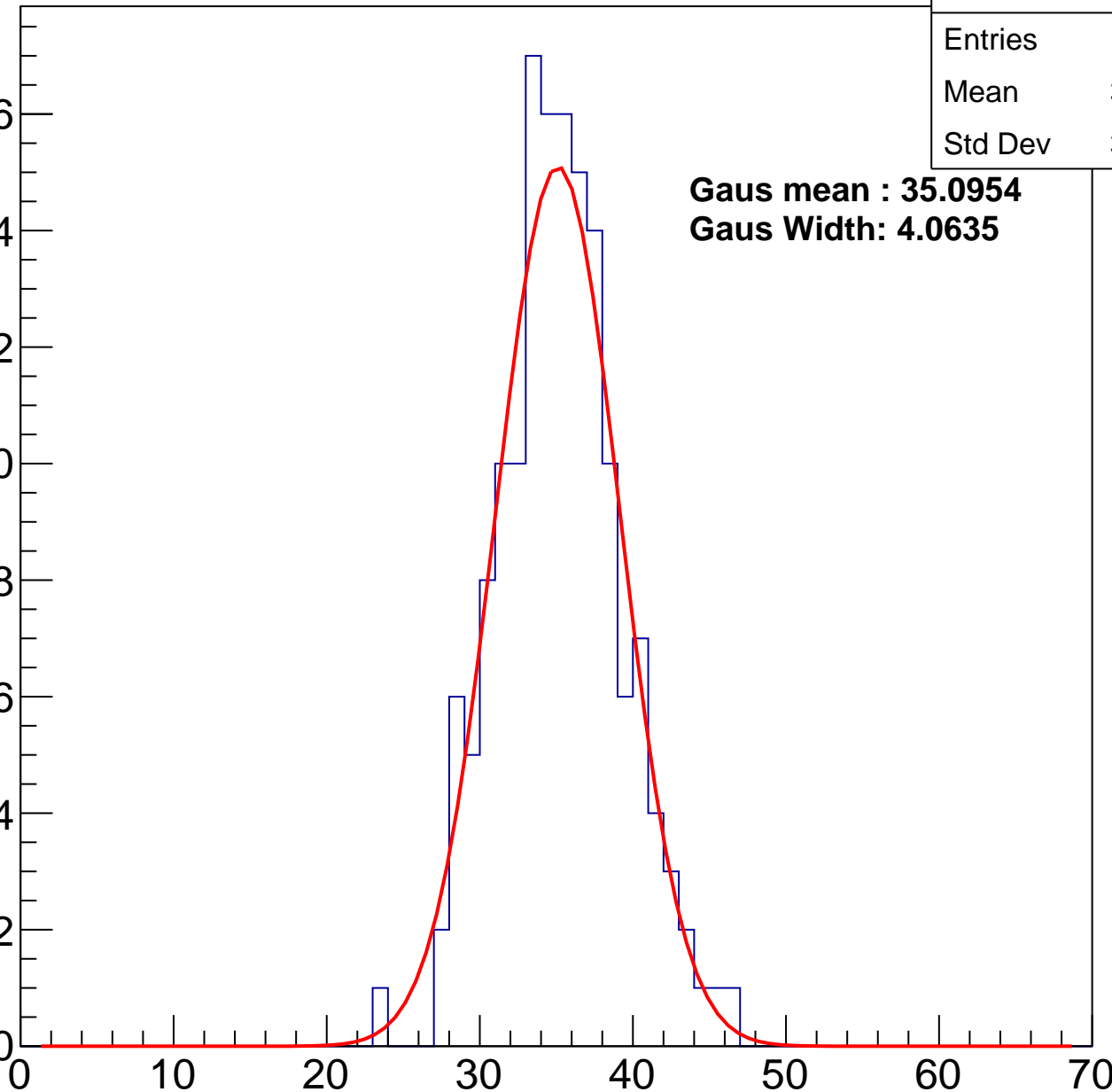
**Gaus mean : 35.0954**

**Gaus Width: 4.0635**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

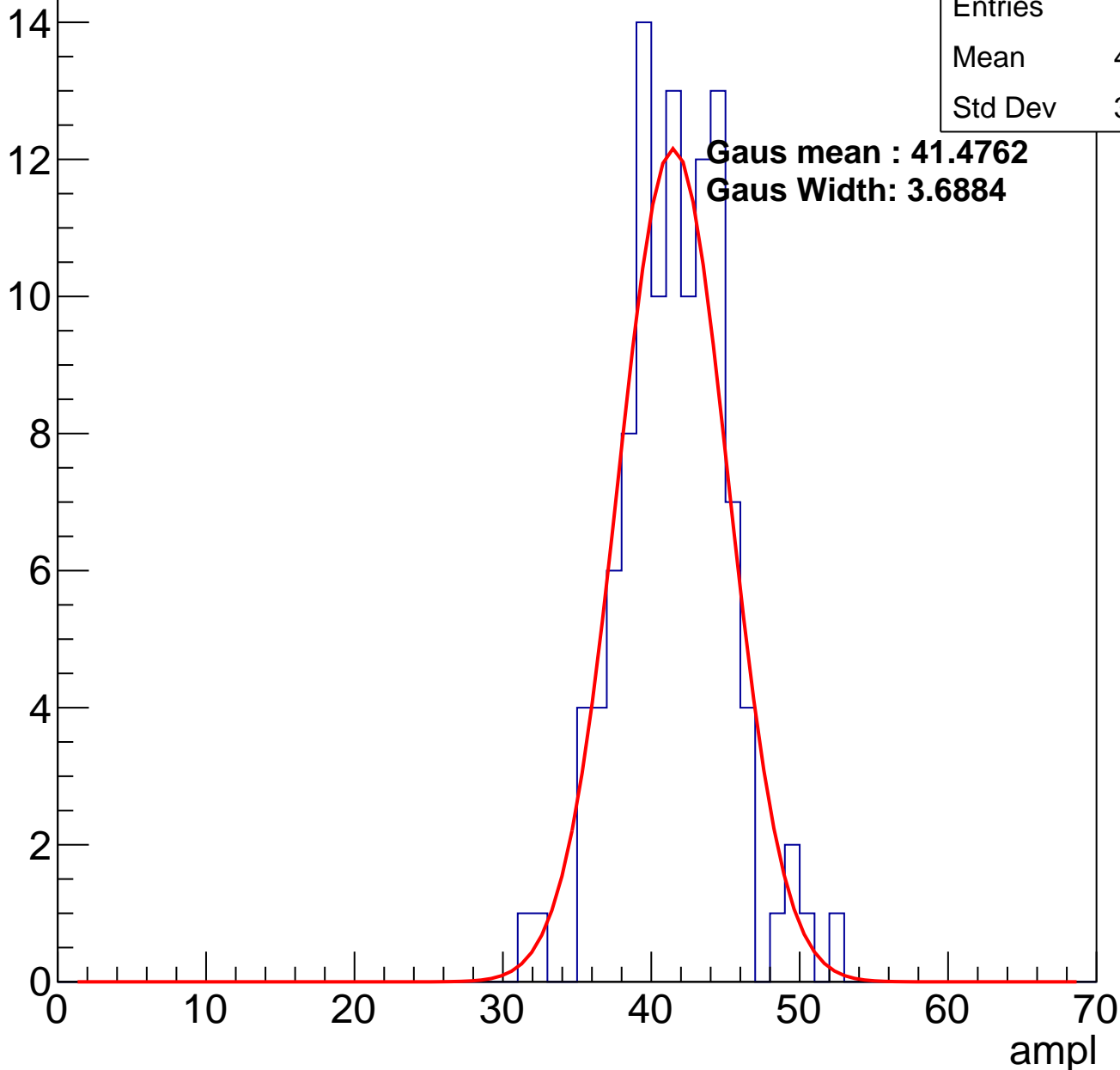
ampl



# B0L101S, U16-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

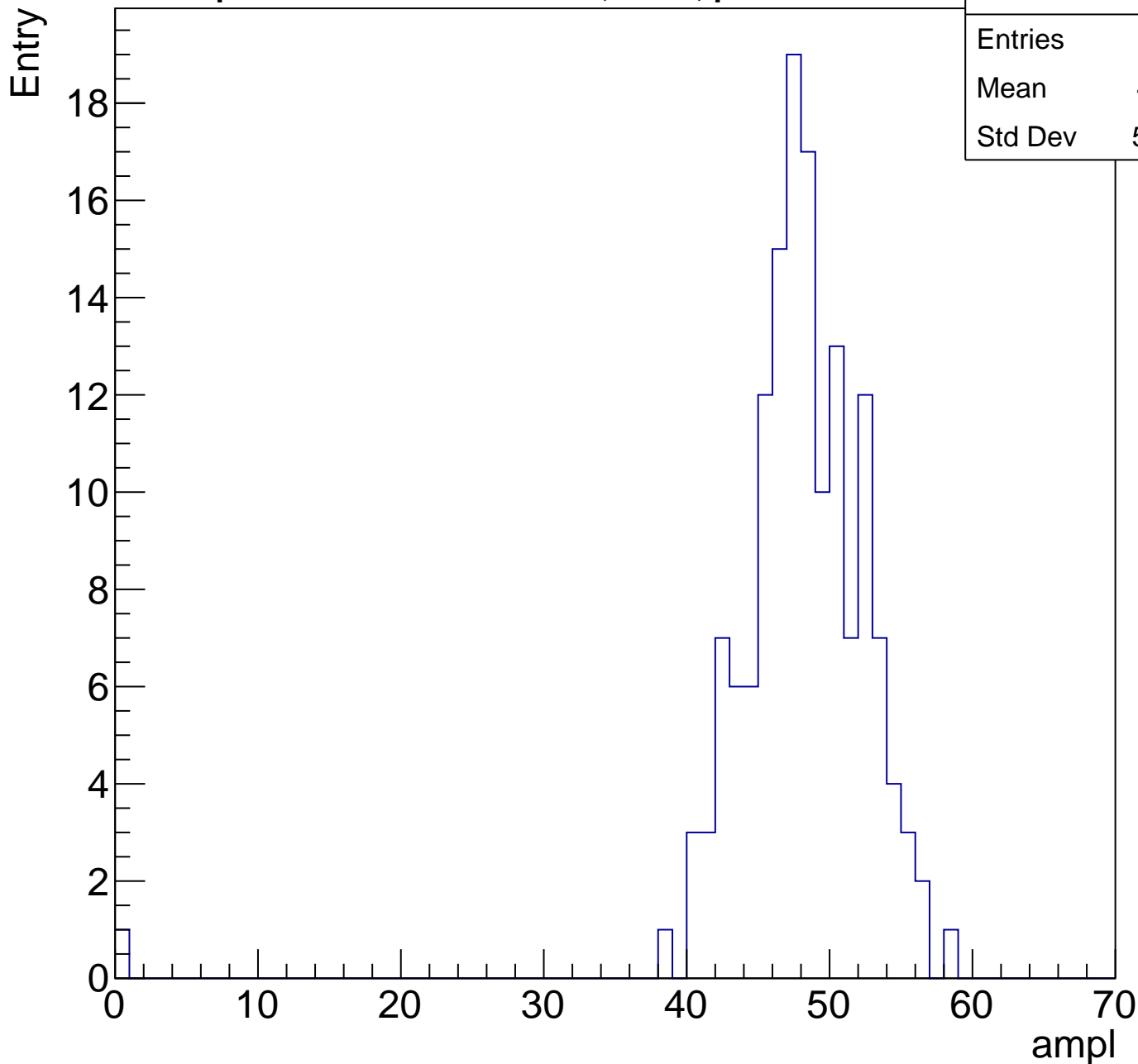
Entry



# B0L101S, U16-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

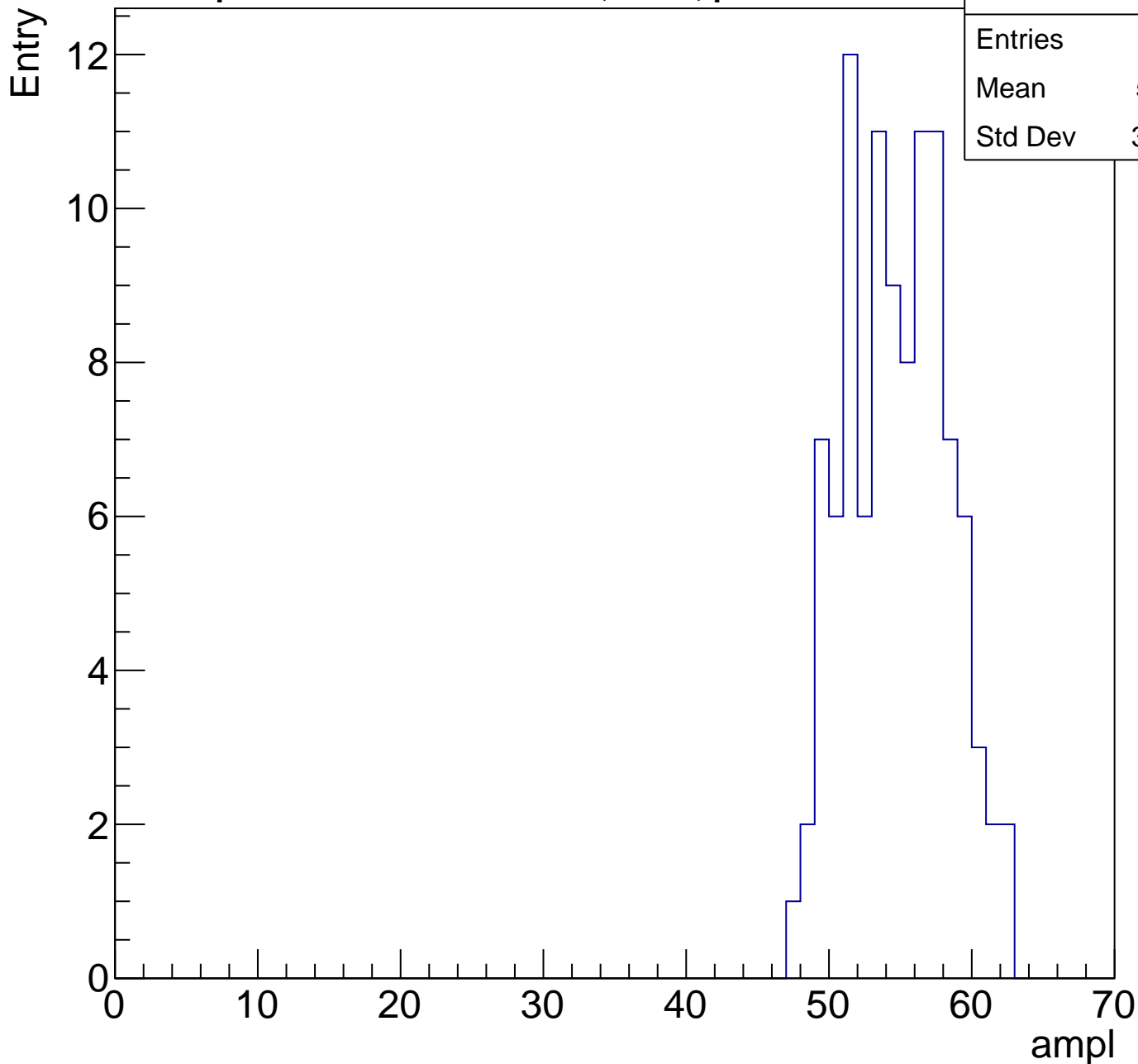
|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 47.51 |
| Std Dev | 5.436 |



# B0L101S, U16-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 54.31 |
| Std Dev | 3.497 |



# B0L101S, U16-ch52, adc5

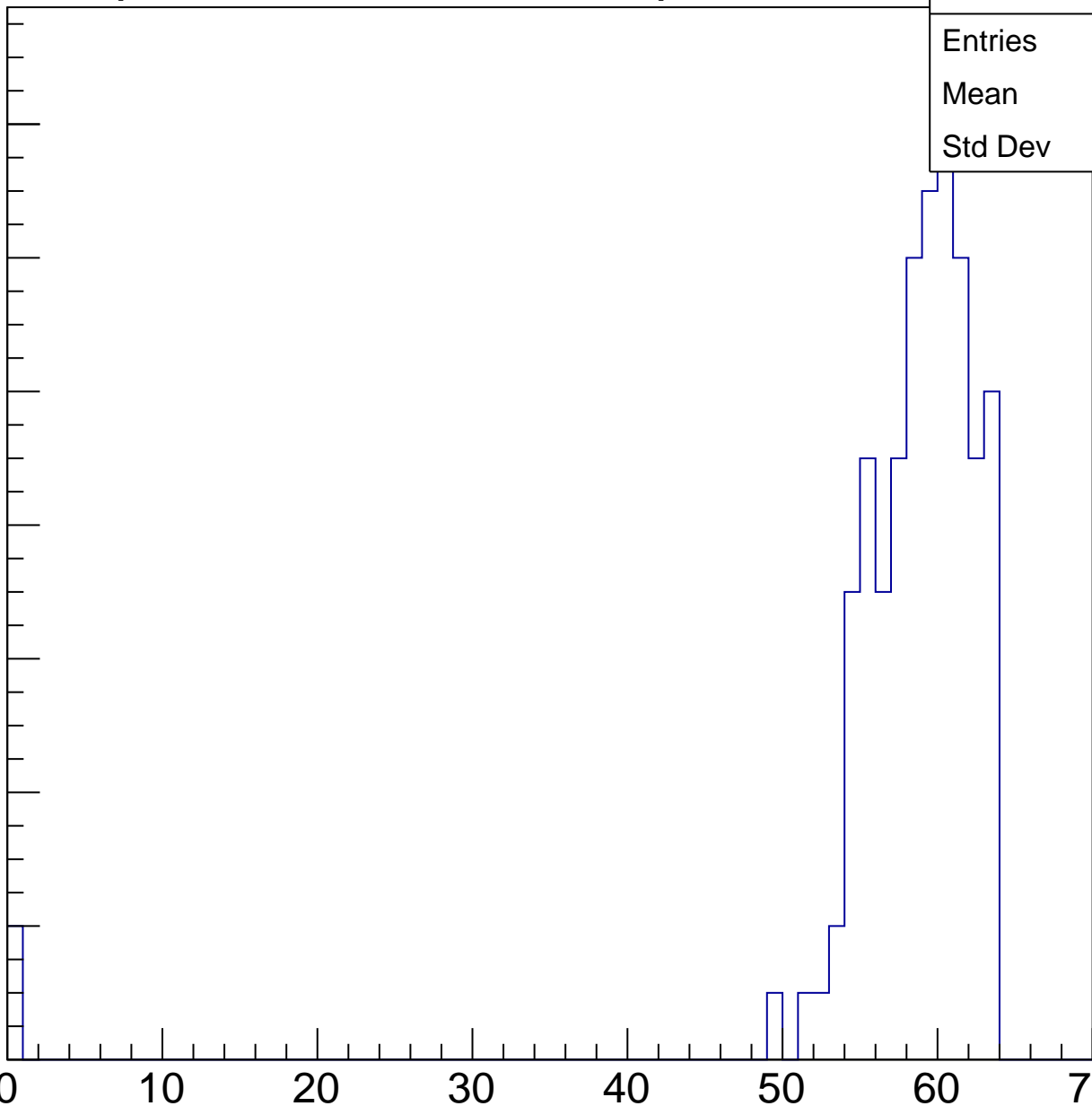
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 57.45 |
| Std Dev | 8.375 |

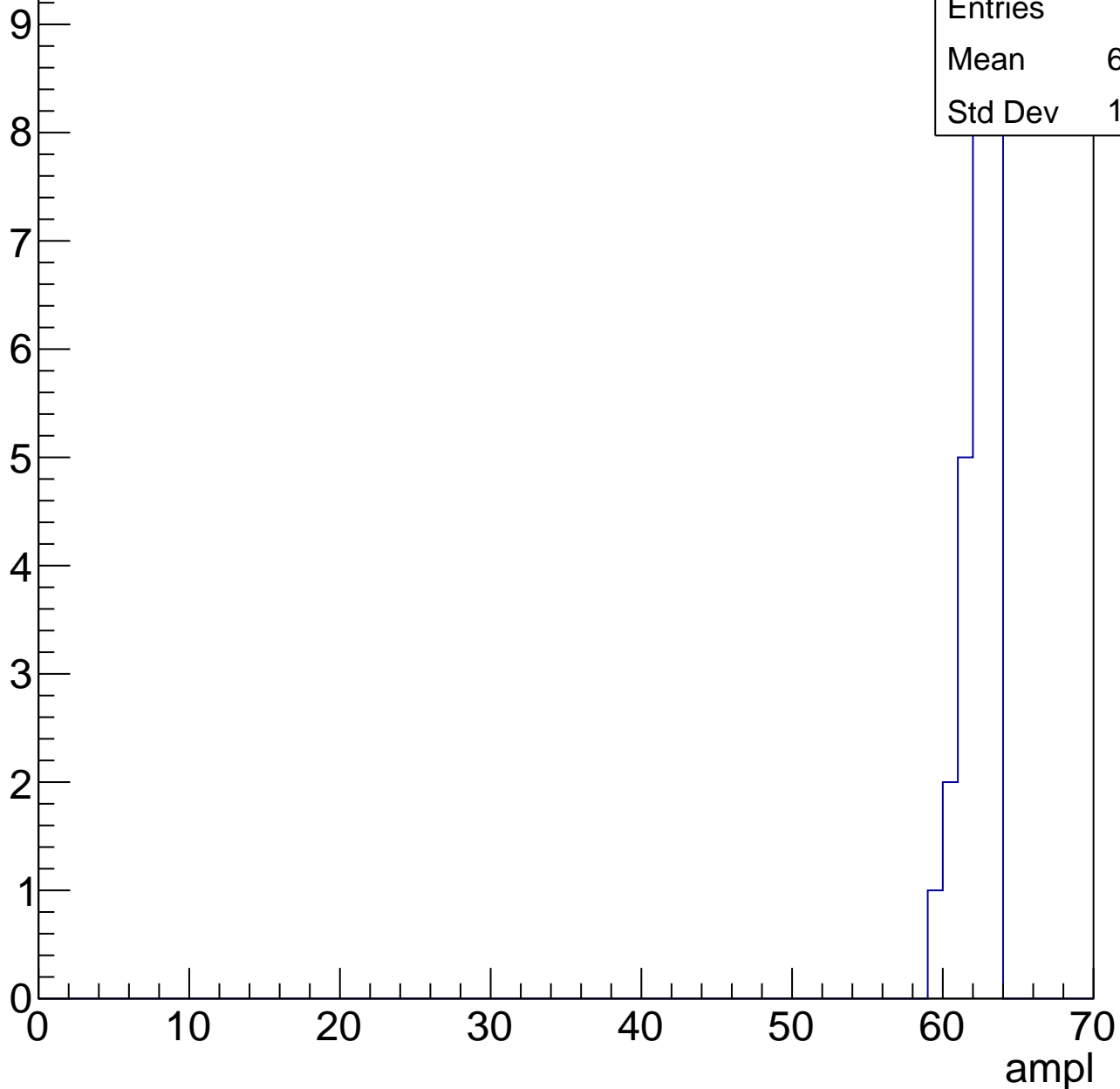
ampl



# B0L101S, U16-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

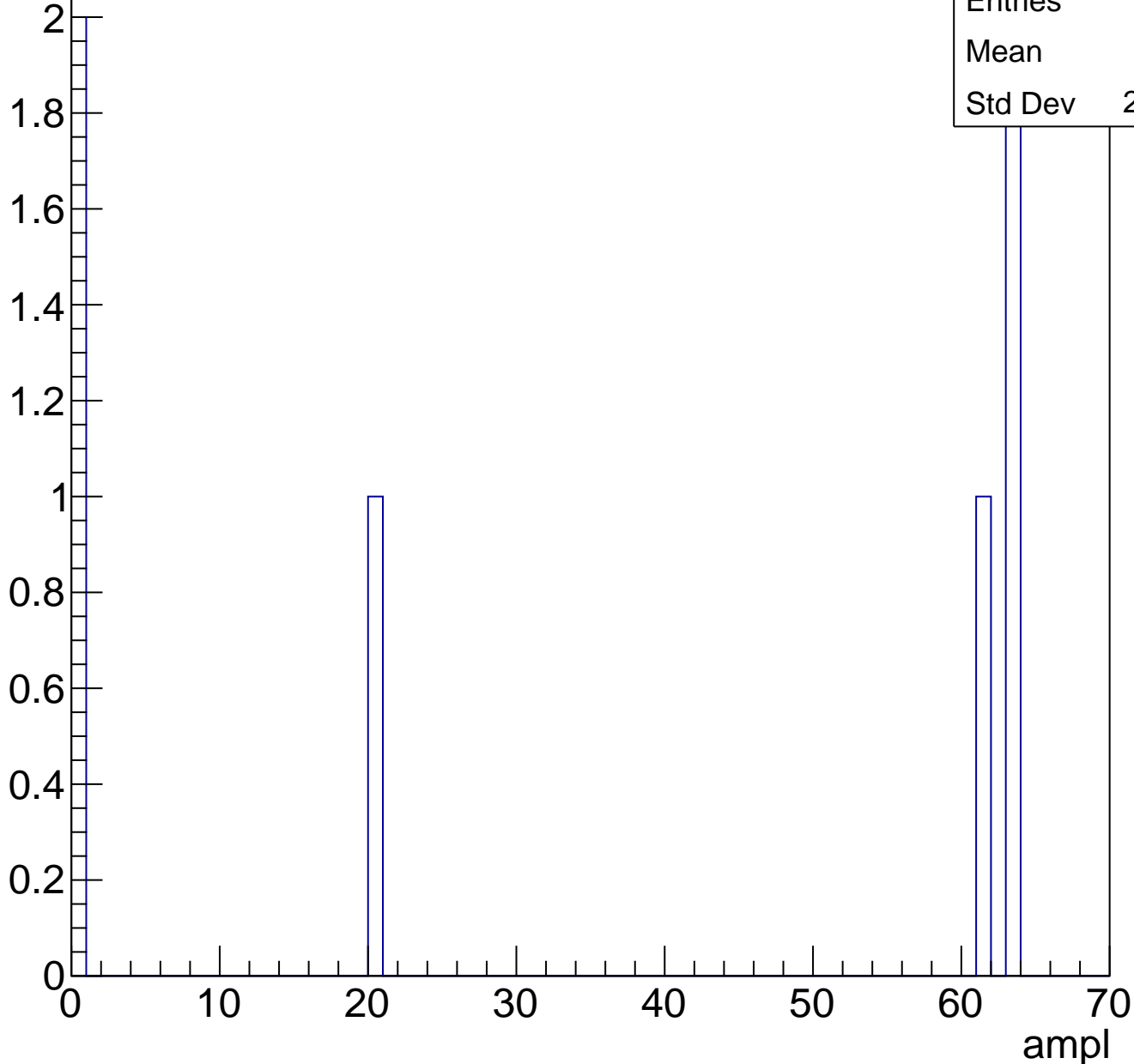




# B0L101S, U16-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 29.57 |
| Std Dev | 4.339 |

**Gaus mean : 30.4725**

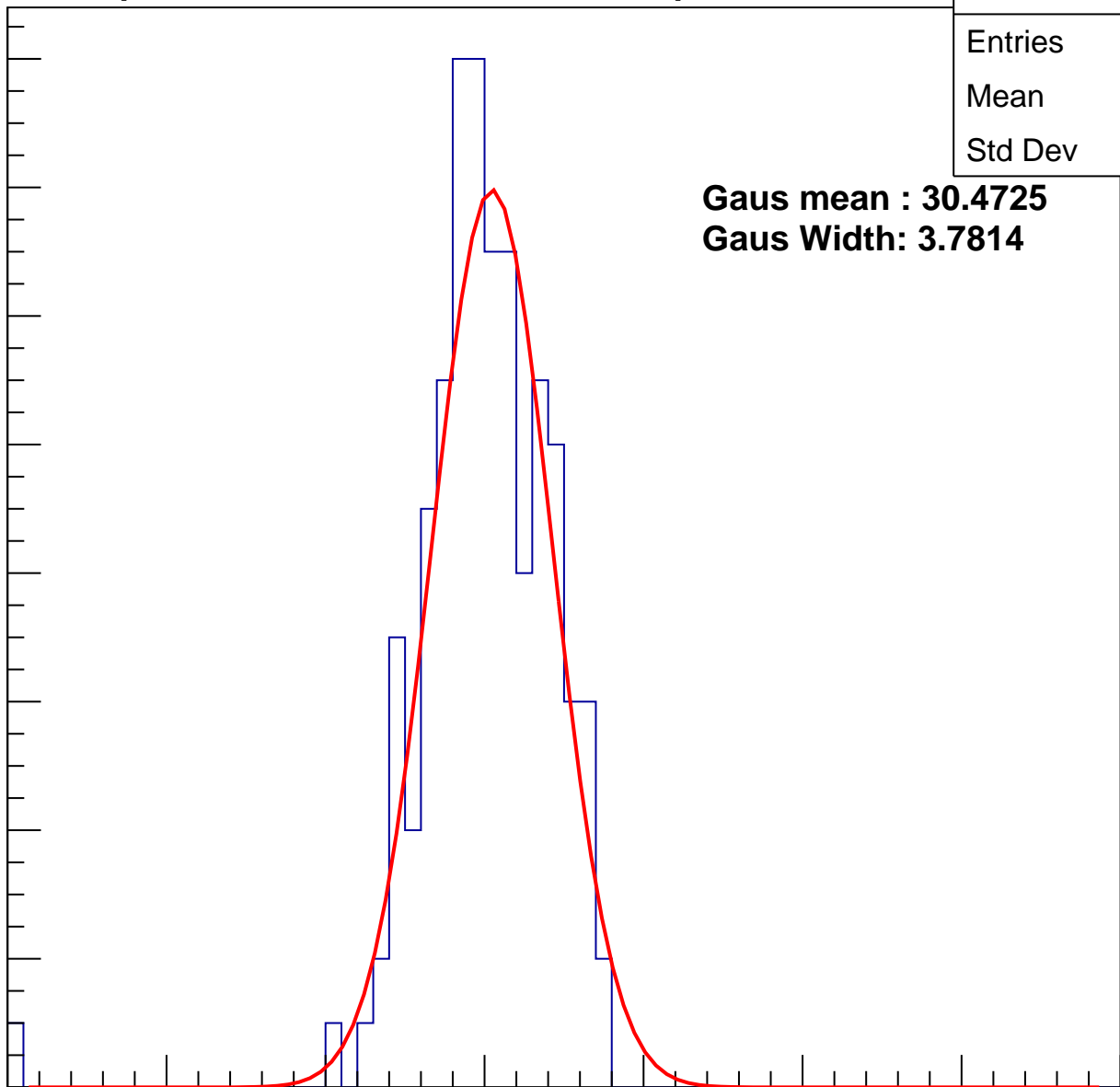
**Gaus Width: 3.7814**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch53, adc1

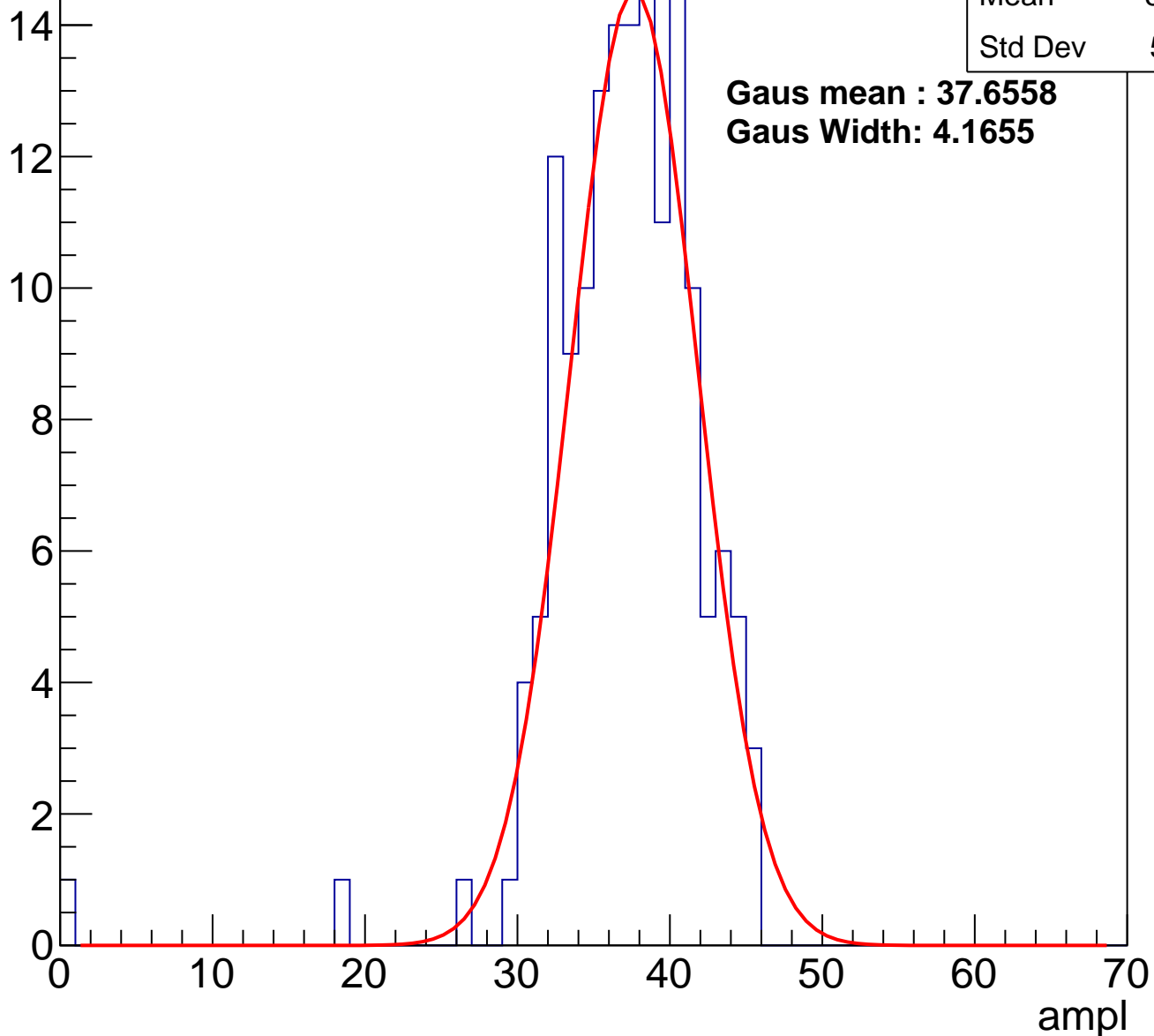
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 36.63 |
| Std Dev | 5.081 |

**Gaus mean : 37.6558**

**Gaus Width: 4.1655**

Entry



# B0L101S, U16-ch53, adc2

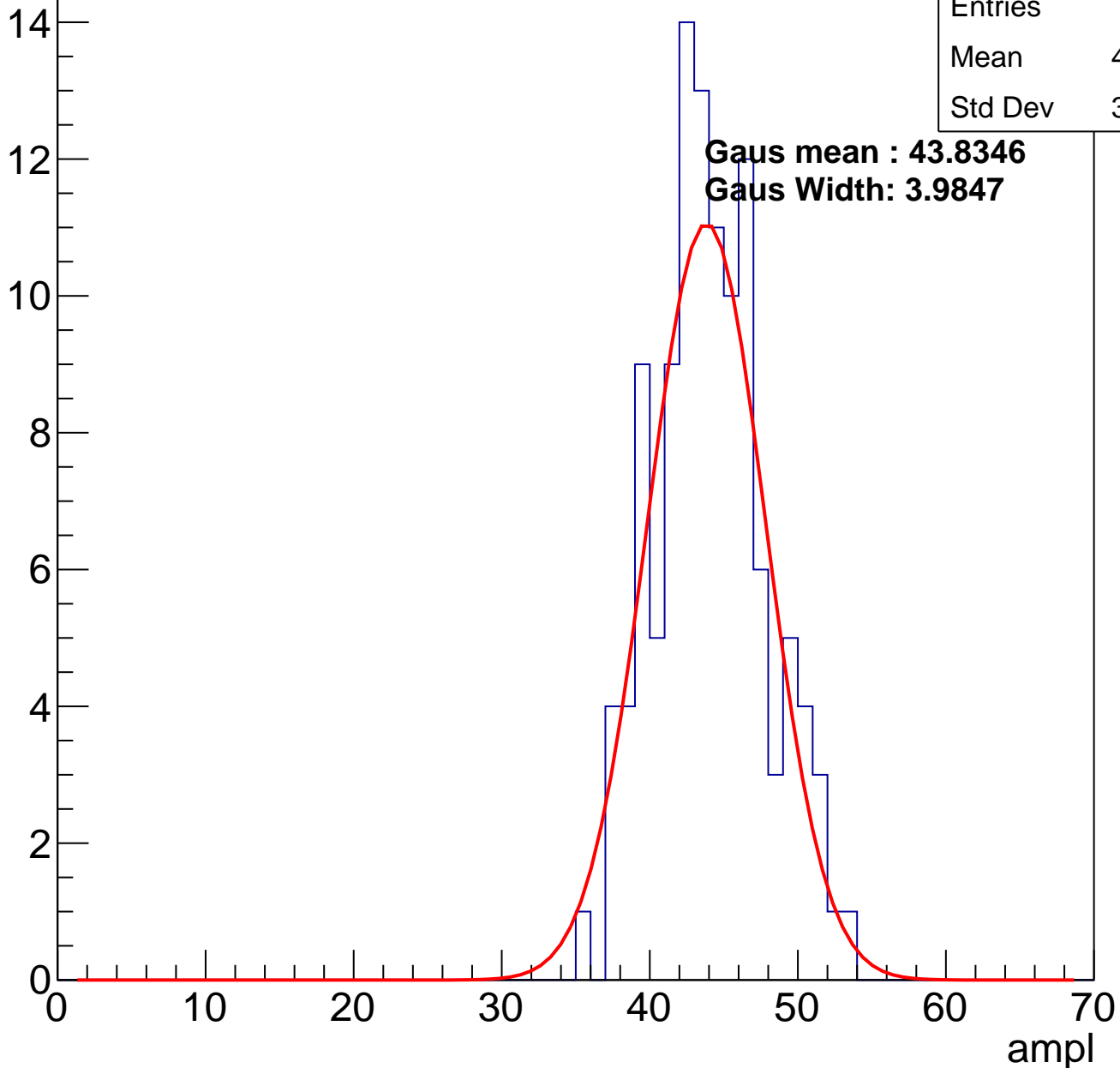
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 43.63 |
| Std Dev | 3.708 |

**Gaus mean : 43.8346**

**Gaus Width: 3.9847**

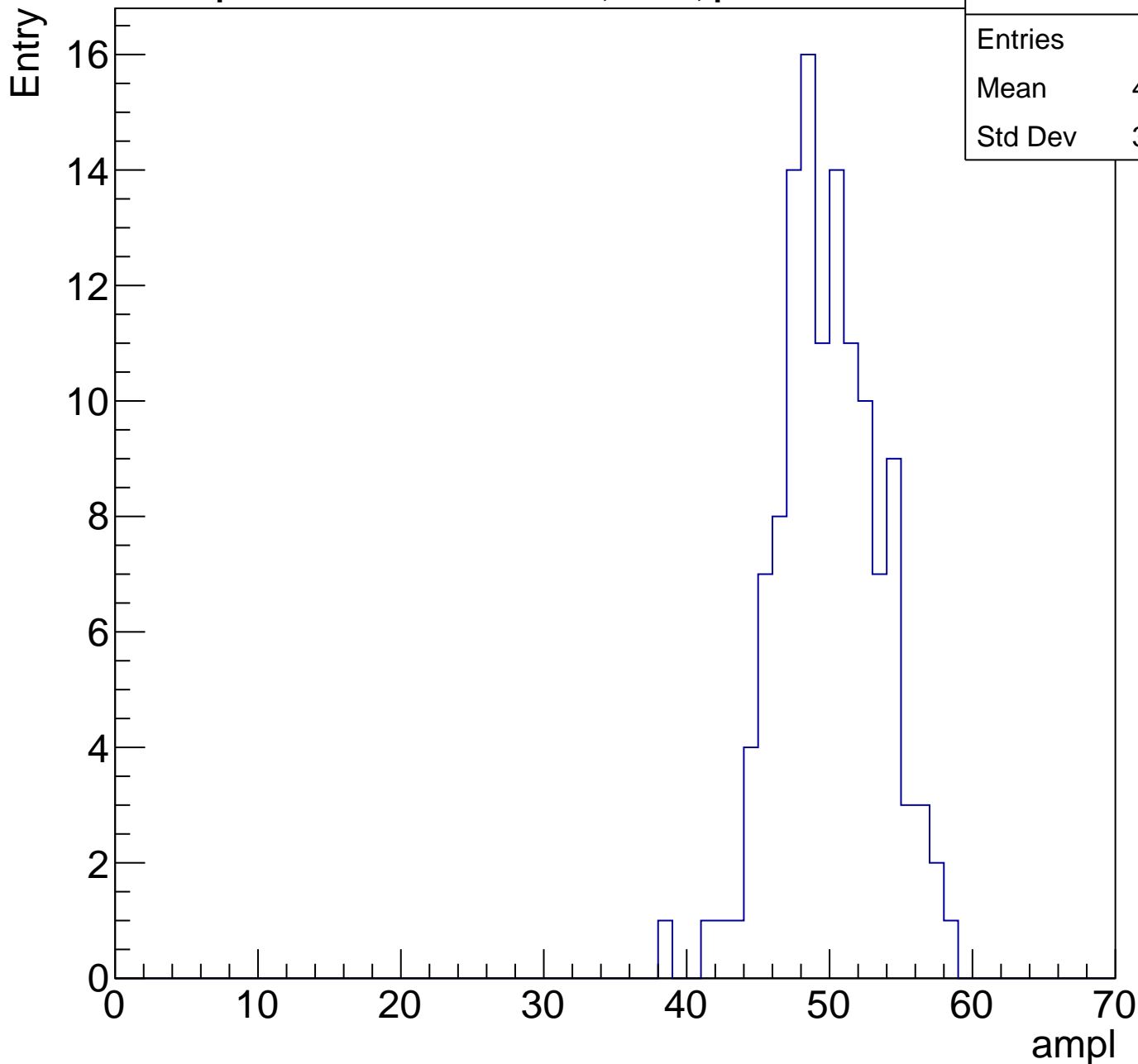
Entry



# B0L101S, U16-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 49.44 |
| Std Dev | 3.536 |



# B0L101S, U16-ch53, adc4

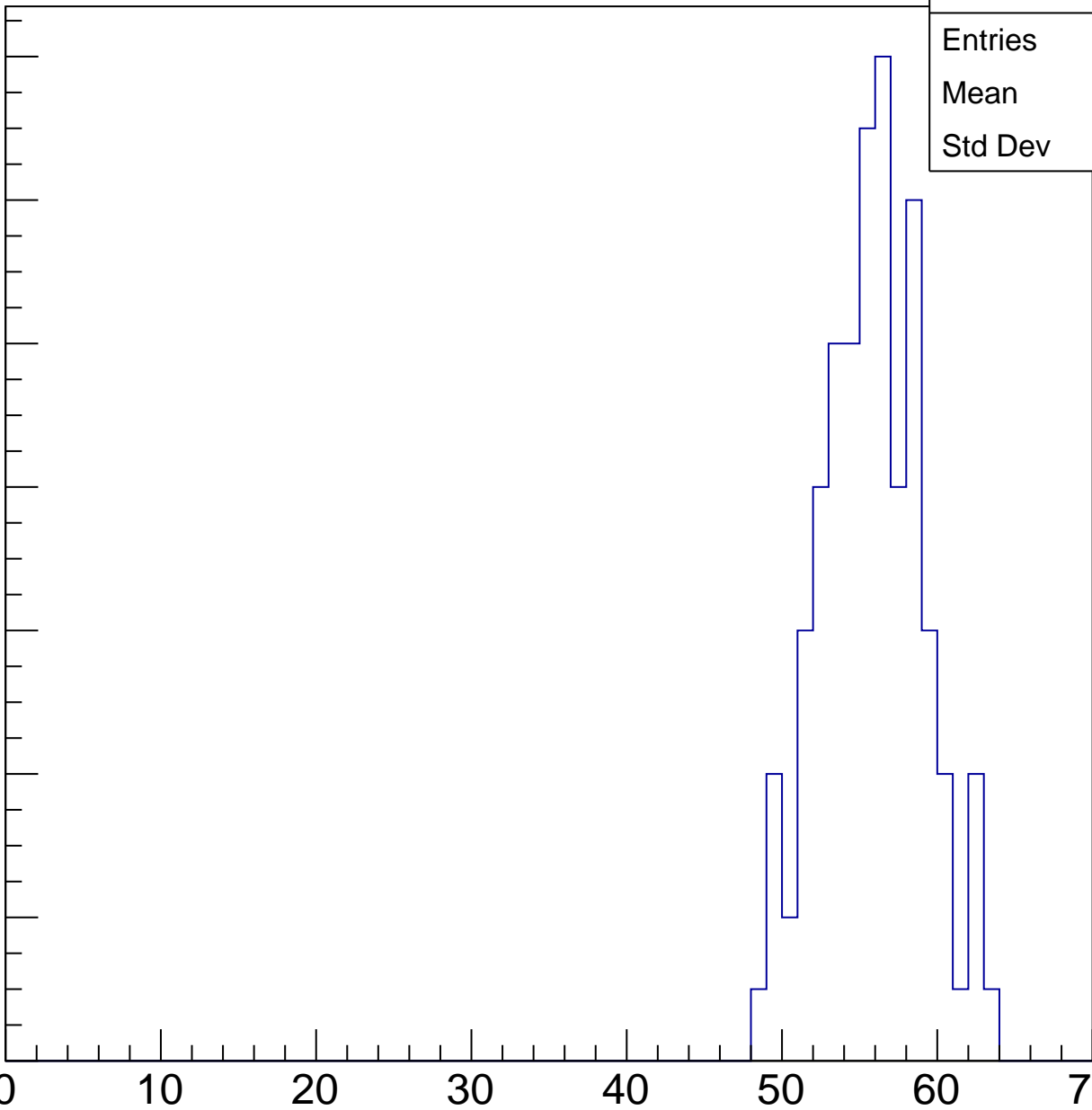
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 55.32 |
| Std Dev | 3.247 |

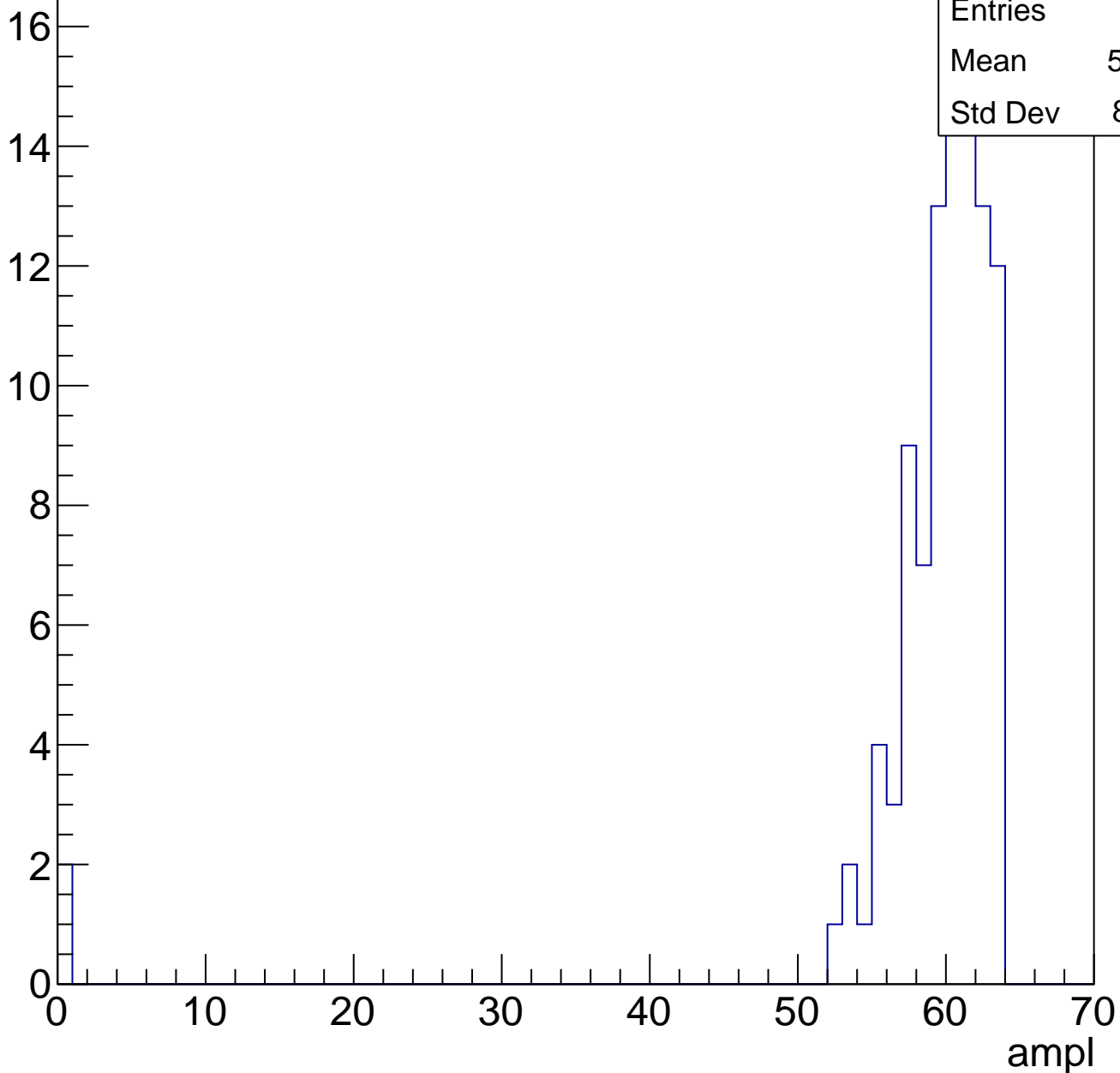
ampl



# B0L101S, U16-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

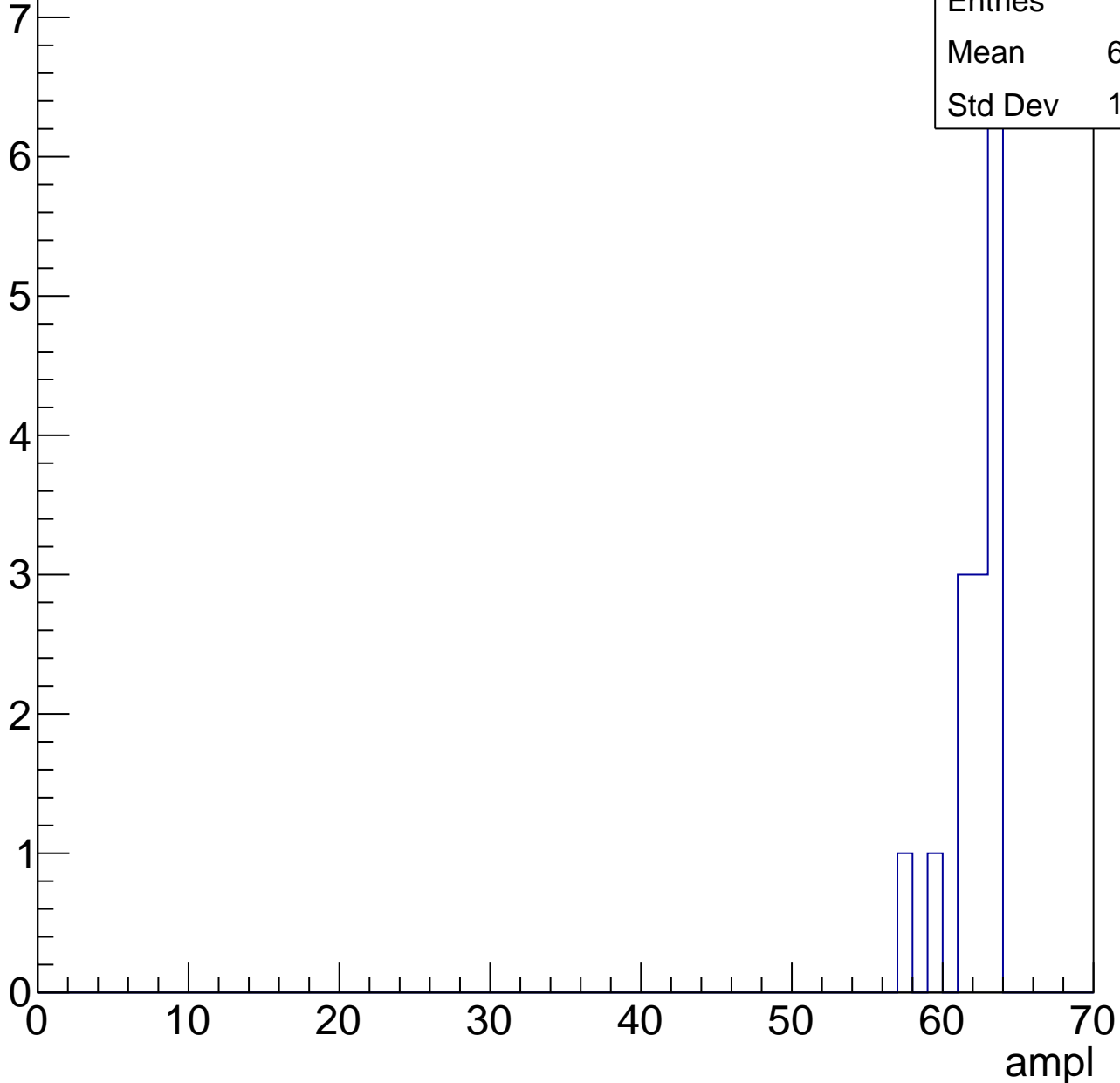


# B0L101S, U16-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 15    |
| Mean    | 61.73 |
| Std Dev | 1.692 |





# B0L101S, U16-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

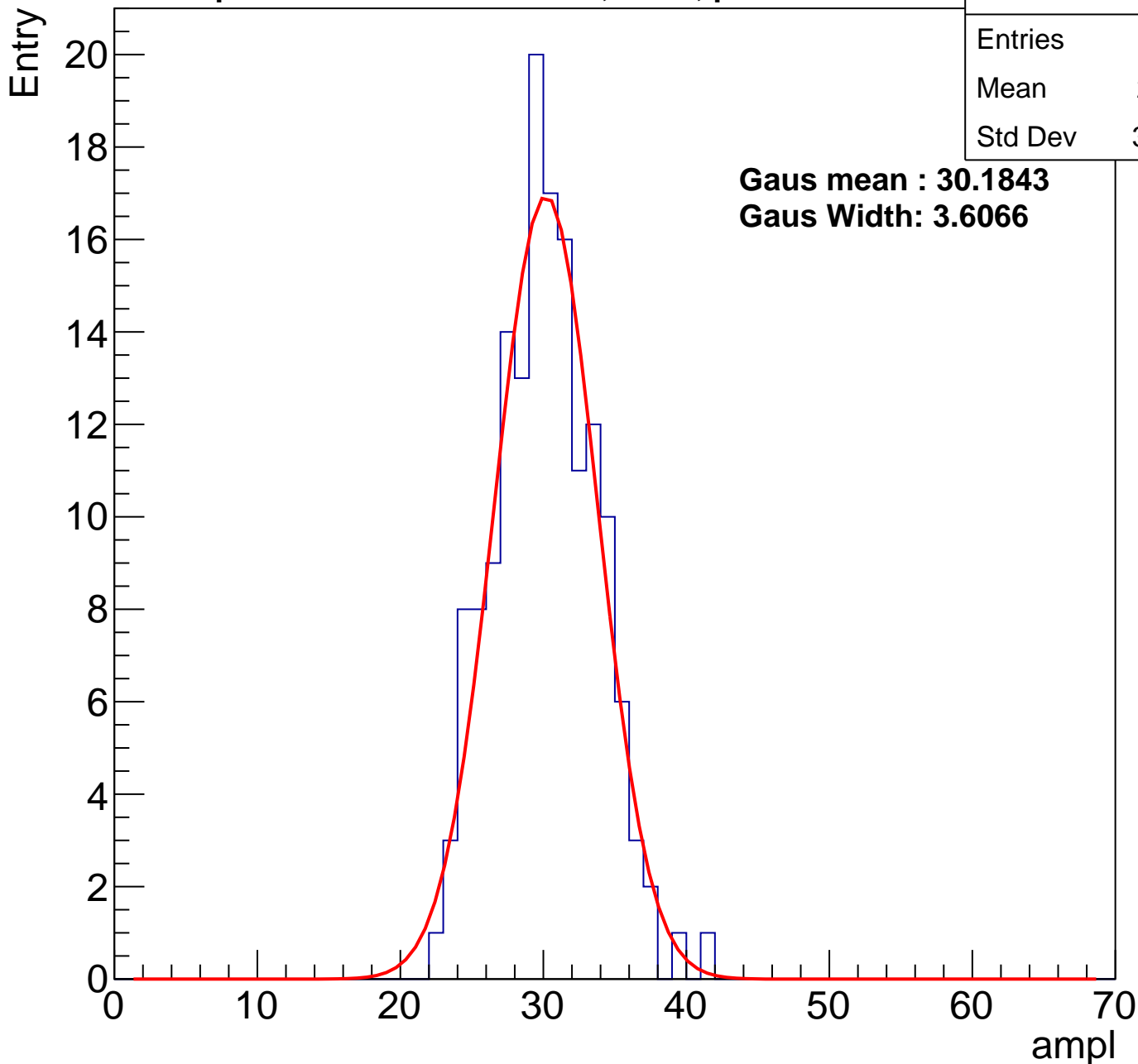
# B0L101S, U16-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 29.71 |
| Std Dev | 3.514 |

**Gaus mean : 30.1843**

**Gaus Width: 3.6066**

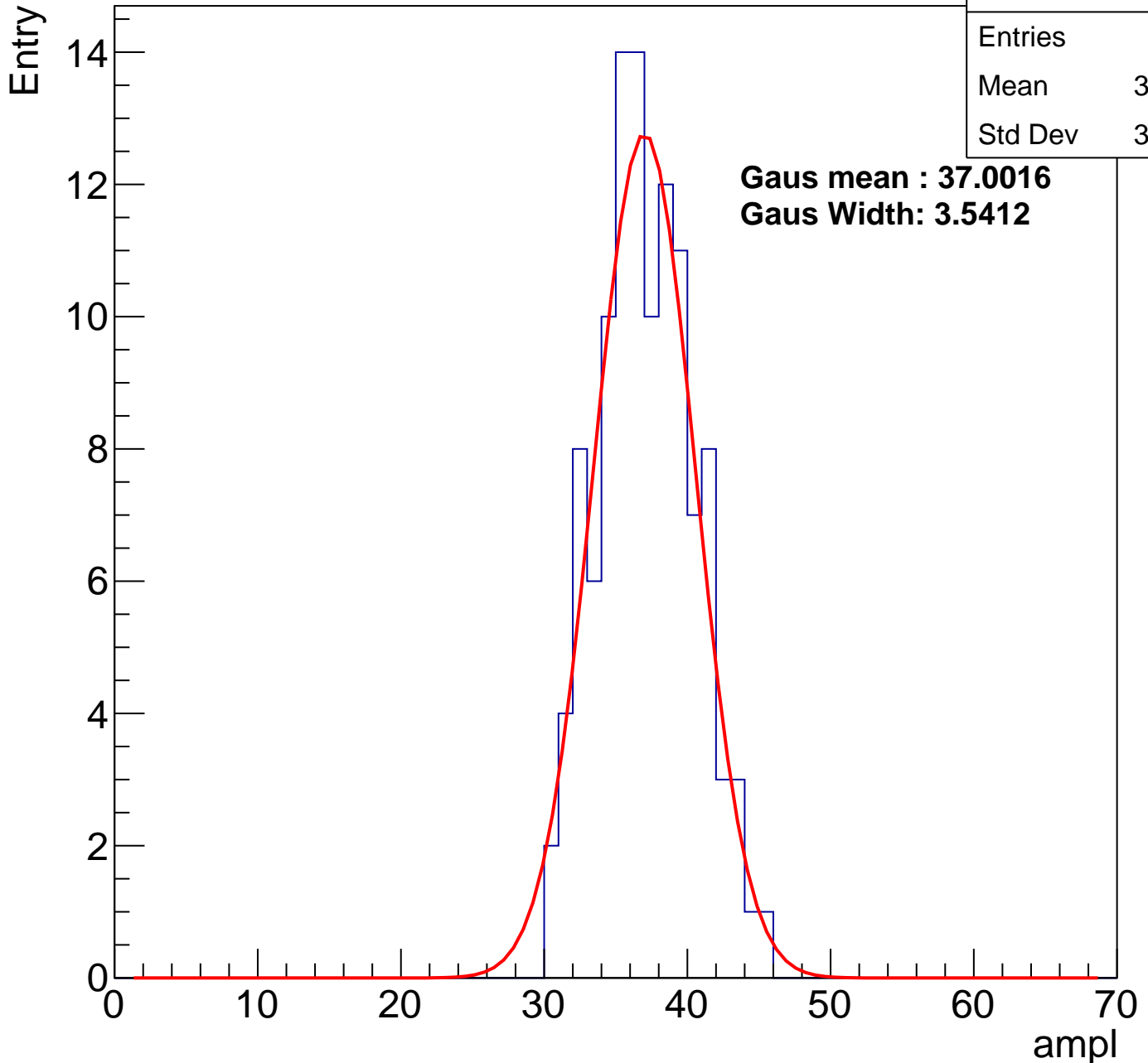


# B0L101S, U16-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 36.66 |
| Std Dev | 3.282 |

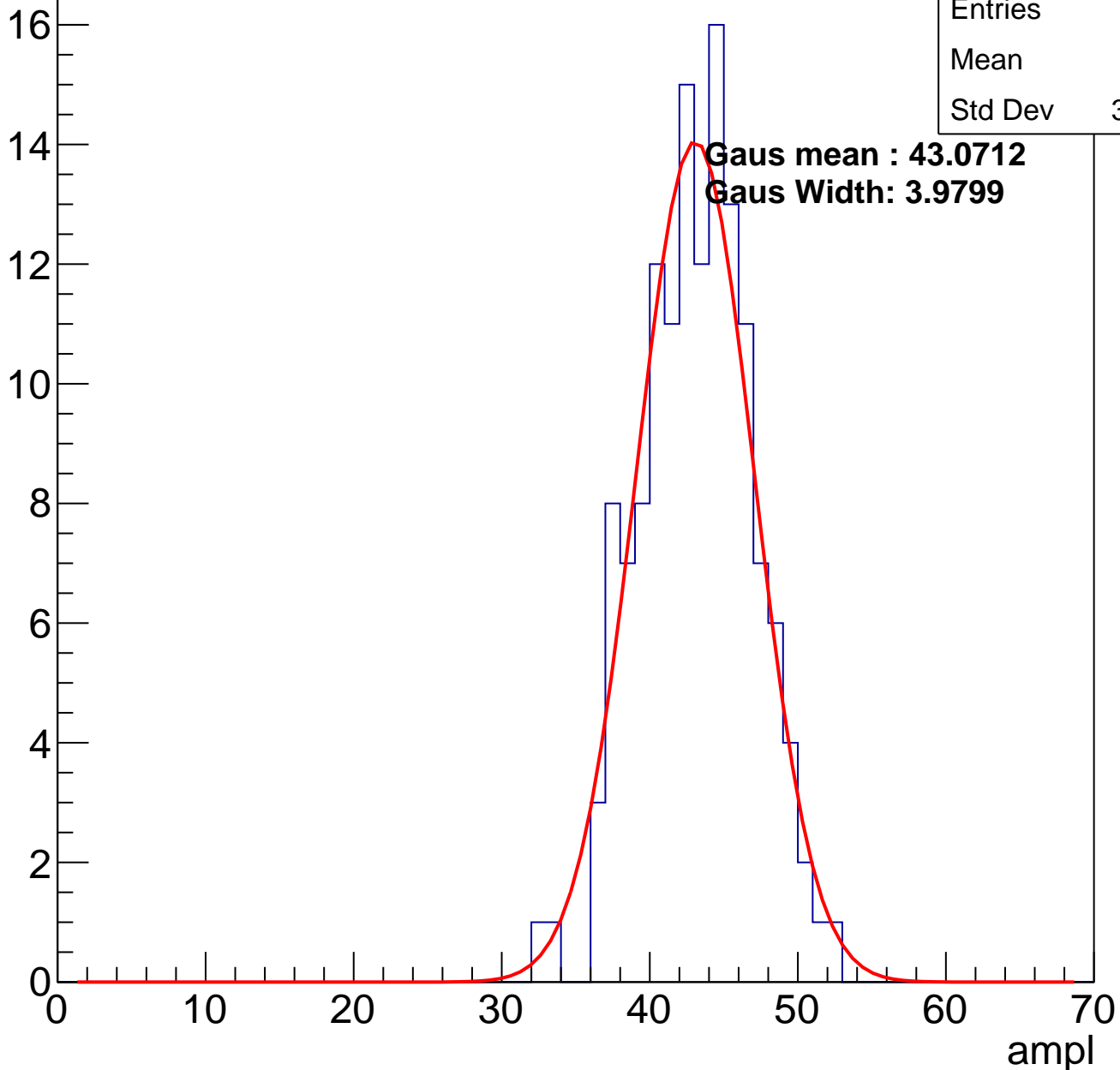
**Gaus mean : 37.0016**  
**Gaus Width: 3.5412**



# B0L101S, U16-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

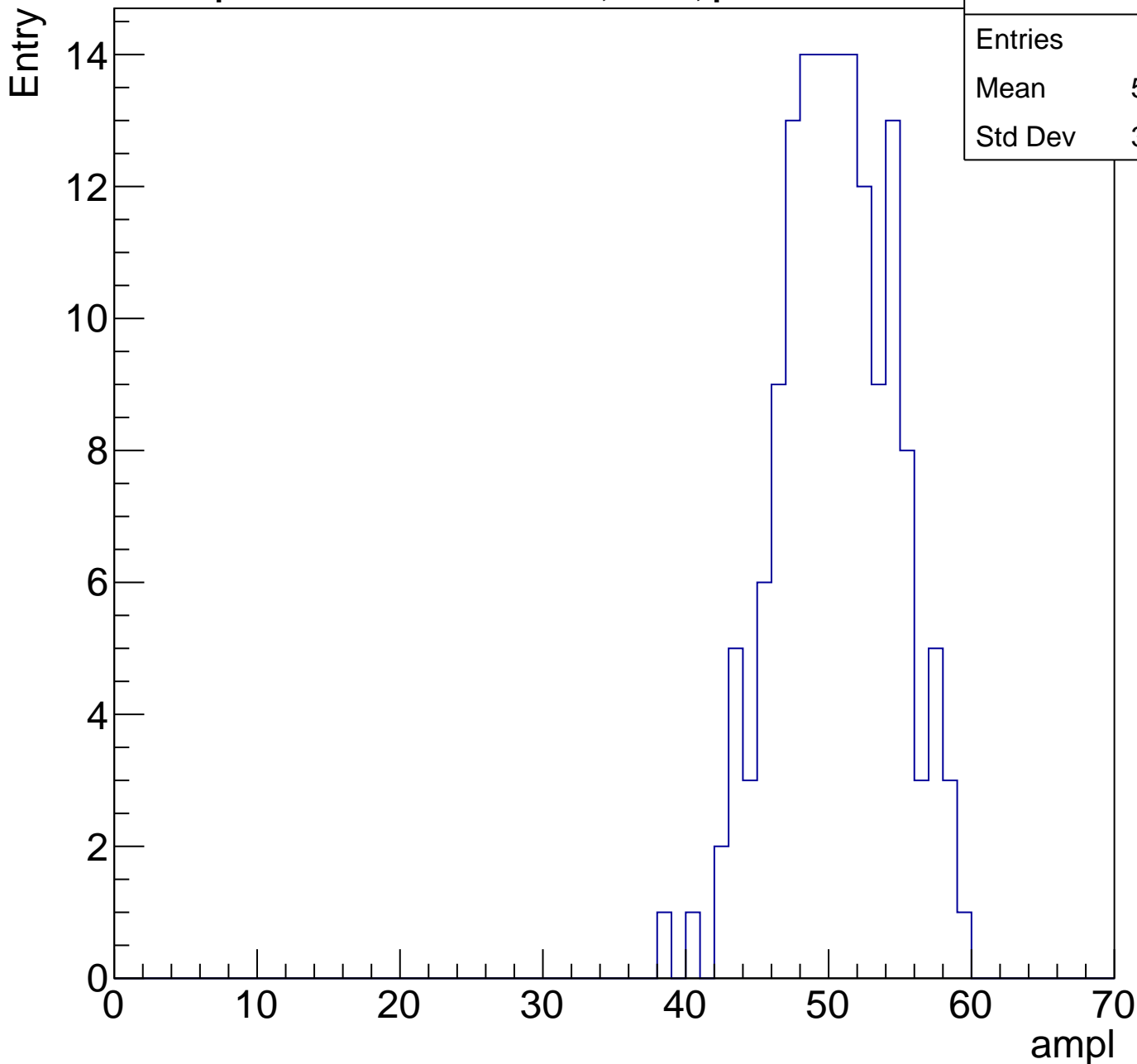
Entry



# B0L101S, U16-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 150   |
| Mean    | 50.03 |
| Std Dev | 3.986 |



# B0L101S, U16-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

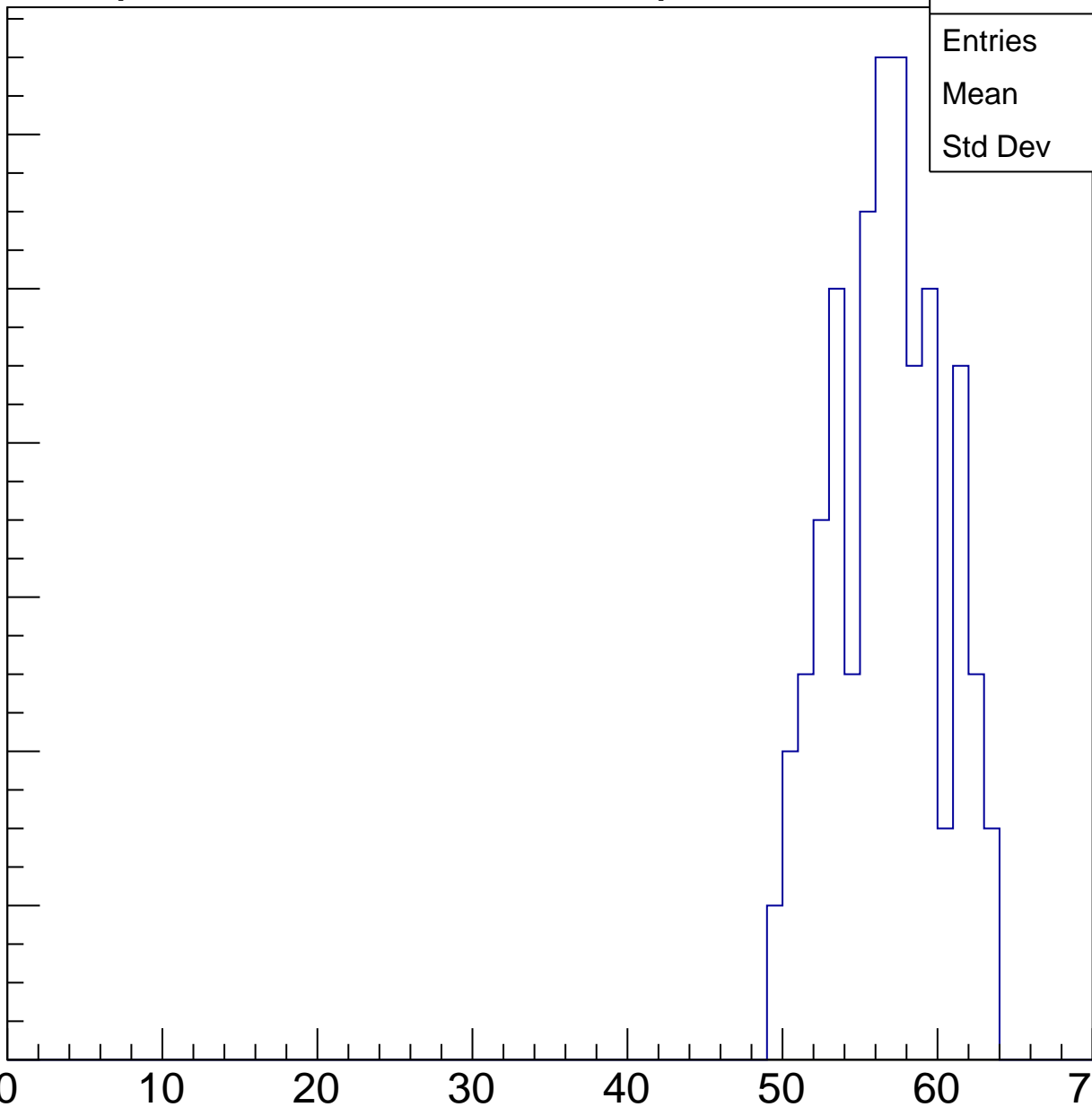
|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 56.25 |
| Std Dev | 3.496 |

Entry

12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch54, adc5

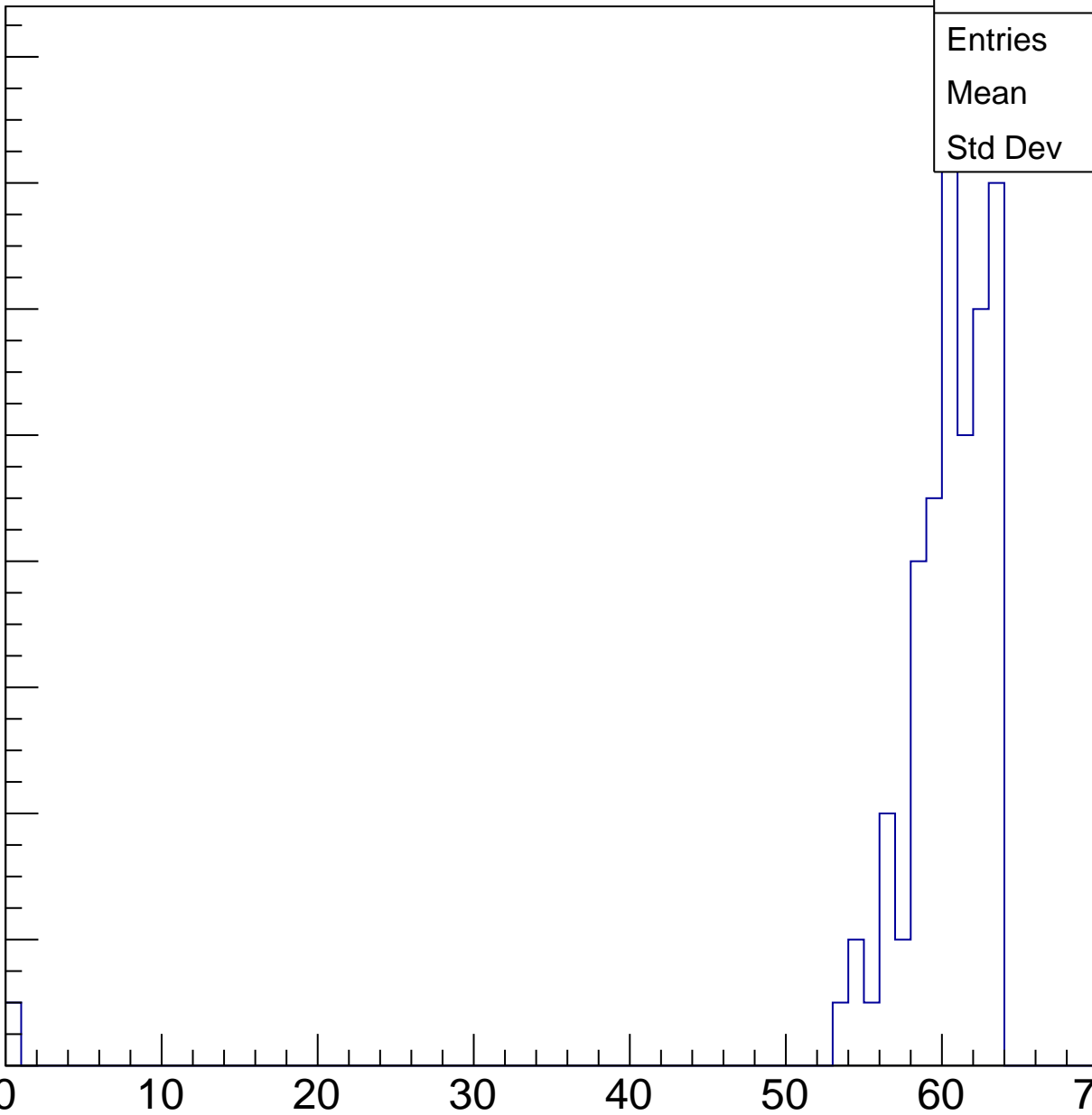
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 59.31 |
| Std Dev | 7.084 |

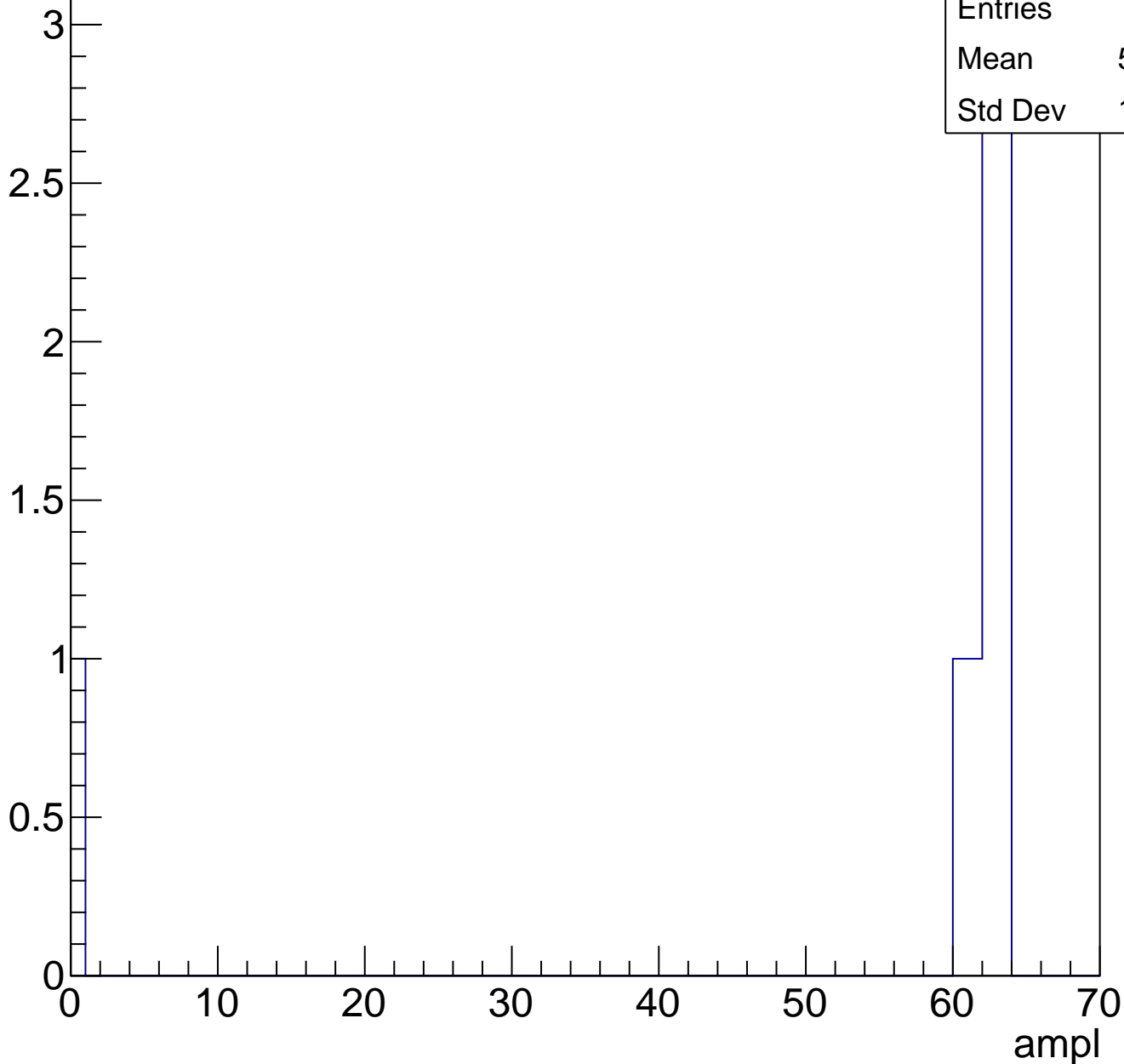
ampl



# B0L101S, U16-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

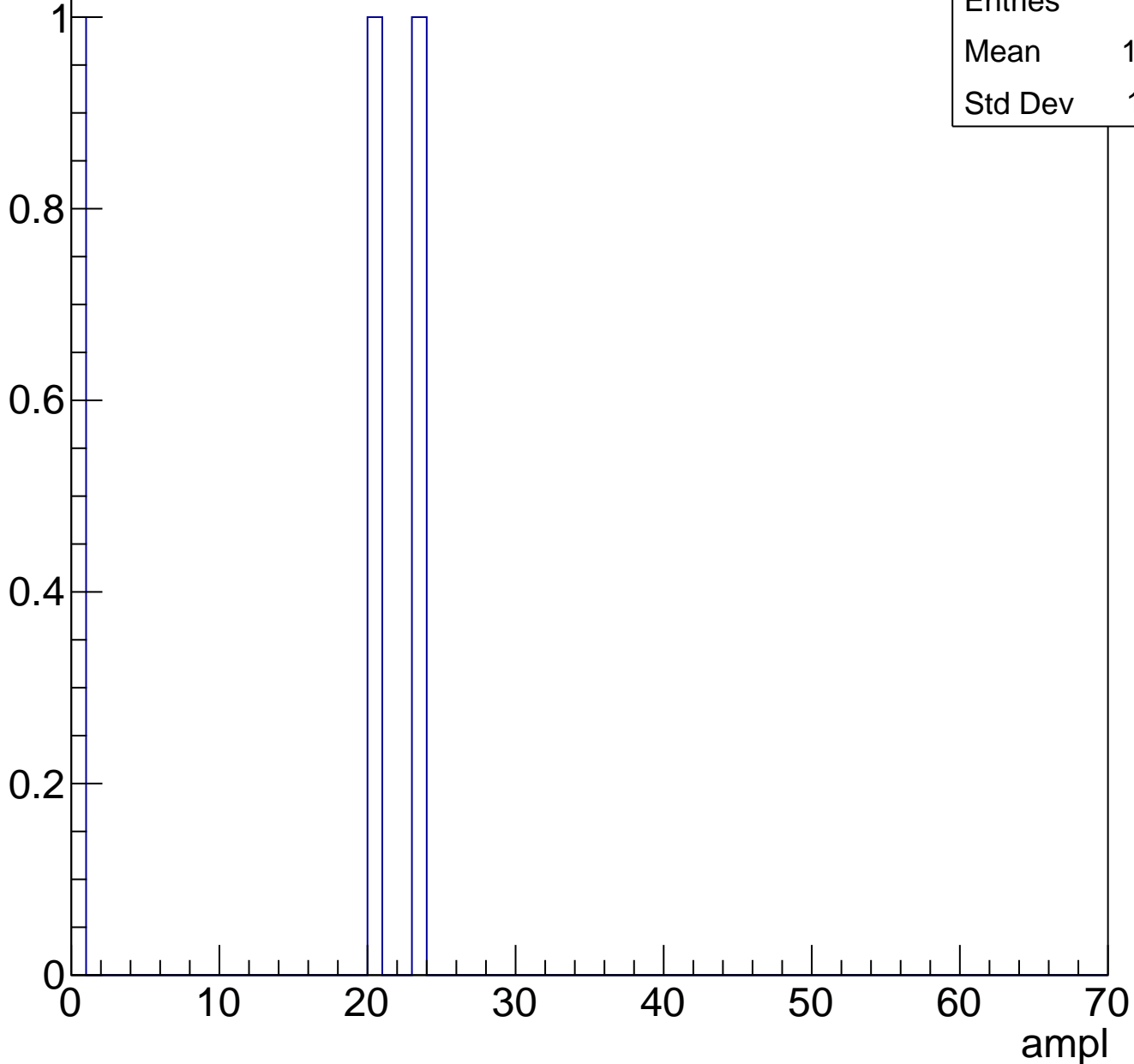




# B0L101S, U16-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 14.33 |
| Std Dev | 10.21 |

# B0L101S, U16-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 29.33 |
| Std Dev | 4.513 |

**Gaus mean : 30.3823**

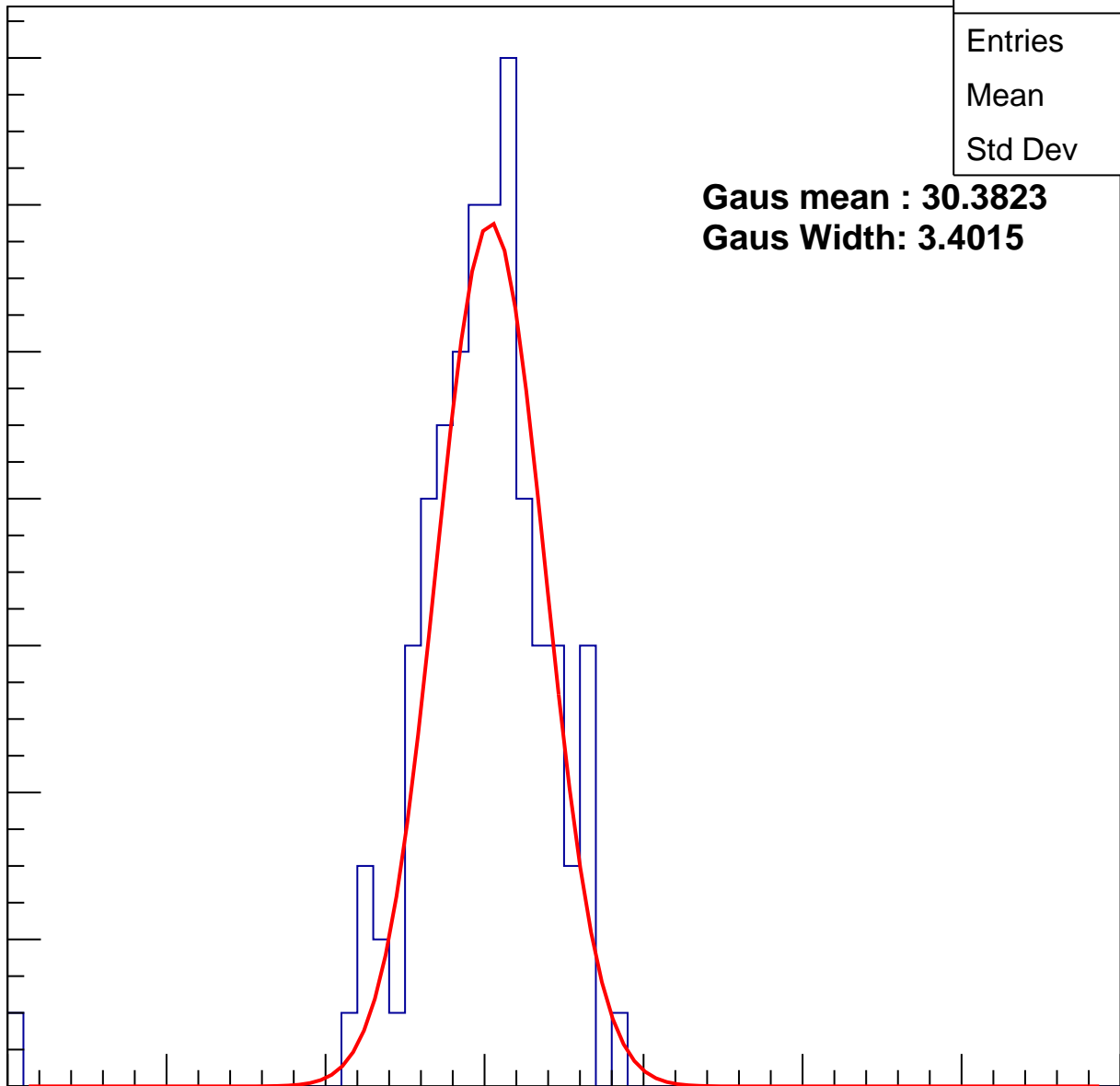
**Gaus Width: 3.4015**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

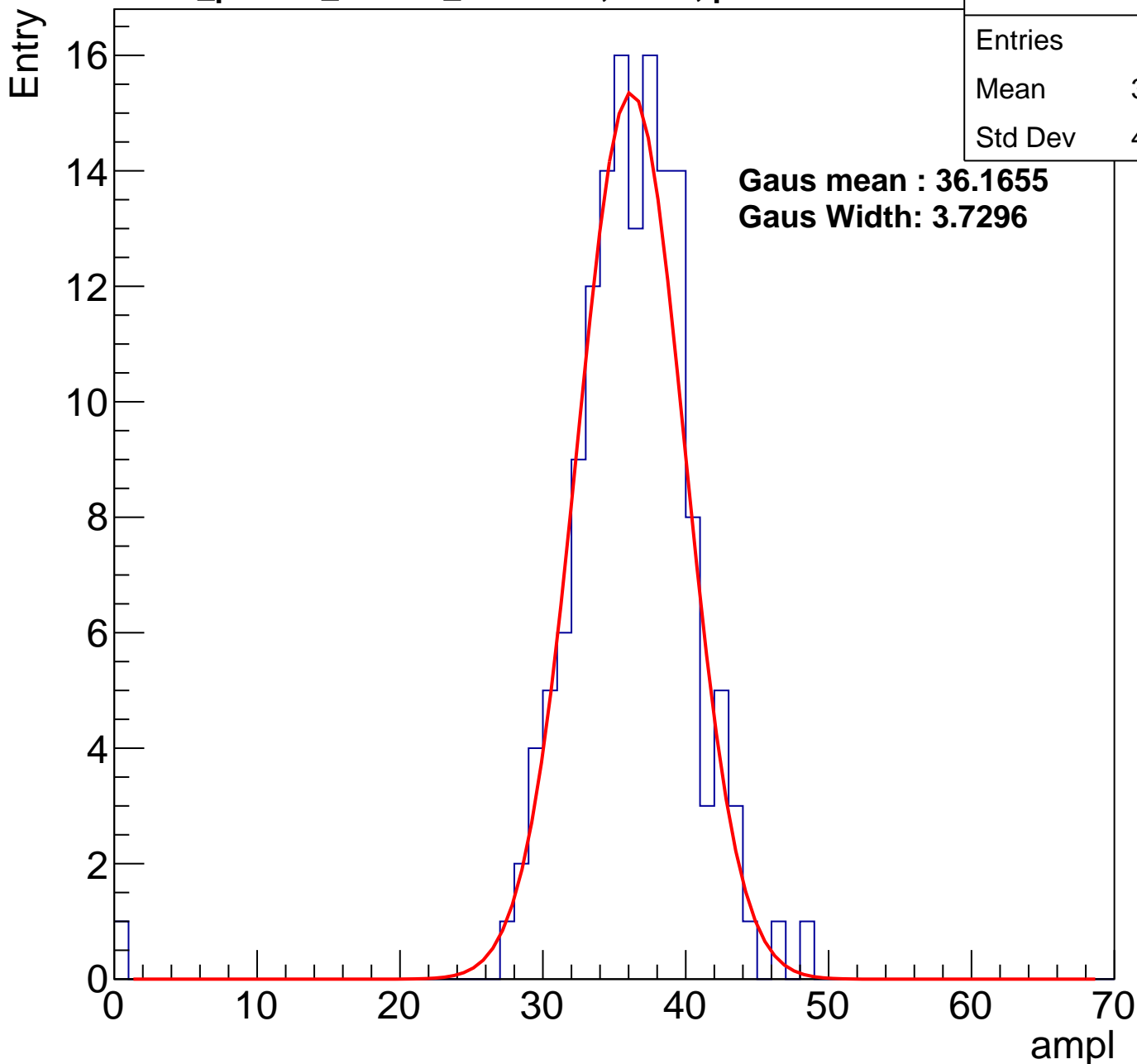


# B0L101S, U16-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 35.66 |
| Std Dev | 4.758 |

**Gaus mean : 36.1655**  
**Gaus Width: 3.7296**



# B0L101S, U16-ch55, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

43.87

Std Dev

3.71

**Gaus mean : 44.3367**

**Gaus Width: 3.7683**

0

10

20

30

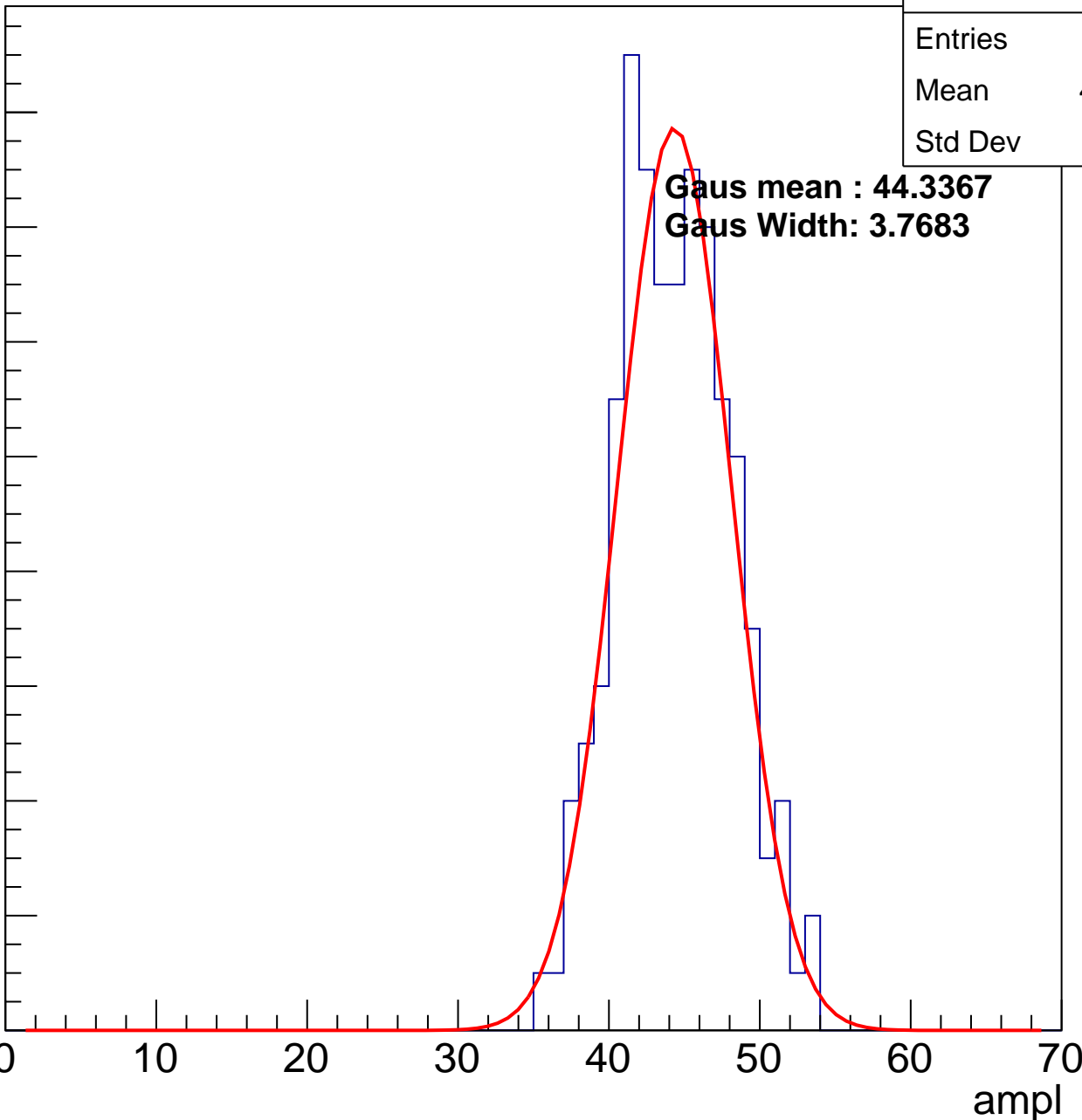
40

50

60

70

ampl

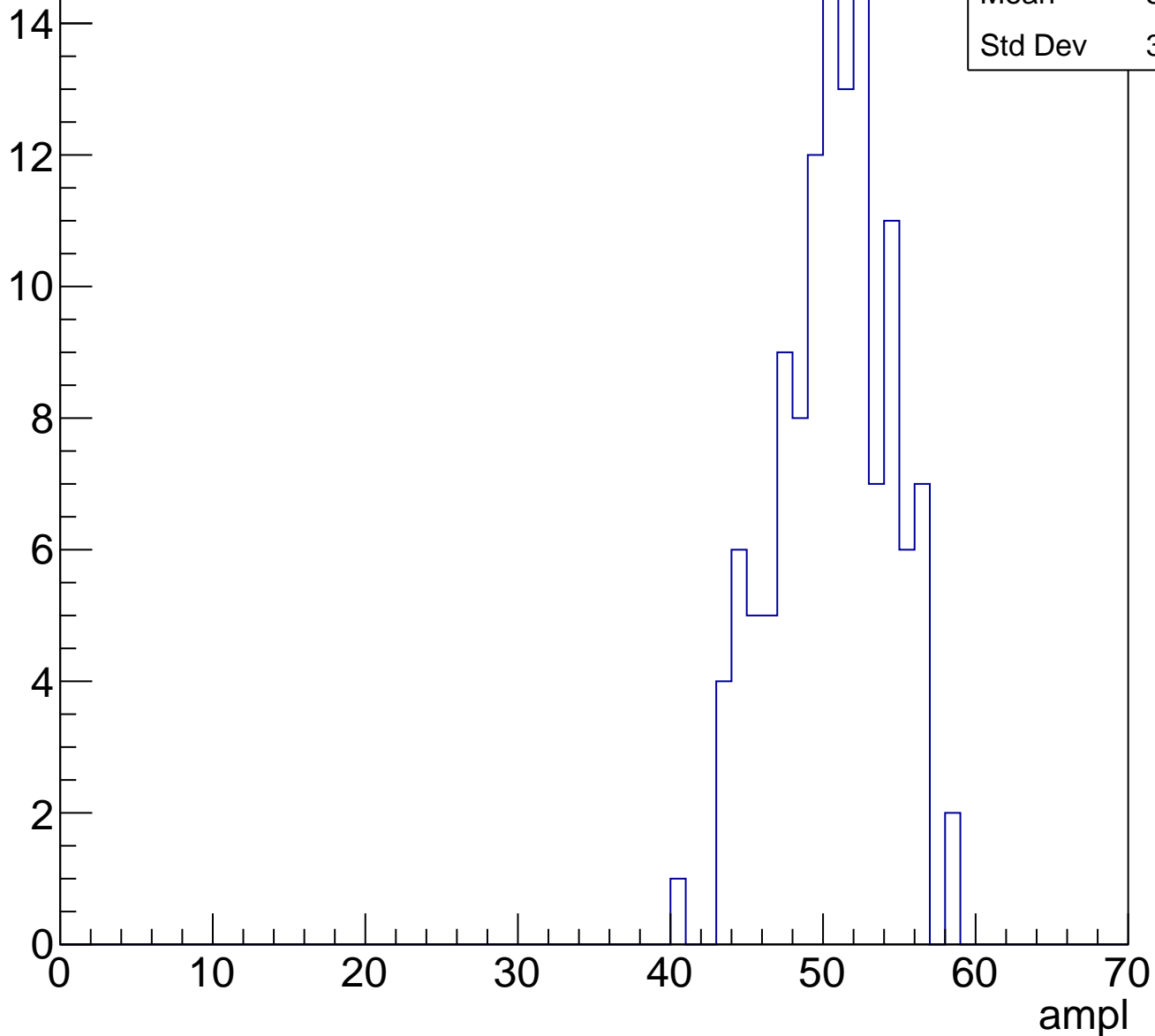


# B0L101S, U16-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 50.17 |
| Std Dev | 3.652 |

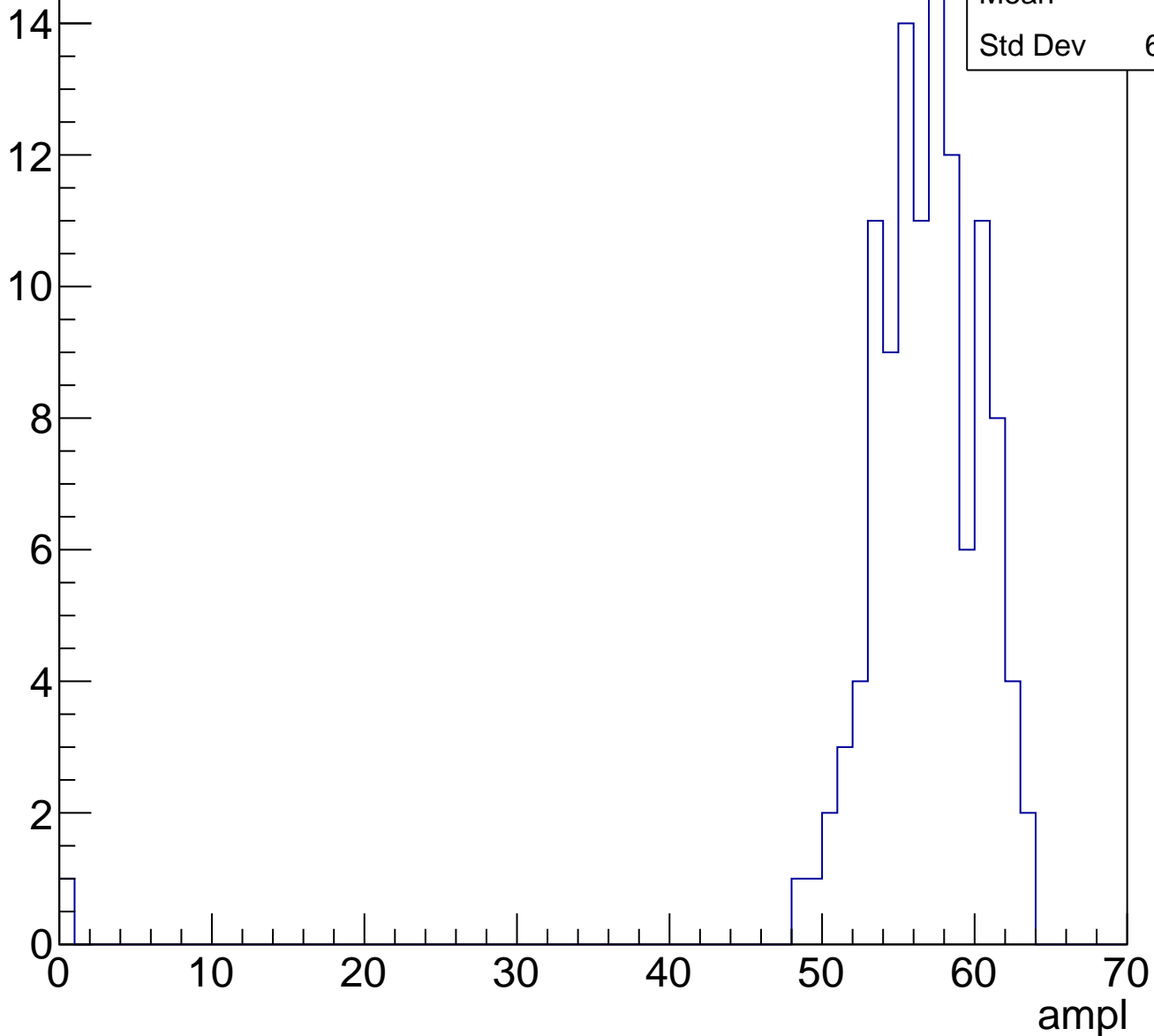
Entry



# B0L101S, U16-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch55, adc5

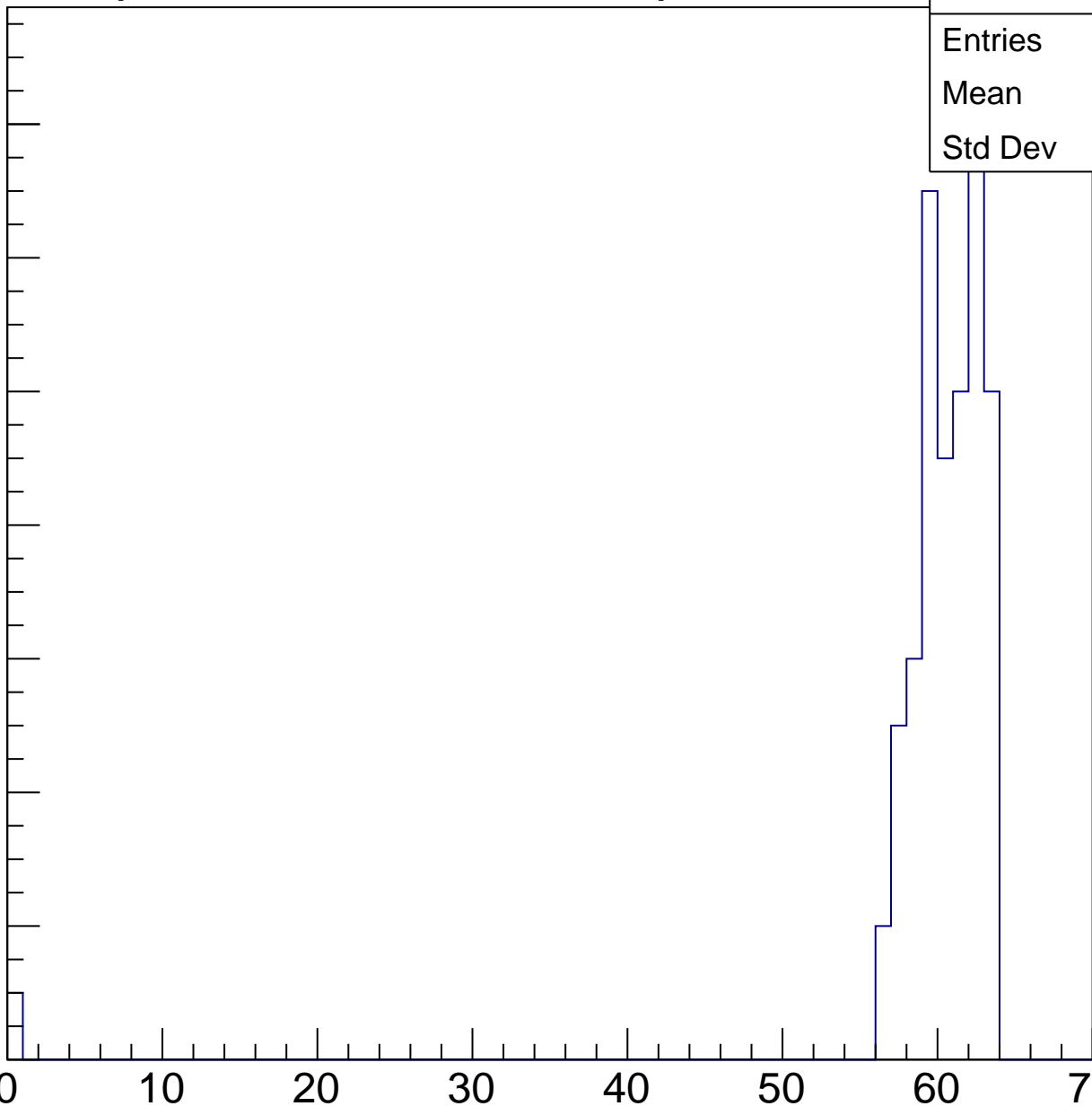
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 59.46 |
| Std Dev | 7.367 |

ampl

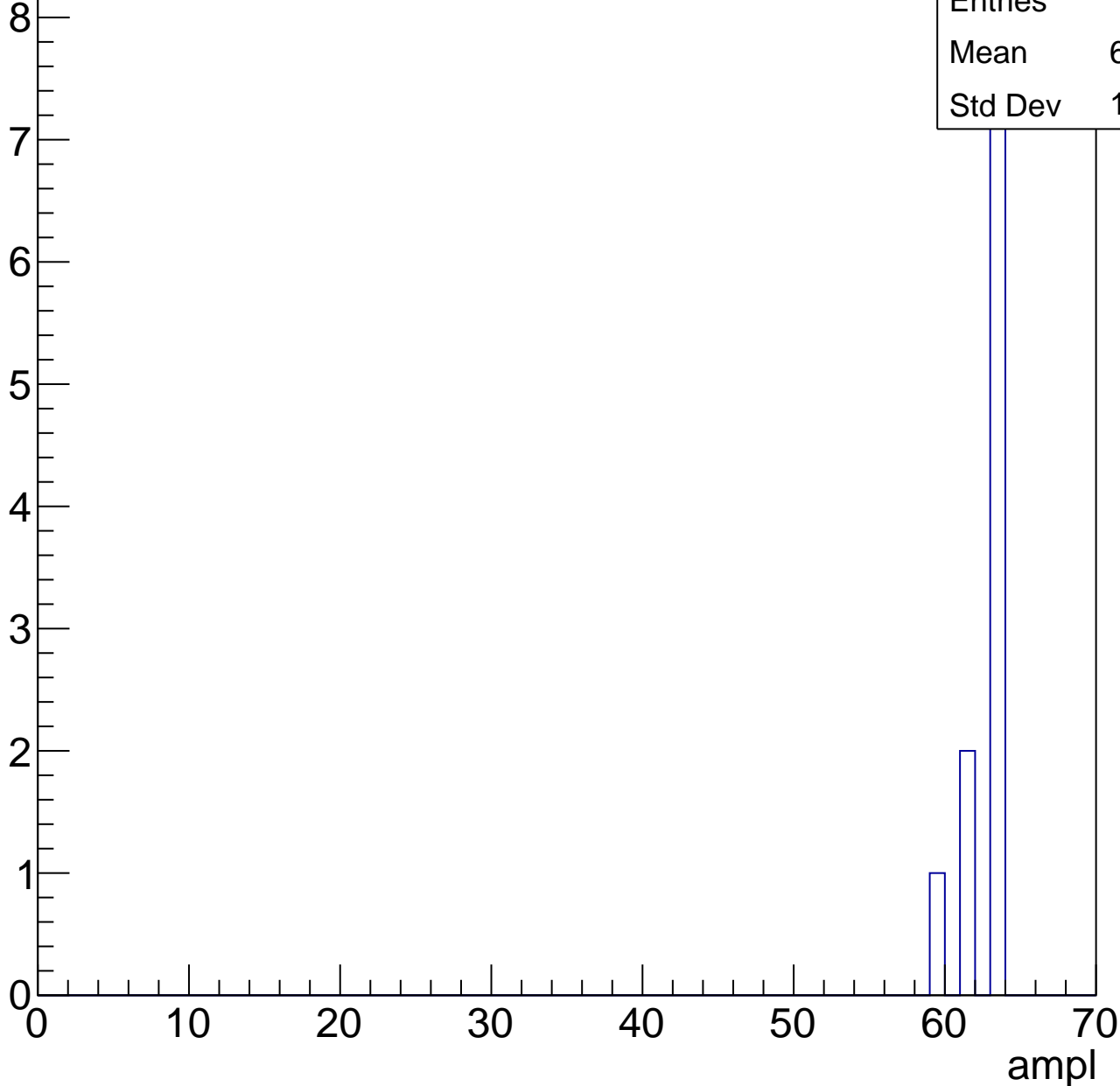


# B0L101S, U16-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 62.27 |
| Std Dev | 1.286 |





# B0L101S, U16-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch56, adc0

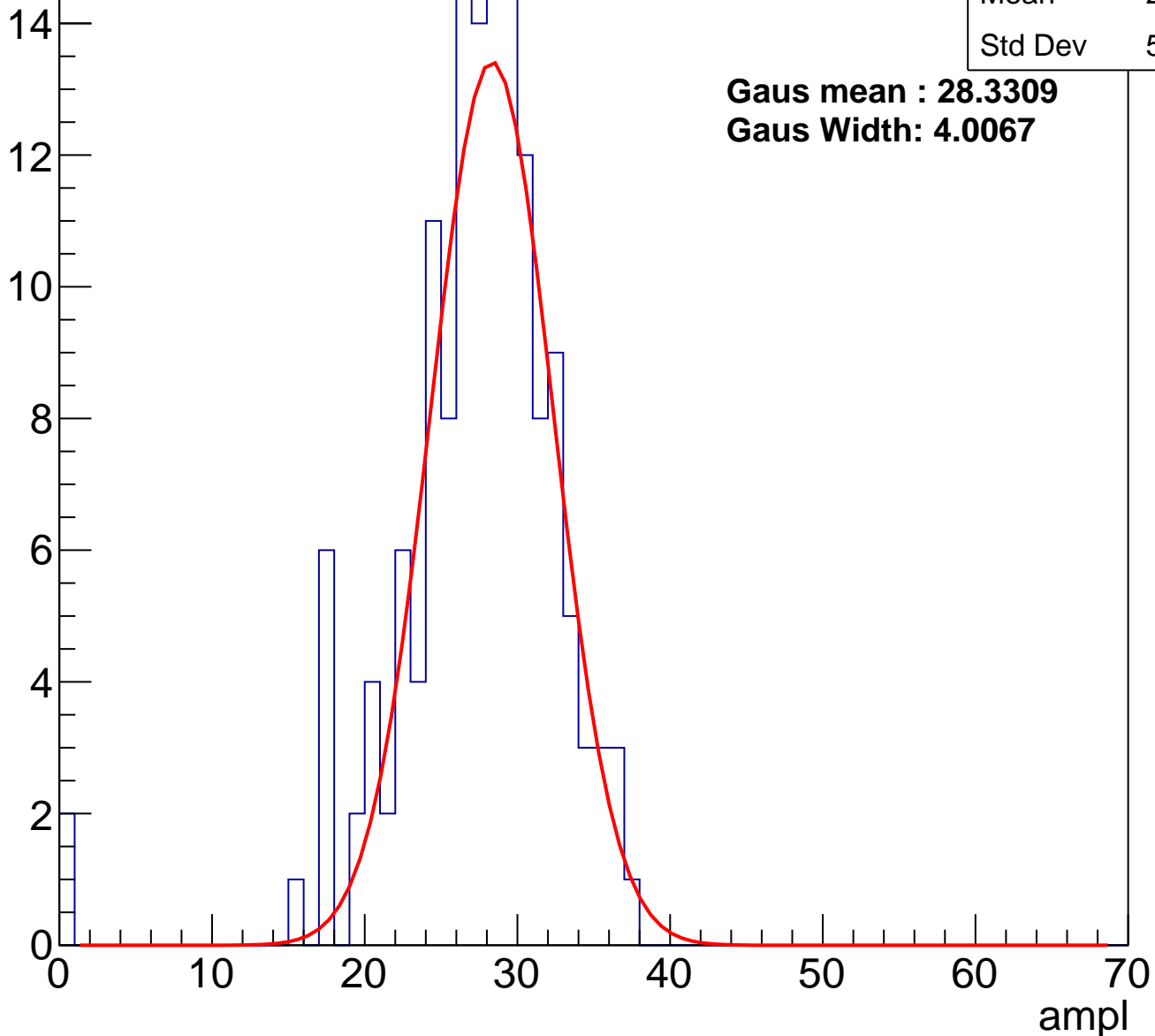
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 26.85 |
| Std Dev | 5.405 |

**Gaus mean : 28.3309**

**Gaus Width: 4.0067**

Entry



# B0L101S, U16-ch56, adc1

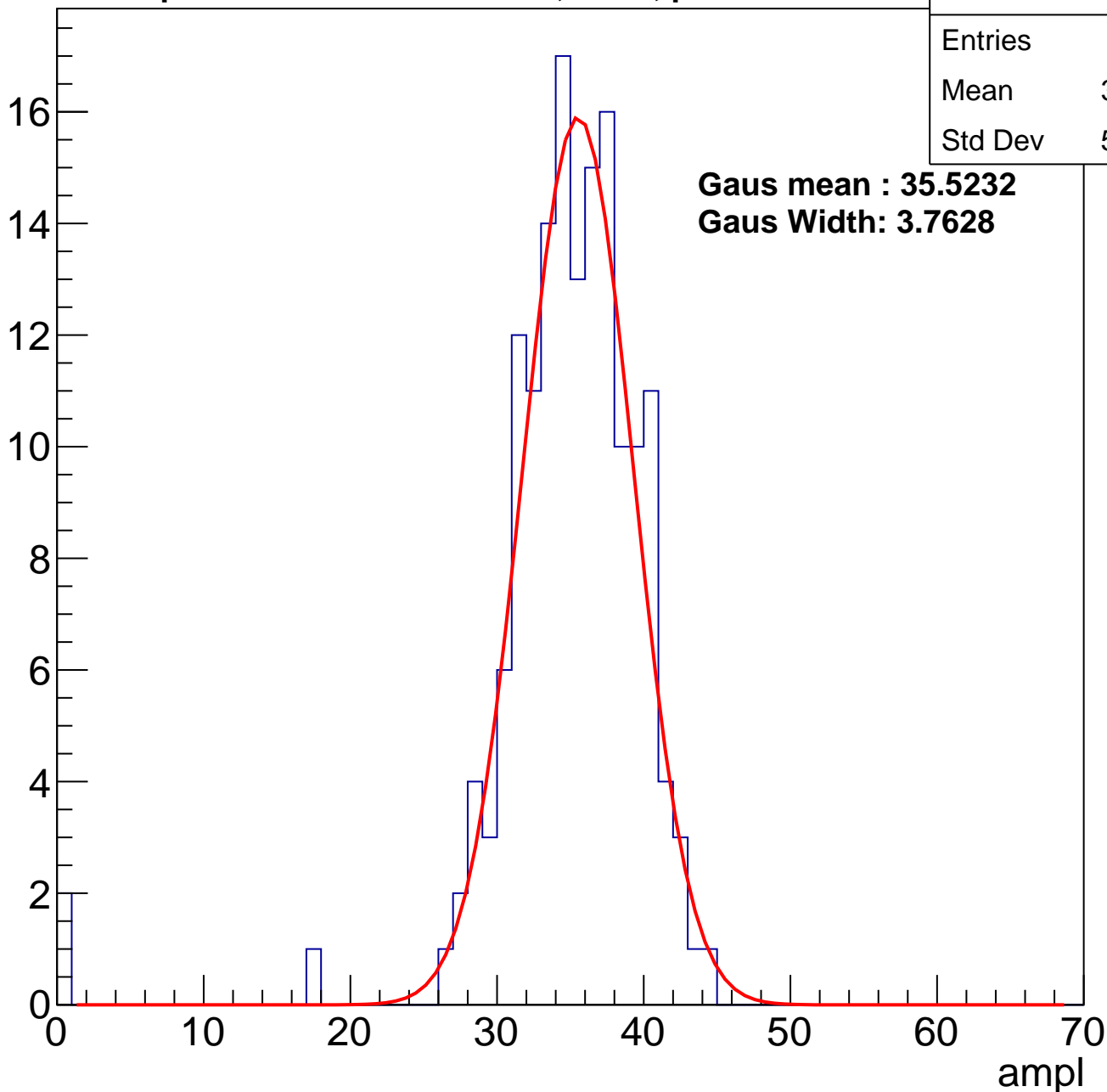
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 157   |
| Mean    | 34.48 |
| Std Dev | 5.529 |

**Gaus mean : 35.5232**

**Gaus Width: 3.7628**

Entry



# B0L101S, U16-ch56, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries 110

Mean 41.14

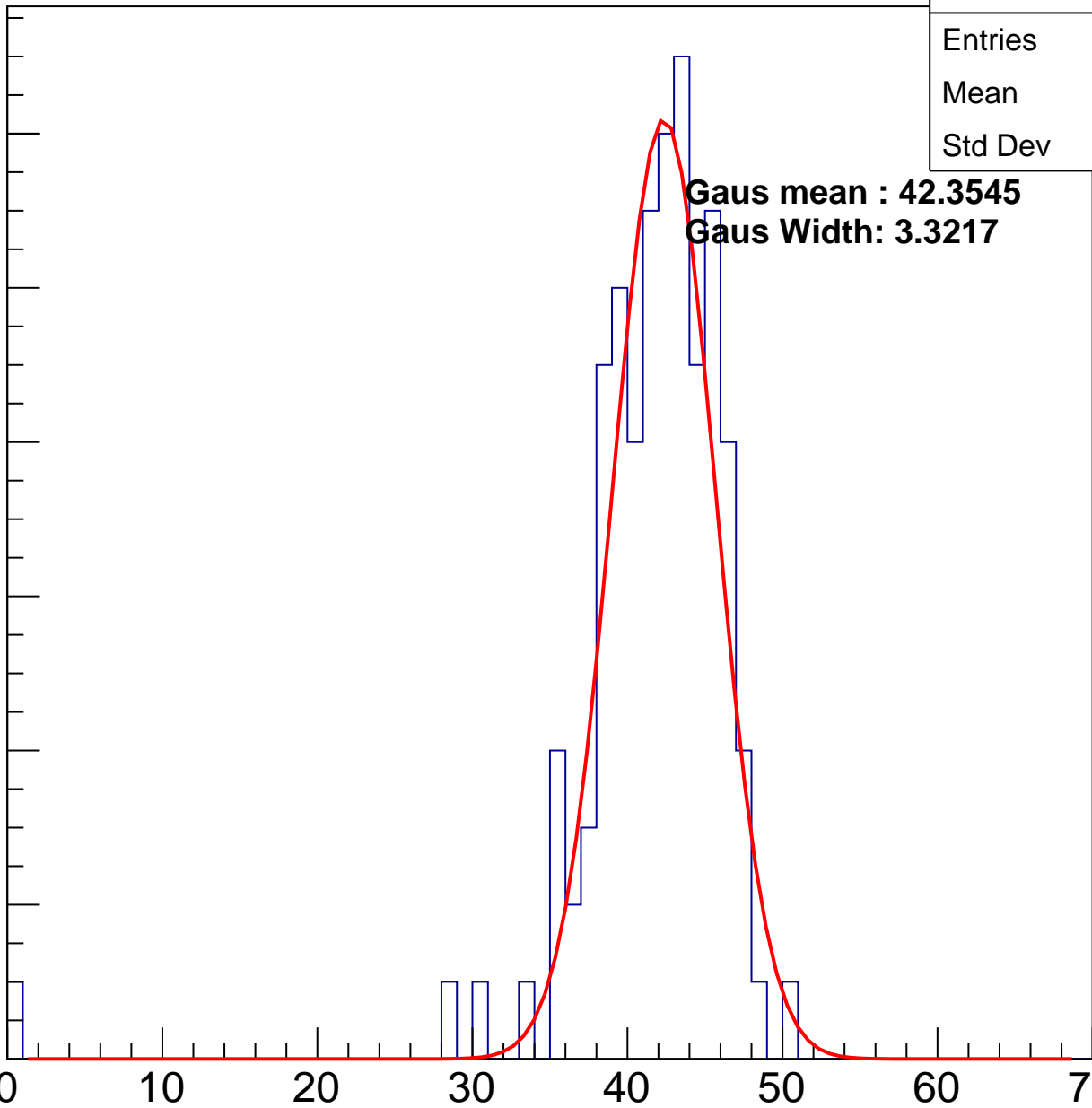
Std Dev 5.393

**Gaus mean : 42.3545**

**Gaus Width: 3.3217**

ampl

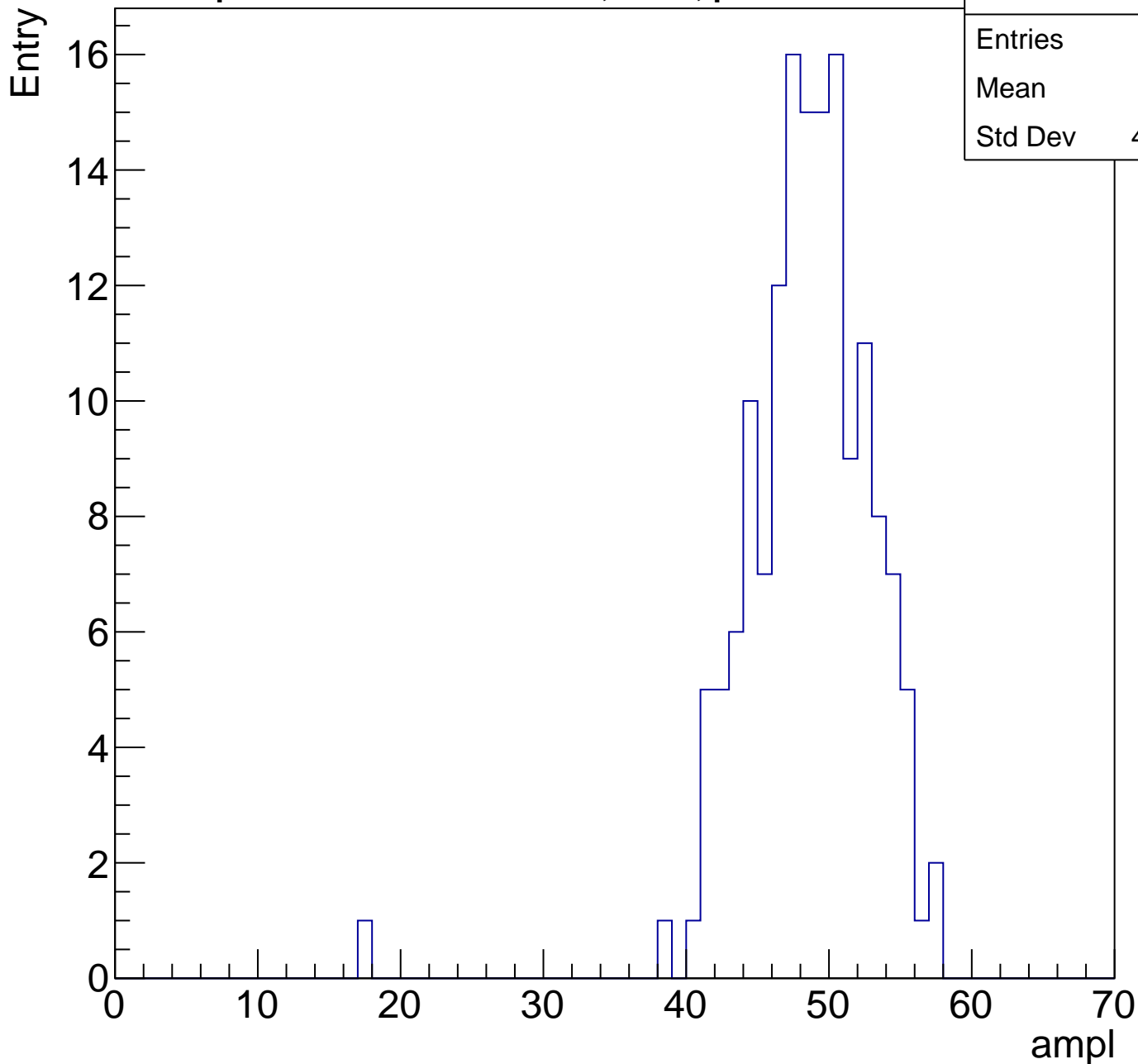
0 10 20 30 40 50 60 70



# B0L101S, U16-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 153   |
| Mean    | 48.1  |
| Std Dev | 4.605 |



# B0L101S, U16-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

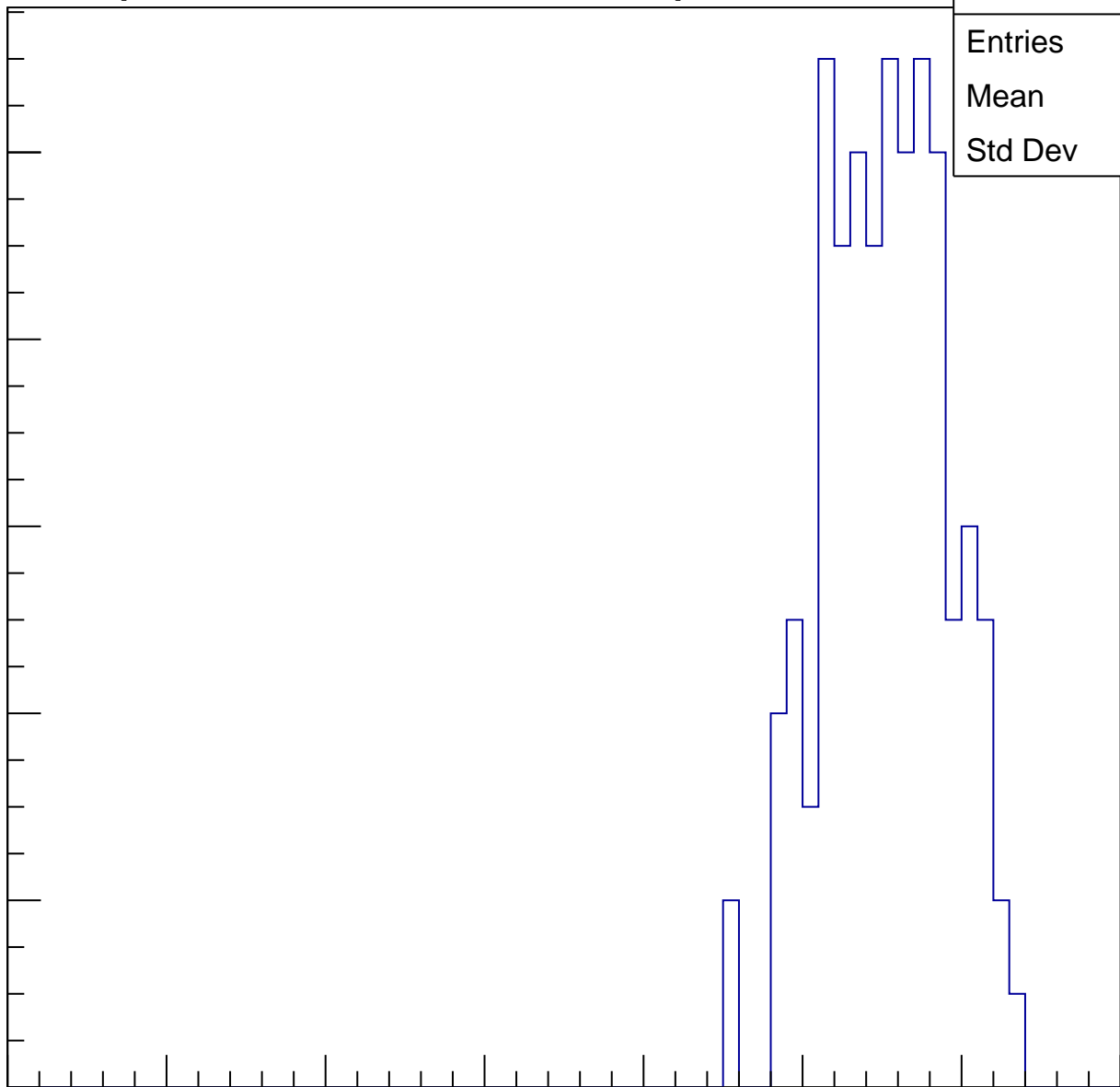
40

50

60

ampl

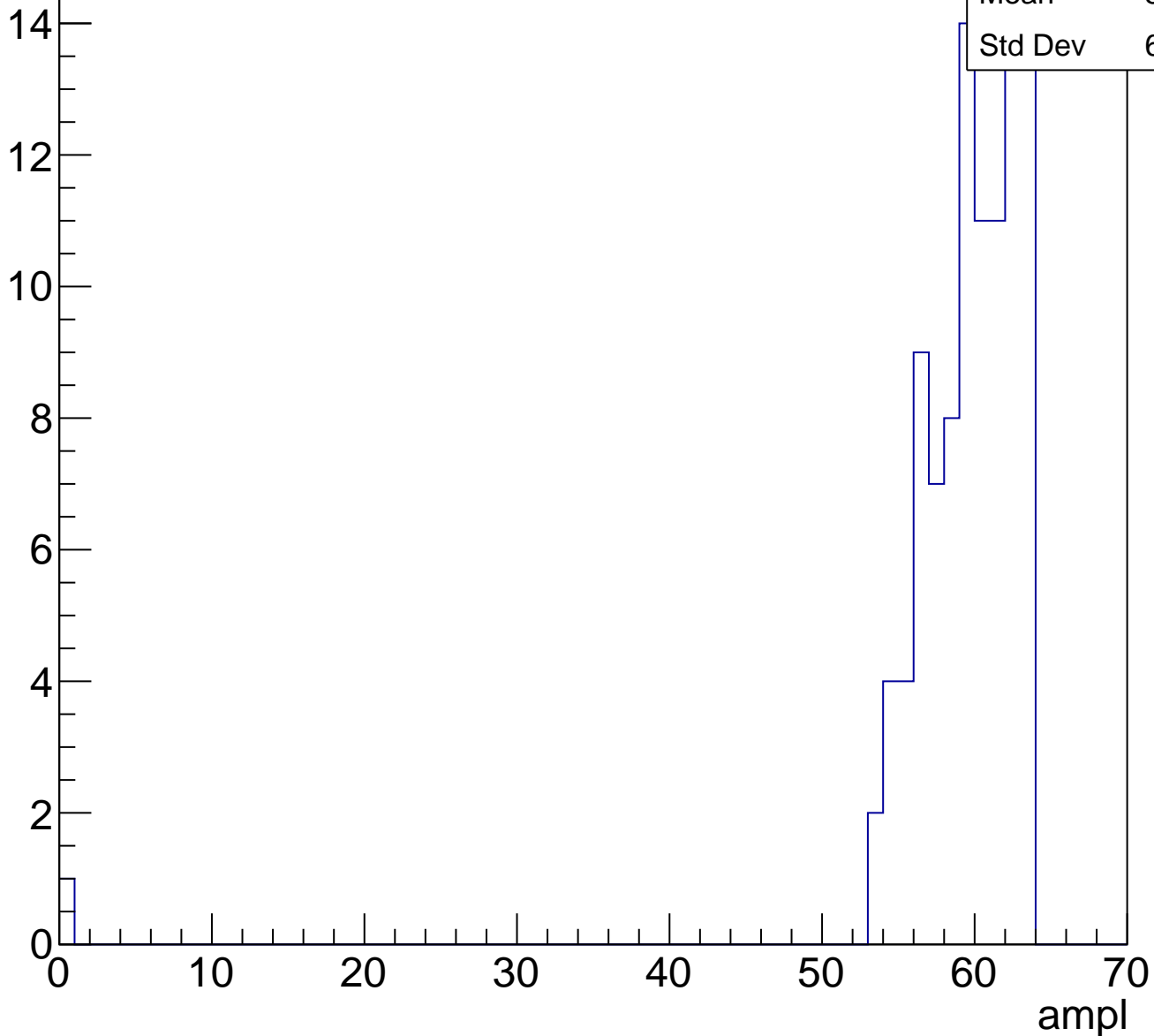
|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 54.75 |
| Std Dev | 3.822 |



# B0L101S, U16-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

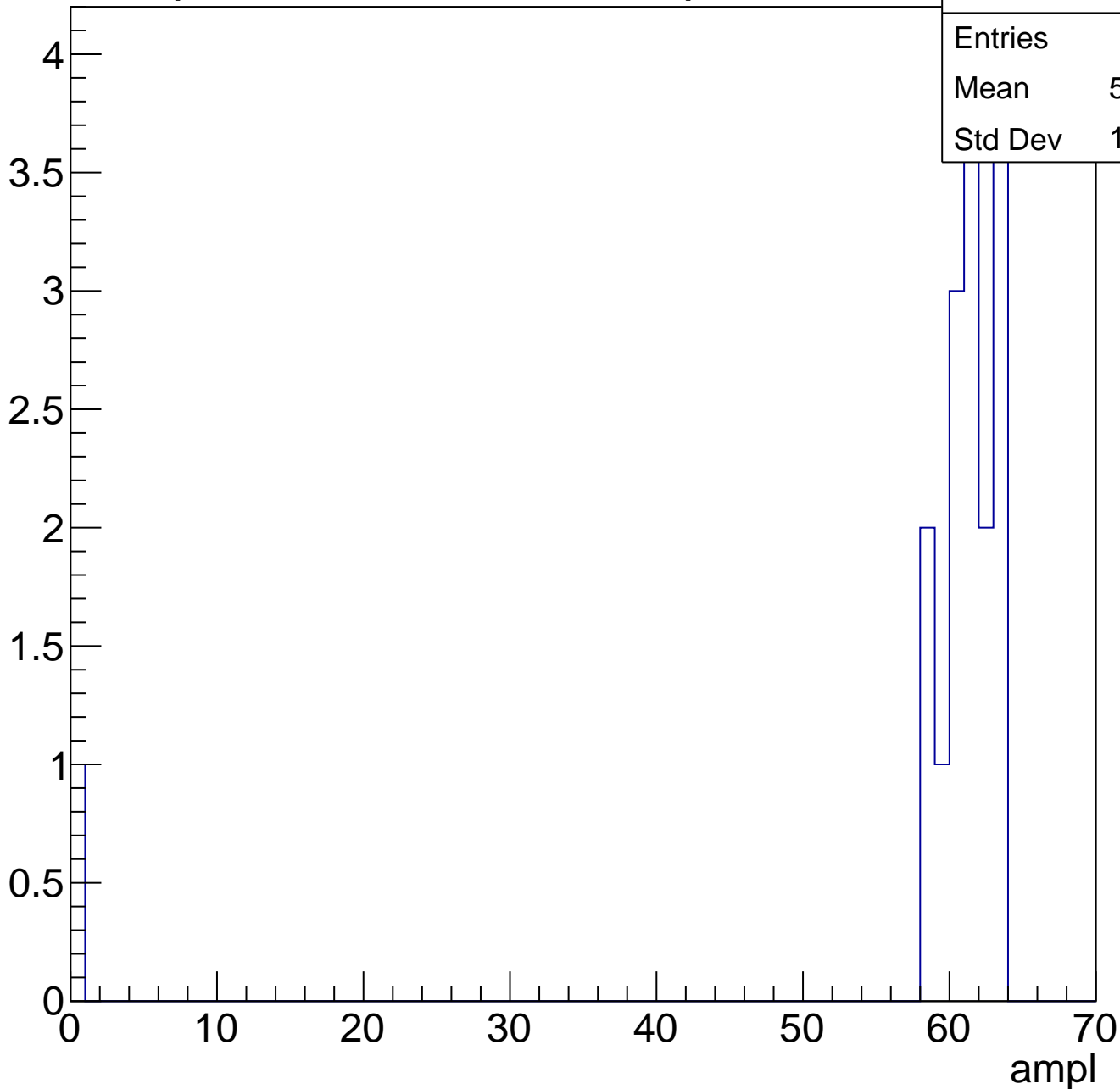
Entry



# B0L101S, U16-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

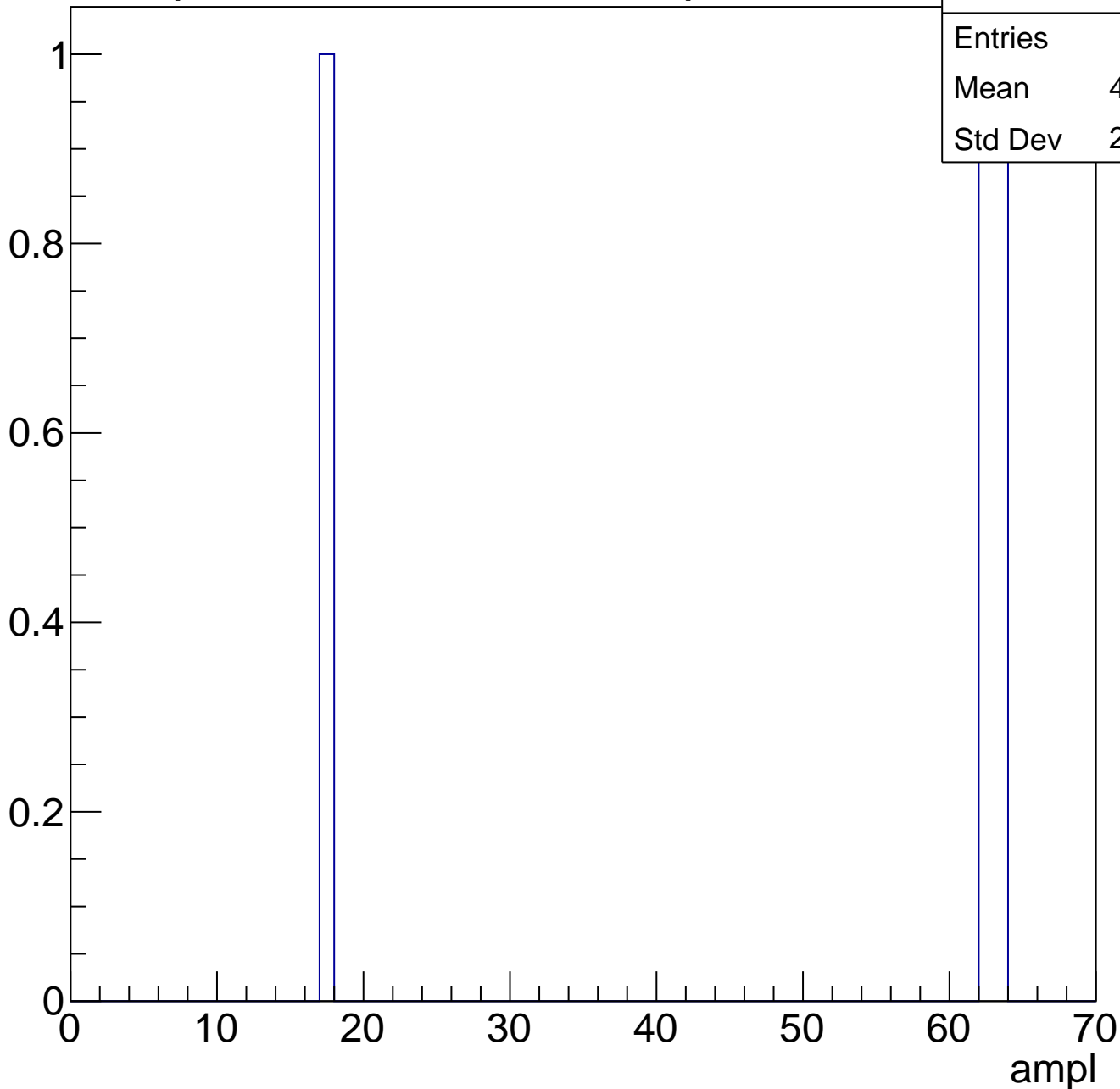




# B0L101S, U16-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 47.33 |
| Std Dev | 21.45 |

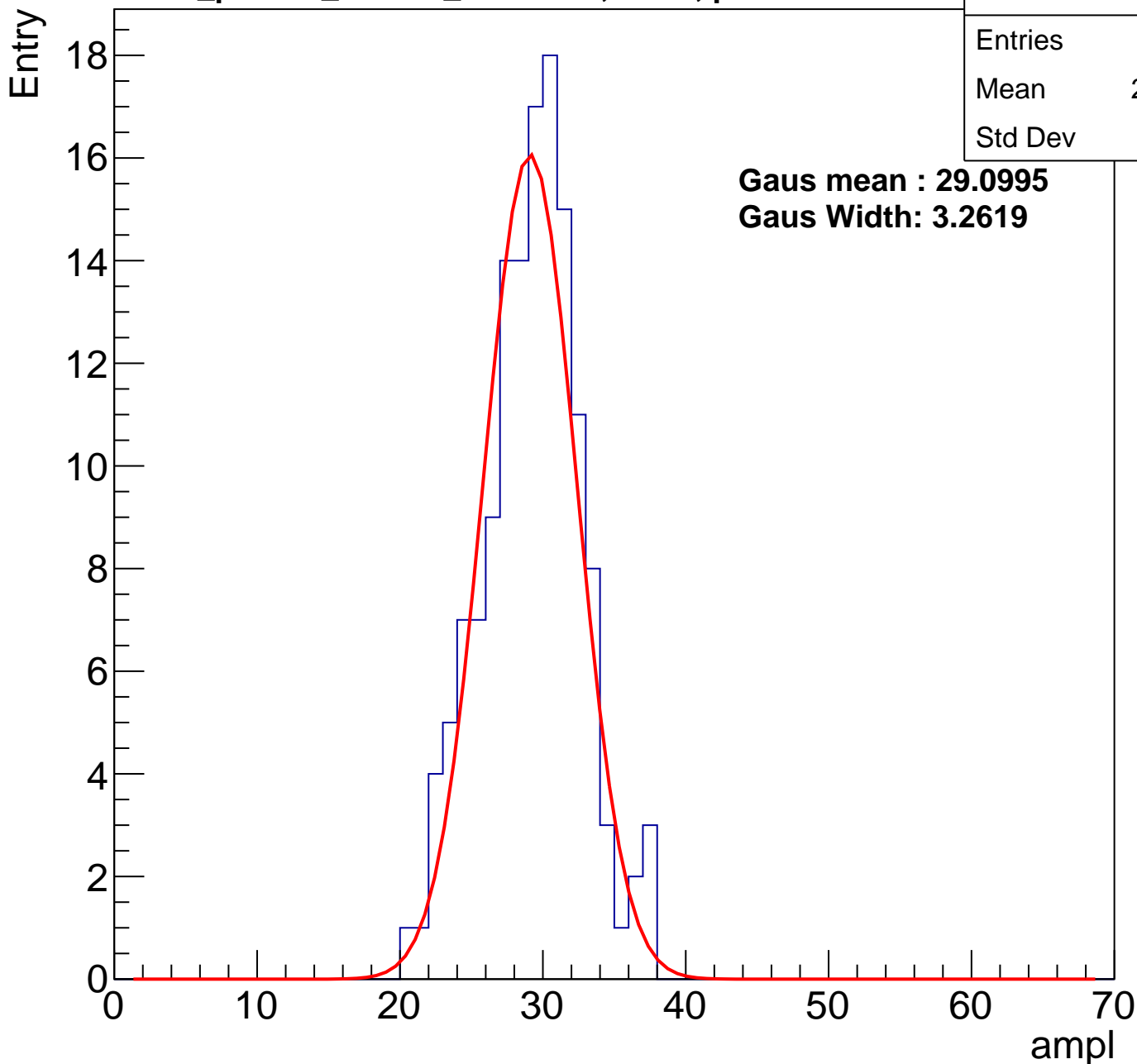
# B0L101S, U16-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 28.75 |
| Std Dev | 3.44  |

**Gaus mean : 29.0995**

**Gaus Width: 3.2619**



# B0L101S, U16-ch57, adc1

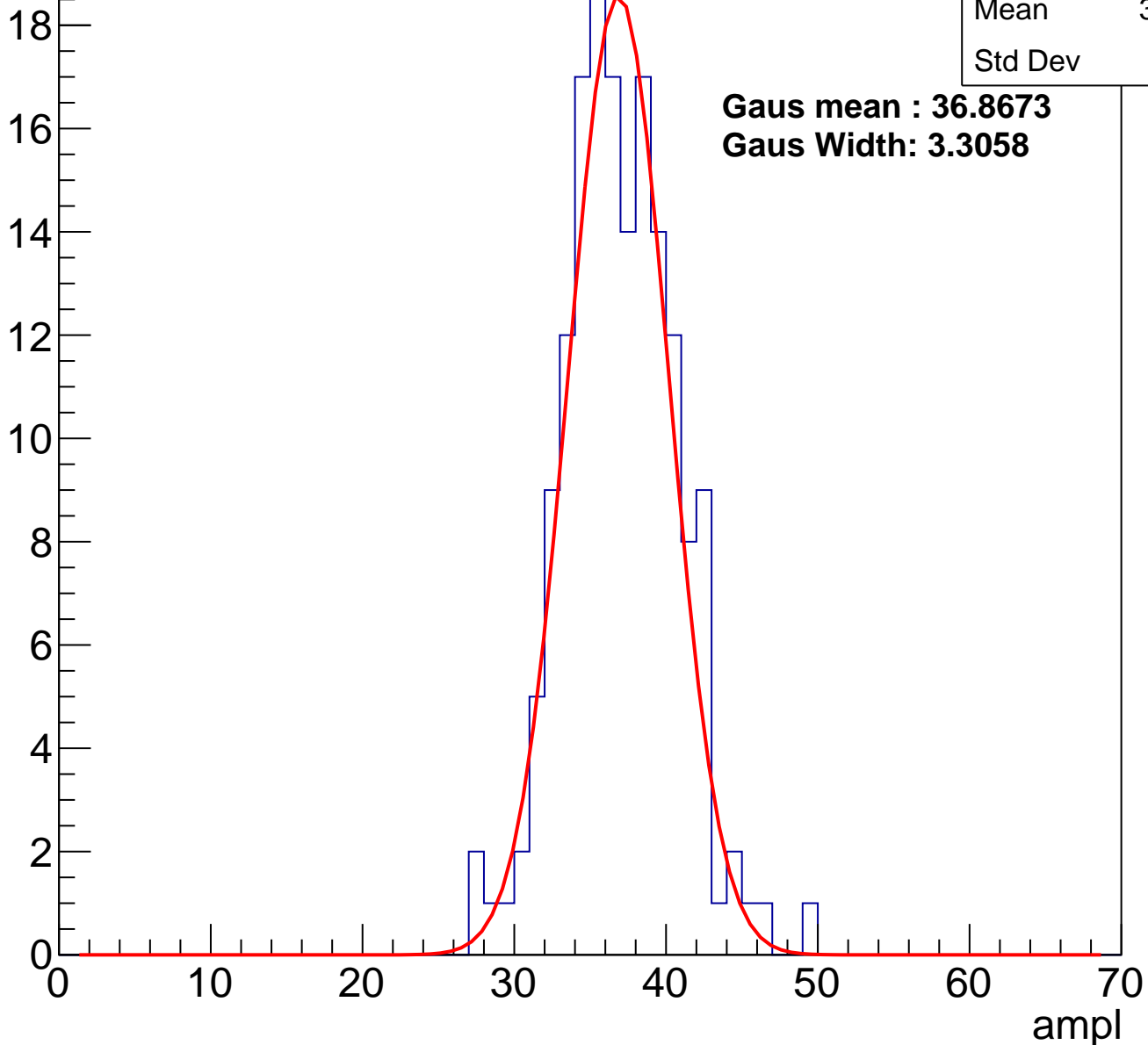
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 165   |
| Mean    | 36.56 |
| Std Dev | 3.66  |

**Gaus mean : 36.8673**

**Gaus Width: 3.3058**

Entry



# B0L101S, U16-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

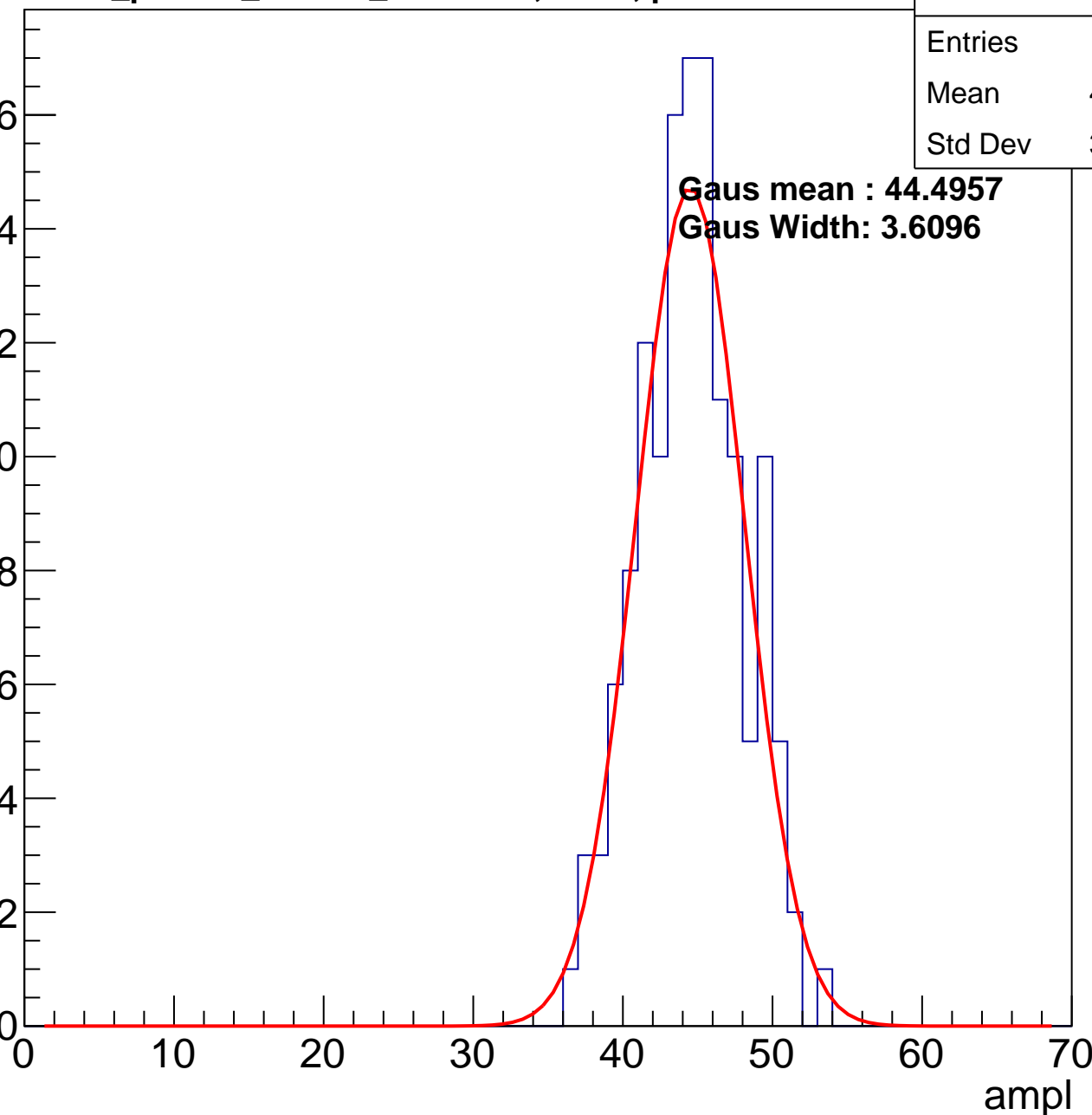
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 44.08 |
| Std Dev | 3.418 |

**Gaus mean : 44.4957**

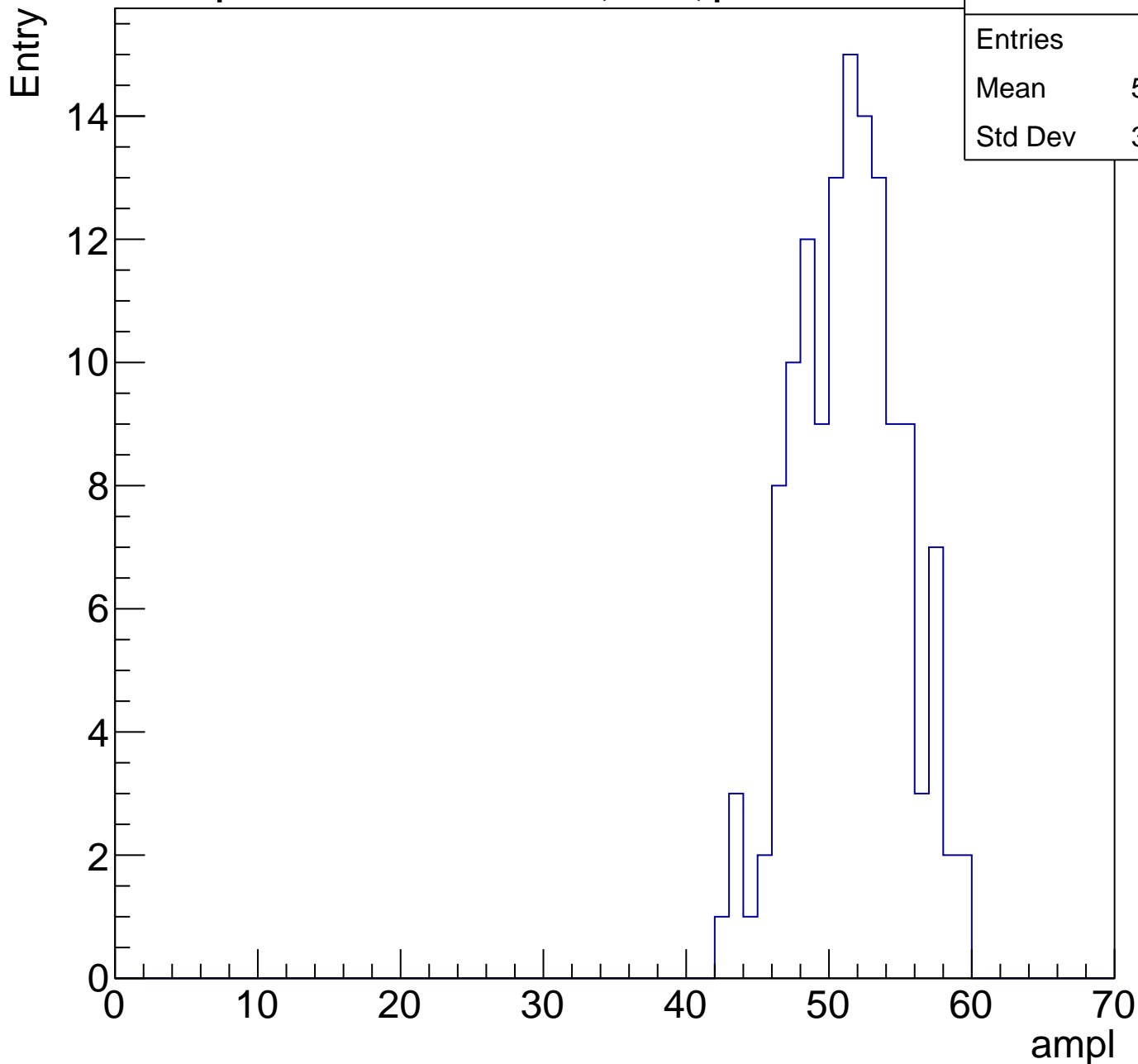
**Gaus Width: 3.6096**



# B0L101S, U16-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

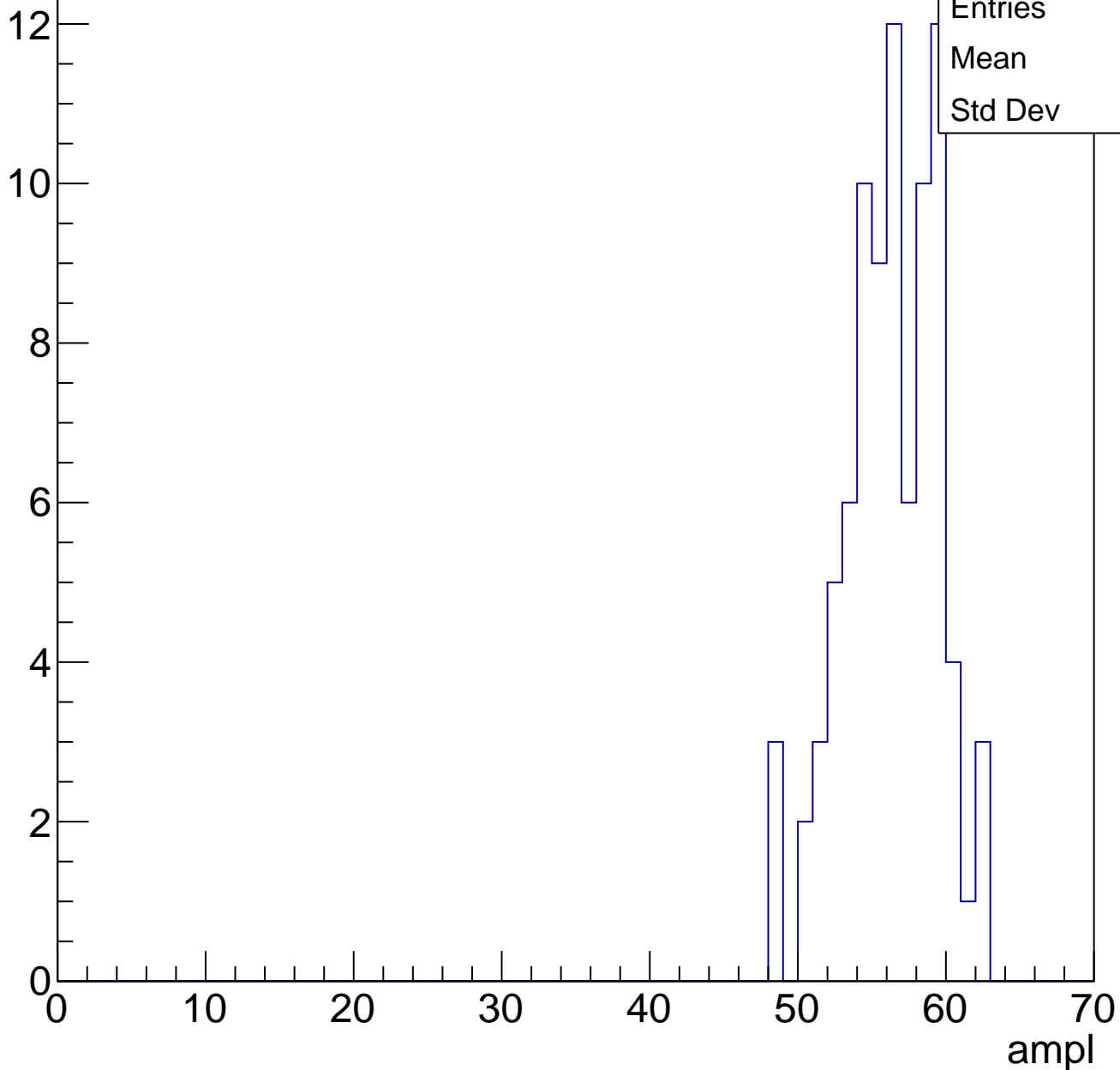
|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 50.93 |
| Std Dev | 3.632 |



# B0L101S, U16-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch57, adc5

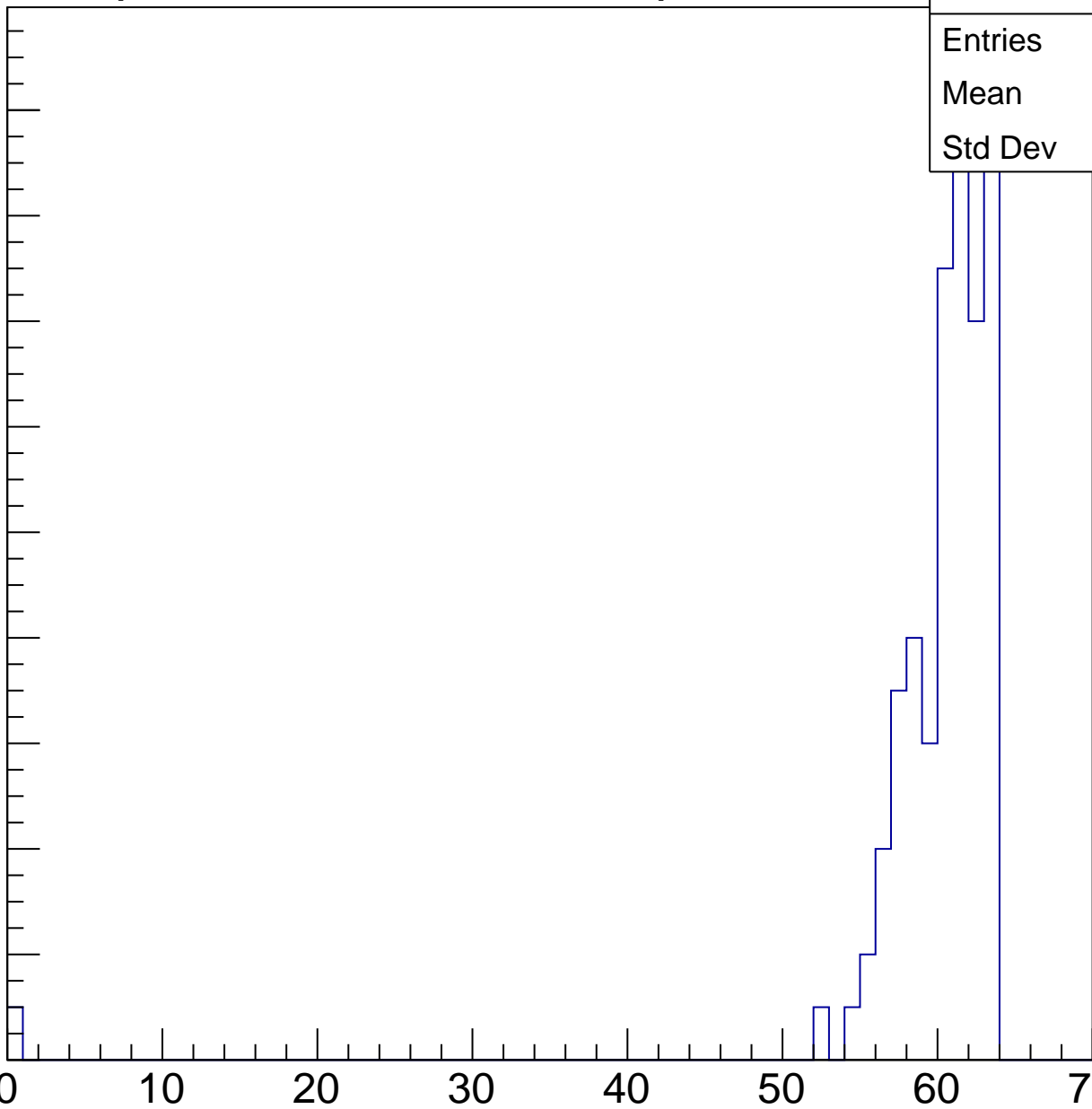
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 59.53 |
| Std Dev | 6.587 |

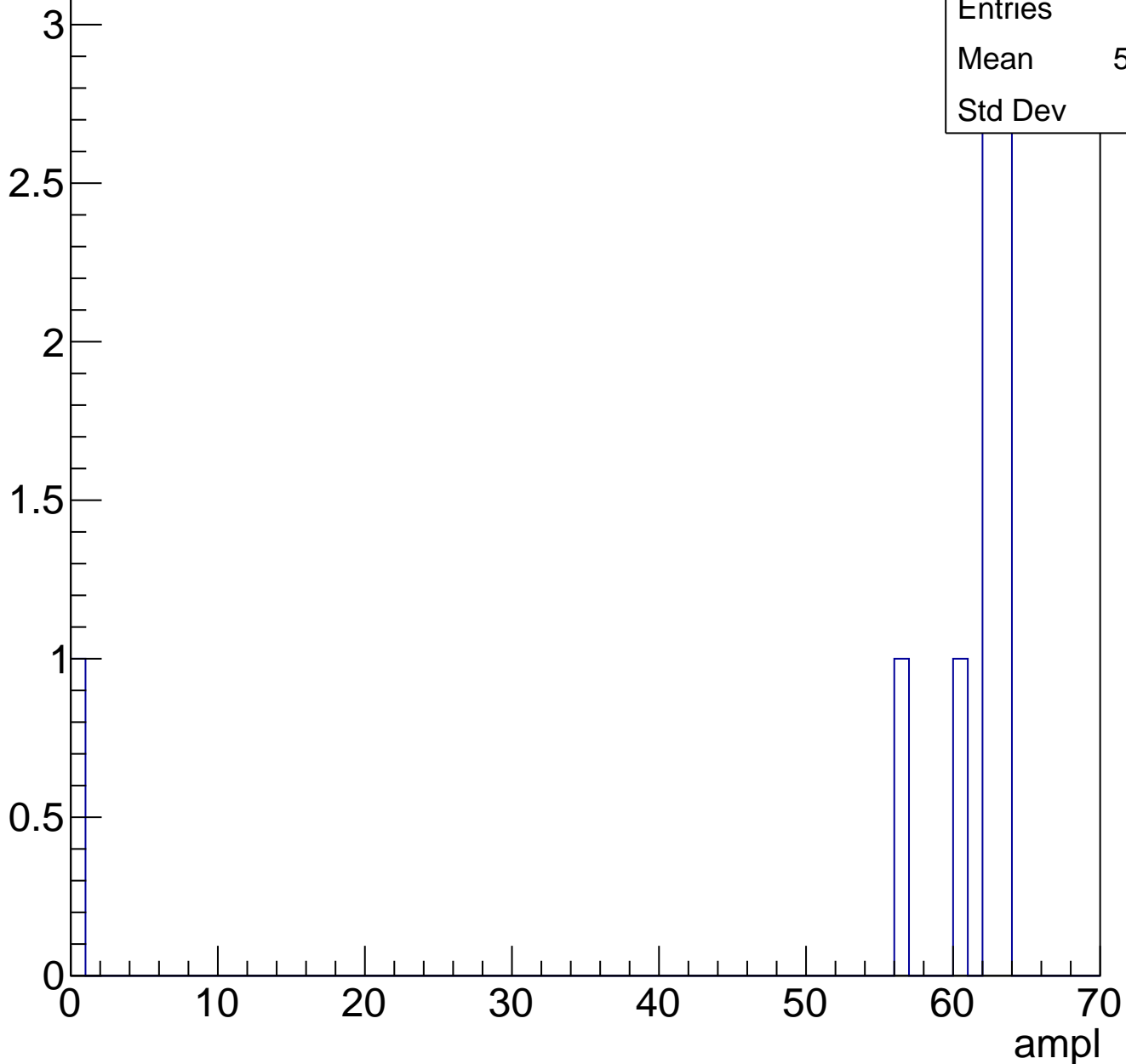
ampl



# B0L101S, U16-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

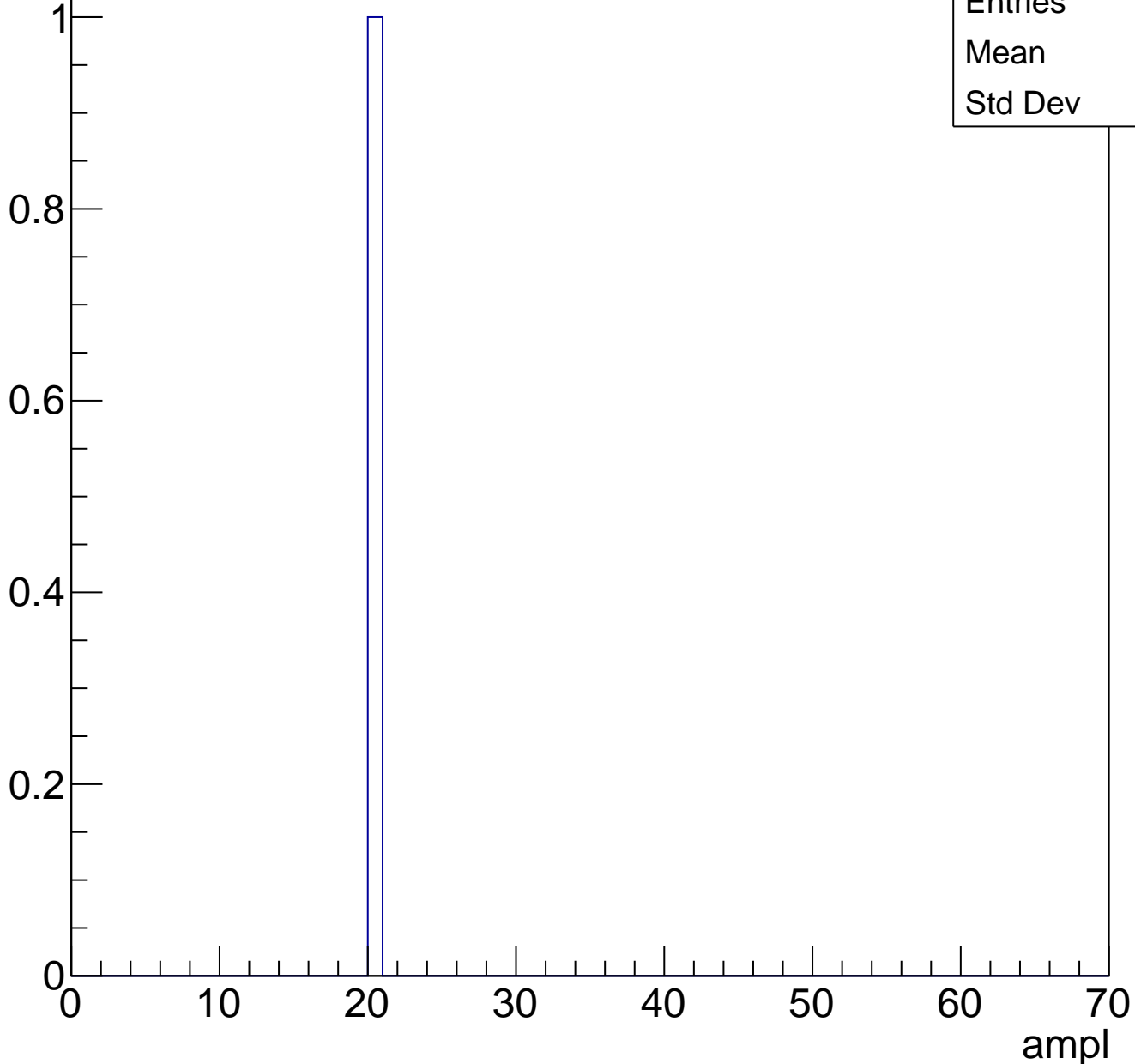




# B0L101S, U16-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 20 |
| Std Dev | 0  |

# B0L101S, U16-ch58, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 28.22 |
| Std Dev | 3.726 |

**Gaus mean : 29.0578**

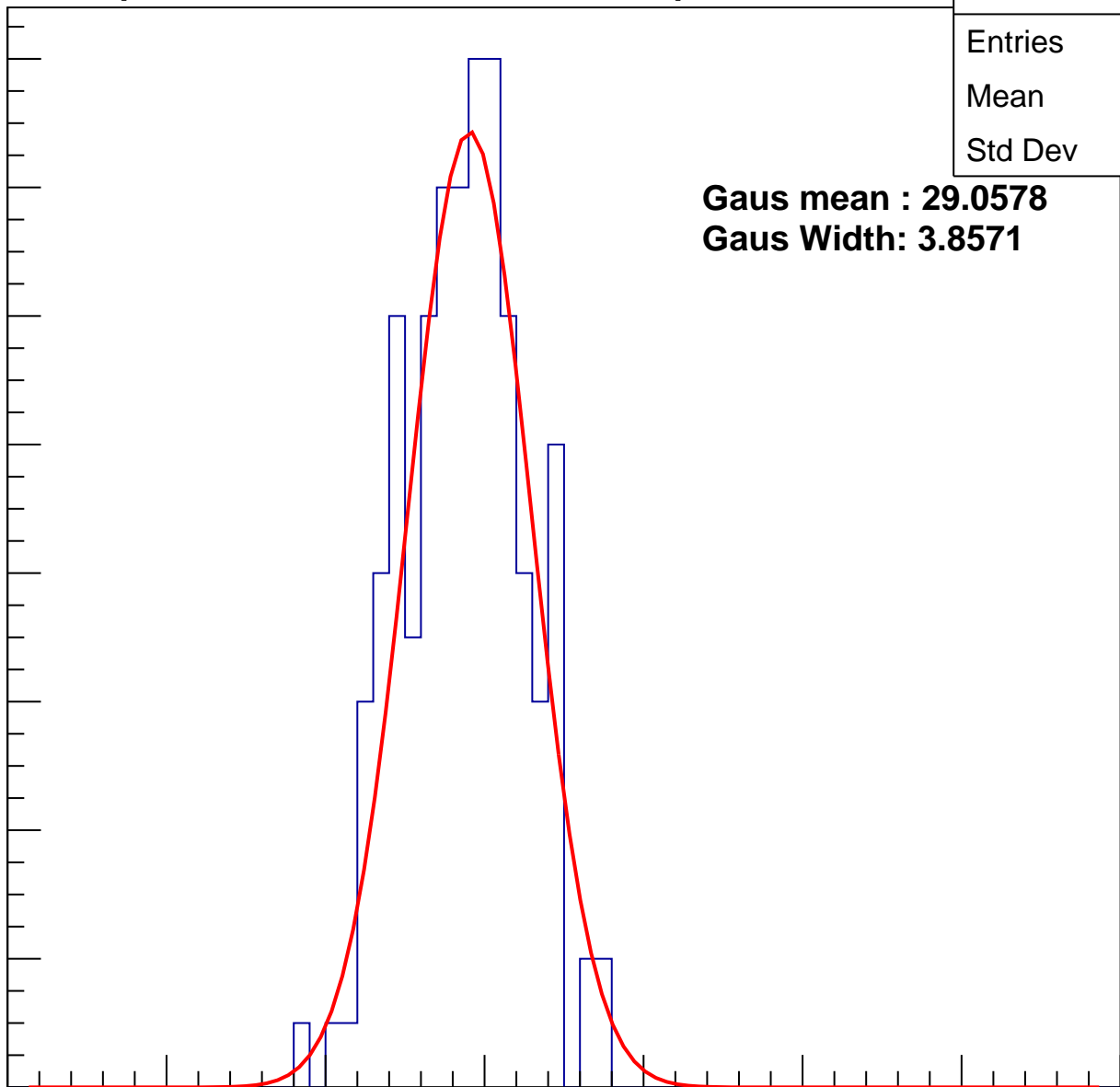
**Gaus Width: 3.8571**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch58, adc1

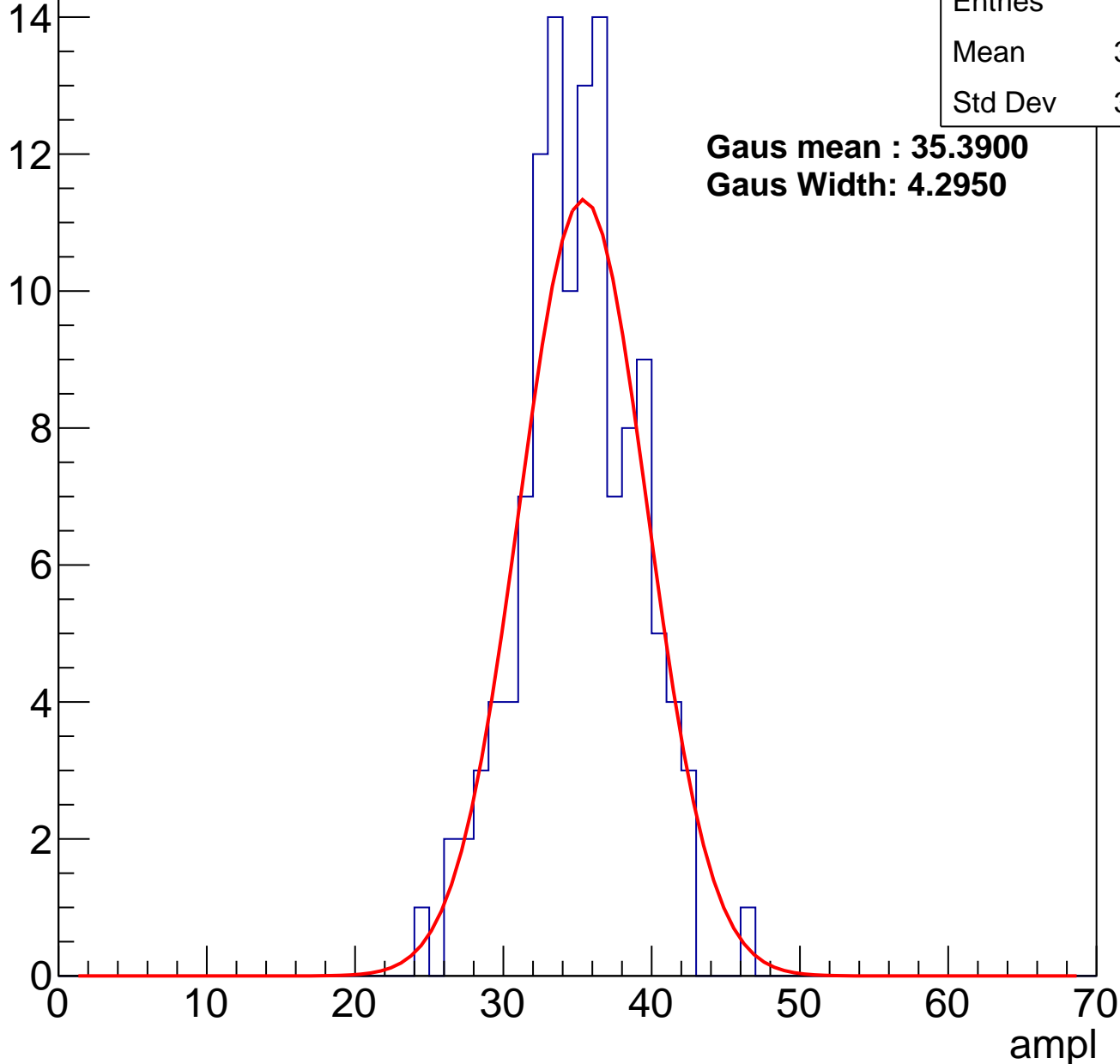
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 34.65 |
| Std Dev | 3.897 |

**Gaus mean : 35.3900**

**Gaus Width: 4.2950**

Entry



# B0L101S, U16-ch58, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 41.1  |
| Std Dev | 3.414 |

**Gaus mean : 41.2018**

**Gaus Width: 3.2926**

0

10

20

30

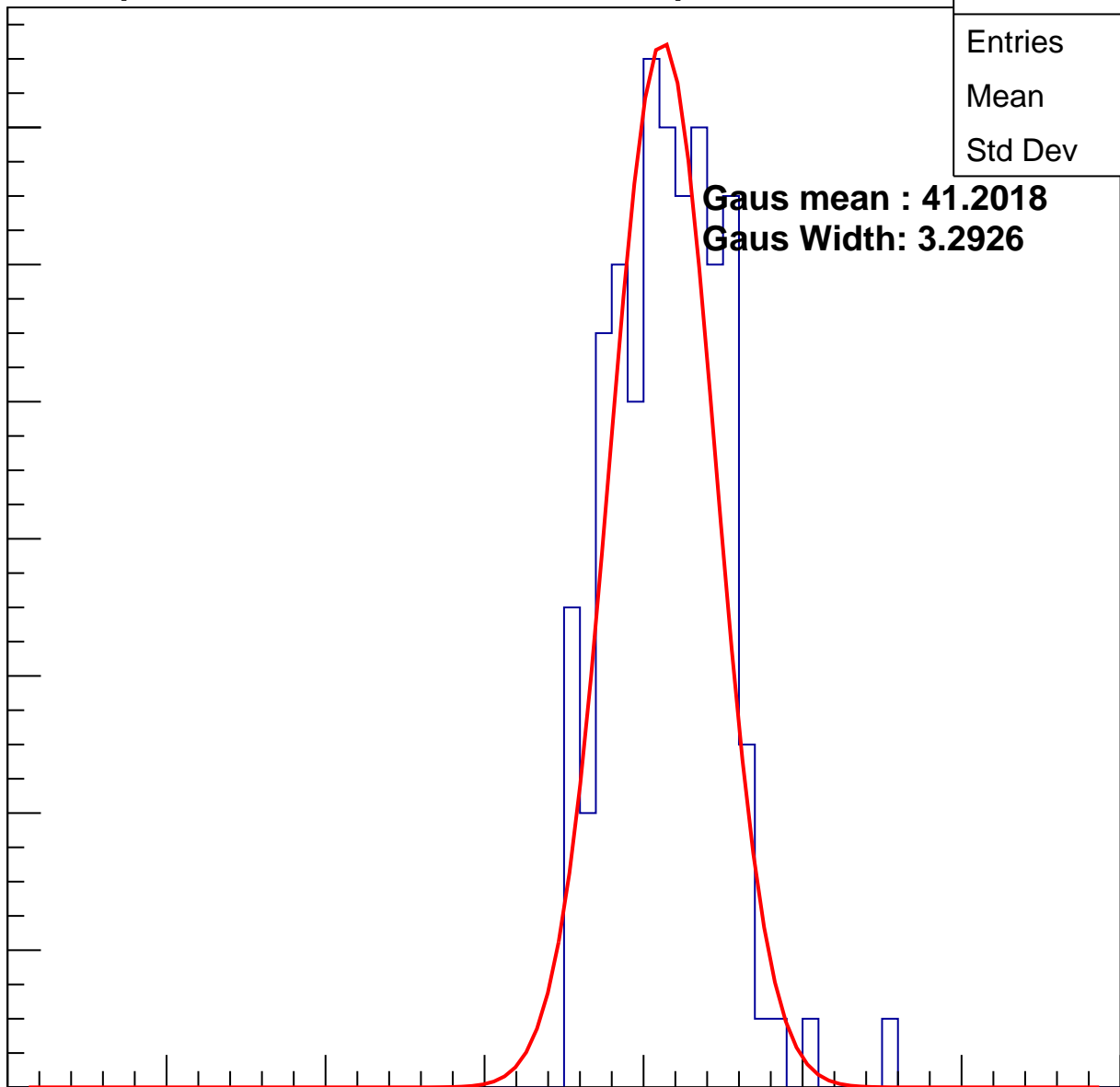
40

50

60

70

ampl



# B0L101S, U16-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

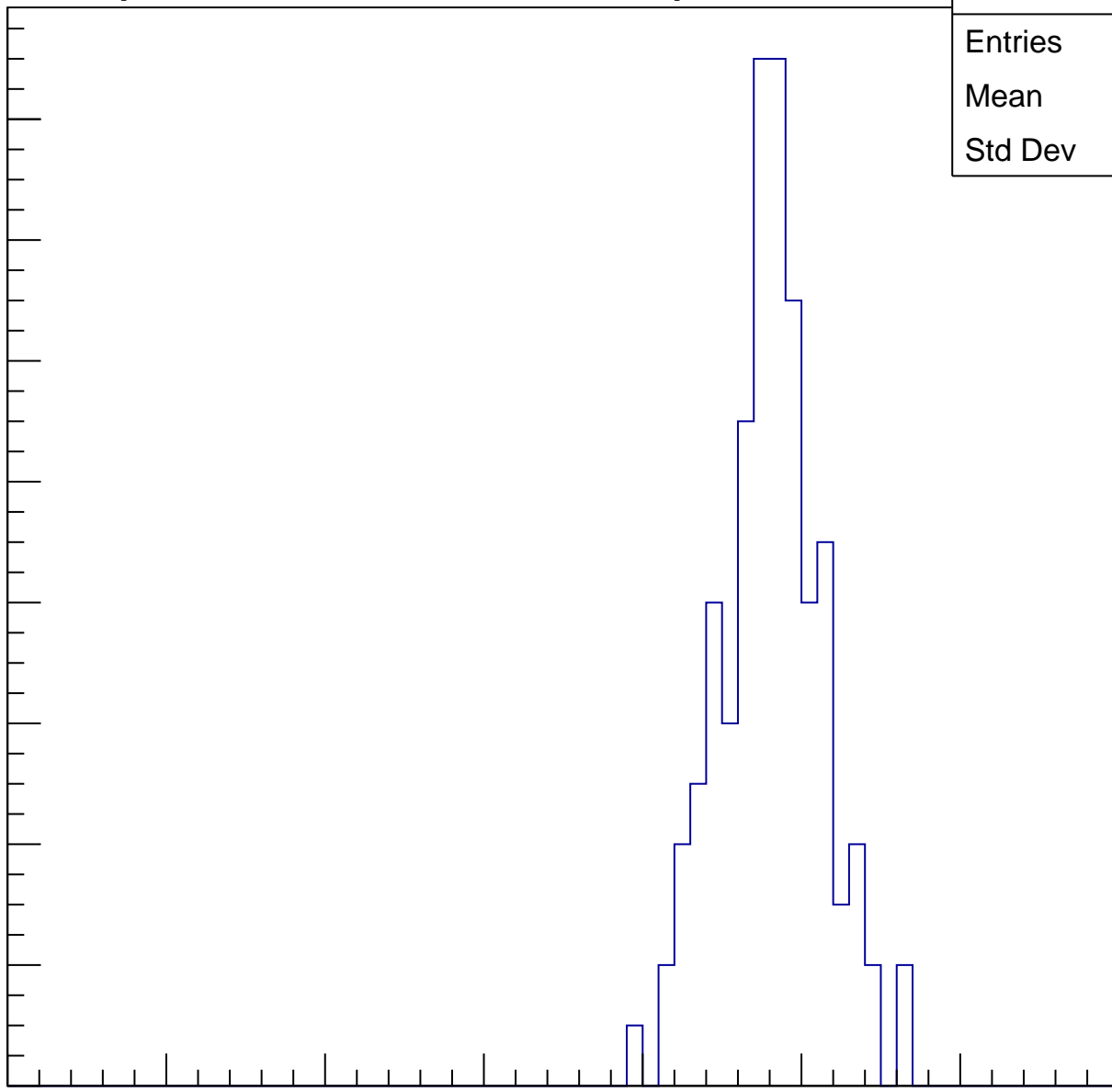
|         |      |
|---------|------|
| Entries | 112  |
| Mean    | 47.6 |
| Std Dev | 3.2  |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

172

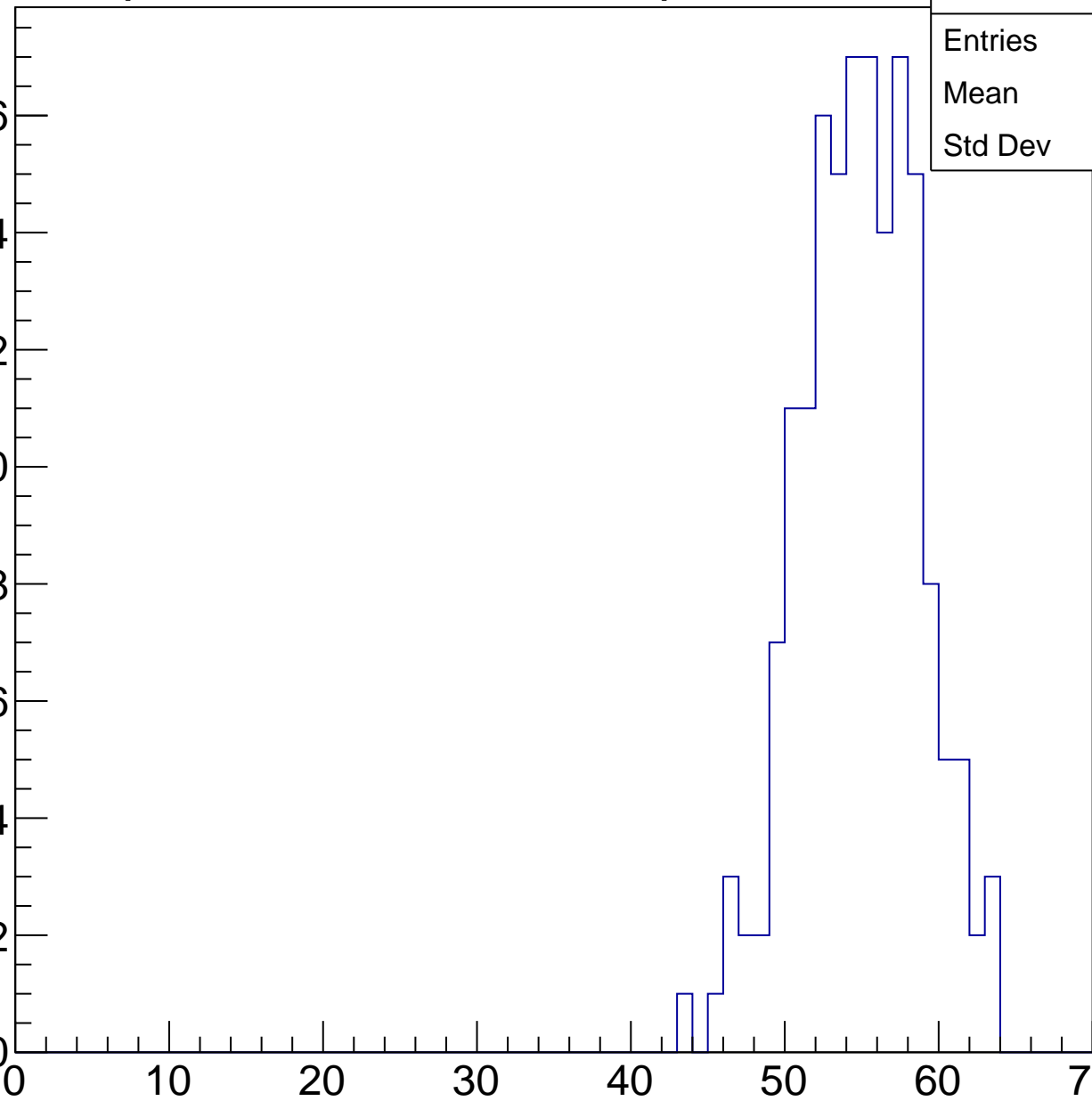
Mean

54.44

Std Dev

3.829

ampl



# B0L101S, U16-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

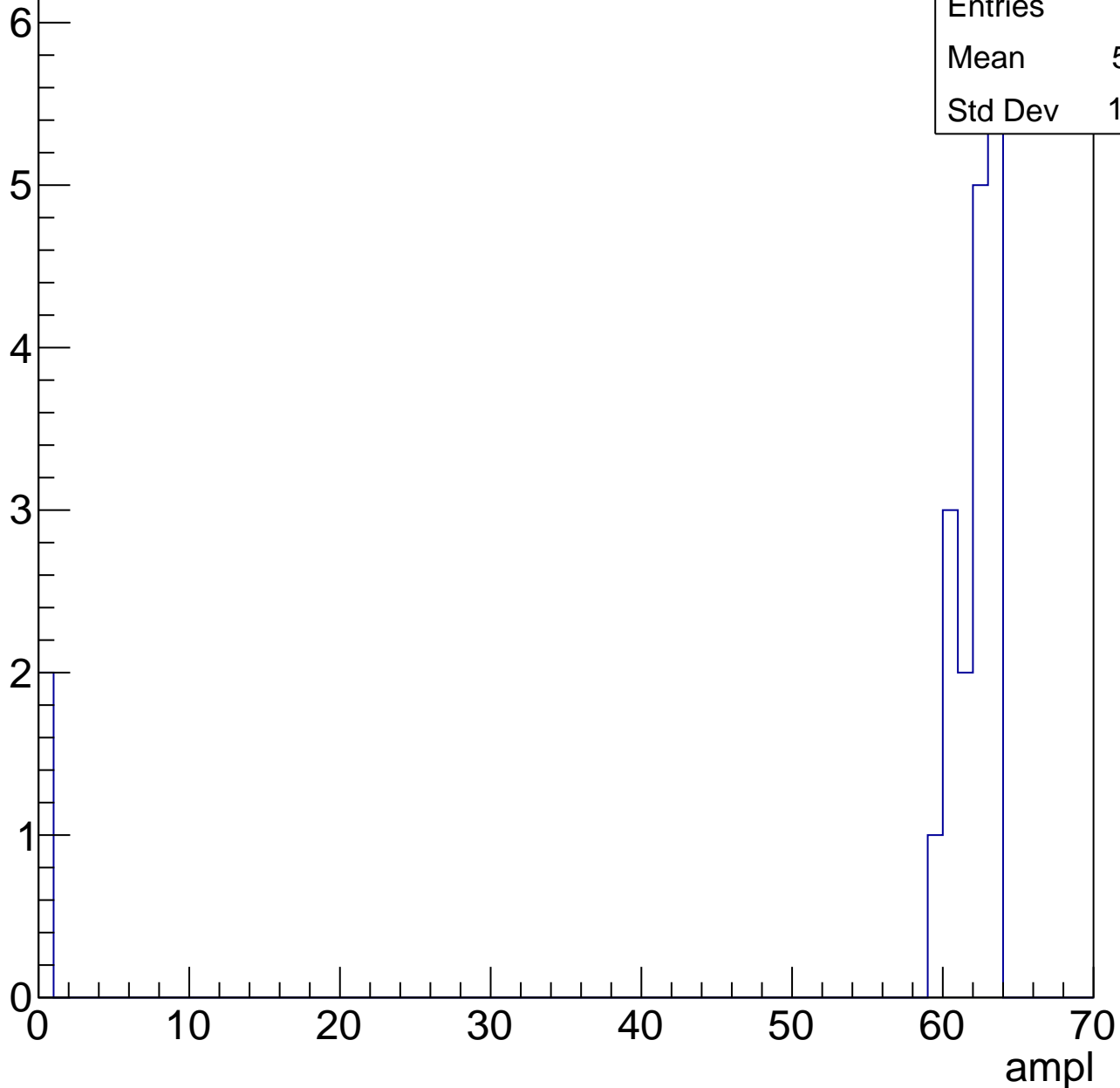
ampl

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 59.84 |
| Std Dev | 2.504 |

# B0L101S, U16-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

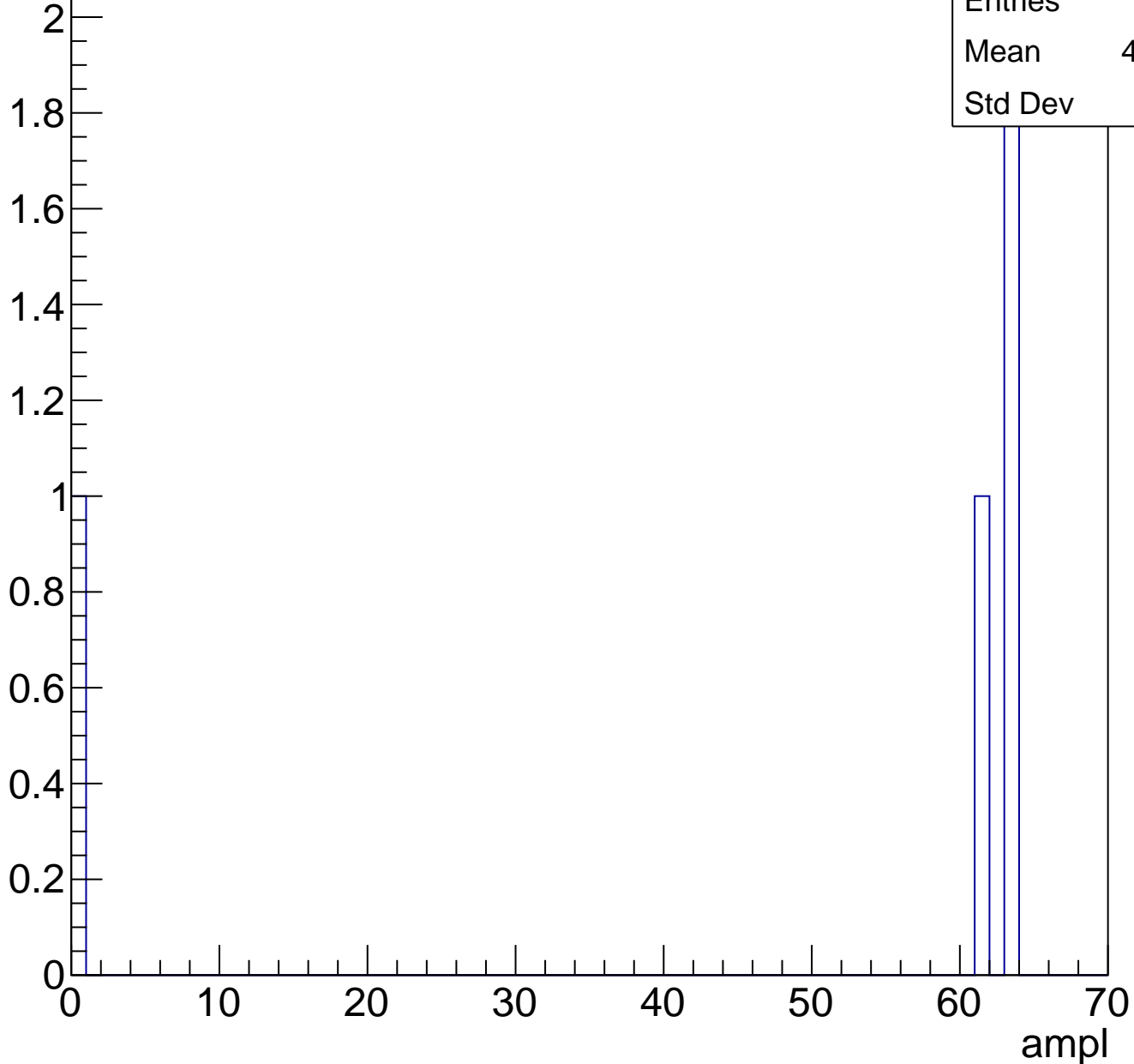




# B0L101S, U16-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



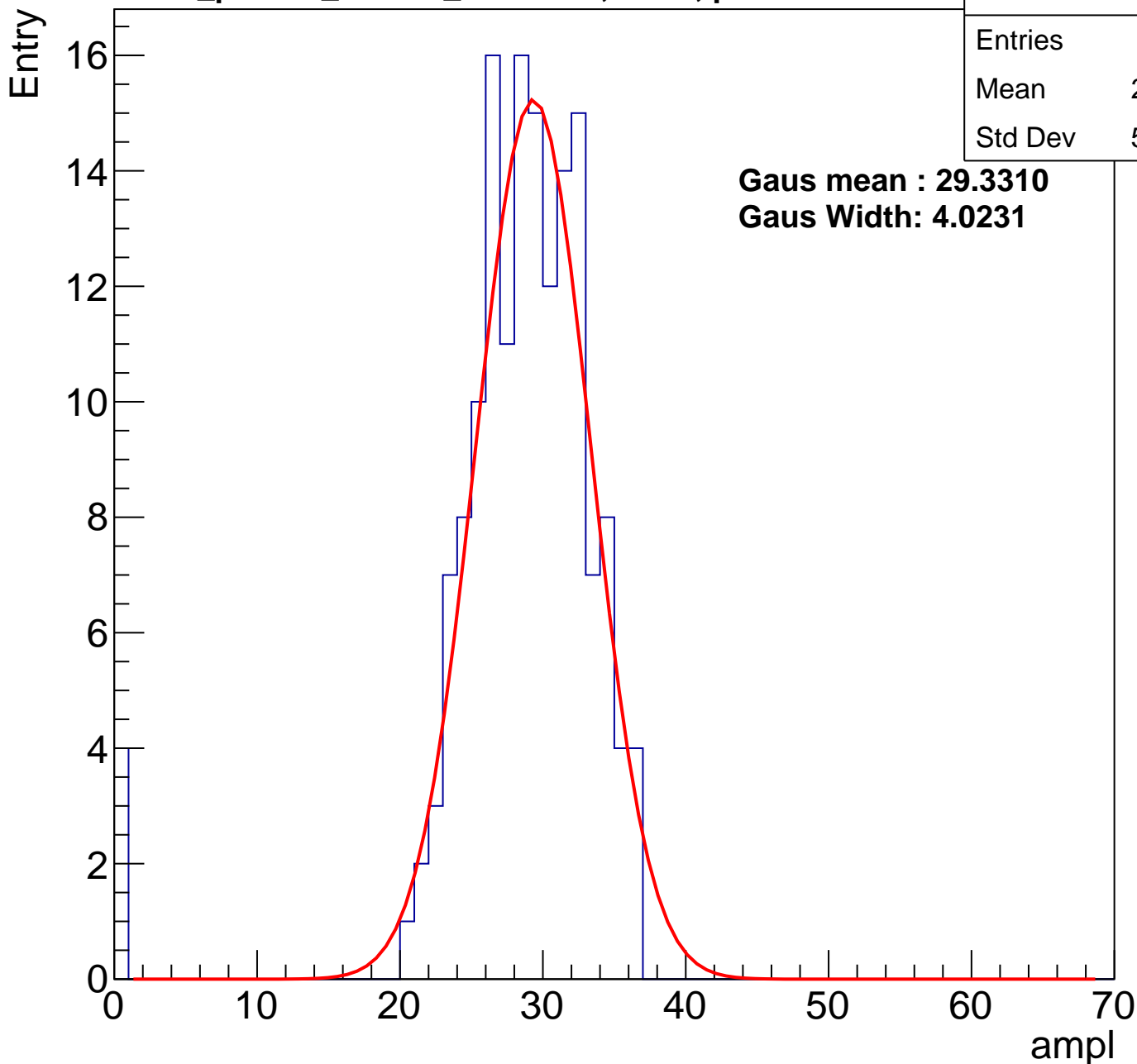
# B0L101S, U16-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 157   |
| Mean    | 27.95 |
| Std Dev | 5.765 |

**Gaus mean : 29.3310**

**Gaus Width: 4.0231**



# B0L101S, U16-ch59, adc1

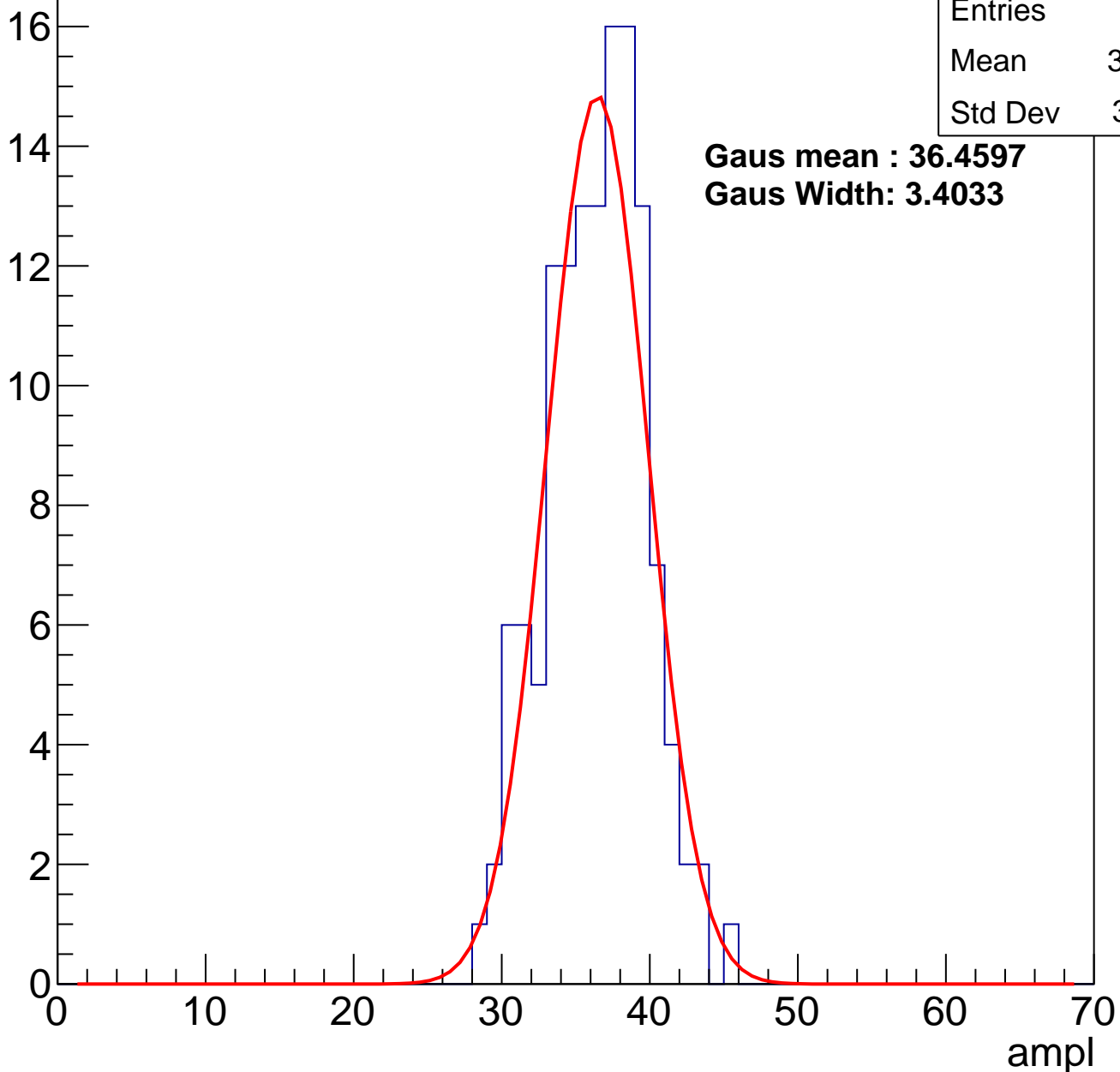
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 35.92 |
| Std Dev | 3.311 |

**Gaus mean : 36.4597**

**Gaus Width: 3.4033**

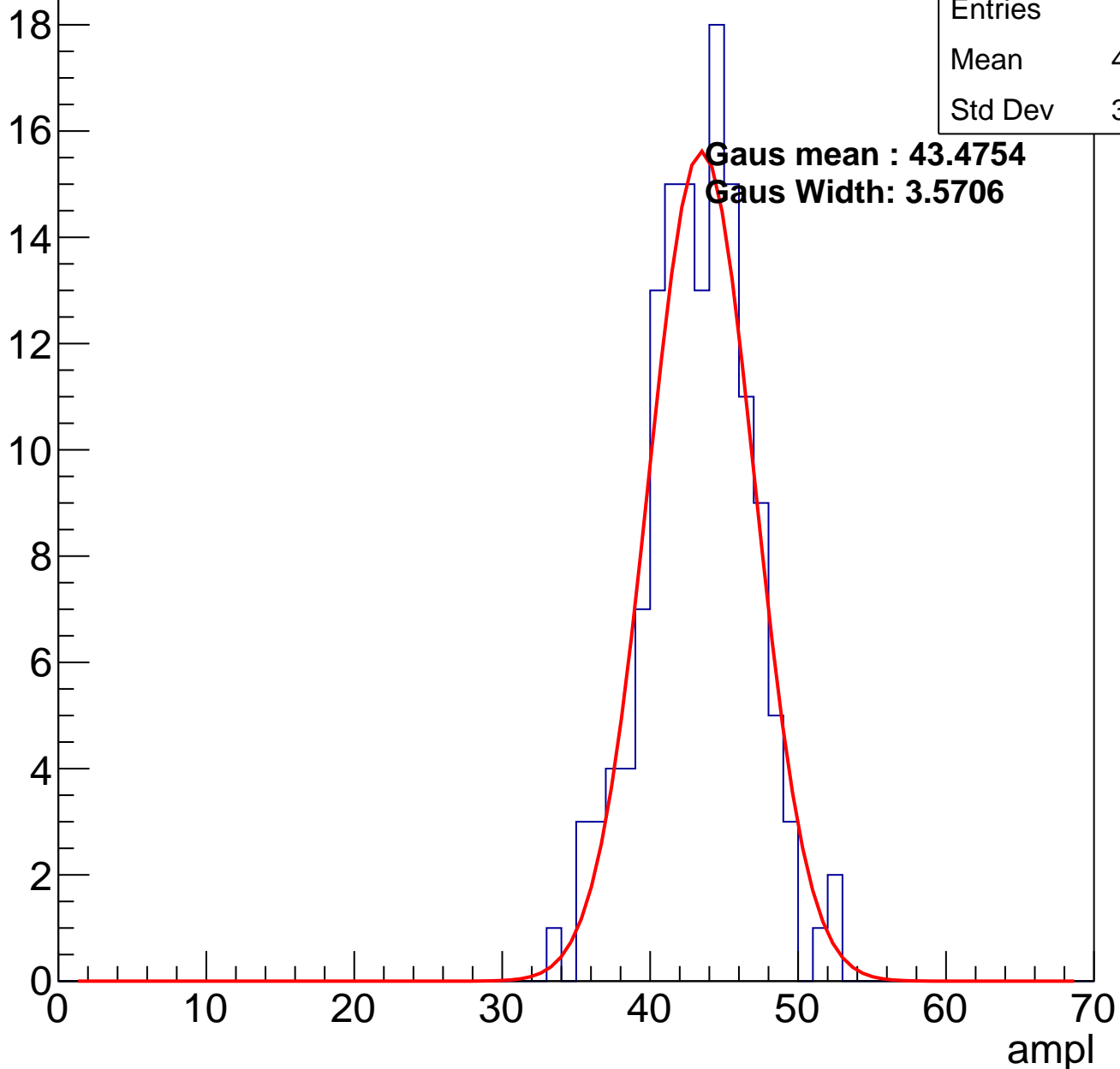
Entry



# B0L101S, U16-ch59, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 154   |
| Mean    | 50.18 |
| Std Dev | 3.646 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

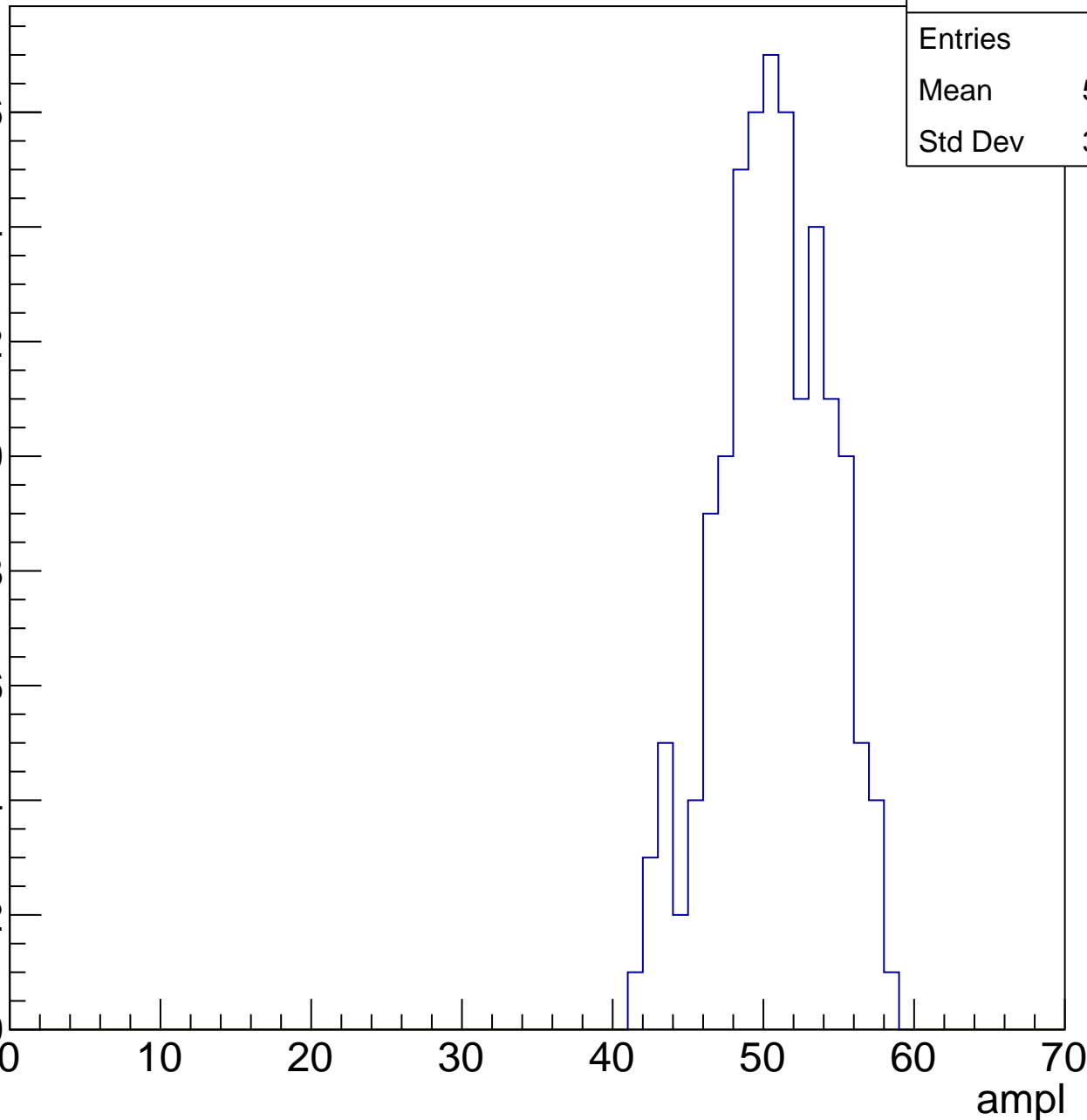
30

40

50

60

ampl

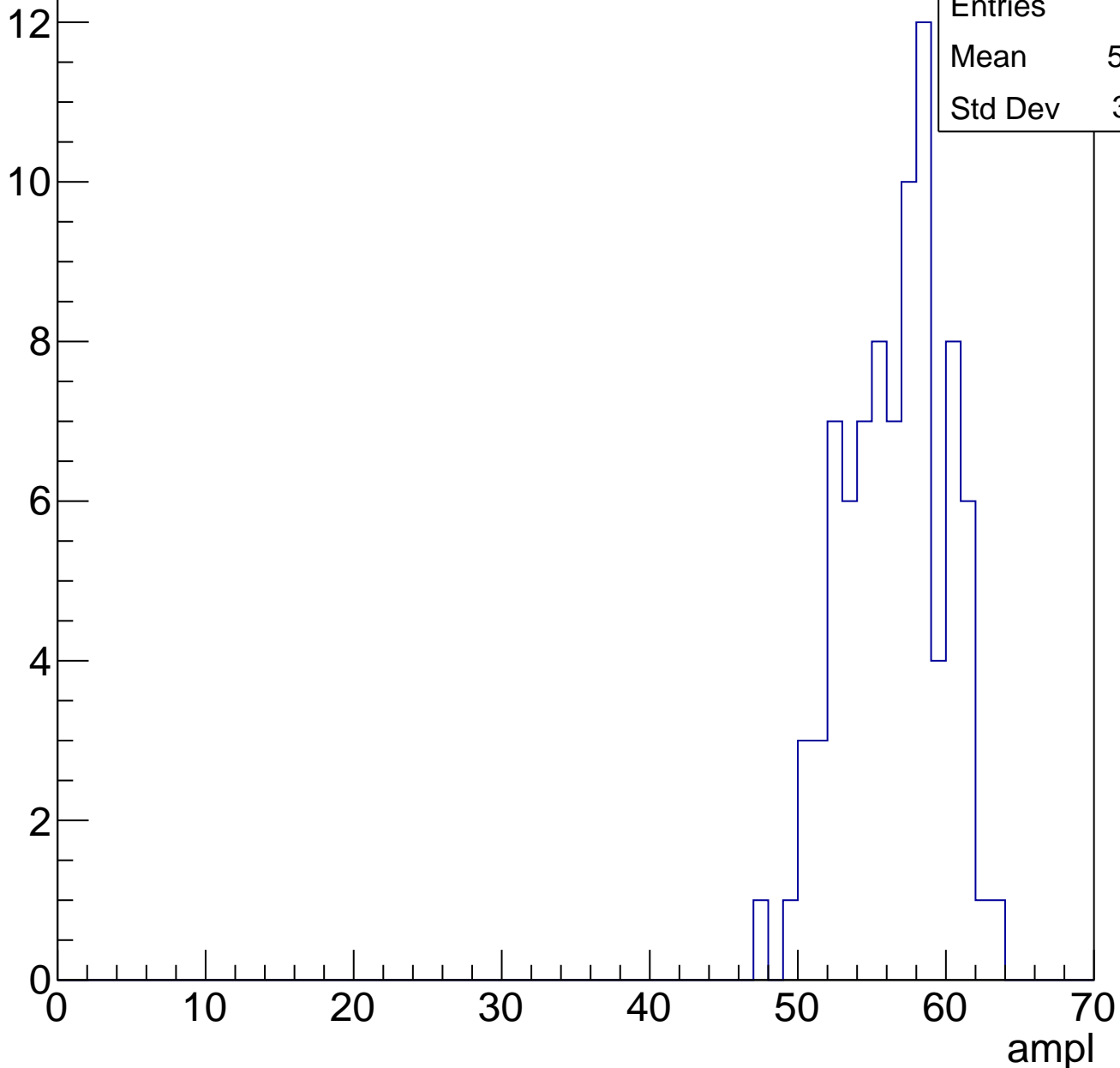


# B0L101S, U16-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 85    |
| Mean    | 56.05 |
| Std Dev | 3.381 |

Entry



# B0L101S, U16-ch59, adc5

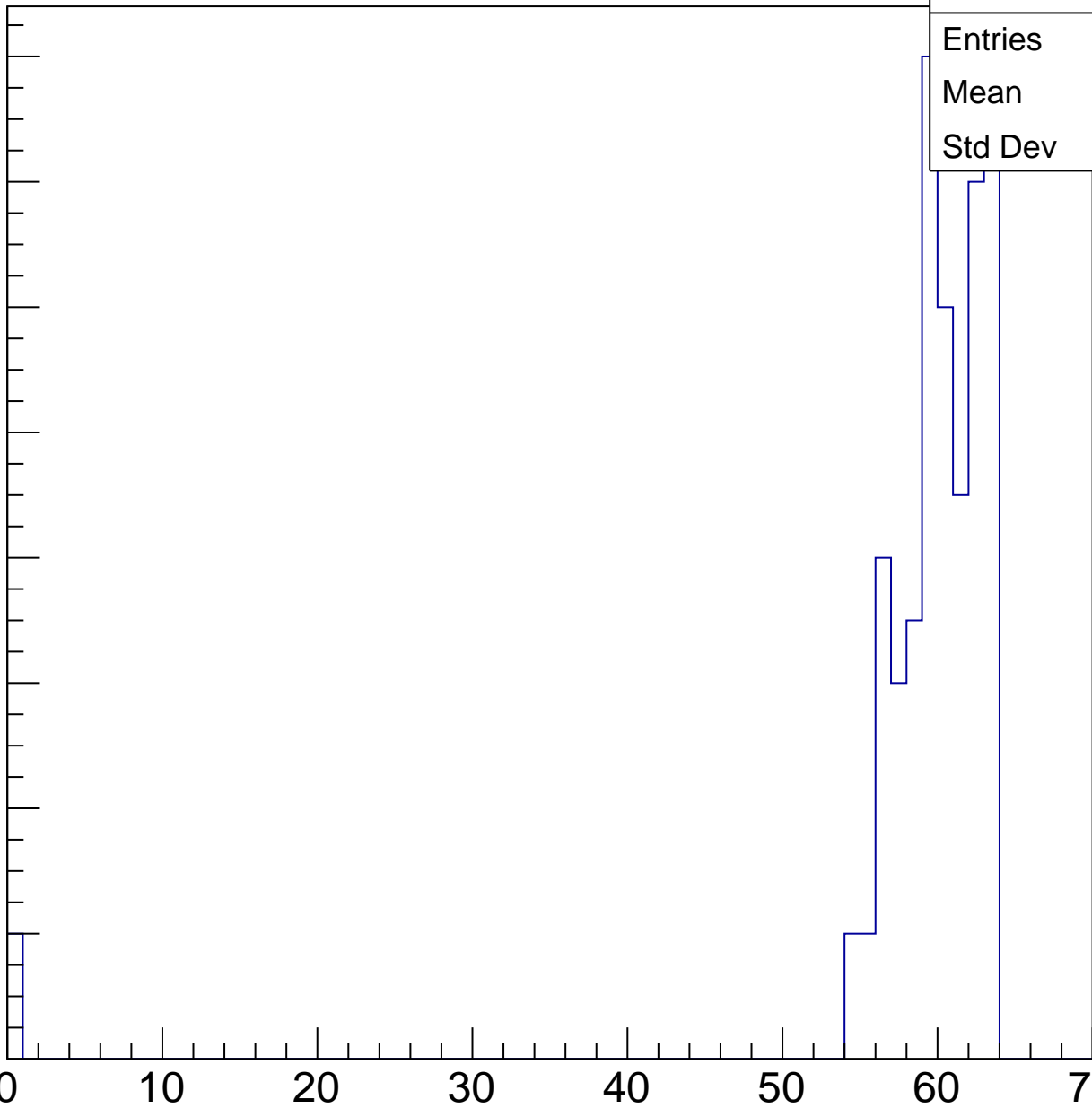
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 93    |
| Mean    | 58.49 |
| Std Dev | 9.003 |

ampl

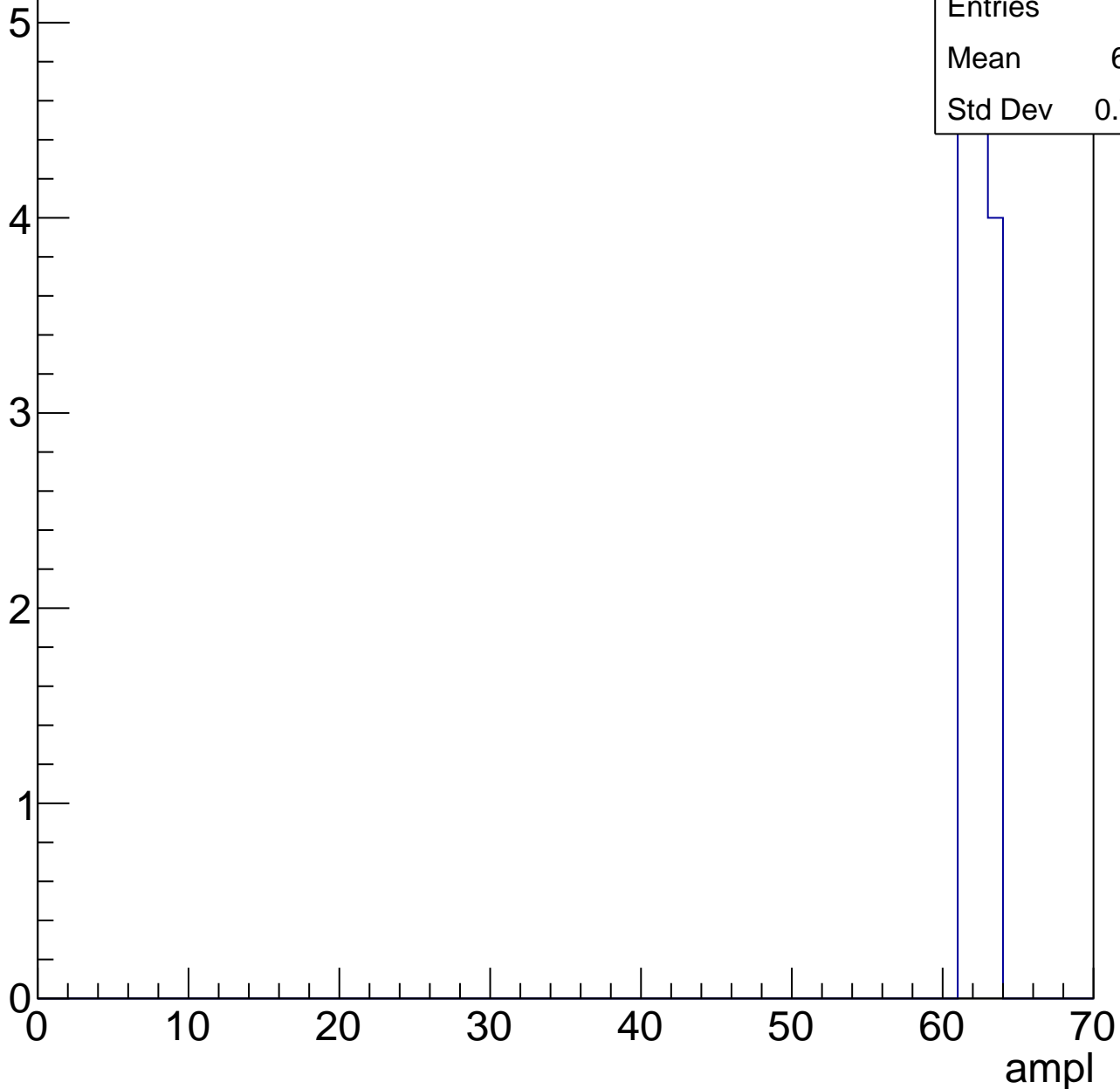


# B0L101S, U16-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 14     |
| Mean    | 61.93  |
| Std Dev | 0.7986 |





# B0L101S, U16-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

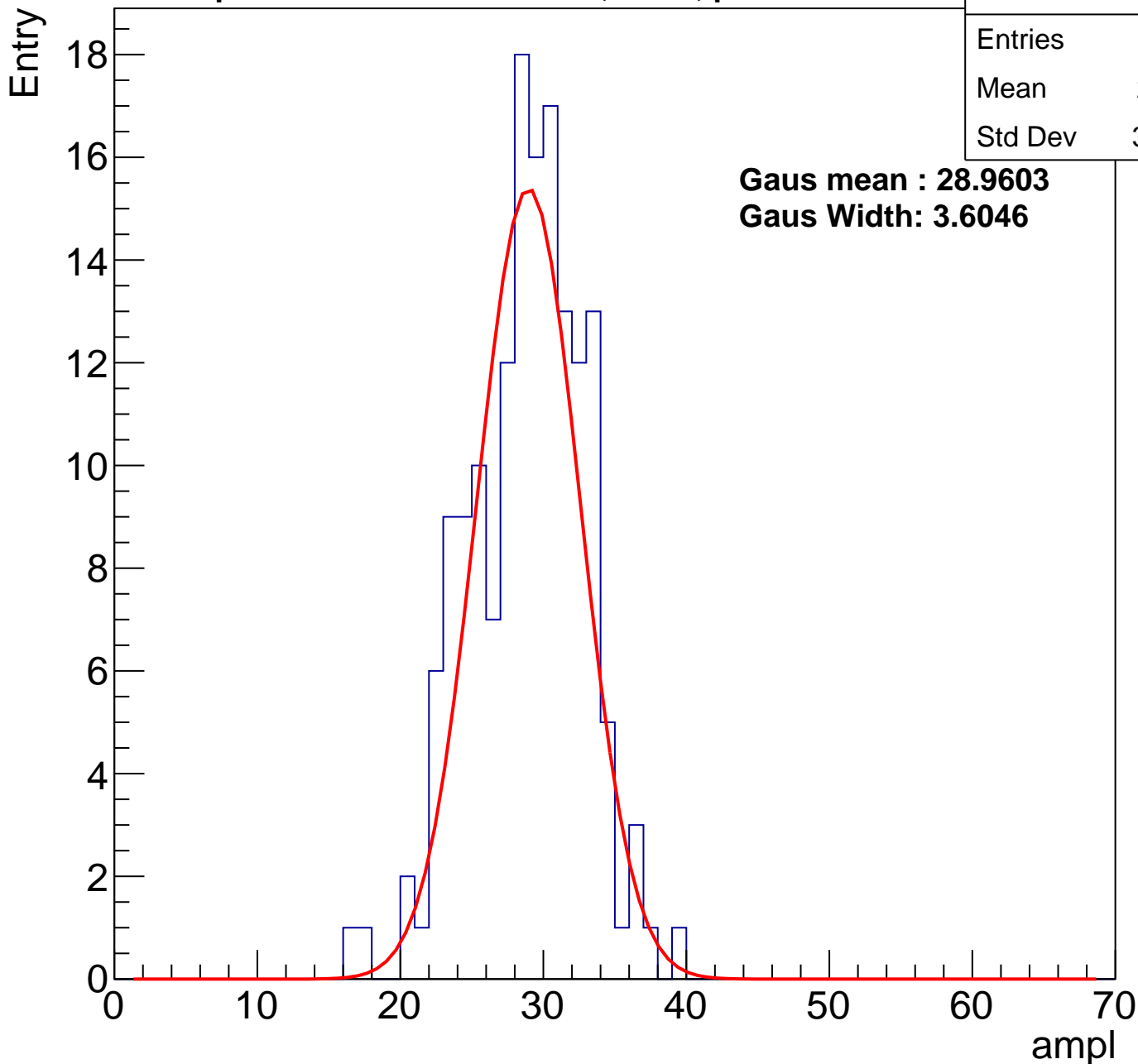
# B0L101S, U16-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 28.41 |
| Std Dev | 3.956 |

**Gaus mean : 28.9603**

**Gaus Width: 3.6046**



# B0L101S, U16-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 36.06 |
| Std Dev | 3.362 |

**Gaus mean : 36.6088**

**Gaus Width: 3.3477**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

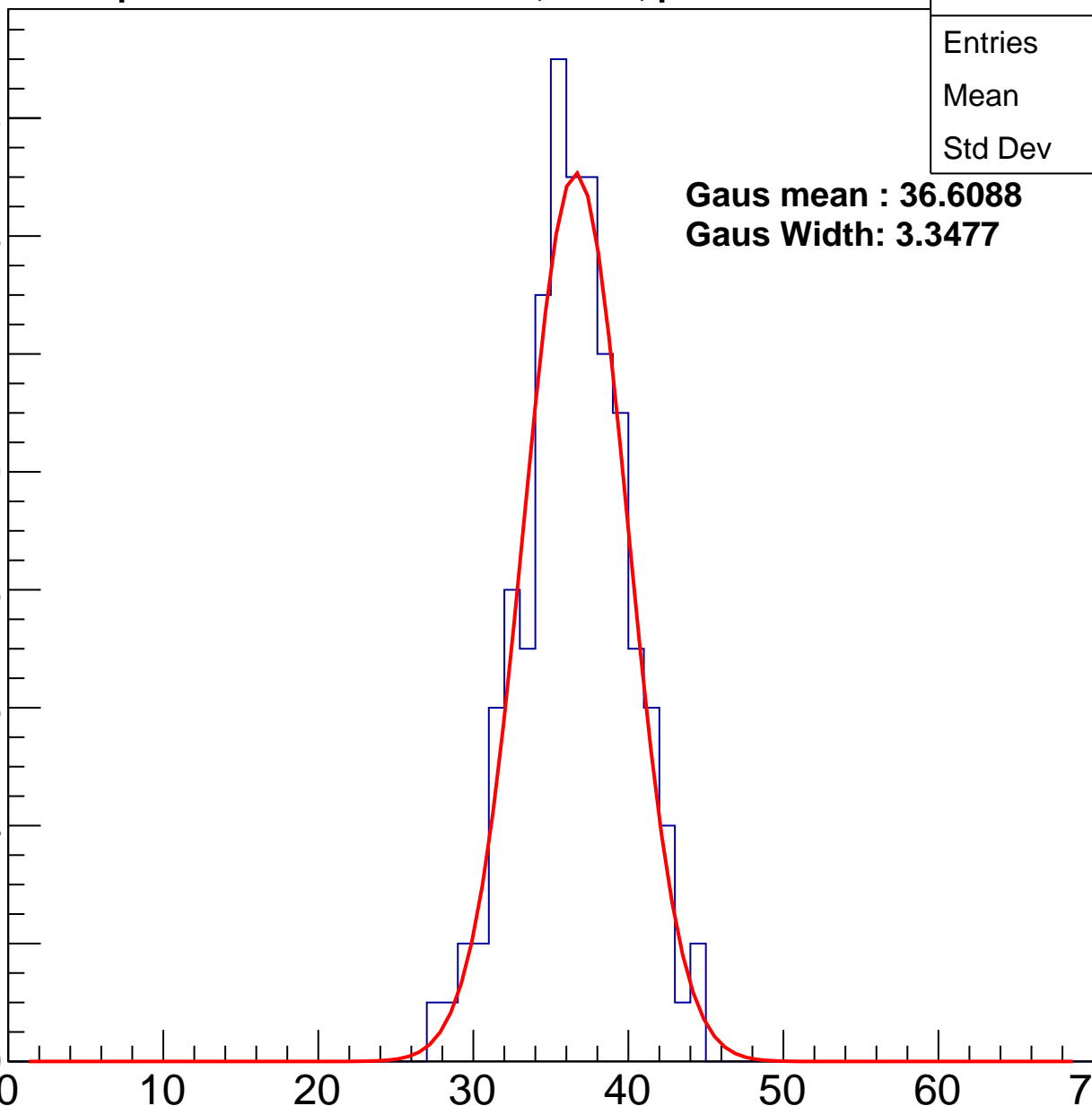
40

50

60

70

ampl



# B0L101S, U16-ch60, adc2

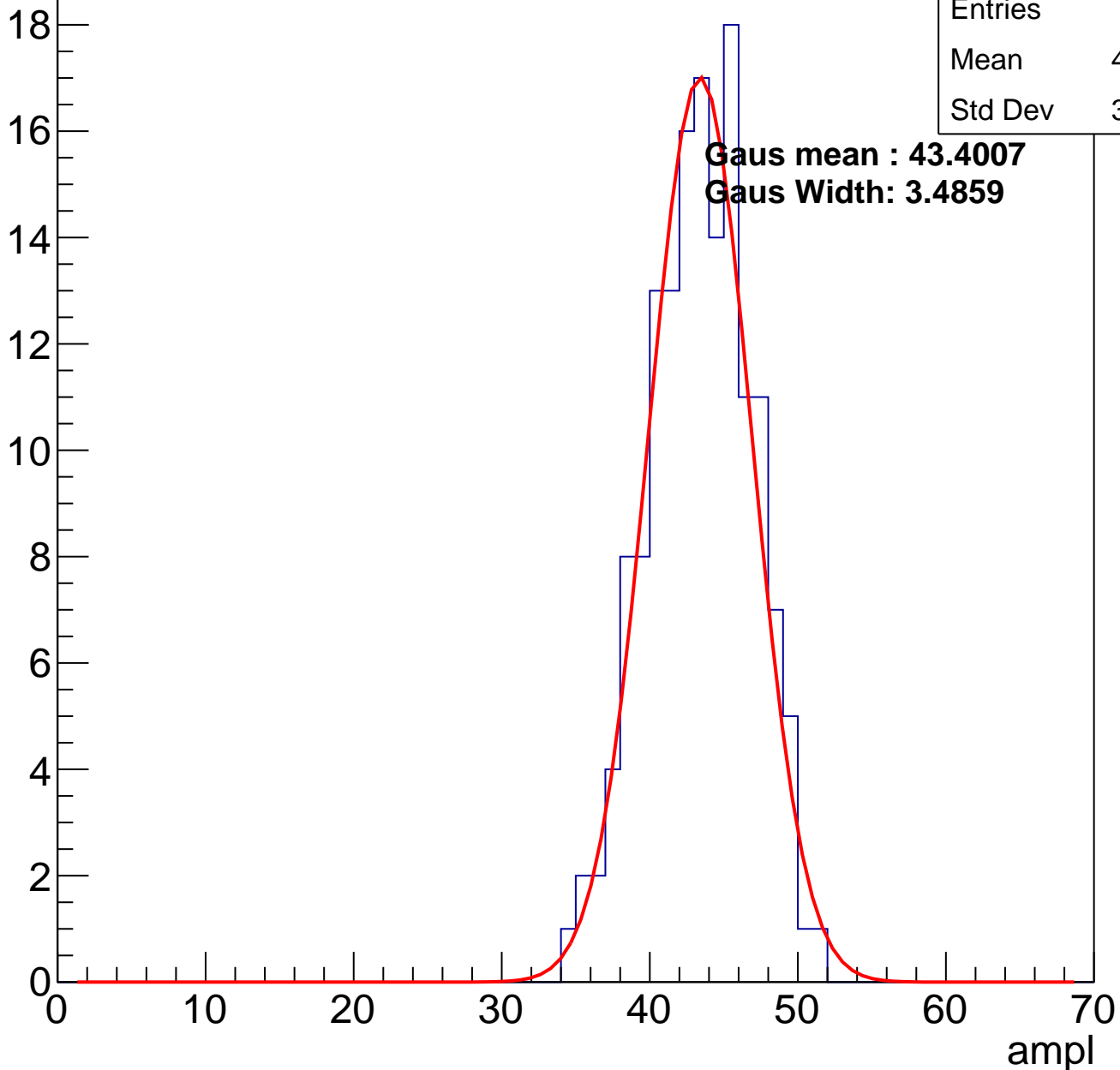
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 152   |
| Mean    | 42.94 |
| Std Dev | 3.434 |

**Gaus mean : 43.4007**

**Gaus Width: 3.4859**

Entry



# B0L101S, U16-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

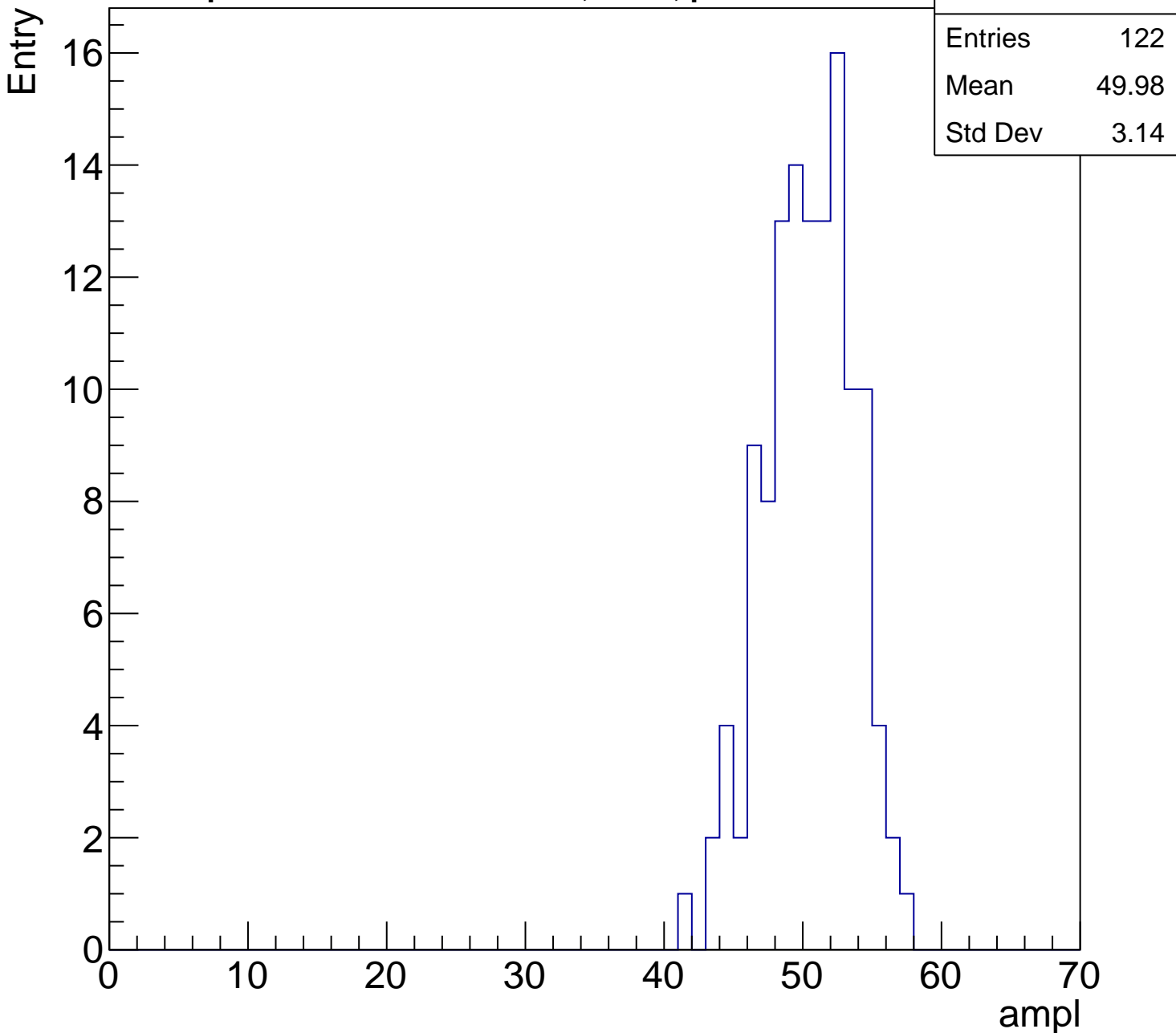
|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 49.98 |
| Std Dev | 3.14  |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch60, adc4

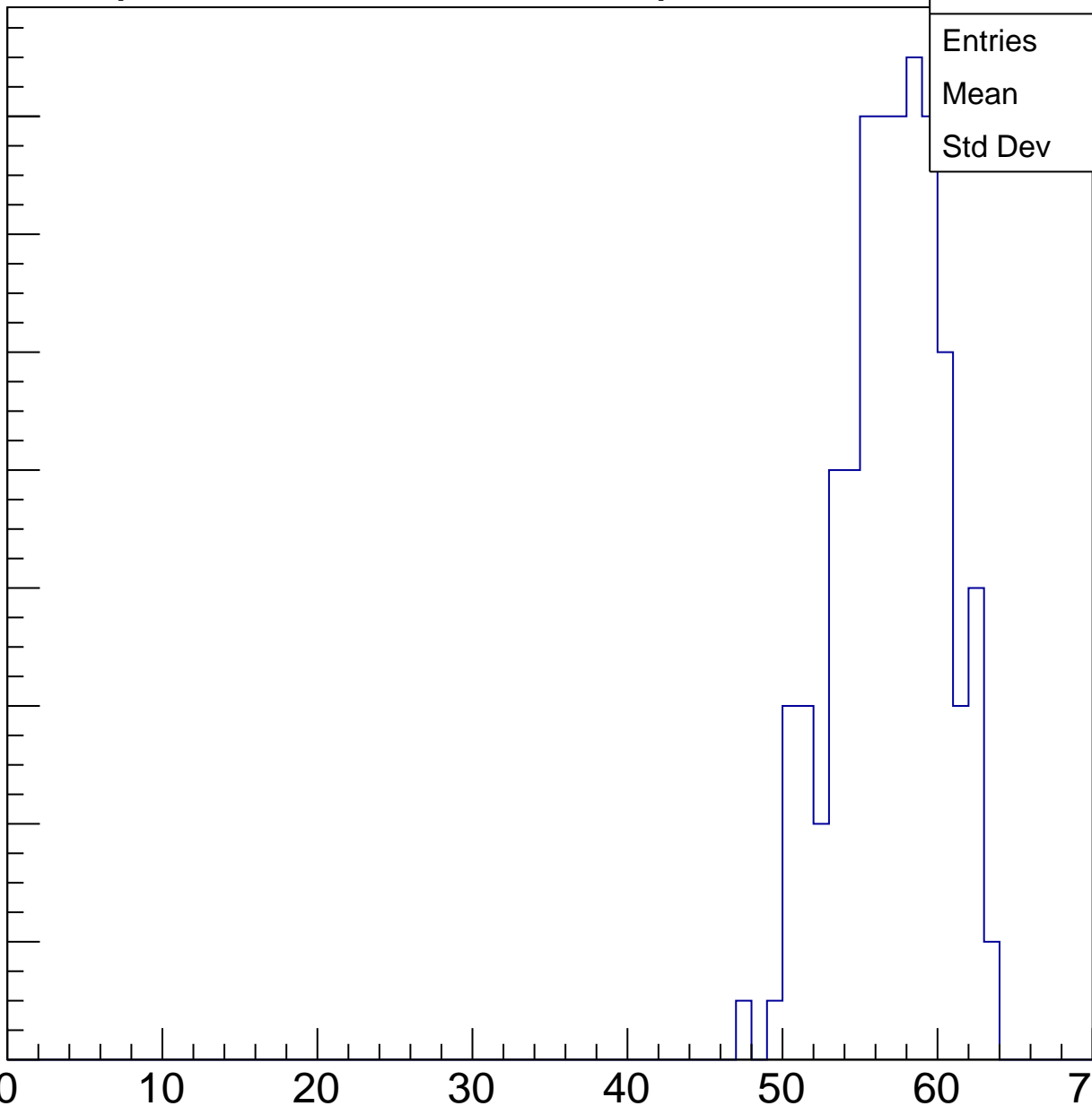
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 56.5  |
| Std Dev | 3.325 |

ampl



# B0L101S, U16-ch60, adc5

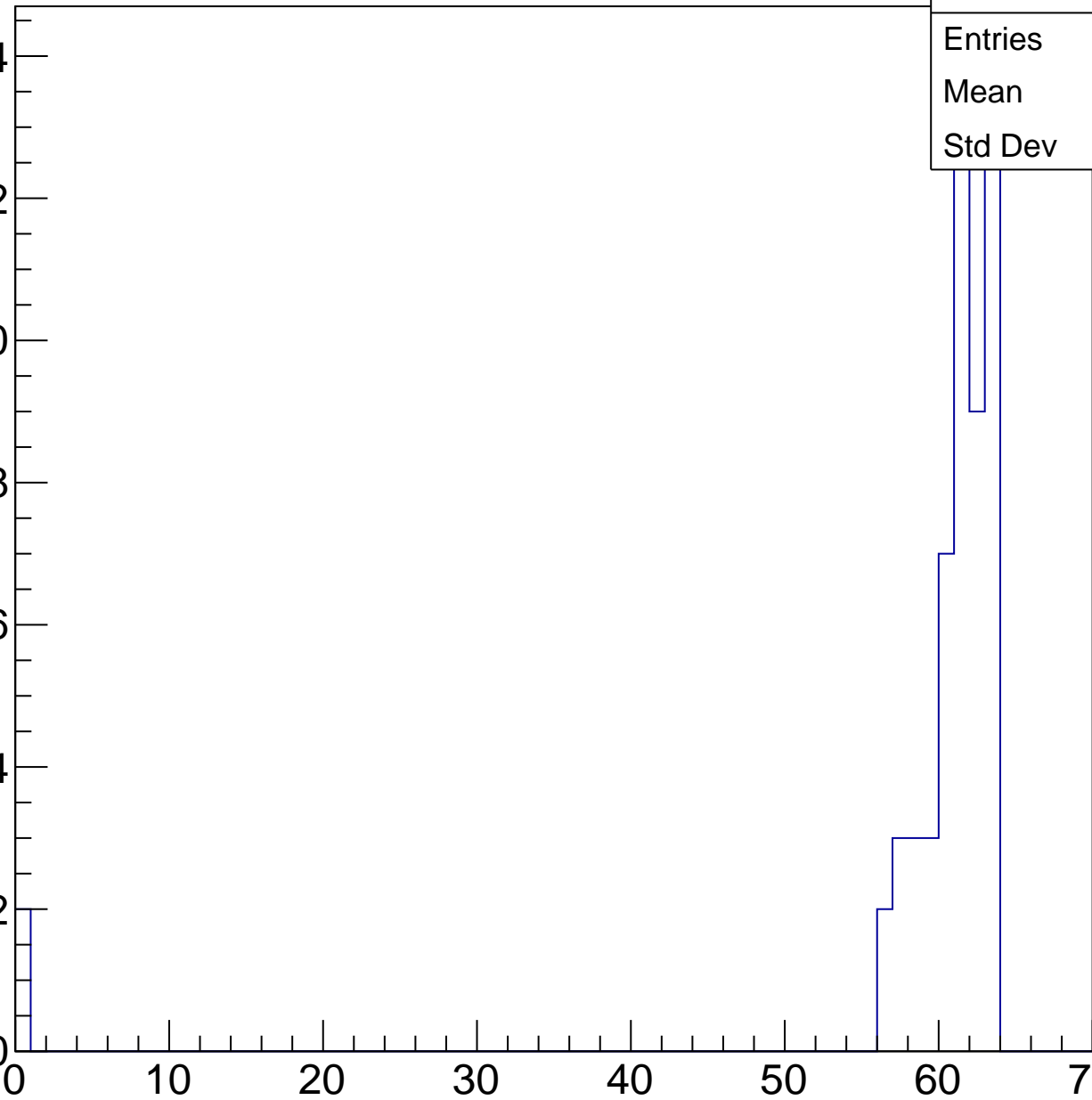
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 58.66 |
| Std Dev | 11.45 |

ampl

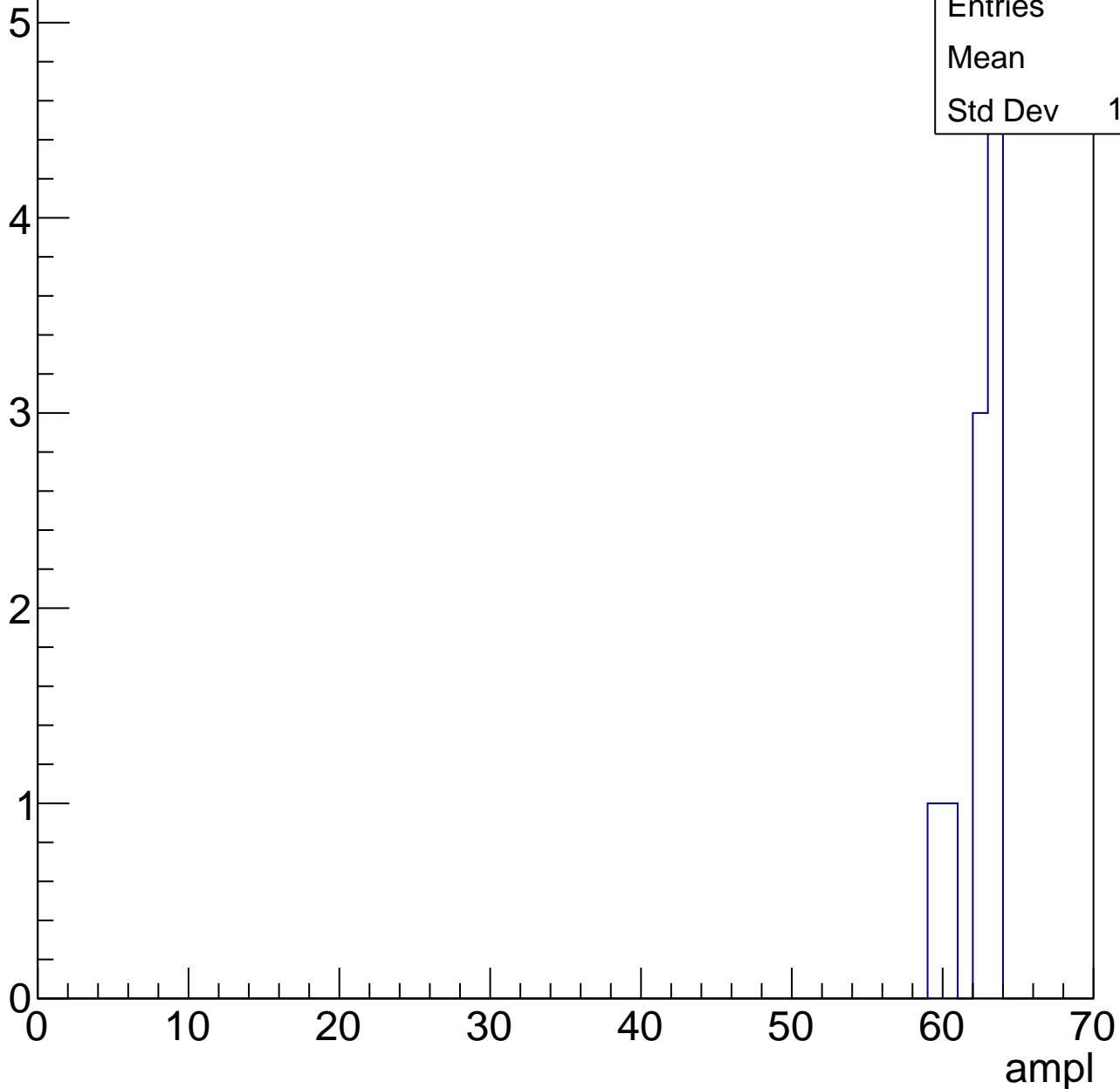


# B0L101S, U16-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 10    |
| Mean    | 62    |
| Std Dev | 1.342 |





# B0L101S, U16-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch61, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 27.8  |
| Std Dev | 4.276 |

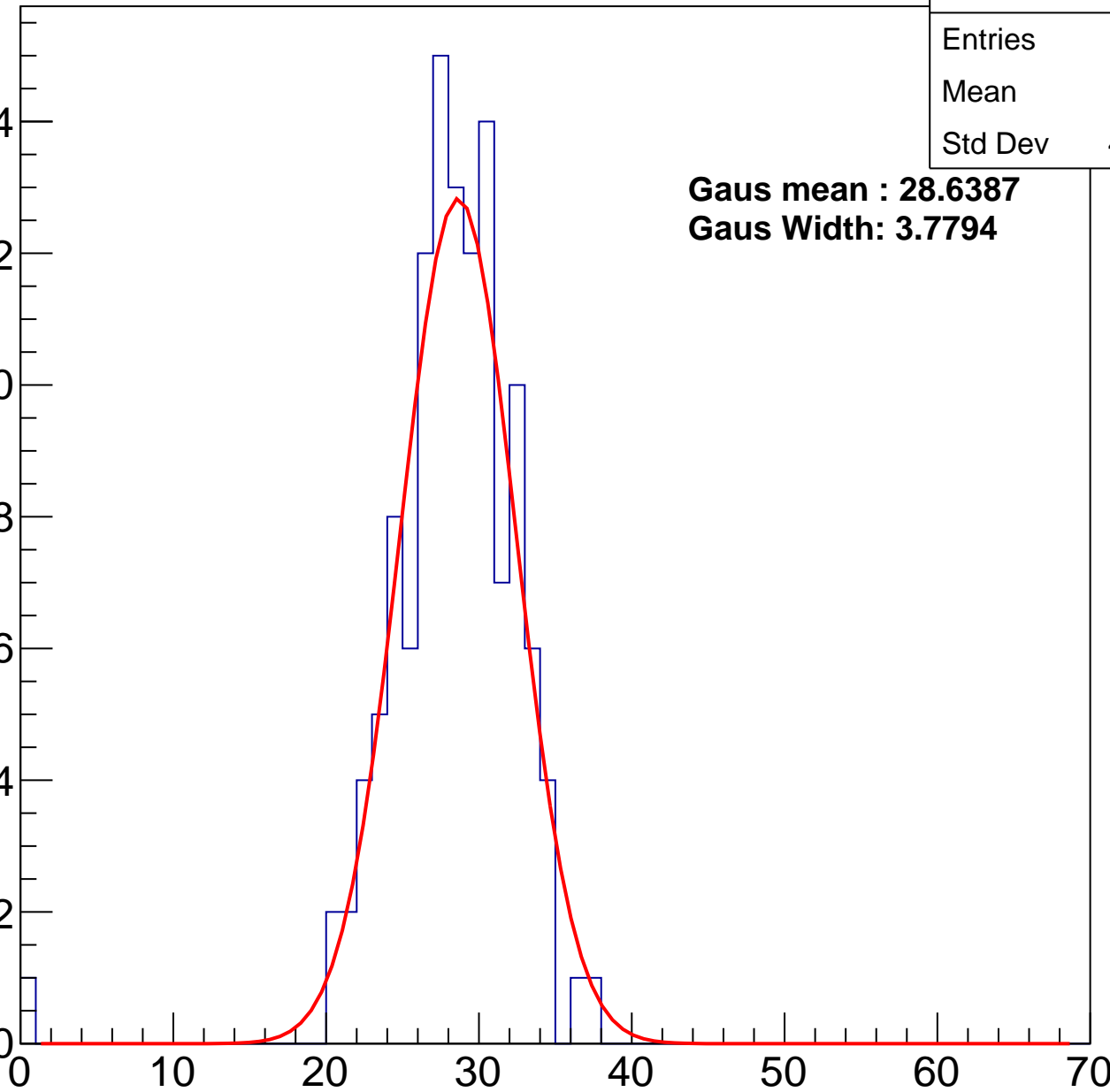
**Gaus mean : 28.6387**

**Gaus Width: 3.7794**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

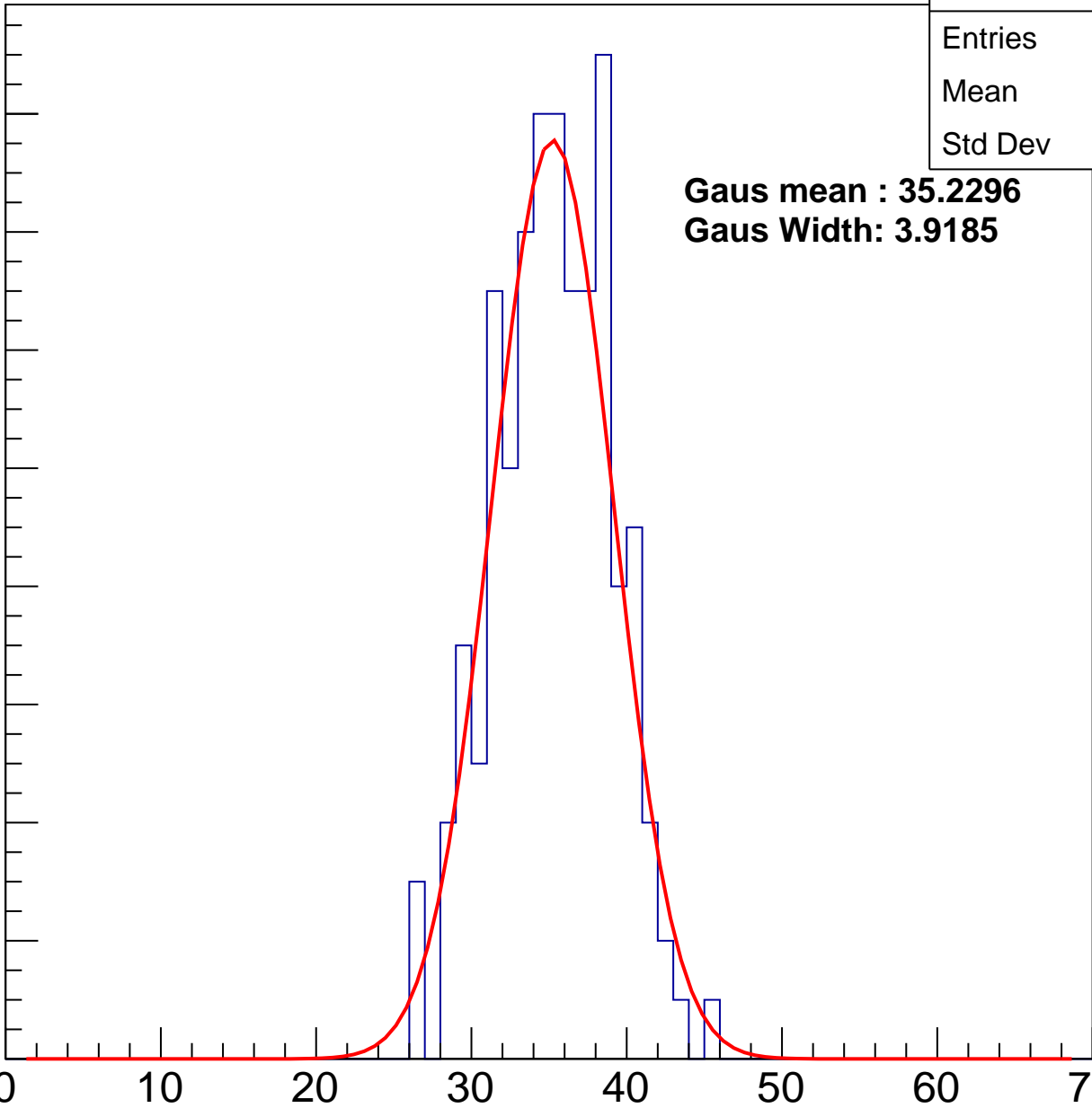
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 34.84 |
| Std Dev | 3.72  |

**Gaus mean : 35.2296**  
**Gaus Width: 3.9185**

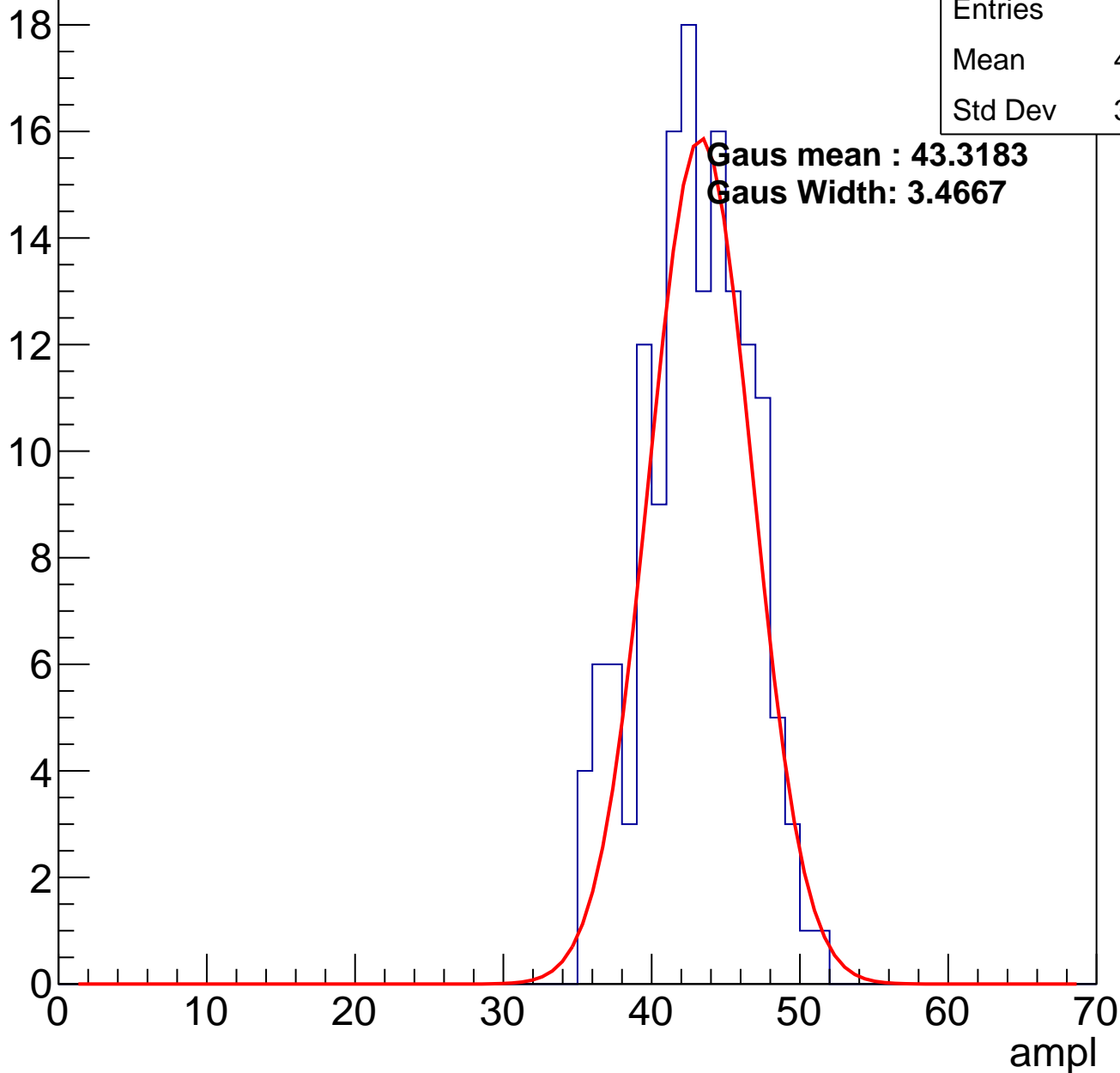
ampl



# B0L101S, U16-ch61, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 49.54 |
| Std Dev | 3.559 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

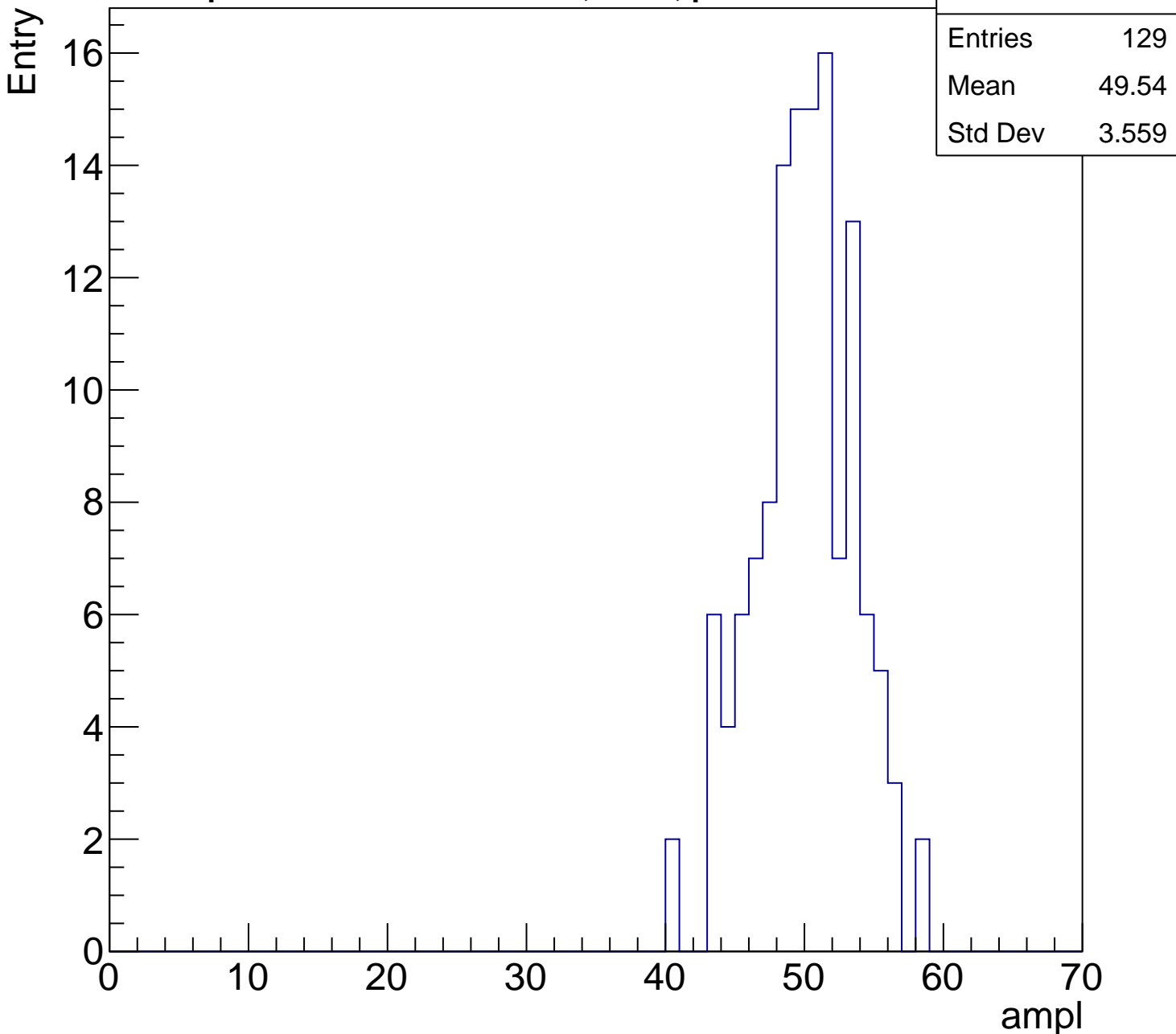
30

40

50

60

ampl



# B0L101S, U16-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

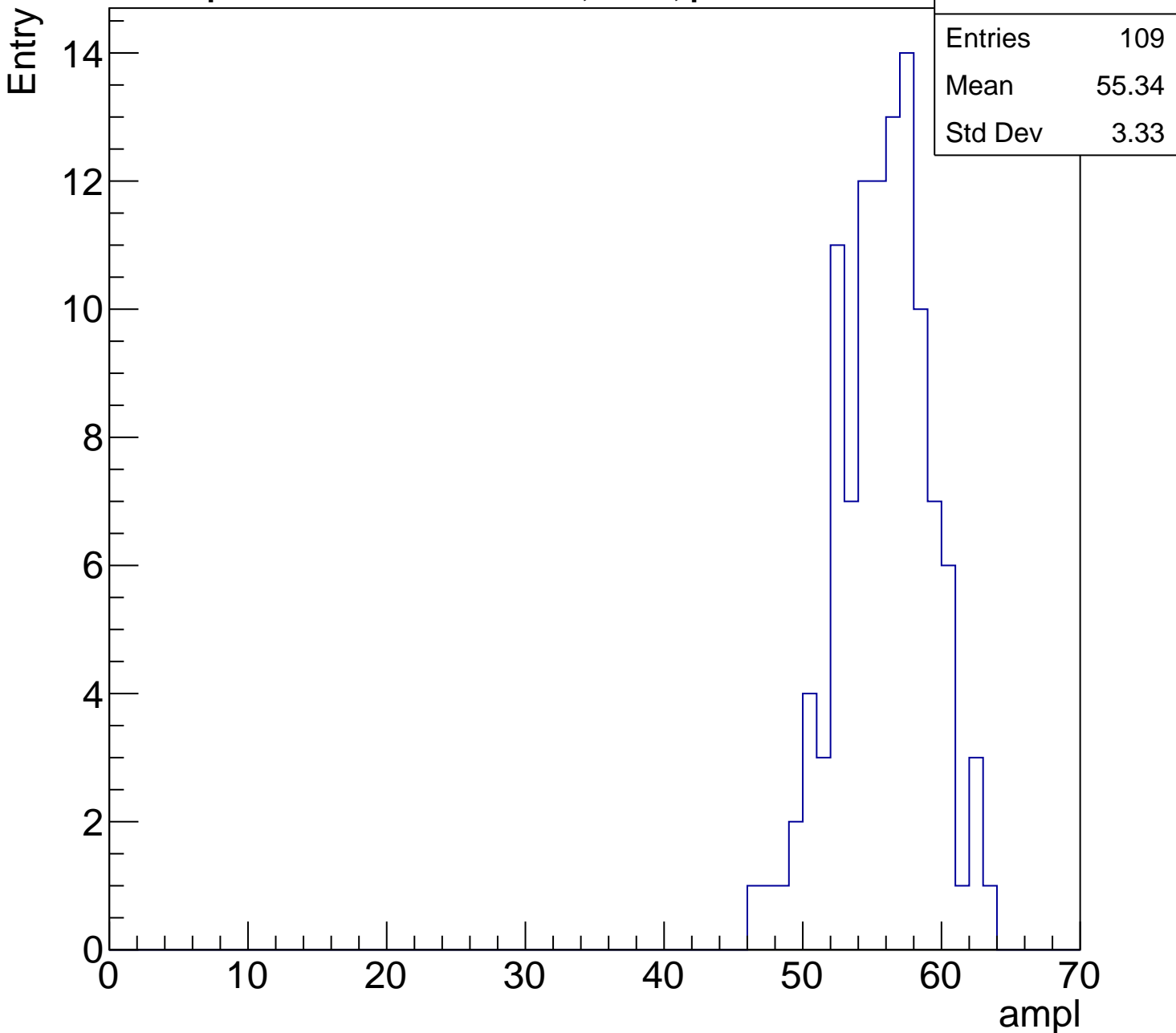
|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 55.34 |
| Std Dev | 3.33  |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

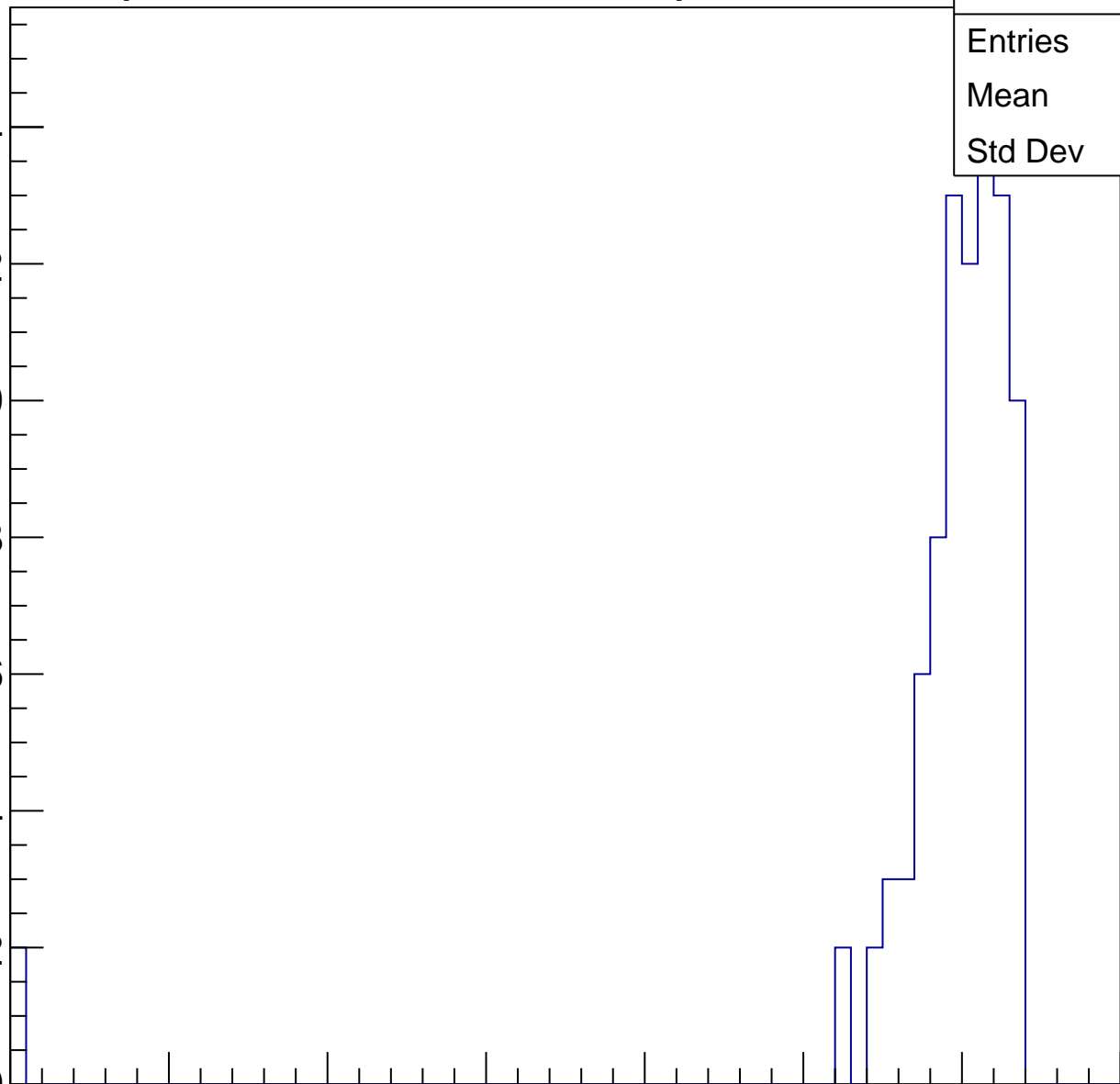
50

60

70

ampl

|         |       |
|---------|-------|
| Entries | 89    |
| Mean    | 58.3  |
| Std Dev | 9.194 |

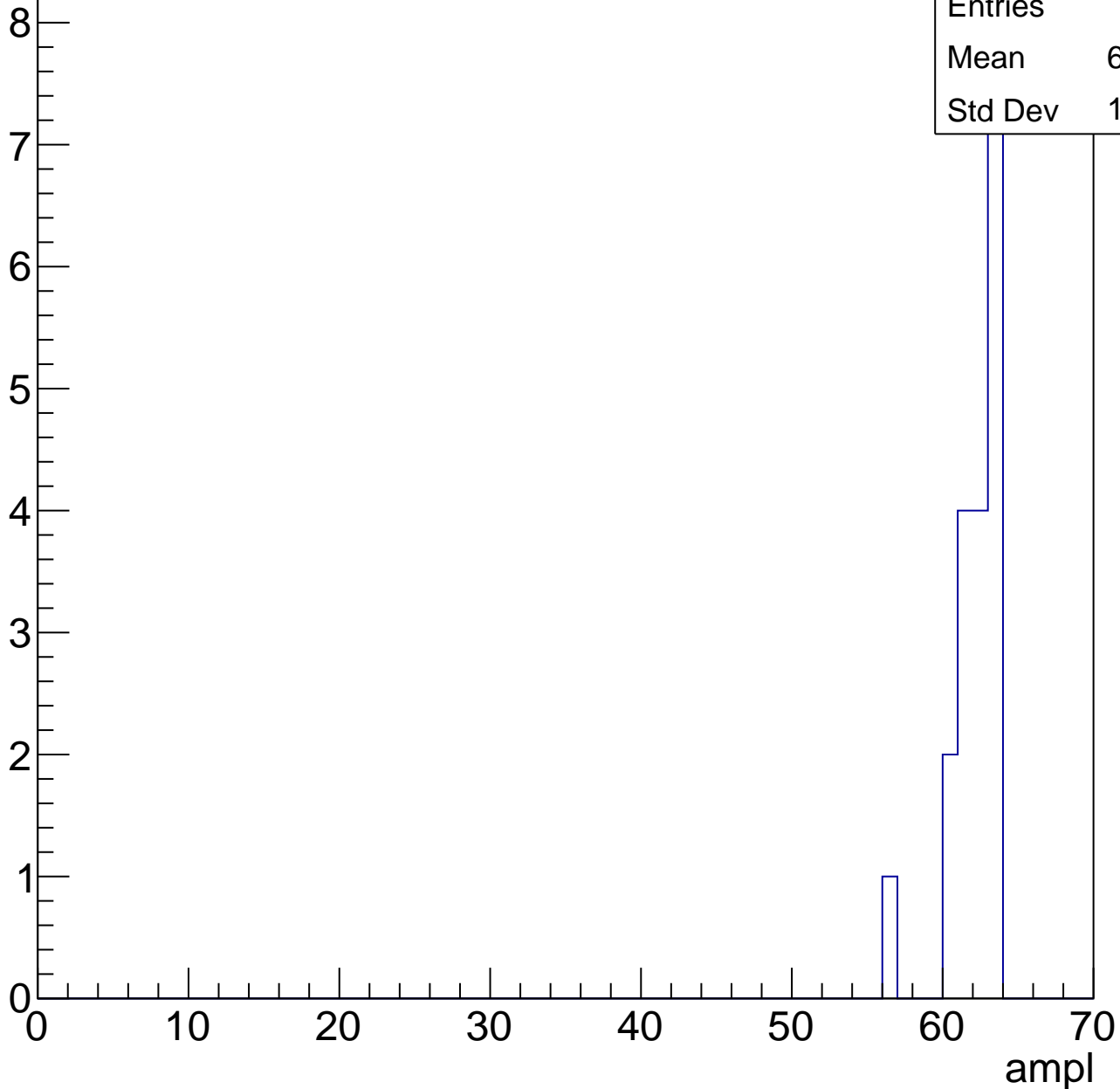


# B0L101S, U16-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 19    |
| Mean    | 61.68 |
| Std Dev | 1.687 |





# B0L101S, U16-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



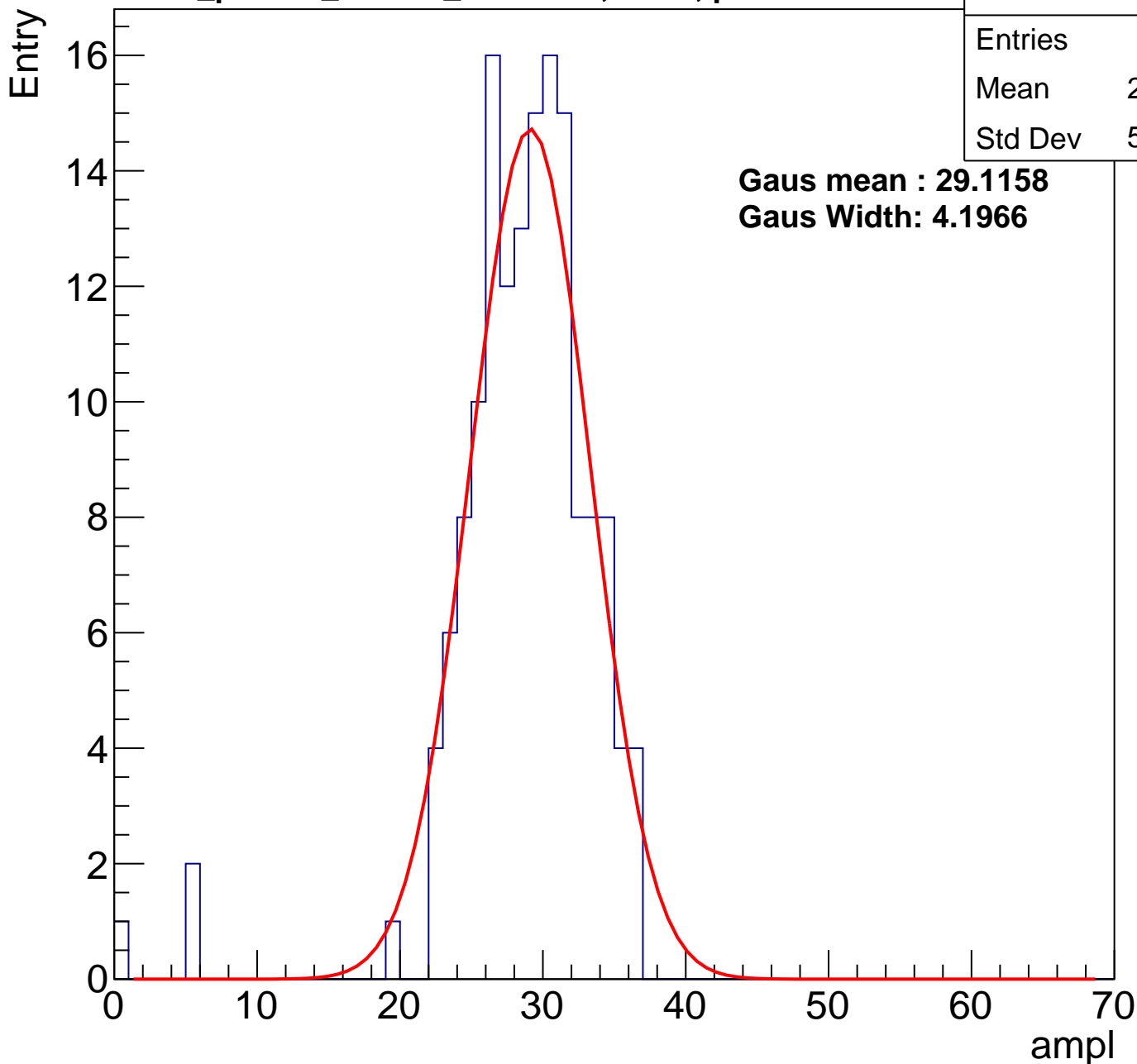
# B0L101S, U16-ch62, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 28.19 |
| Std Dev | 5.008 |

**Gaus mean : 29.1158**

**Gaus Width: 4.1966**



# B0L101S, U16-ch62, adc1

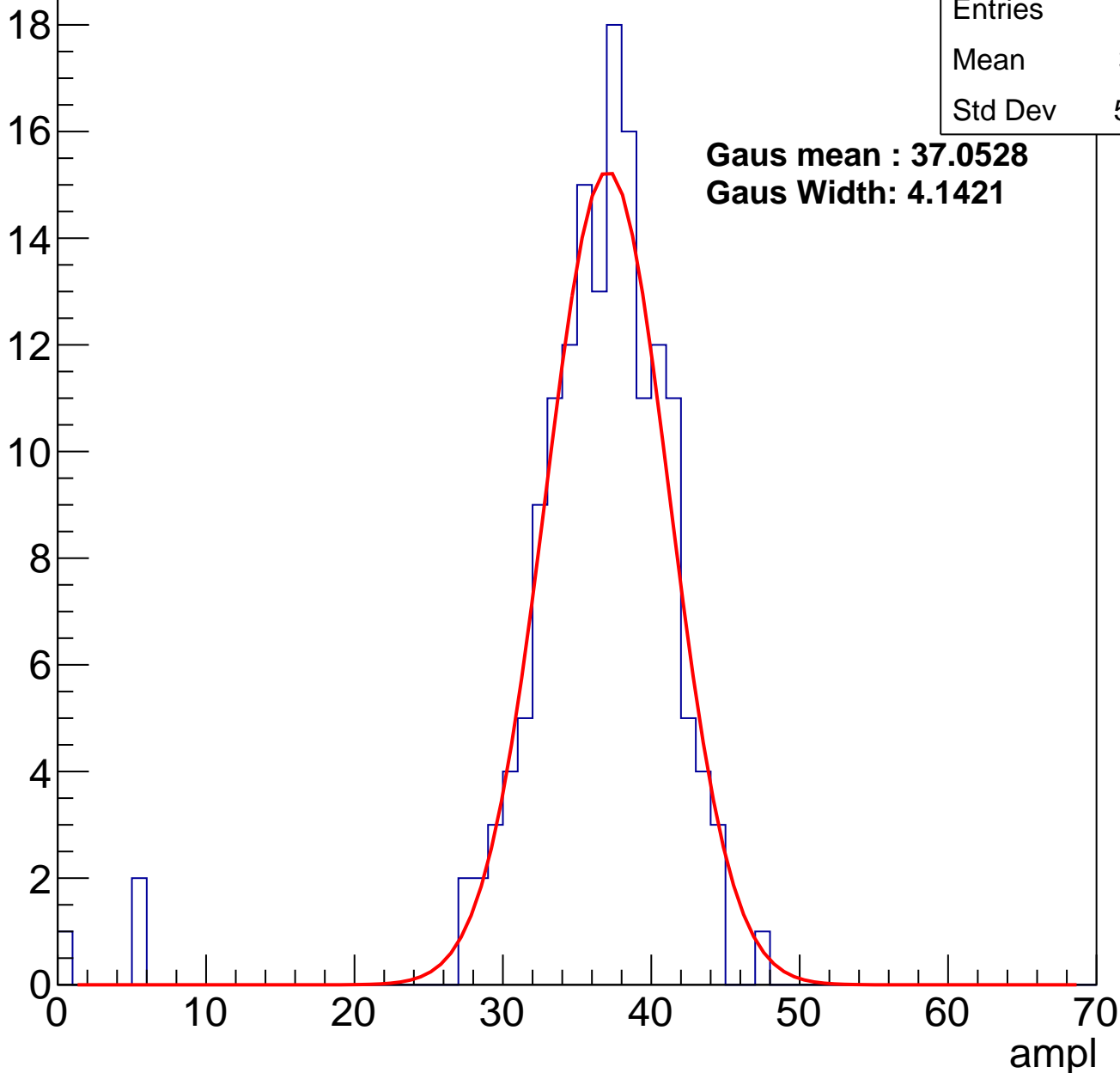
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 35.81 |
| Std Dev | 5.895 |

**Gaus mean : 37.0528**

**Gaus Width: 4.1421**

Entry



# B0L101S, U16-ch62, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

124

Mean

43.56

Std Dev

4.061

**Gaus mean : 44.1013**

**Gaus Width: 4.1937**

0

10

20

30

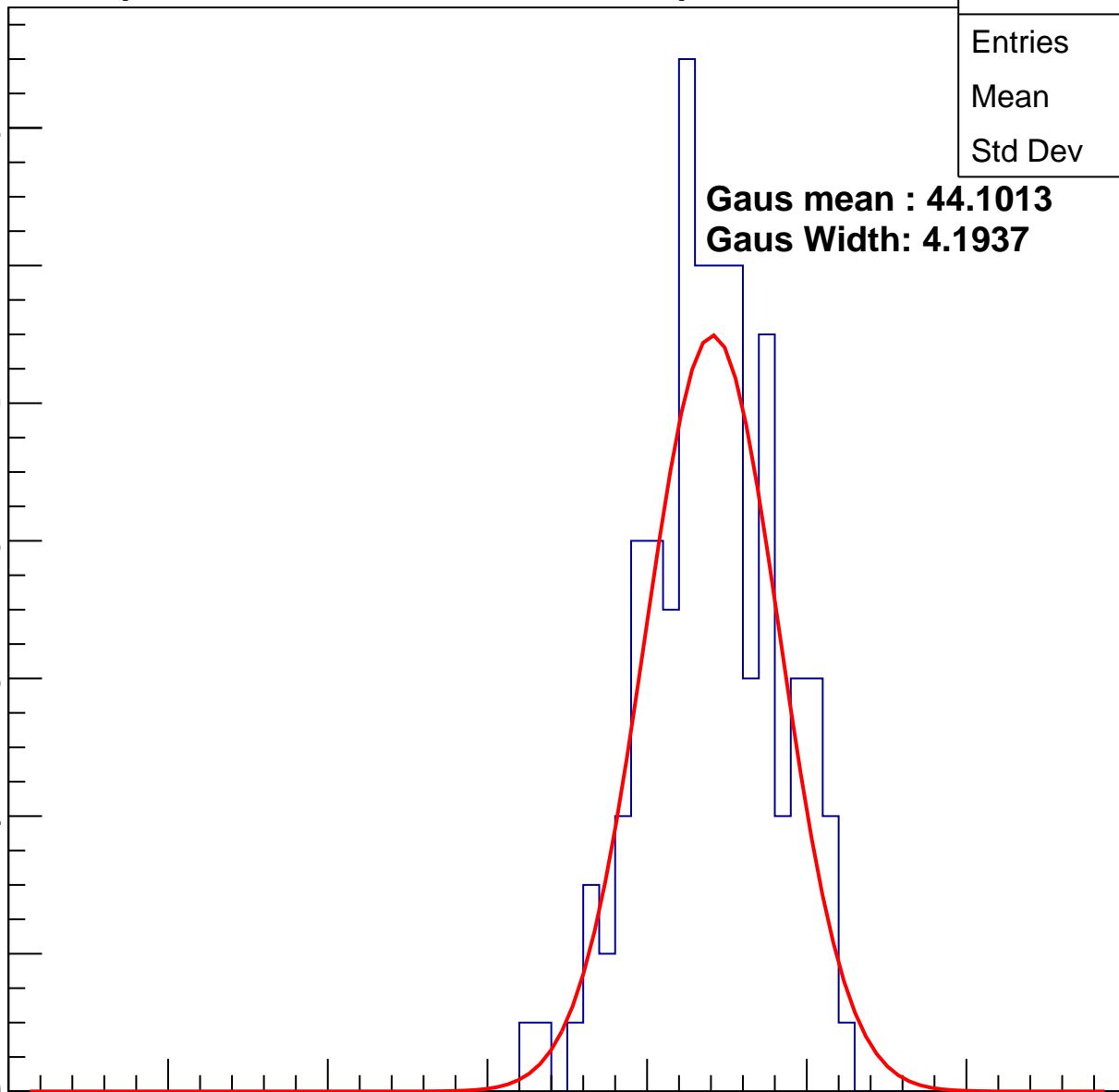
40

50

60

70

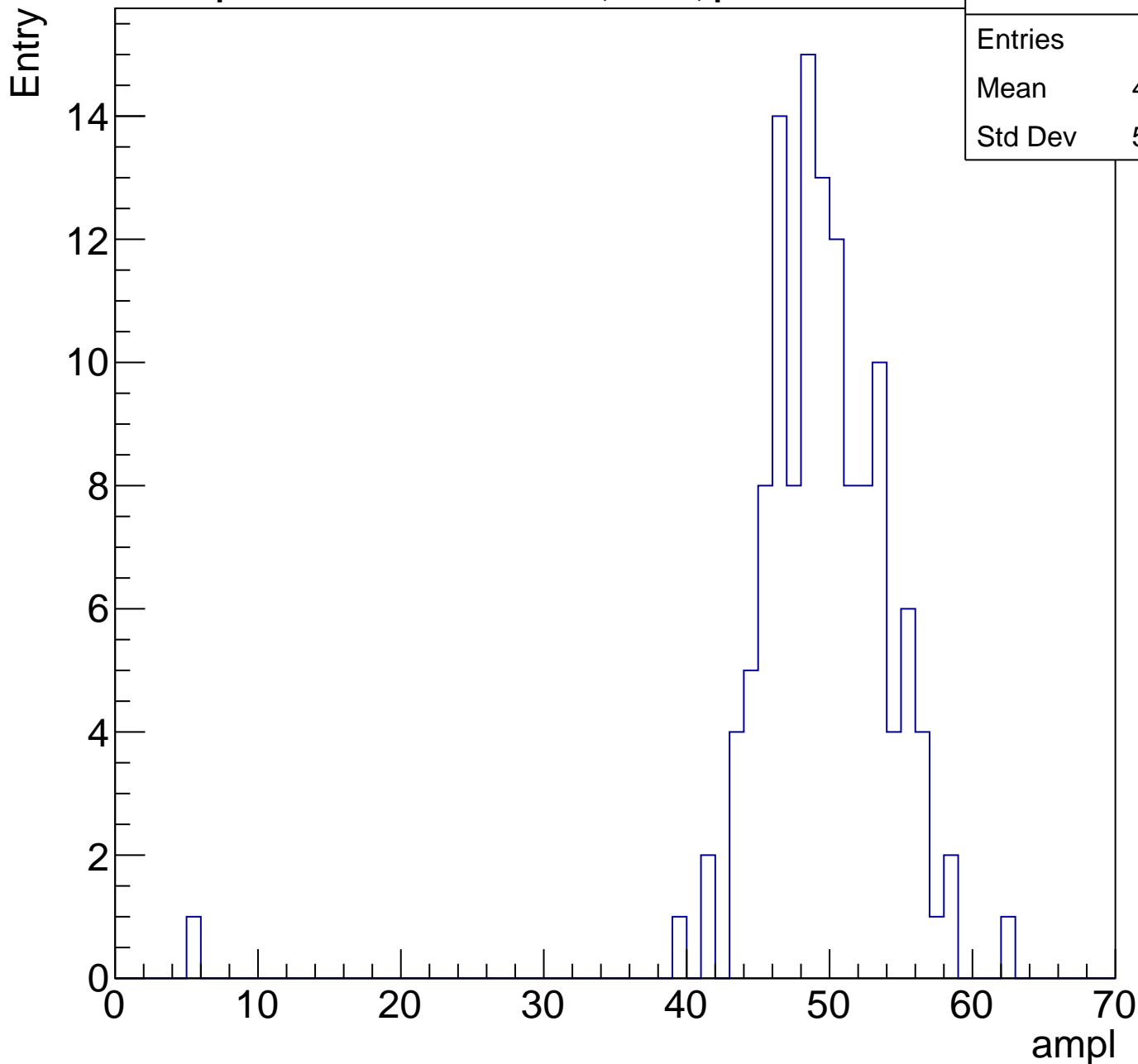
ampl



# B0L101S, U16-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

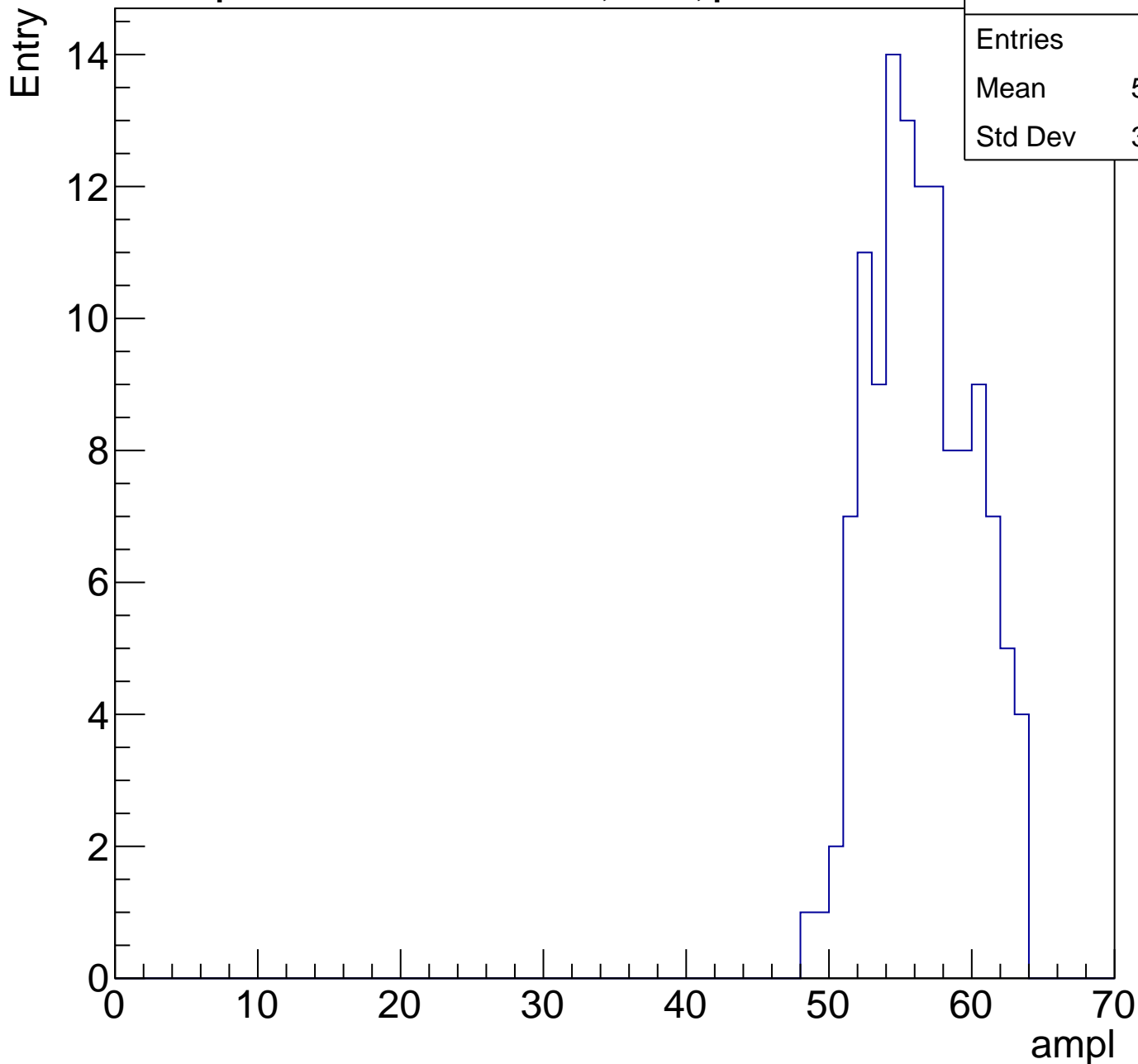
|         |       |
|---------|-------|
| Entries | 127   |
| Mean    | 48.93 |
| Std Dev | 5.553 |



# B0L101S, U16-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 56.06 |
| Std Dev | 3.488 |



# B0L101S, U16-ch62, adc5

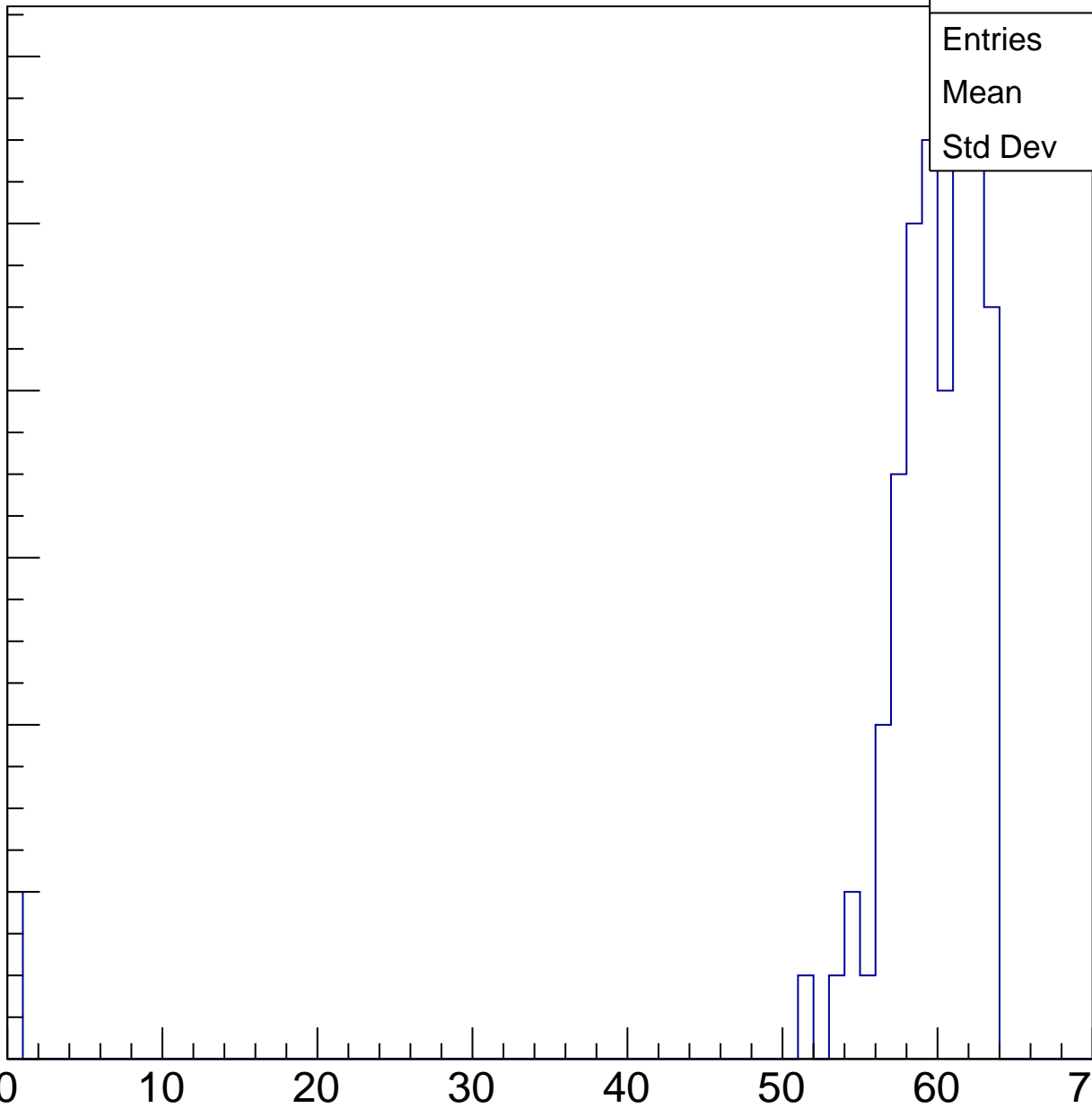
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 57.97 |
| Std Dev | 9.69  |

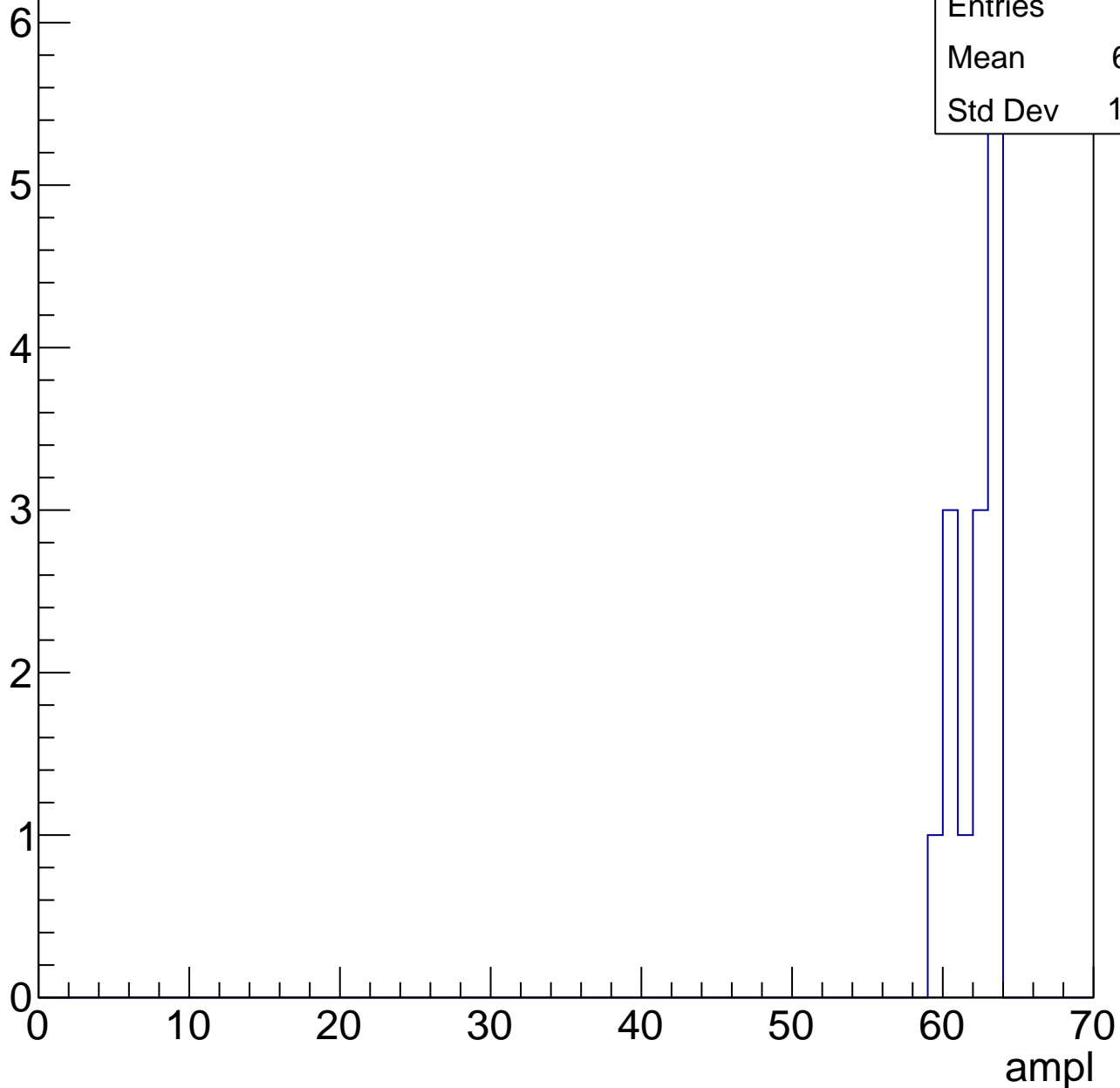
ampl



# B0L101S, U16-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

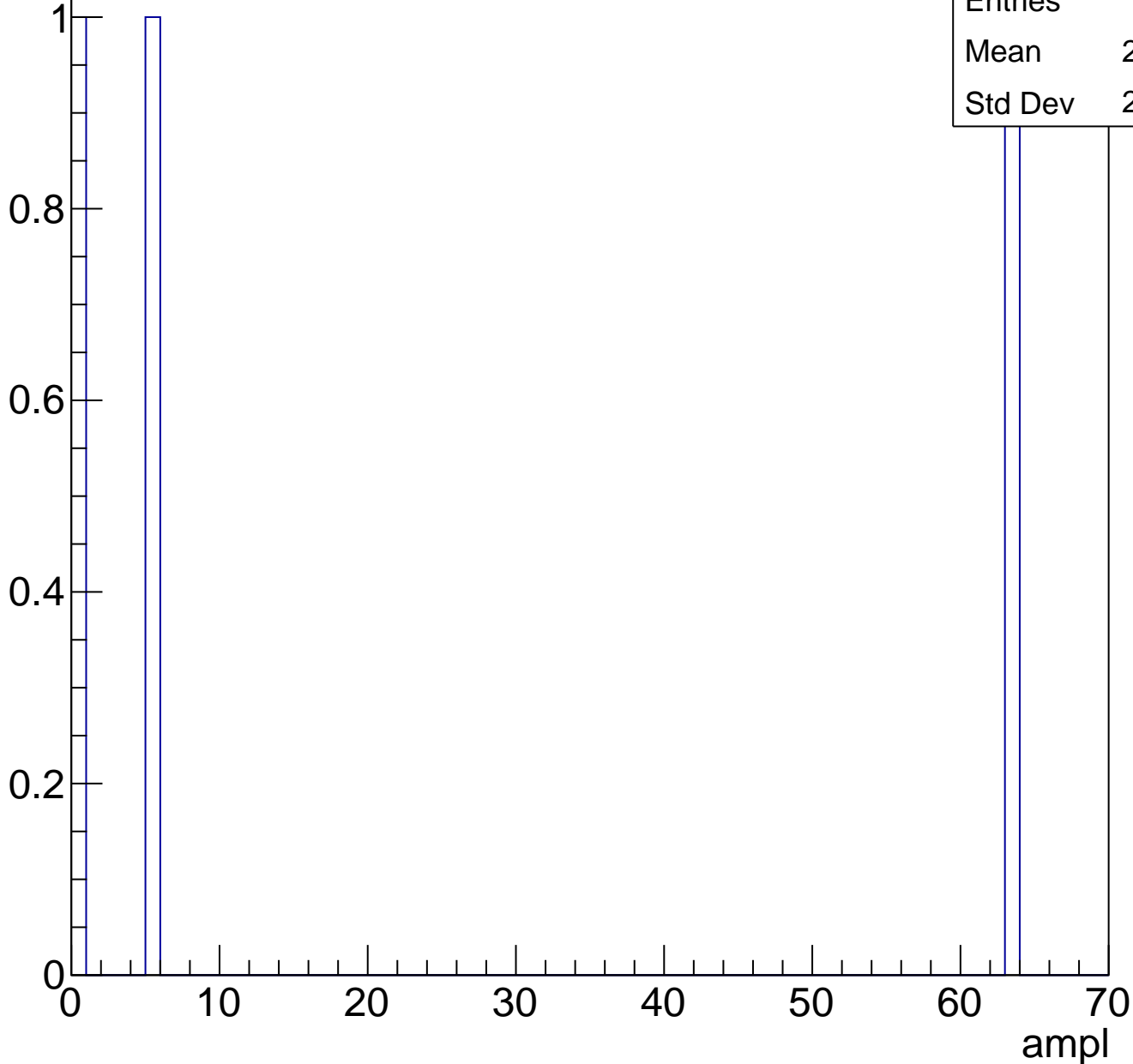




# B0L101S, U16-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



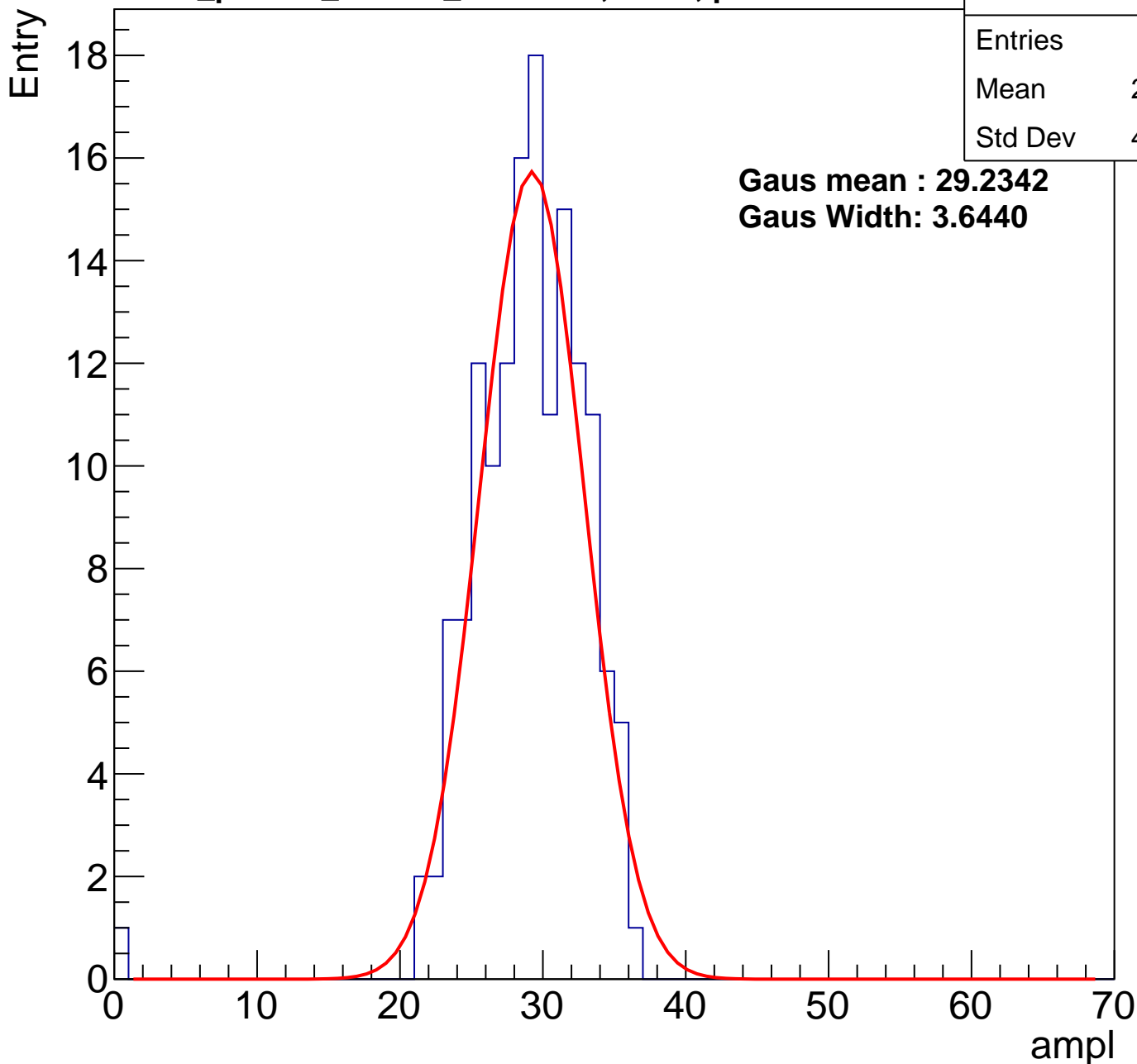
# B0L101S, U16-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 28.55 |
| Std Dev | 4.146 |

**Gaus mean : 29.2342**

**Gaus Width: 3.6440**



# B0L101S, U16-ch63, adc1

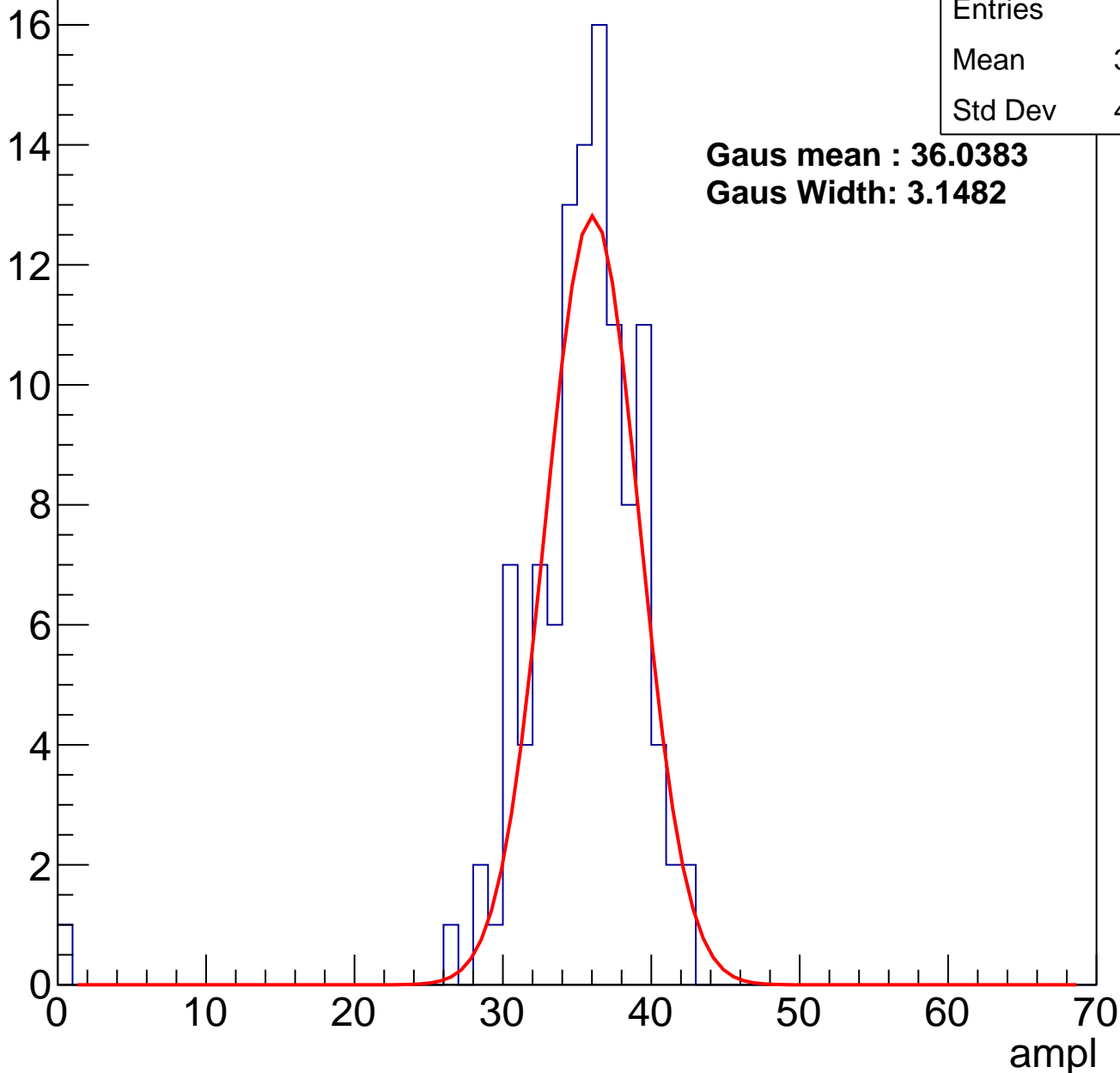
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 34.92 |
| Std Dev | 4.628 |

**Gaus mean : 36.0383**

**Gaus Width: 3.1482**

Entry



# B0L101S, U16-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 41.73 |
| Std Dev | 3.575 |

**Gaus mean : 42.6197**

**Gaus Width: 3.5047**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

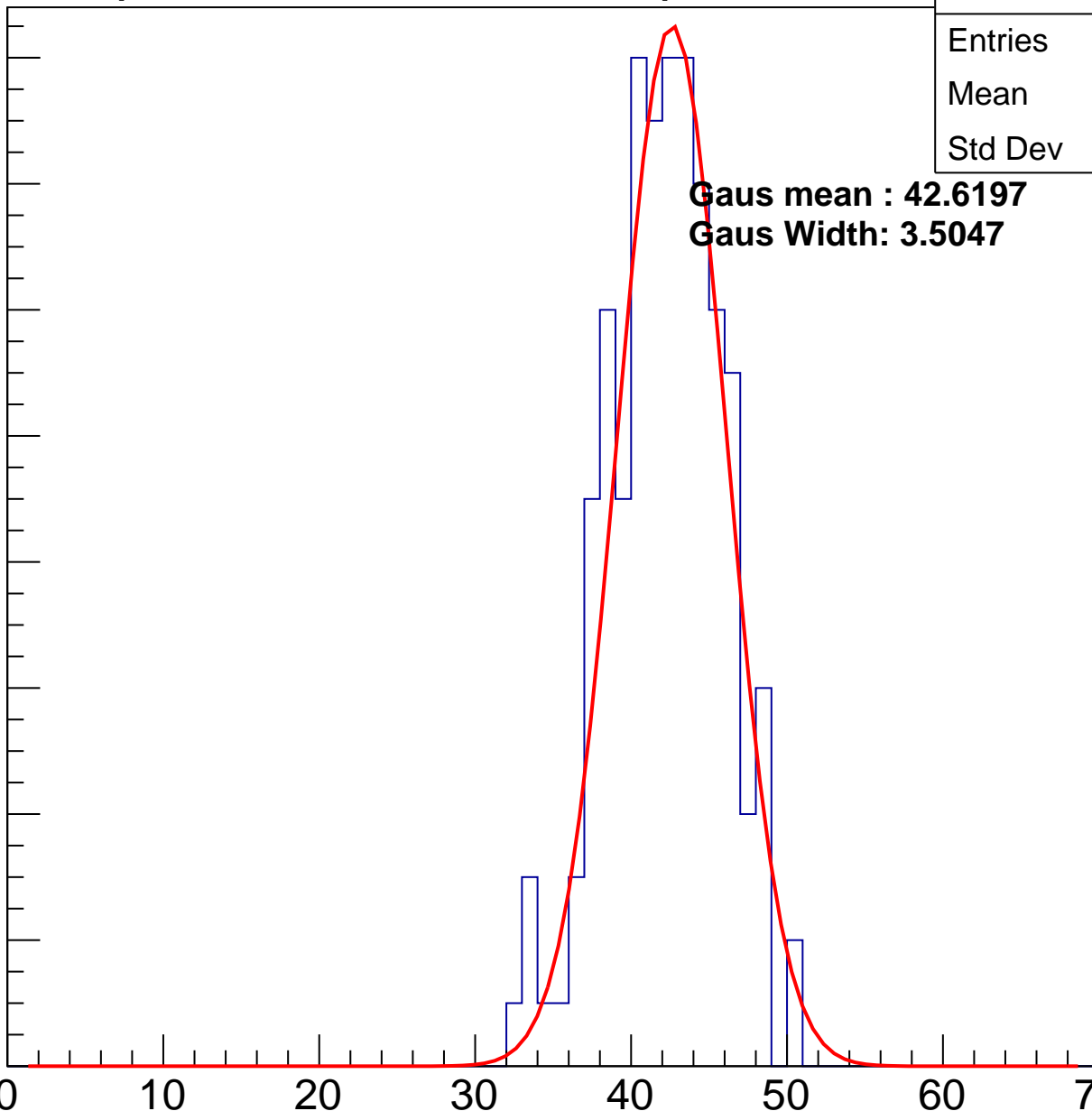
40

50

60

70

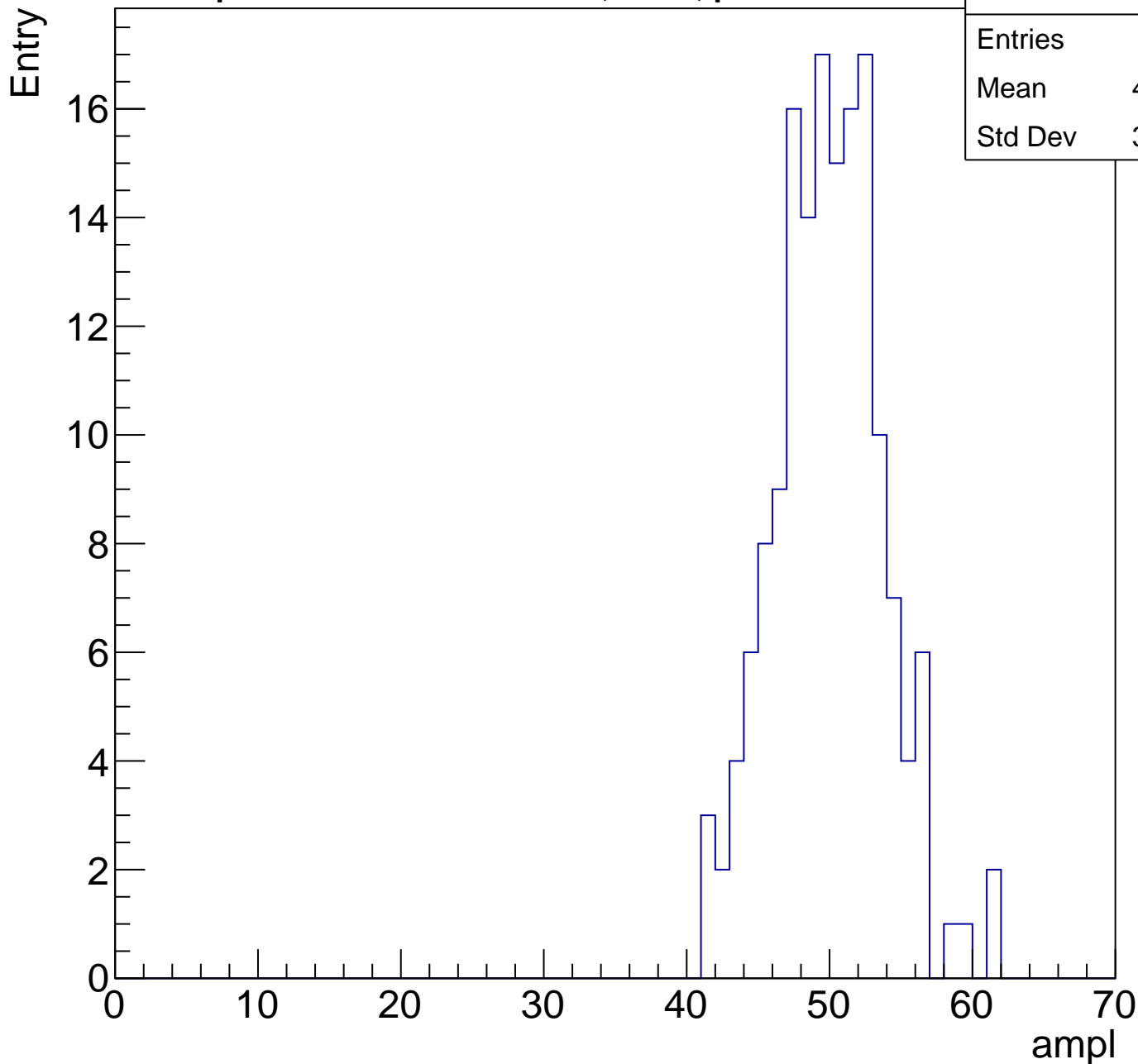
ampl



# B0L101S, U16-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 49.54 |
| Std Dev | 3.815 |



# B0L101S, U16-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

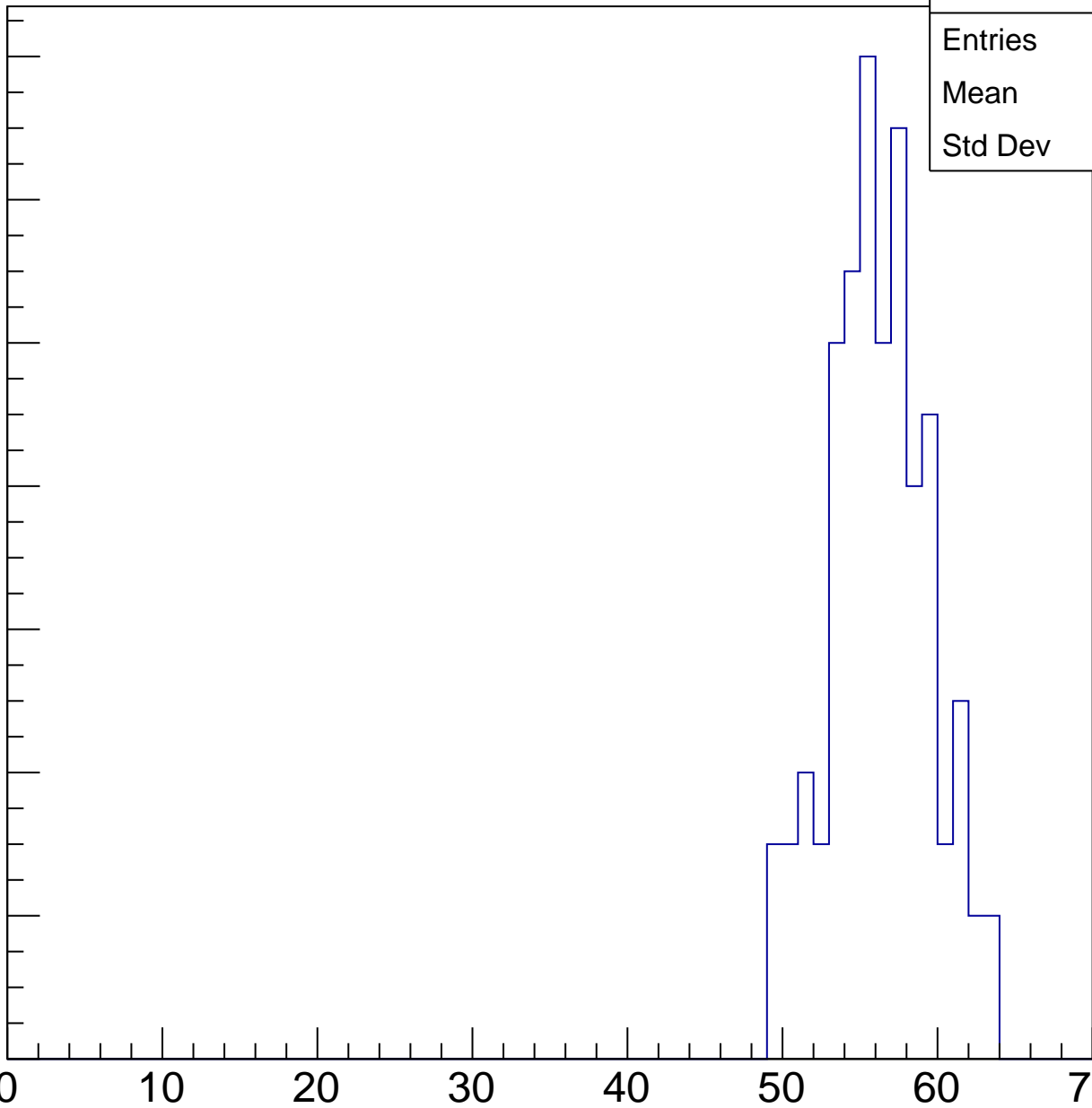
|         |       |
|---------|-------|
| Entries | 100   |
| Mean    | 55.82 |
| Std Dev | 3.189 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch63, adc5

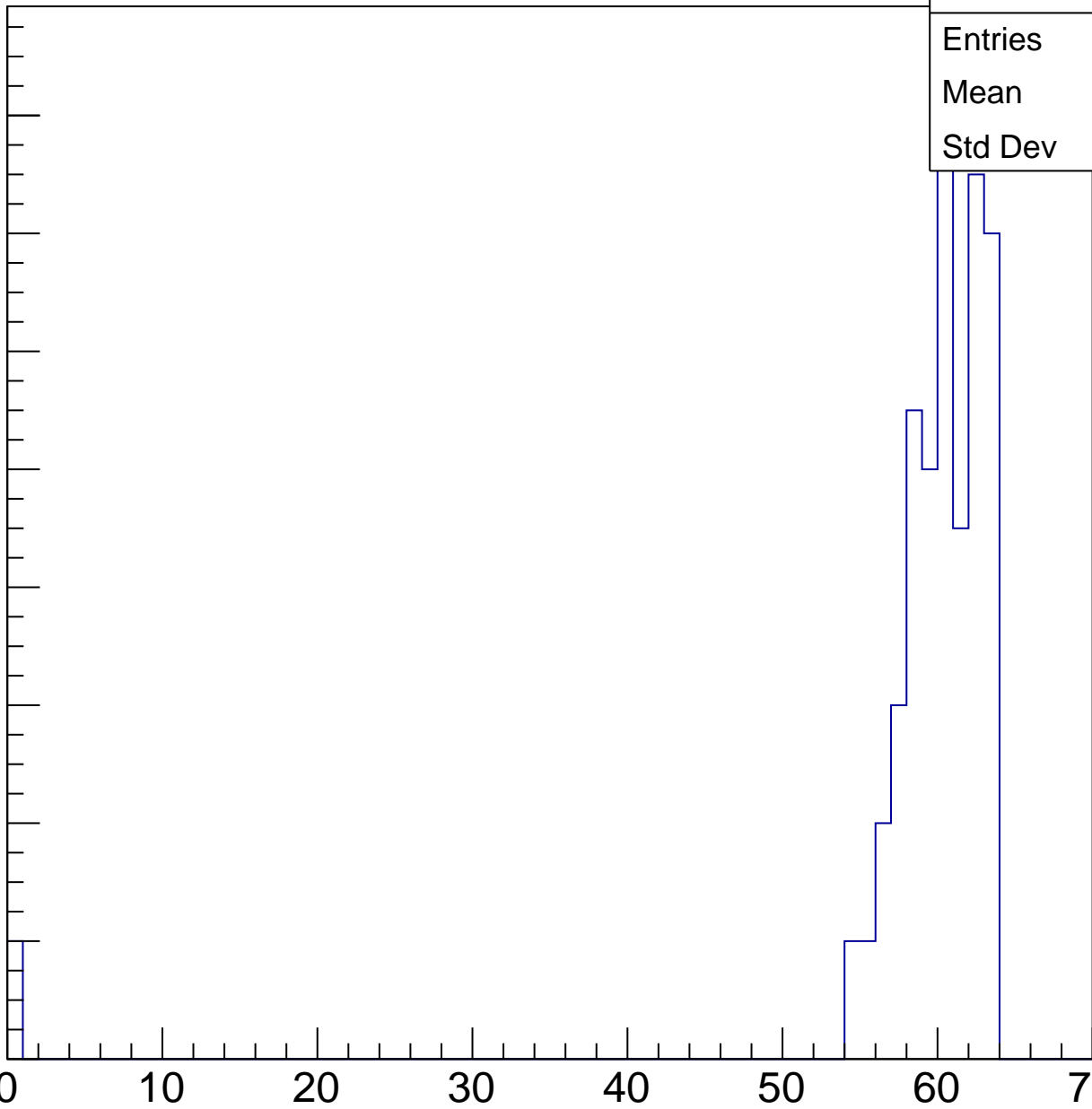
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 58.62 |
| Std Dev | 9.037 |

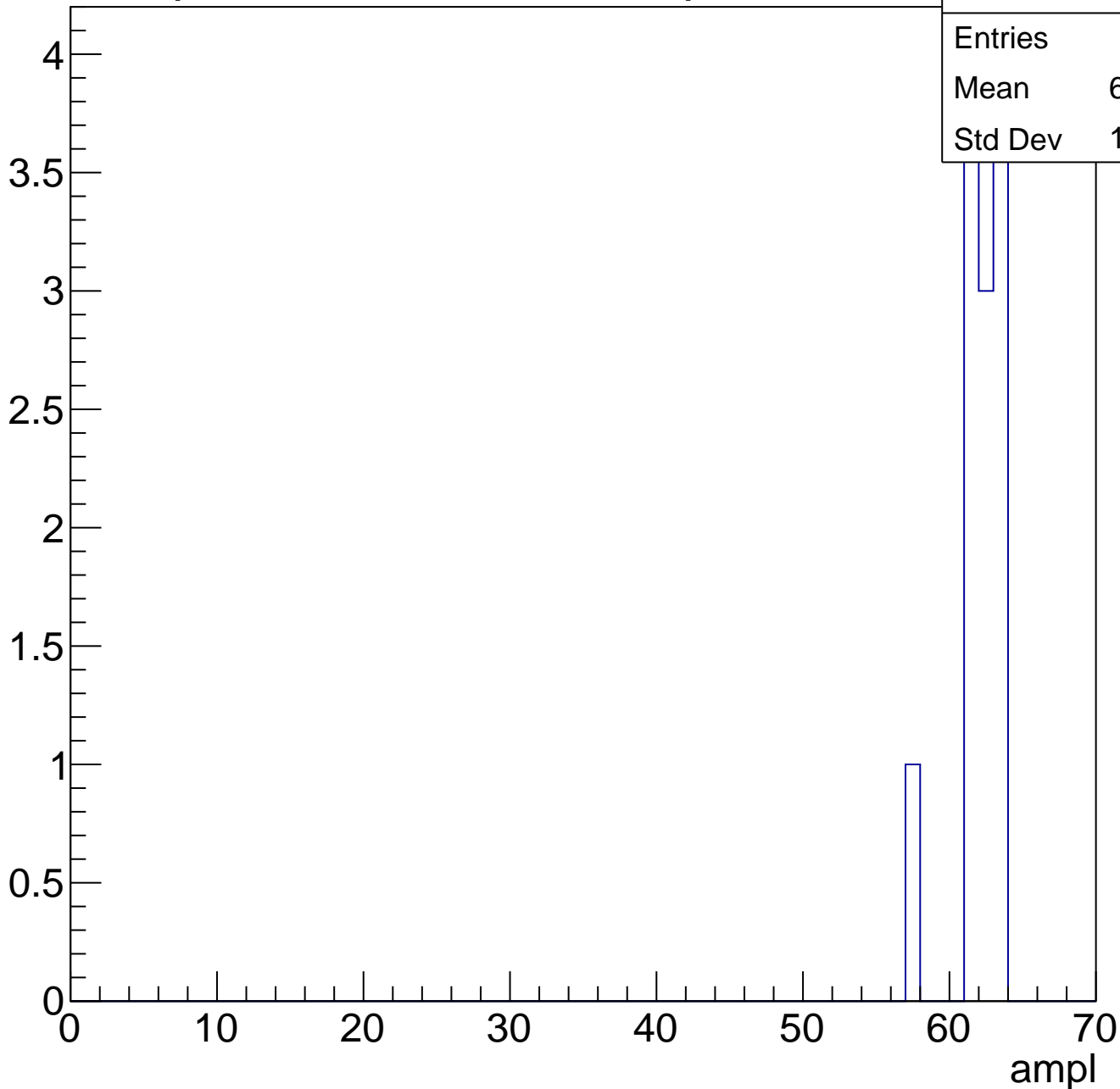
ampl



# B0L101S, U16-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch64, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

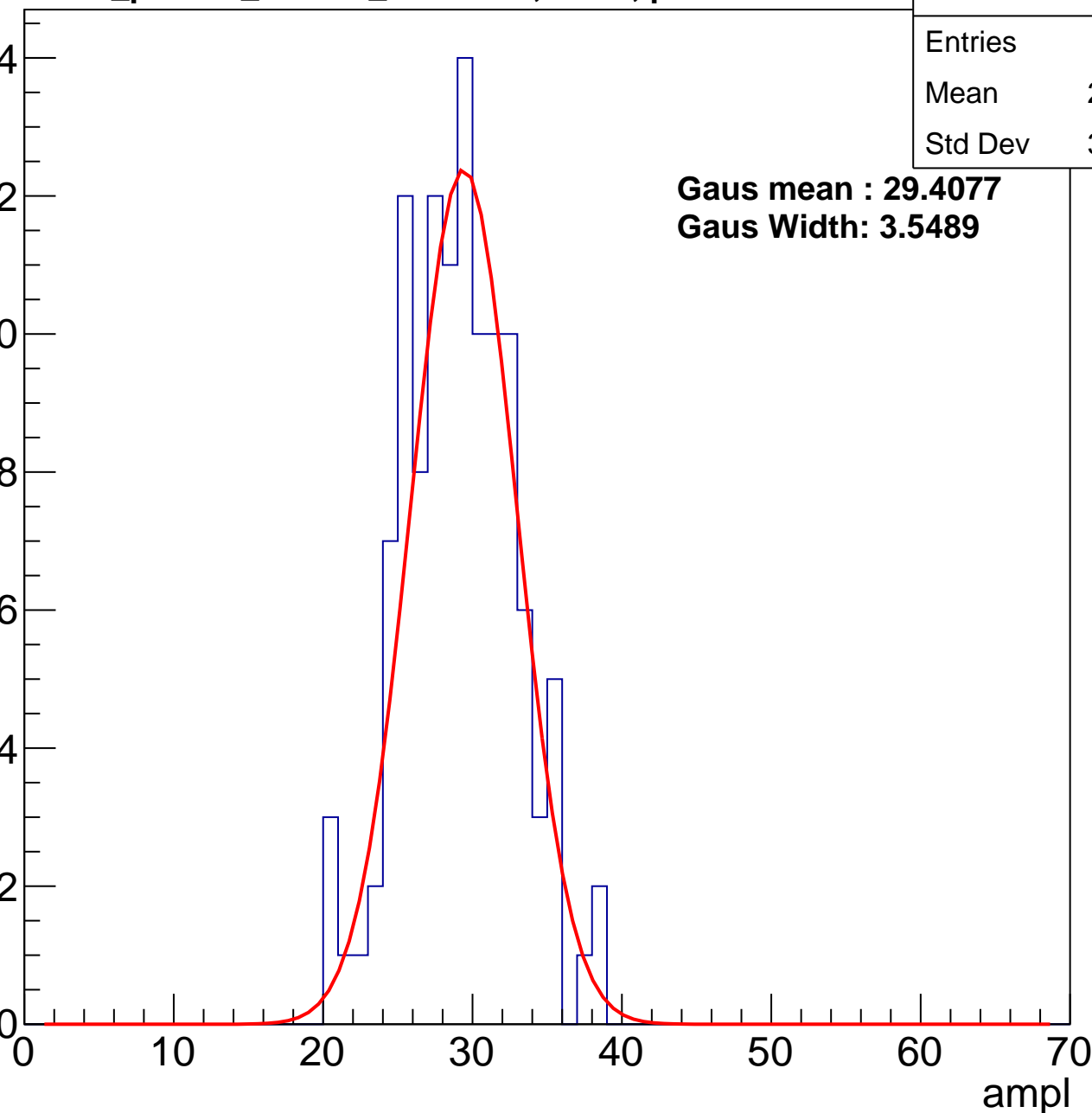
Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 28.65 |
| Std Dev | 3.715 |

**Gaus mean : 29.4077**

**Gaus Width: 3.5489**



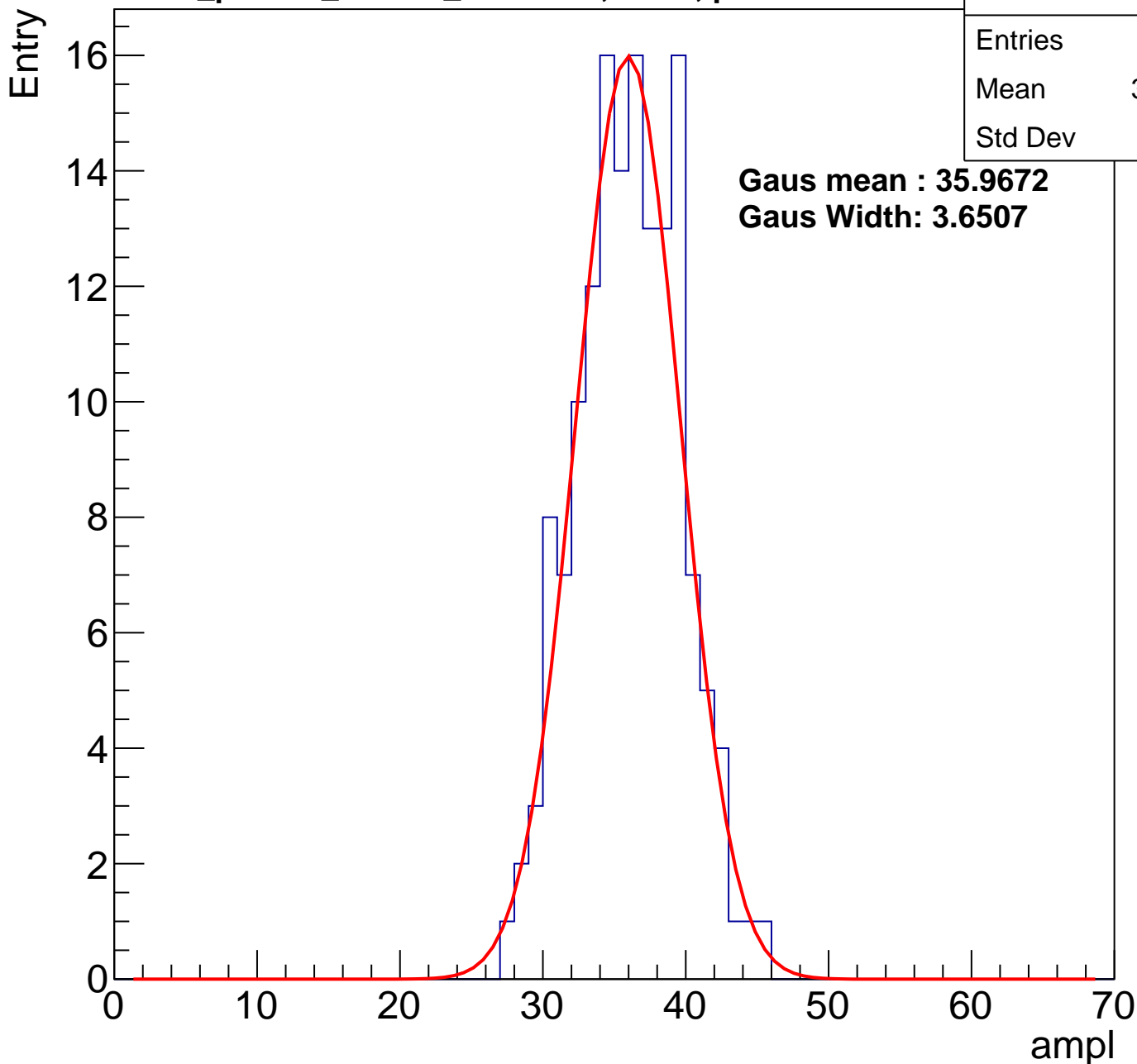
# B0L101S, U16-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 150   |
| Mean    | 35.58 |
| Std Dev | 3.58  |

**Gaus mean : 35.9672**

**Gaus Width: 3.6507**



# B0L101S, U16-ch64, adc2

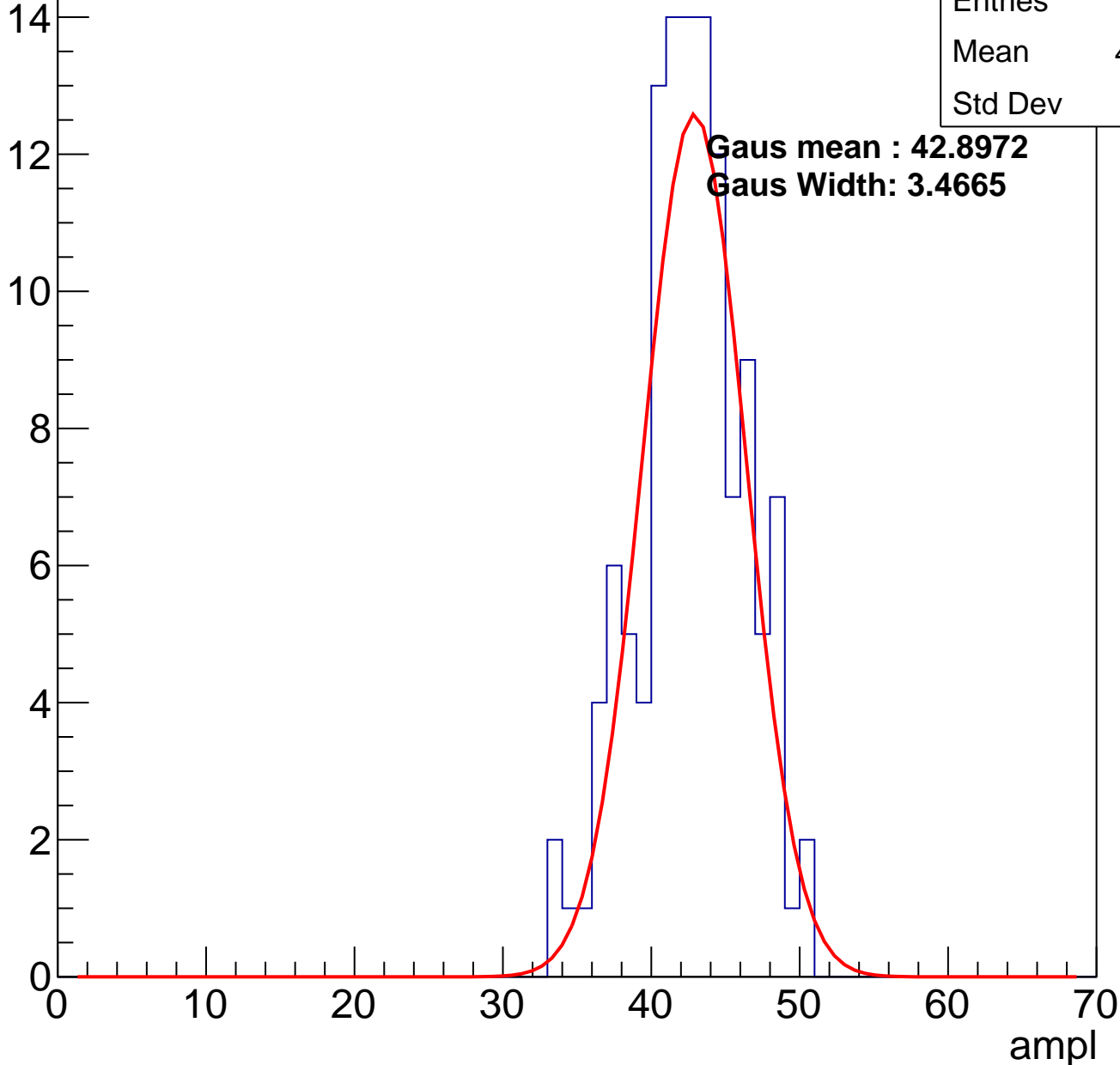
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 42.21 |
| Std Dev | 3.6   |

**Gaus mean : 42.8972**

**Gaus Width: 3.4665**

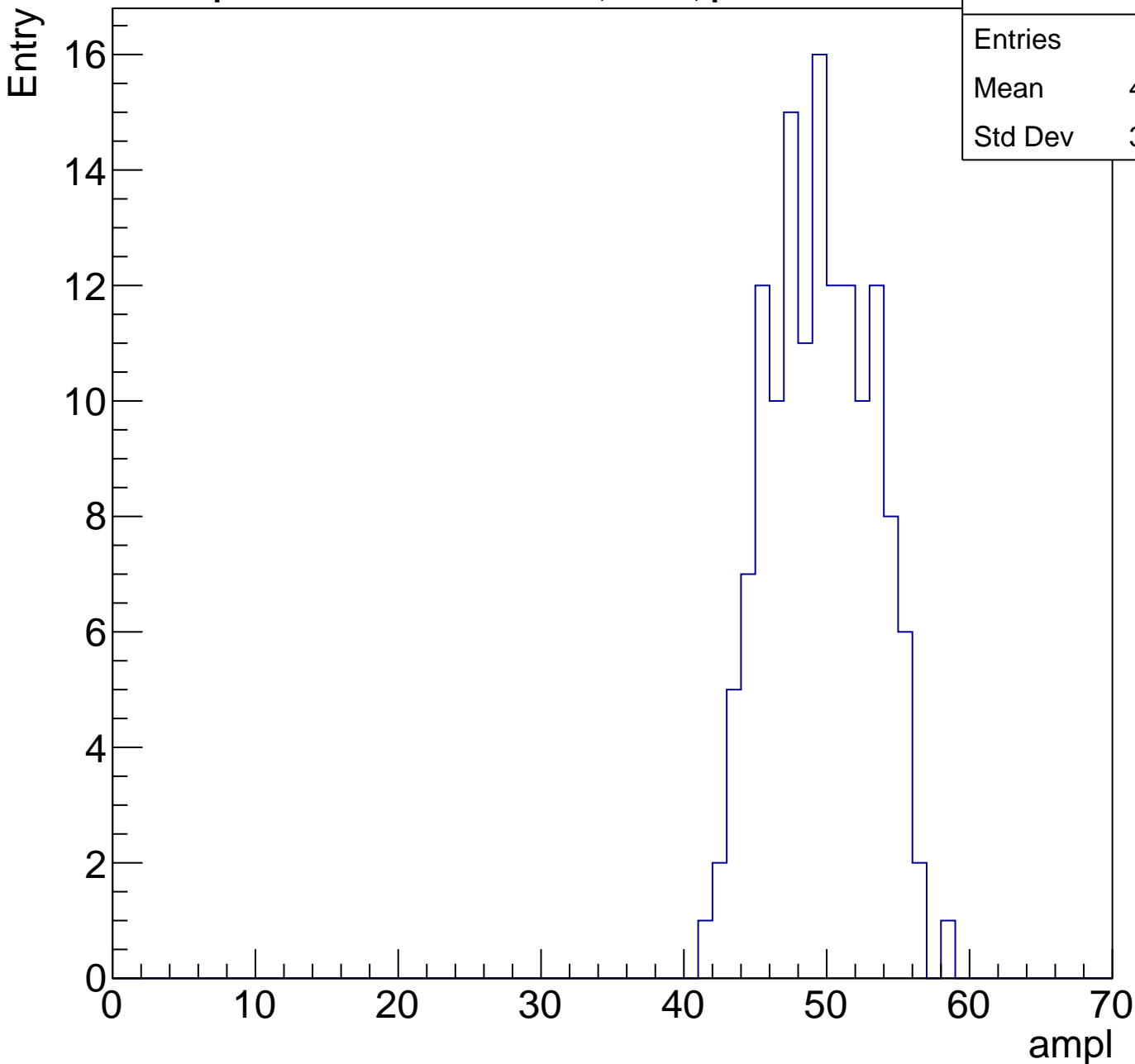
Entry



# B0L101S, U16-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

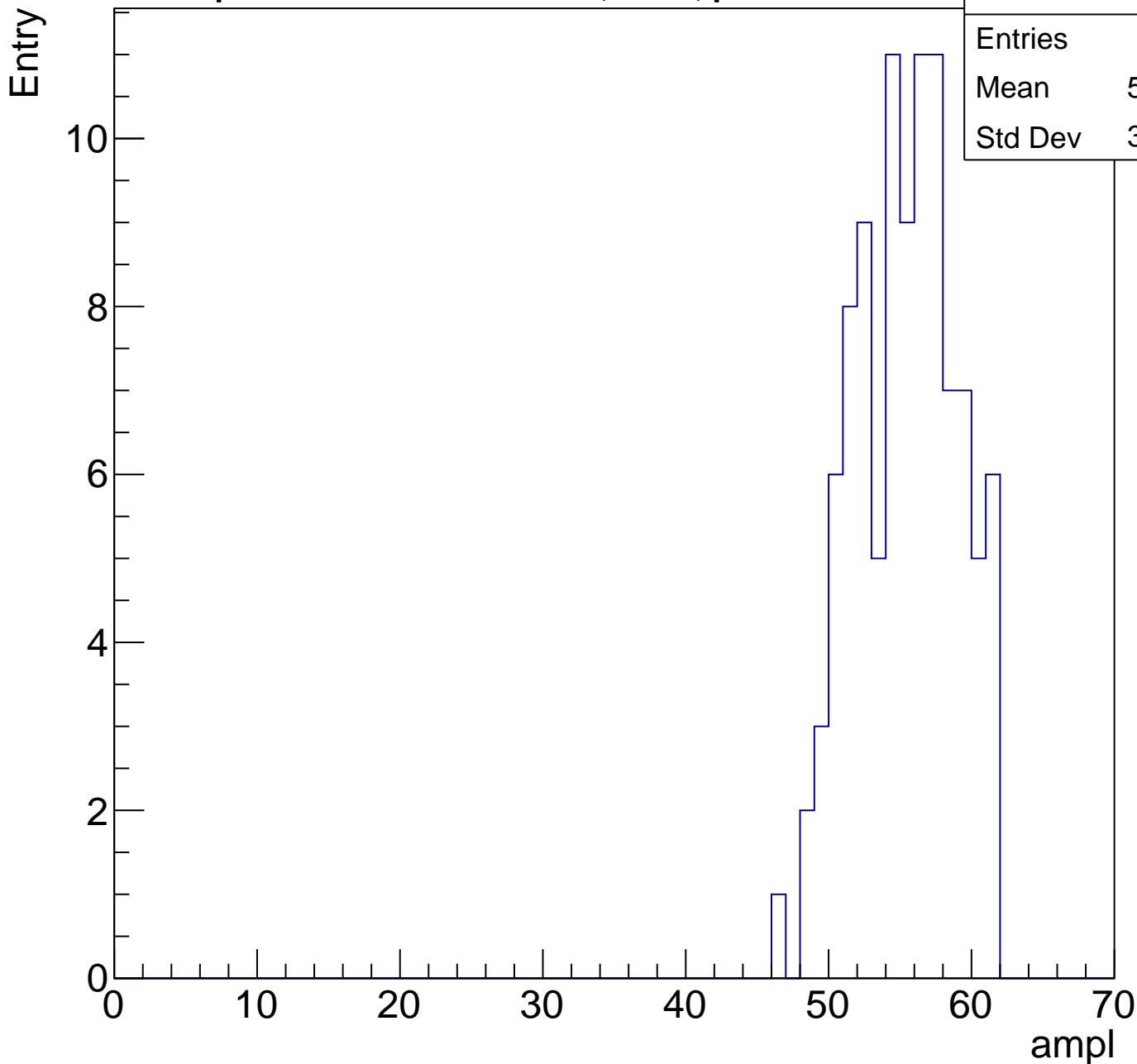
|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 49.05 |
| Std Dev | 3.567 |



# B0L101S, U16-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 54.92 |
| Std Dev | 3.514 |



# B0L101S, U16-ch64, adc5

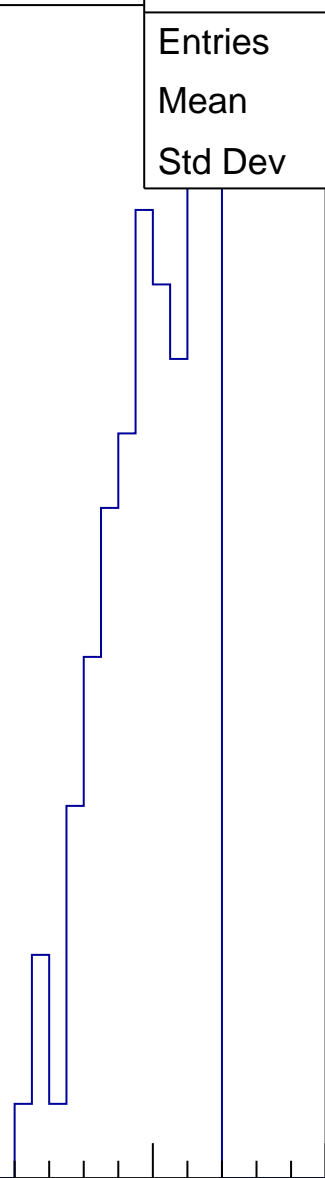
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 59.36 |
| Std Dev | 2.767 |

ampl

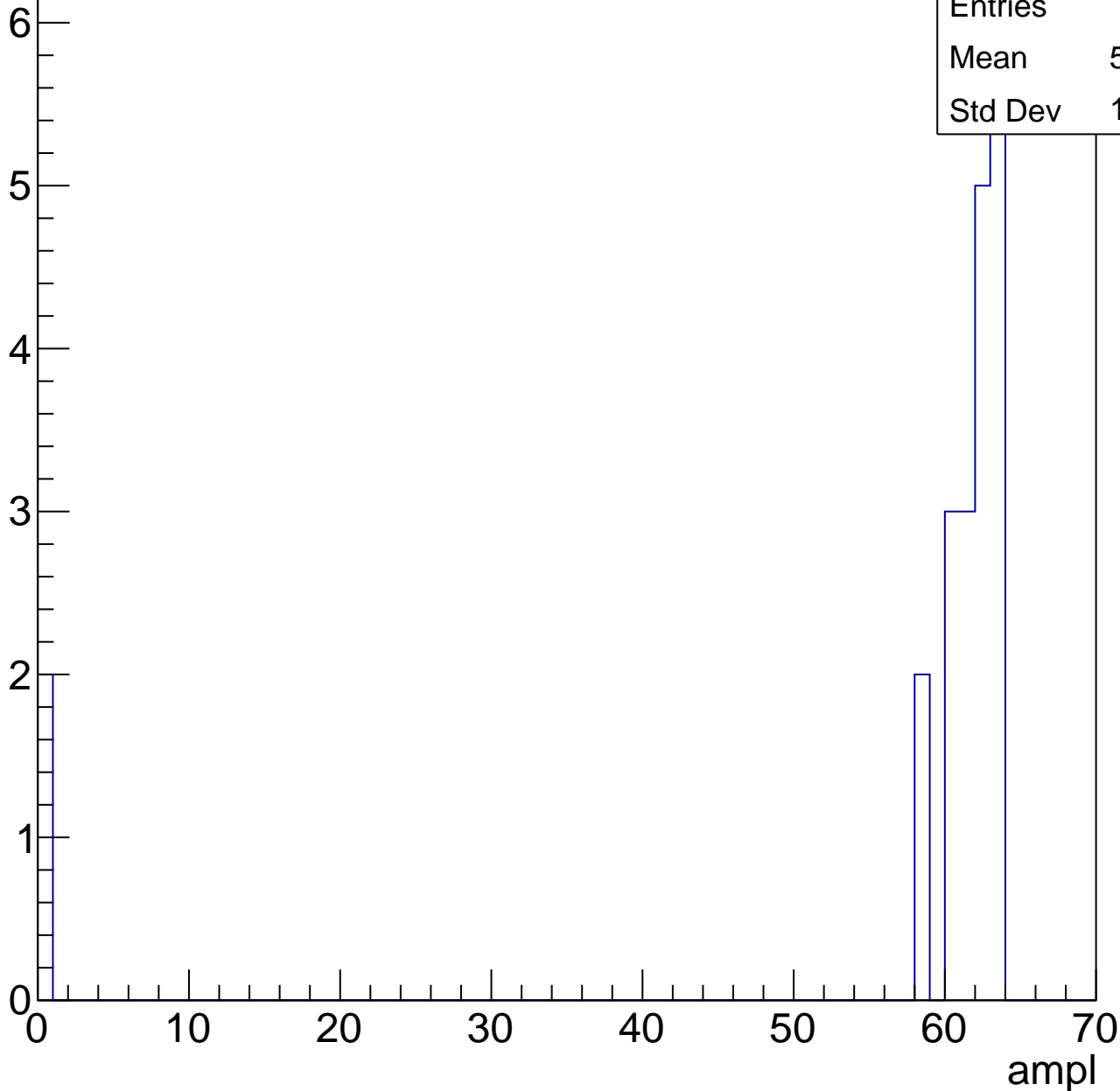


# B0L101S, U16-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 55.57 |
| Std Dev | 18.09 |





# B0L101S, U16-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 3 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

142

Mean

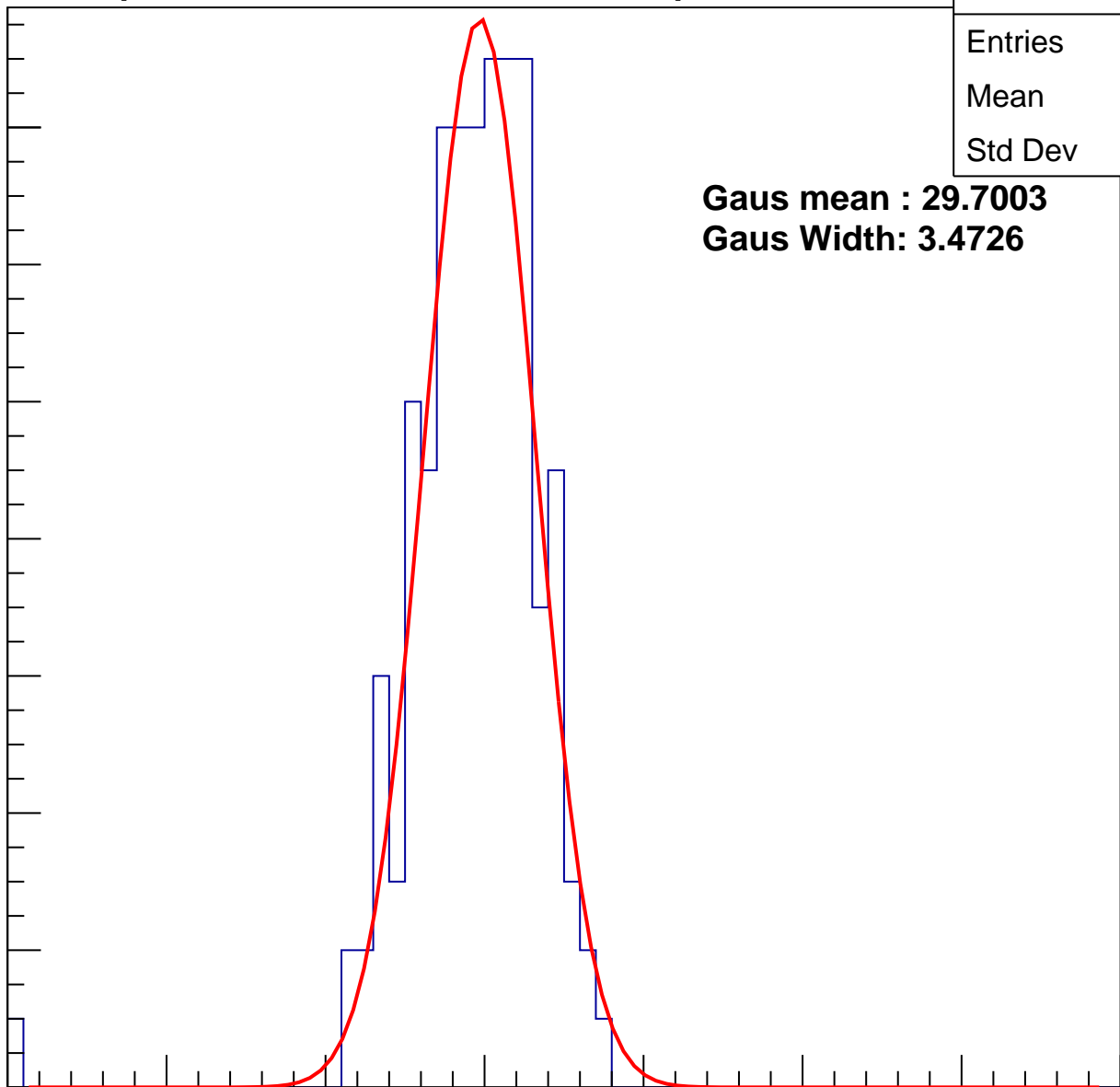
28.89

Std Dev

4.183

**Gaus mean : 29.7003**

**Gaus Width: 3.4726**

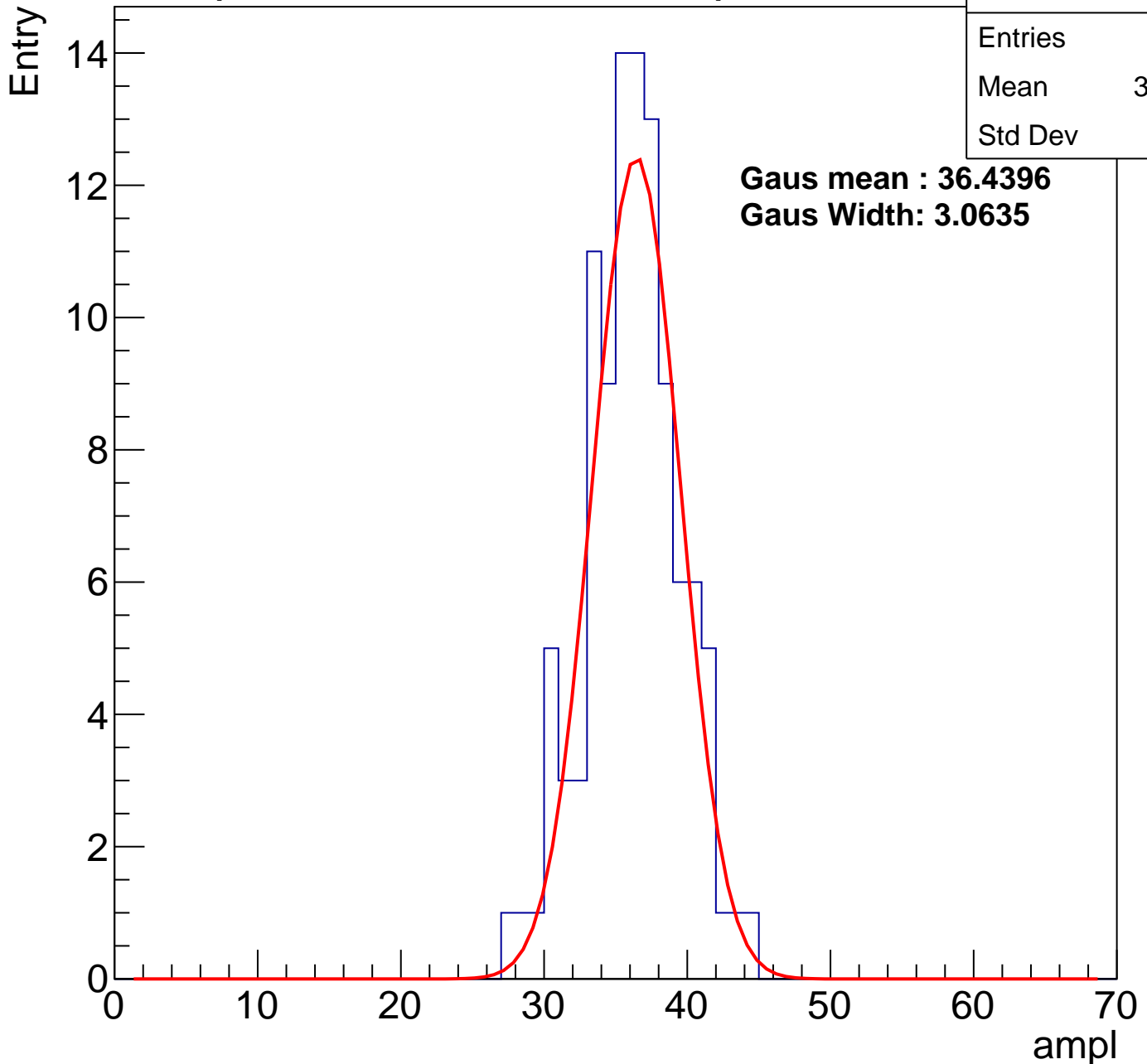


# B0L101S, U16-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 35.74 |
| Std Dev | 3.27  |

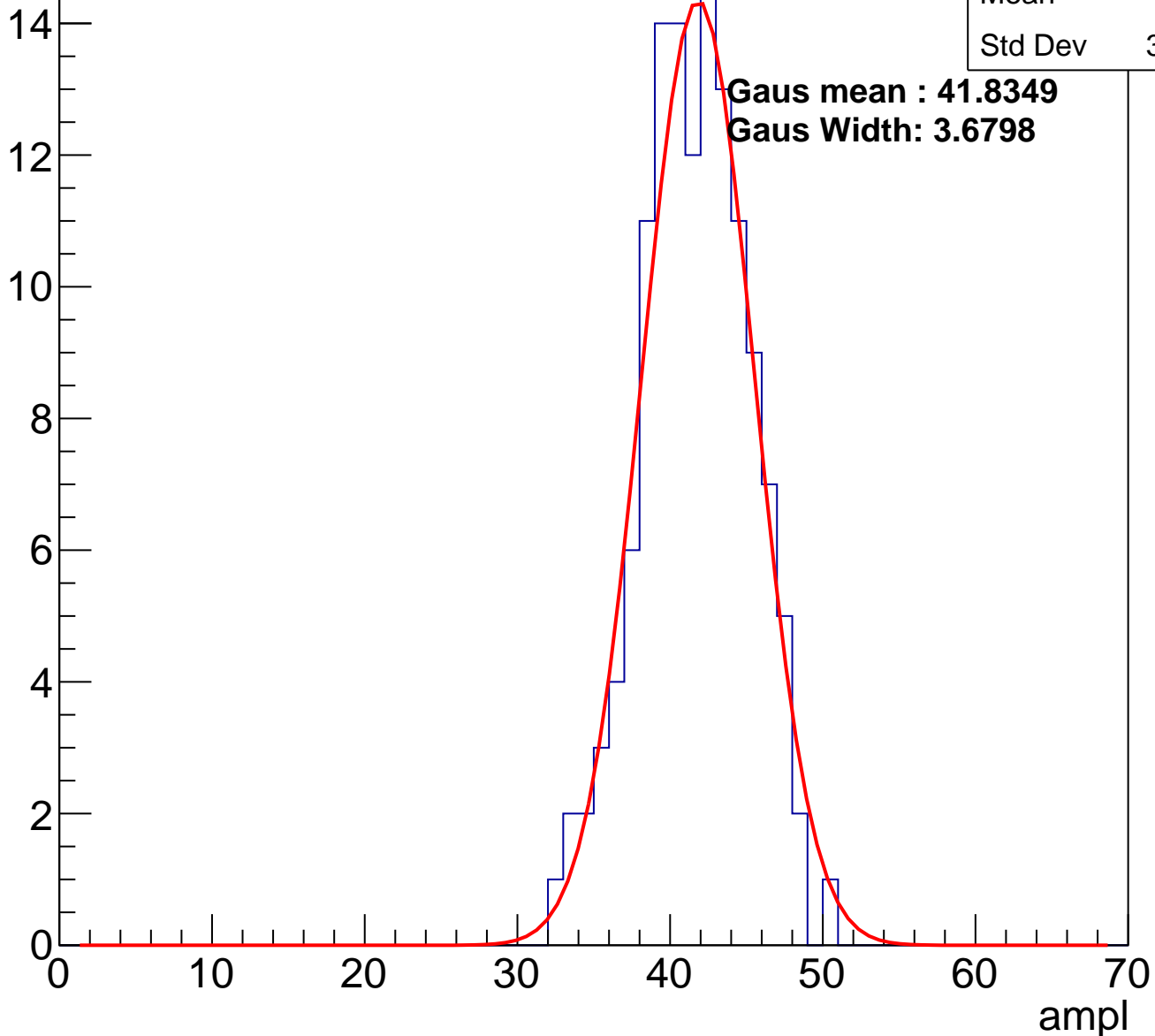
**Gaus mean : 36.4396**  
**Gaus Width: 3.0635**



# B0L101S, U16-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

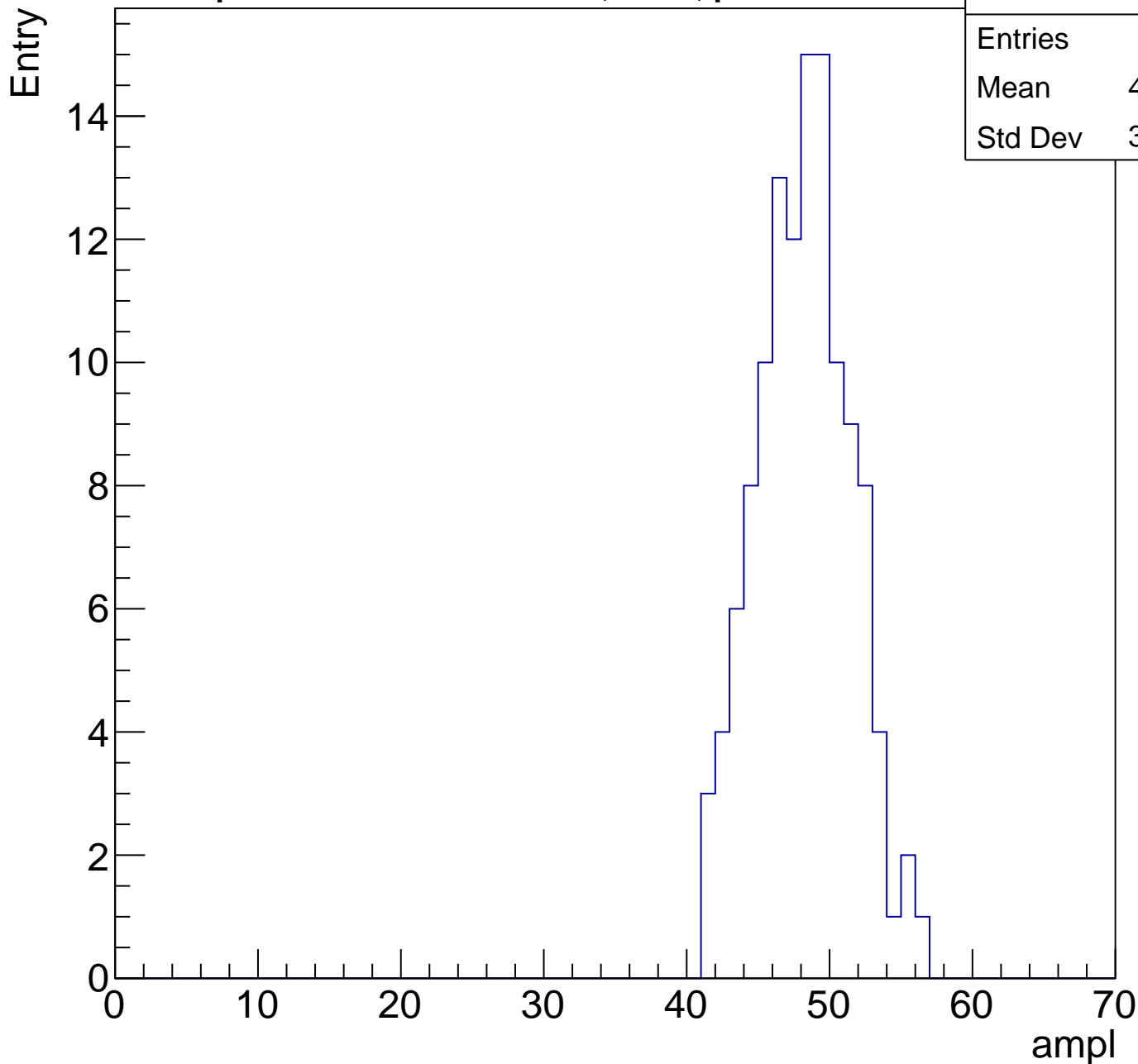


|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 41.17 |
| Std Dev | 3.504 |

# B0L101S, U16-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

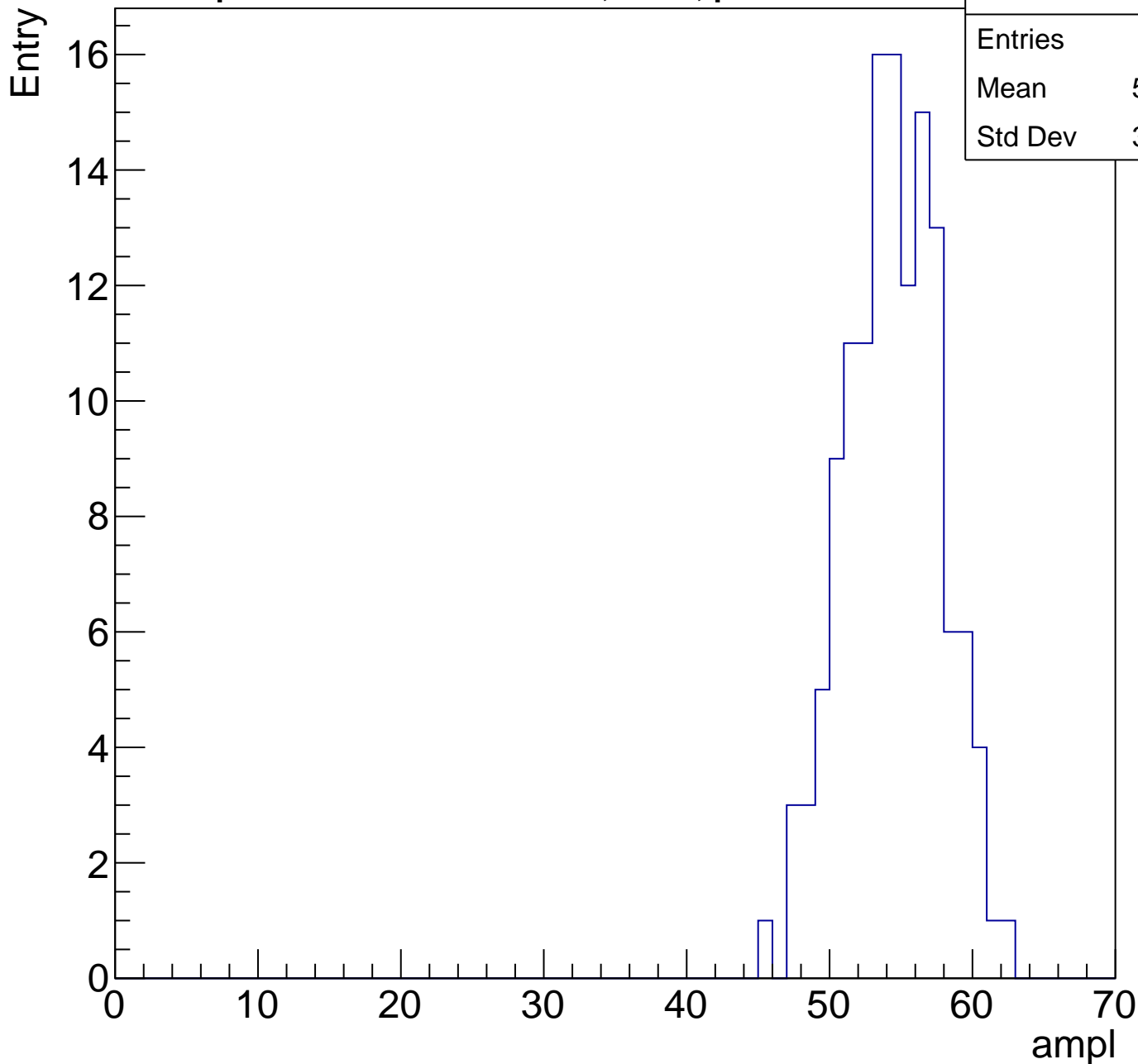
|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 47.73 |
| Std Dev | 3.247 |



# B0L101S, U16-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 53.95 |
| Std Dev | 3.308 |



# B0L101S, U16-ch65, adc5

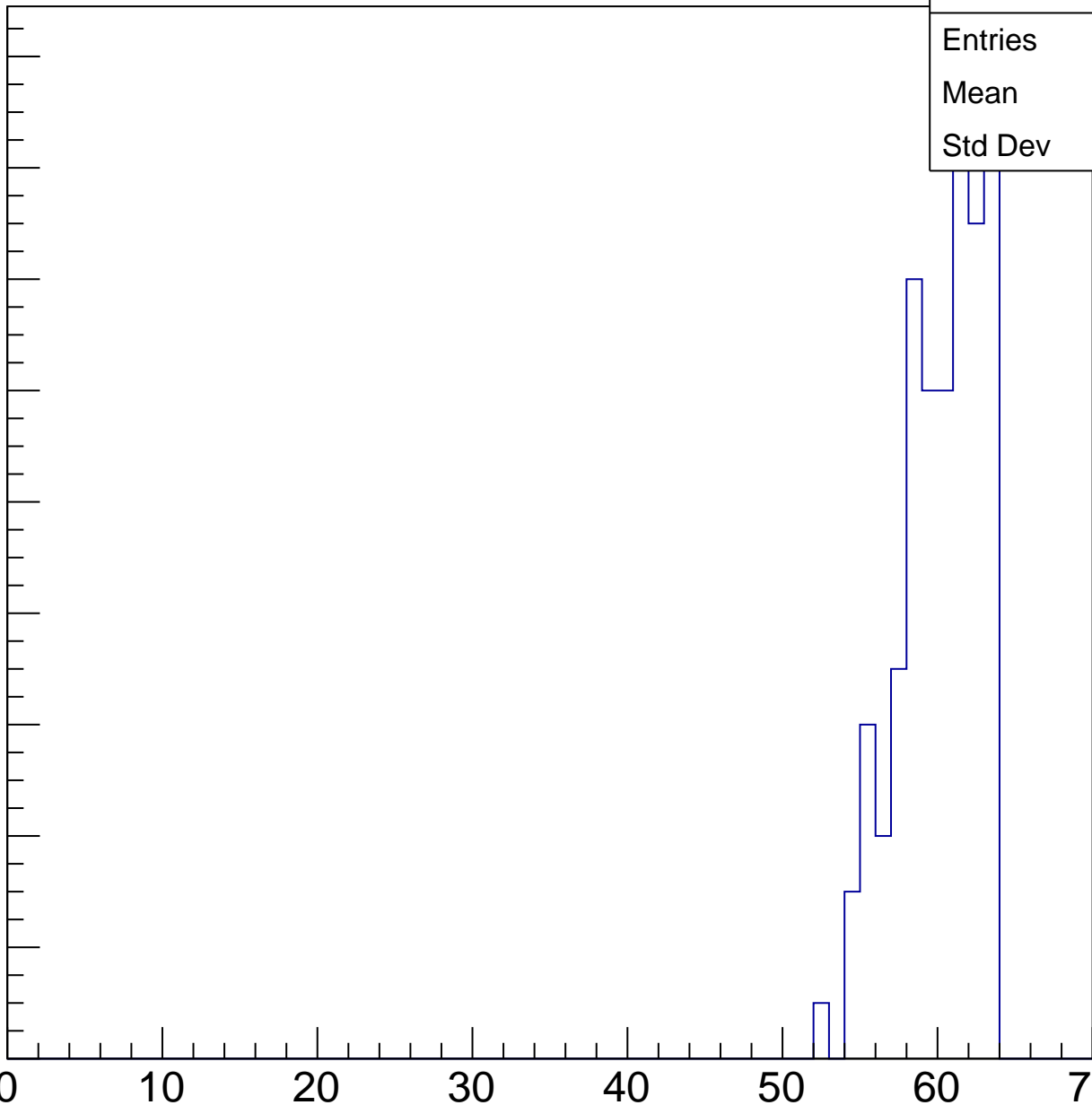
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 59.71 |
| Std Dev | 2.617 |

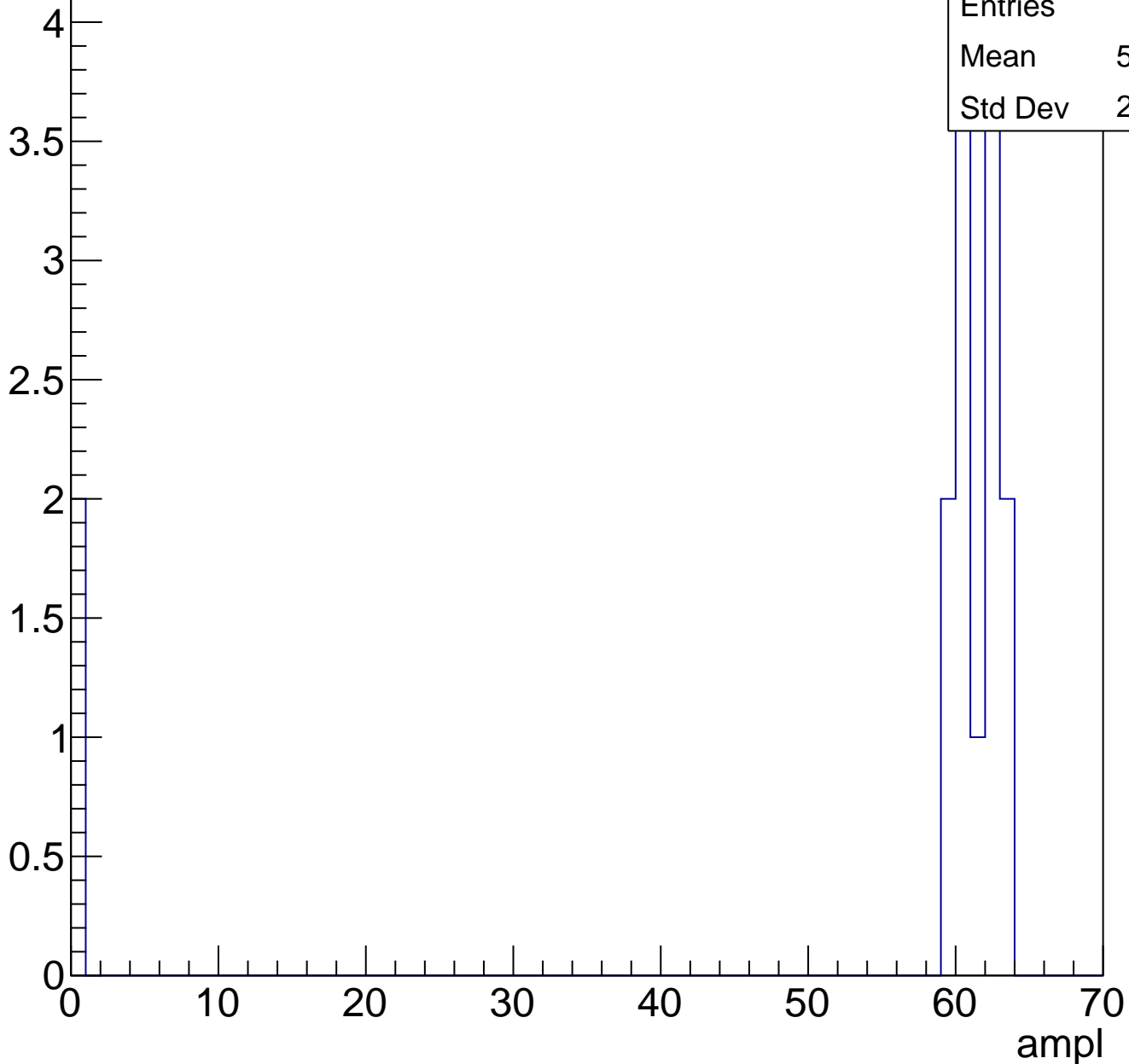
ampl



# B0L101S, U16-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch66, adc0

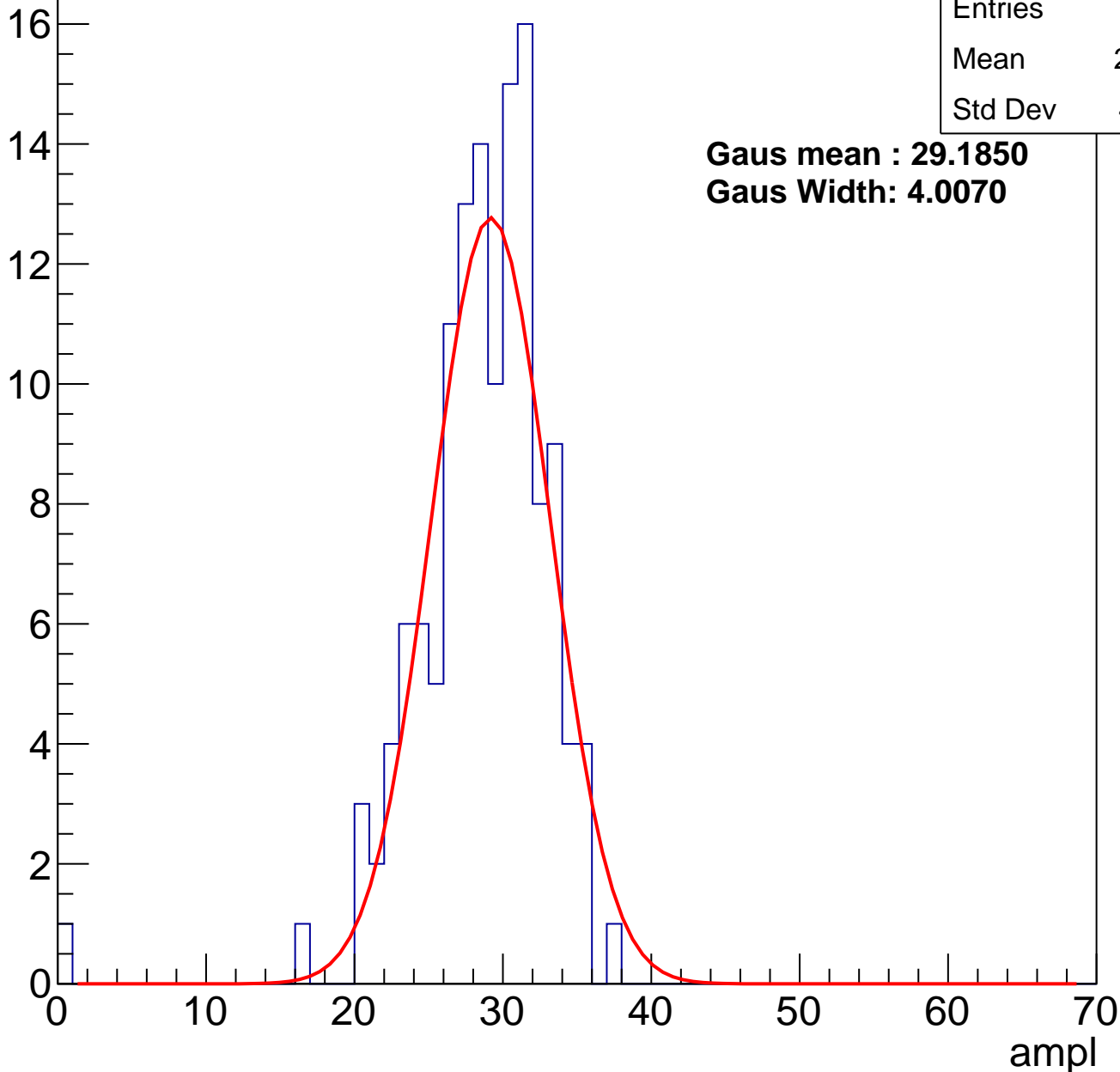
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 28.15 |
| Std Dev | 4.501 |

**Gaus mean : 29.1850**

**Gaus Width: 4.0070**

Entry



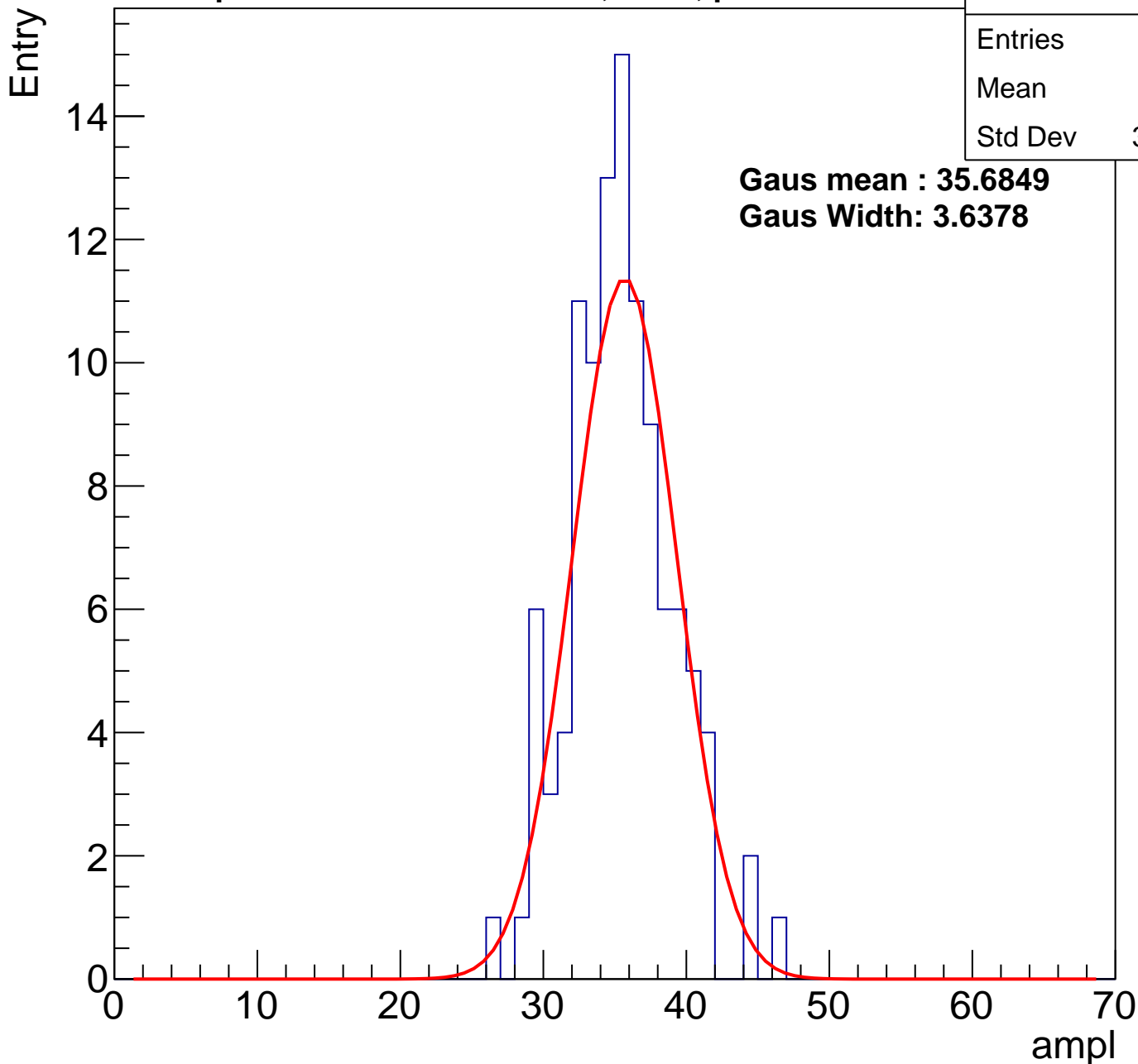
# B0L101S, U16-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 35    |
| Std Dev | 3.582 |

**Gaus mean : 35.6849**

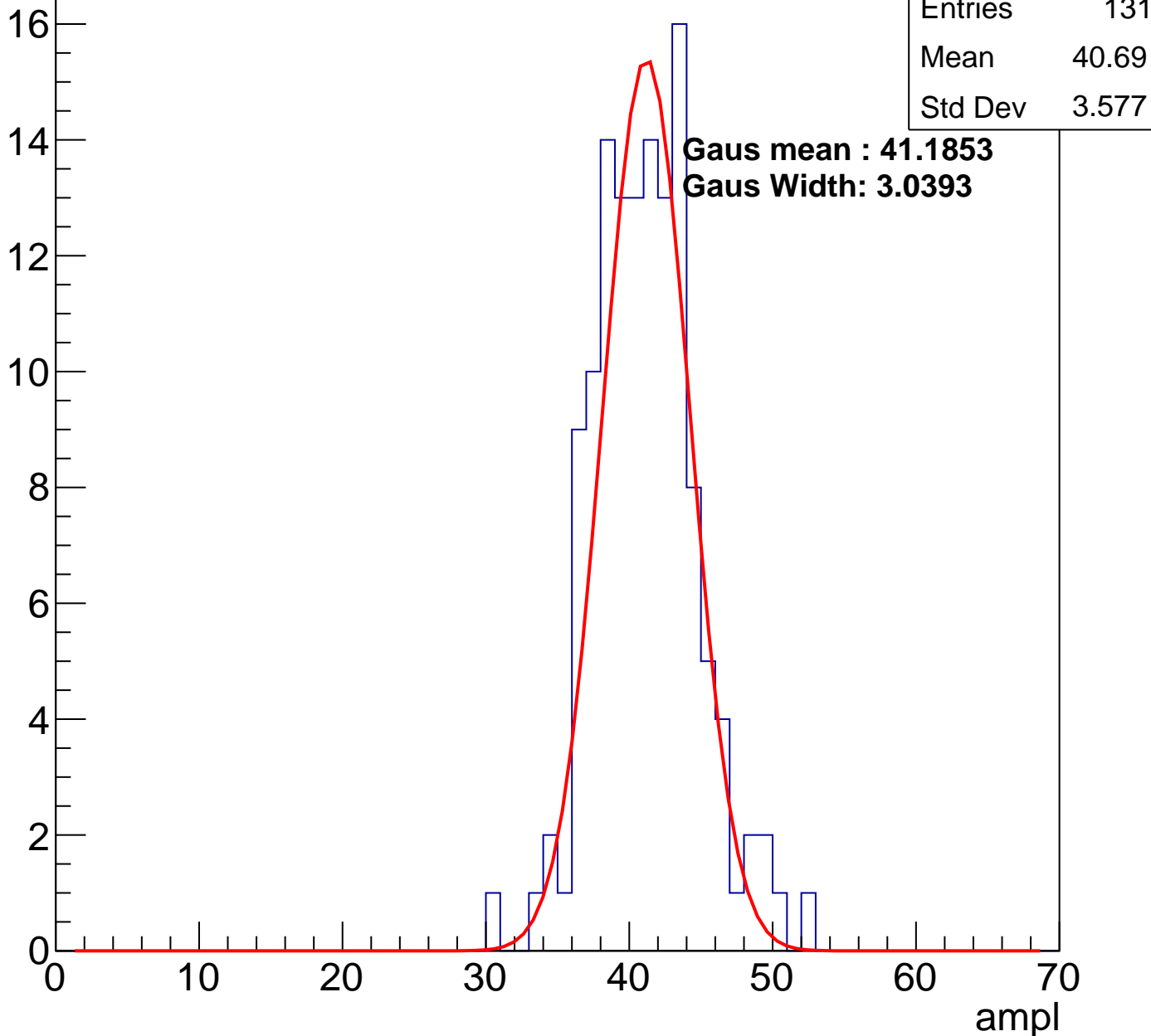
**Gaus Width: 3.6378**



# B0L101S, U16-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

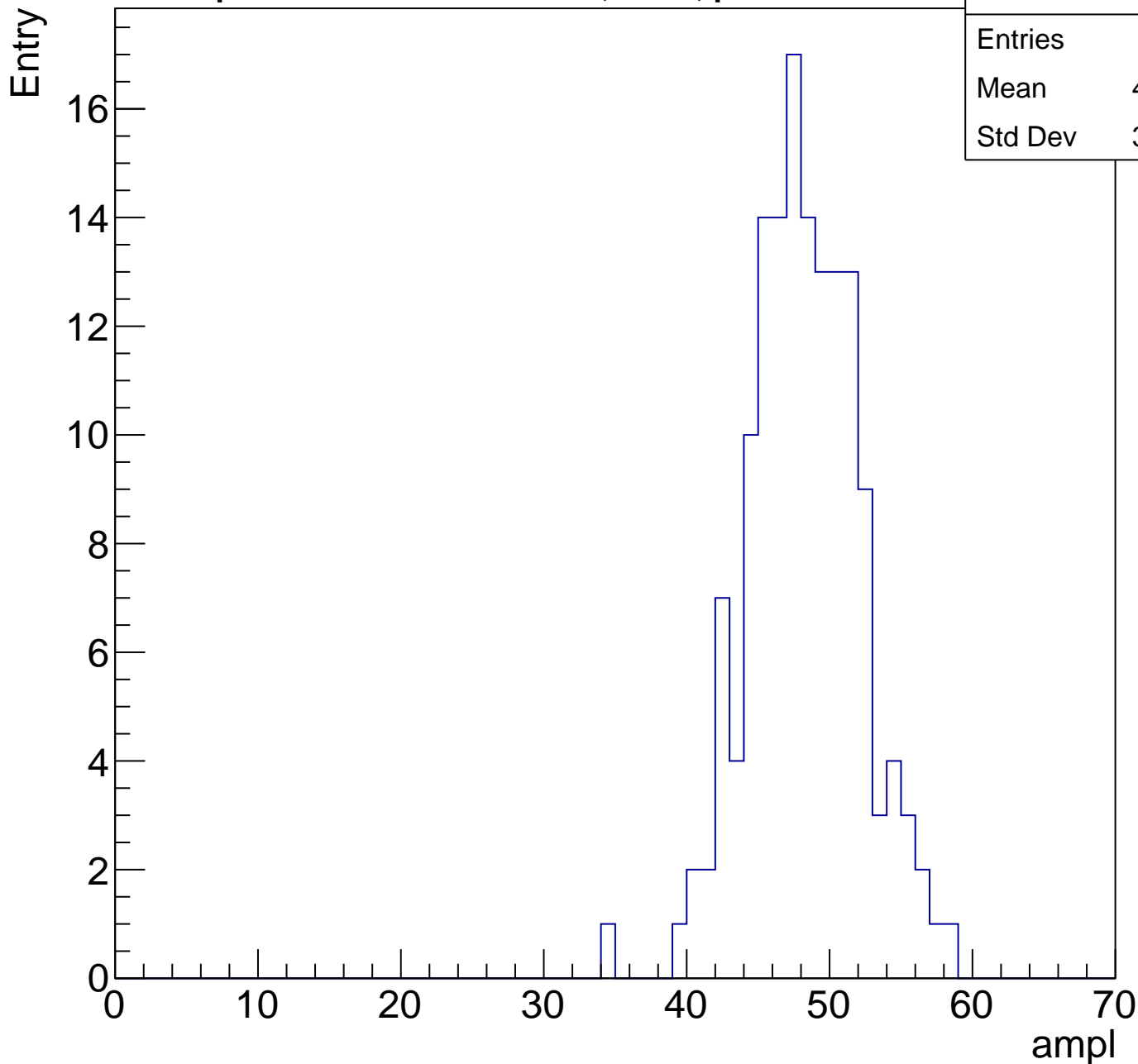
Entry



# B0L101S, U16-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

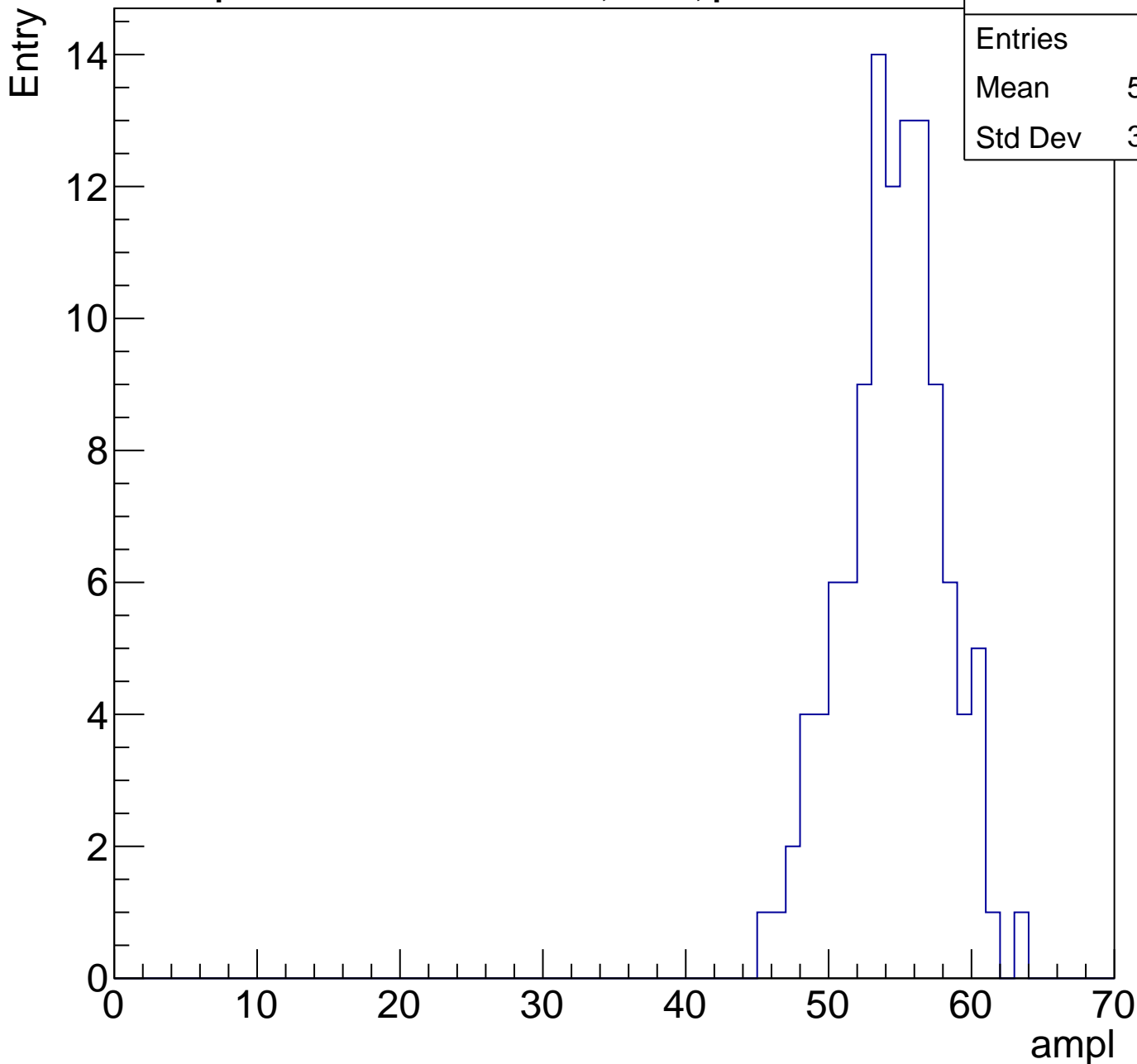
|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 47.78 |
| Std Dev | 3.862 |



# B0L101S, U16-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 54.06 |
| Std Dev | 3.473 |



# B0L101S, U16-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

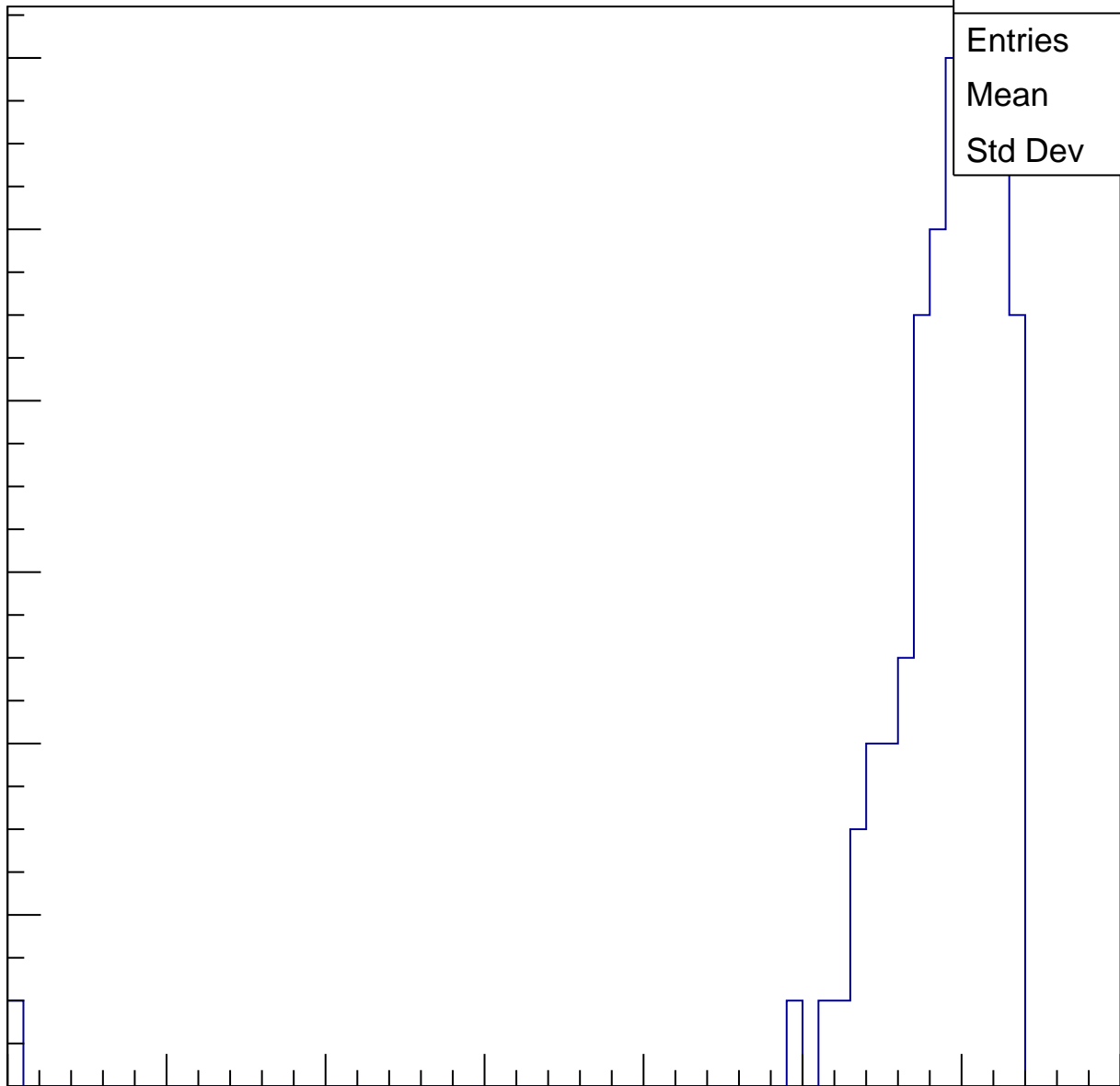
Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 58.22 |
| Std Dev | 6.736 |

ampl

0 10 20 30 40 50 60 70

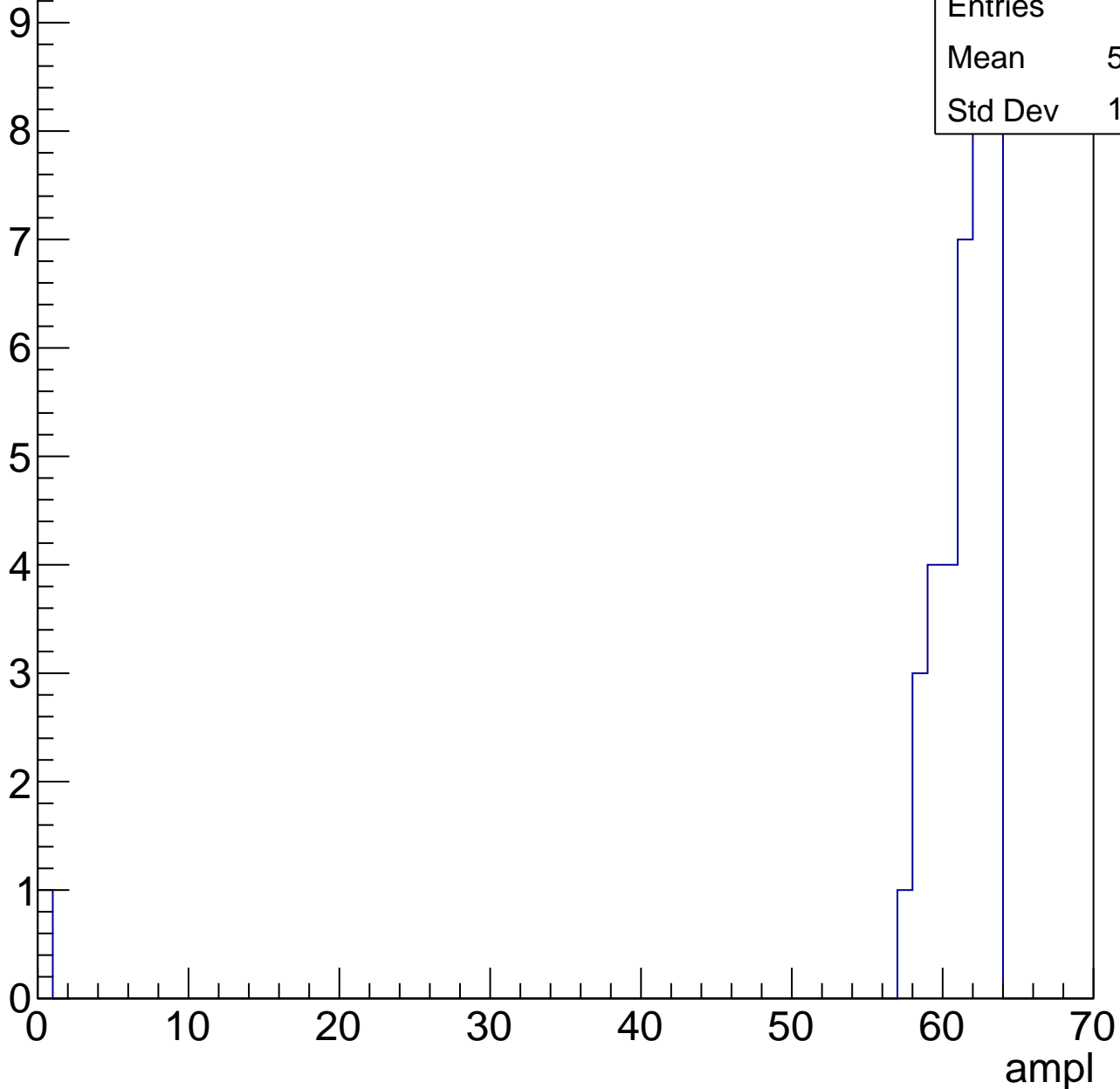


# B0L101S, U16-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 59.38 |
| Std Dev | 10.04 |





# B0L101S, U16-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch67, adc0

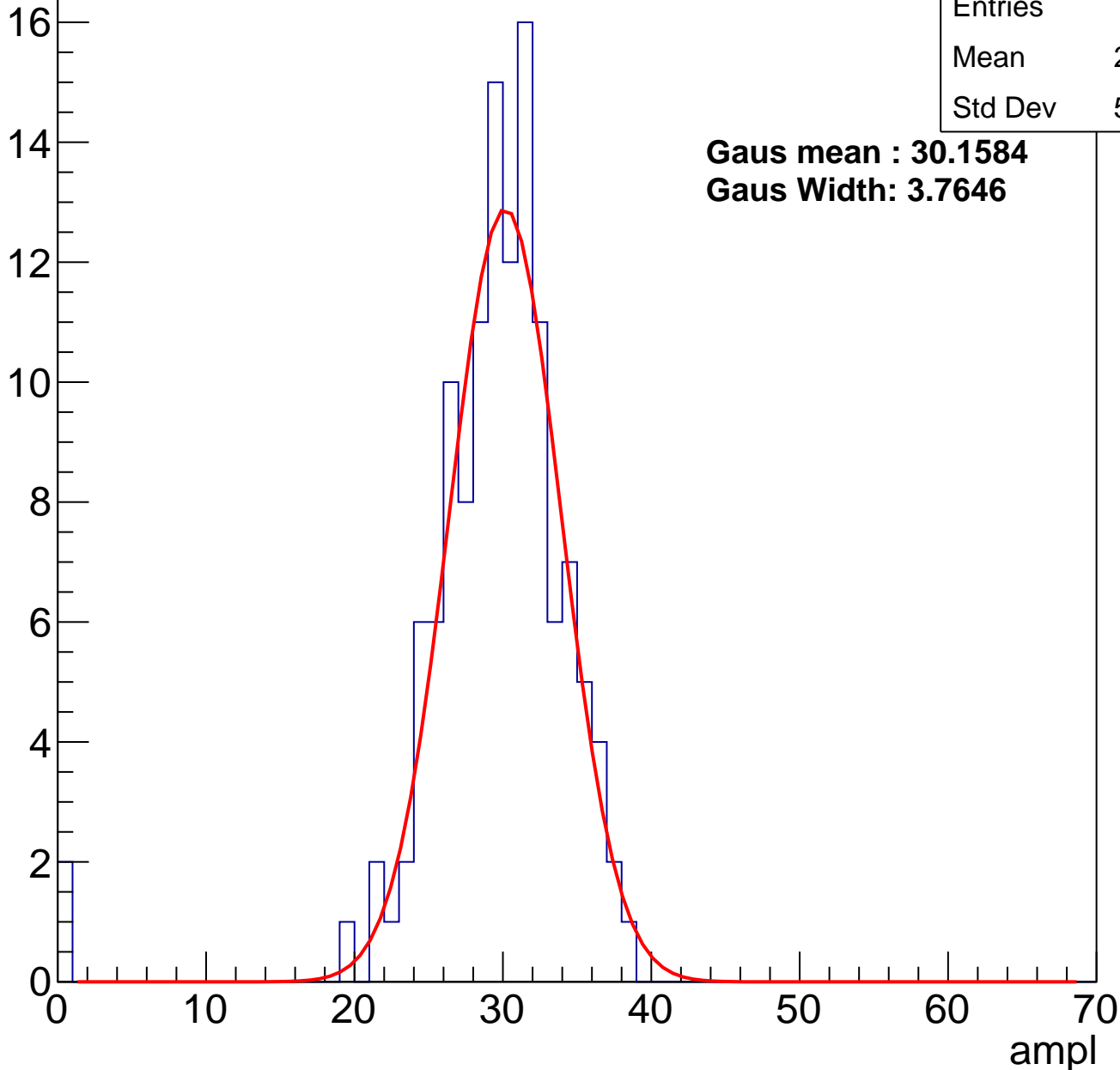
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 29.04 |
| Std Dev | 5.185 |

**Gaus mean : 30.1584**

**Gaus Width: 3.7646**

Entry



# B0L101S, U16-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 36.16 |
| Std Dev | 3.78  |

**Gaus mean : 36.8470**

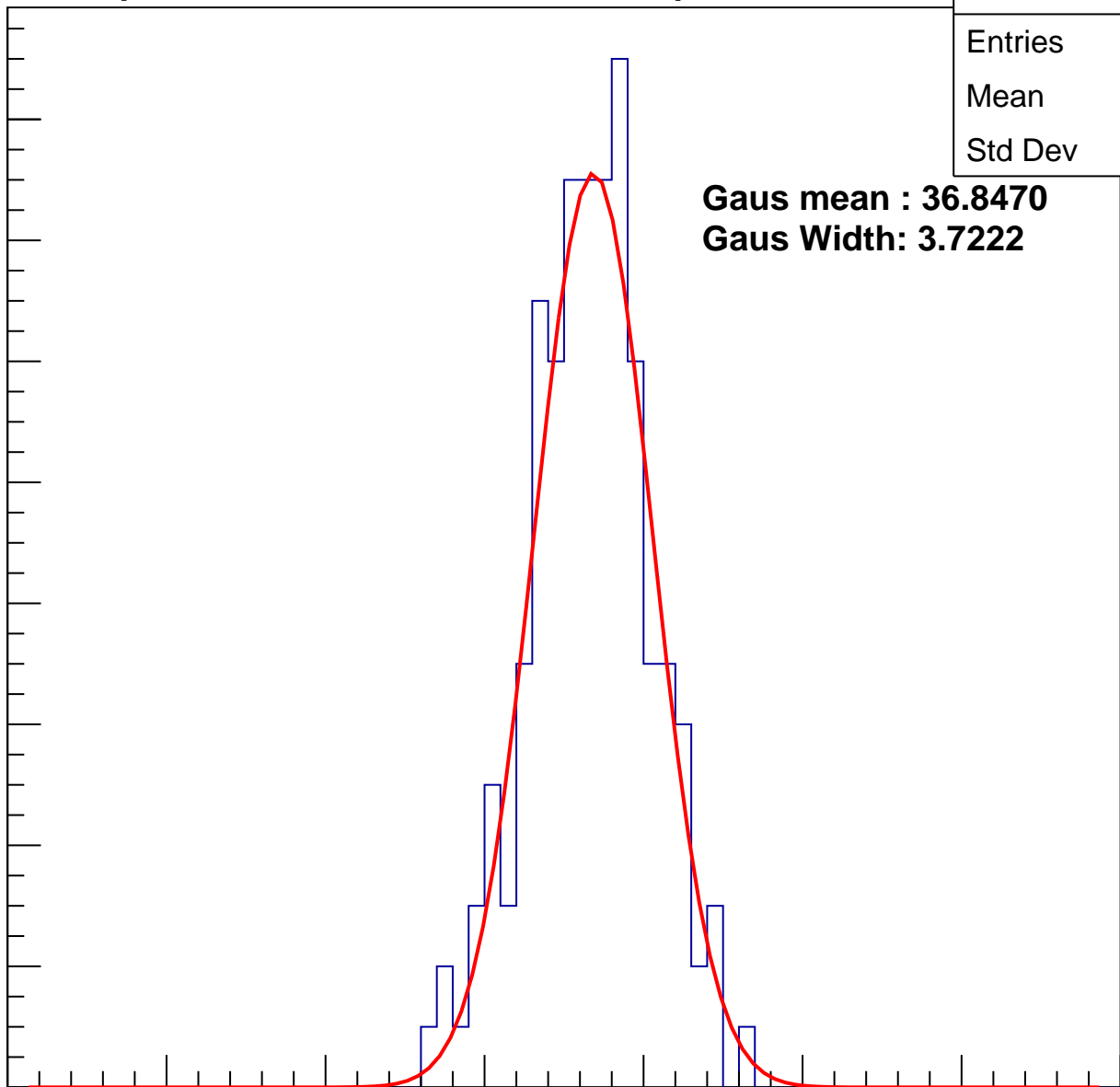
**Gaus Width: 3.7222**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch67, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

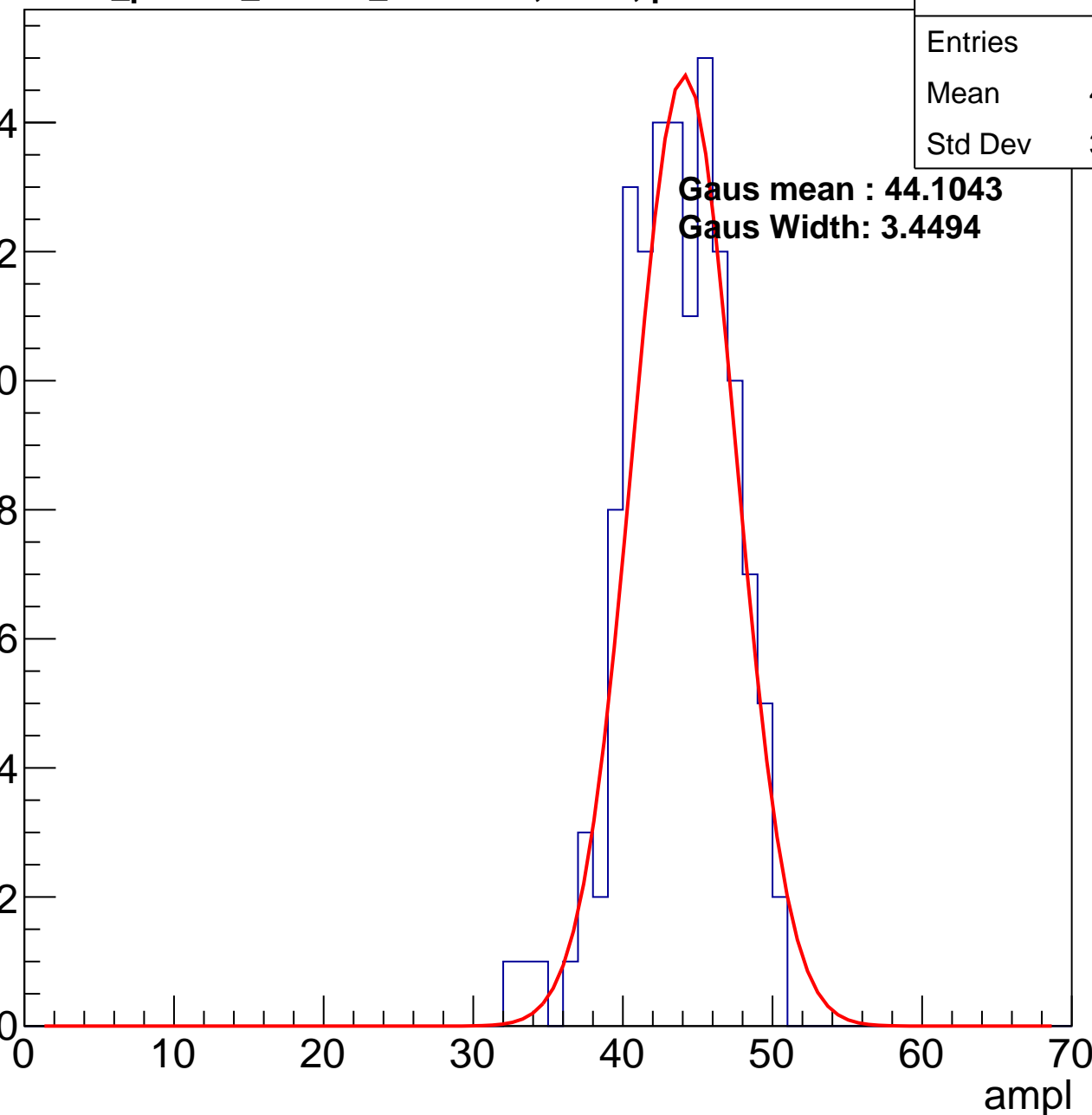
Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 43.17 |
| Std Dev | 3.486 |

**Gaus mean : 44.1043**

**Gaus Width: 3.4494**

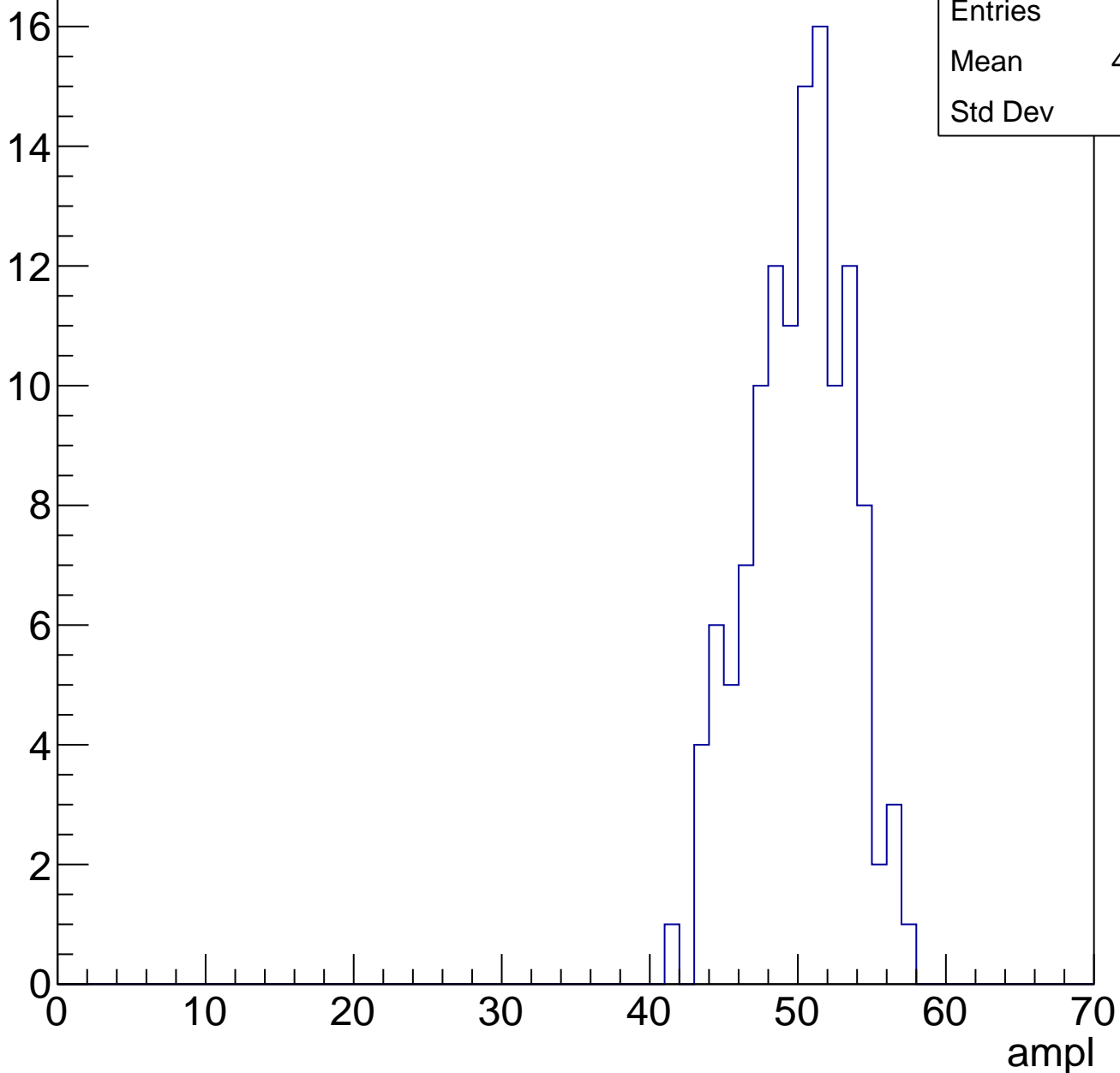


# B0L101S, U16-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 49.58 |
| Std Dev | 3.32  |

Entry



# B0L101S, U16-ch67, adc4

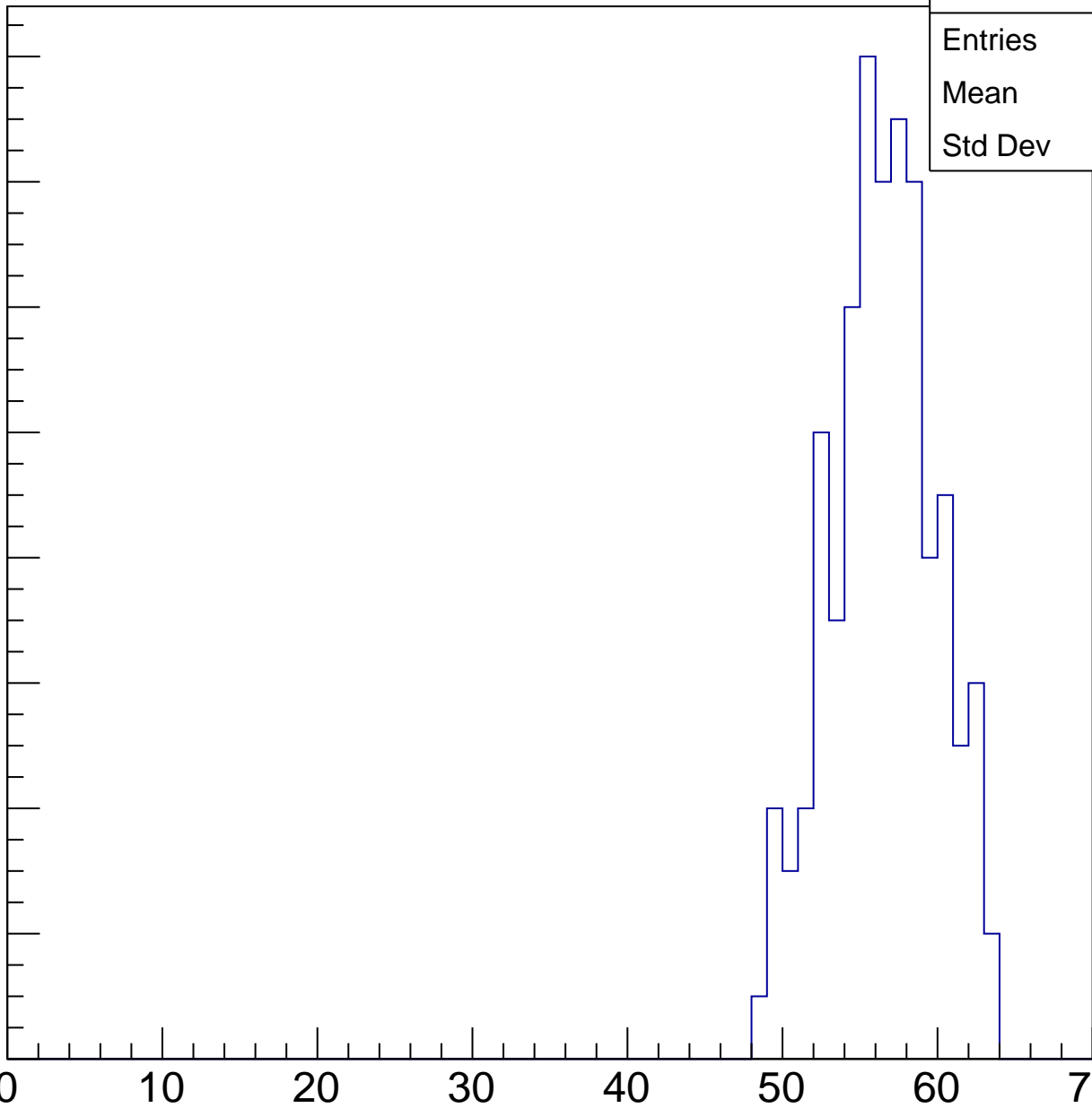
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 56.02 |
| Std Dev | 3.375 |

ampl



# B0L101S, U16-ch67, adc5

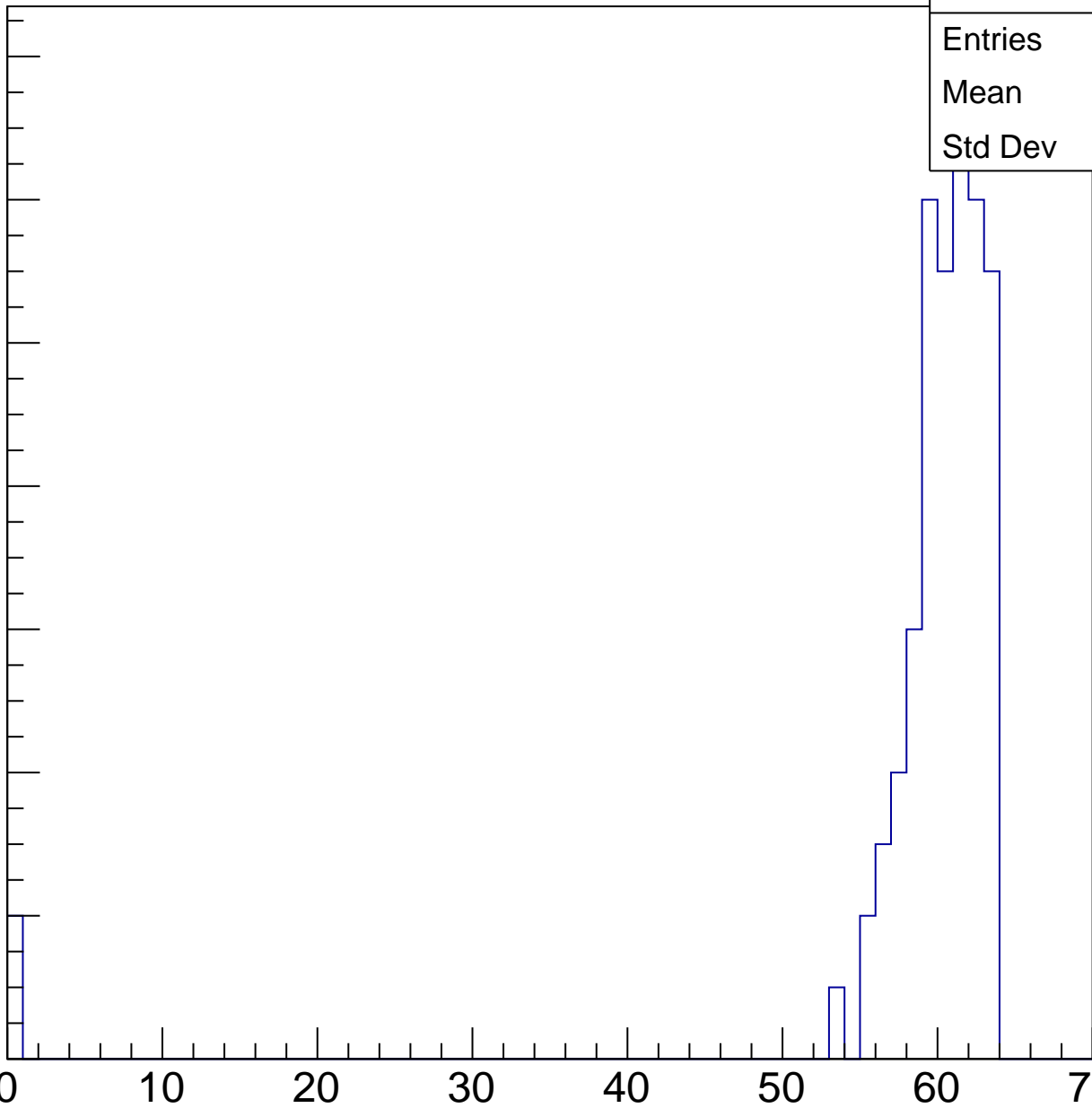
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 58.54 |
| Std Dev | 9.749 |

ampl

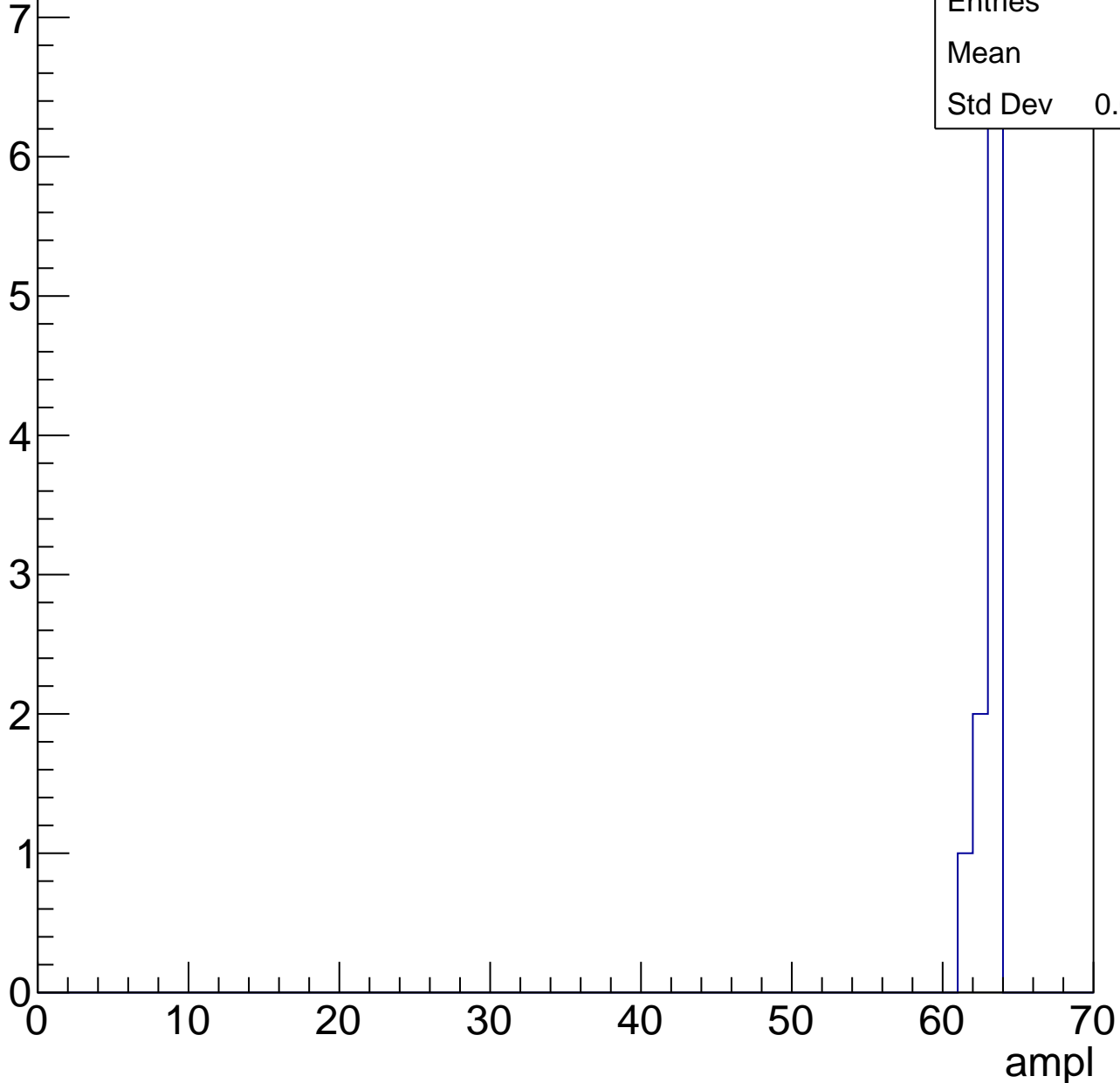


# B0L101S, U16-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 10     |
| Mean    | 62.6   |
| Std Dev | 0.6633 |

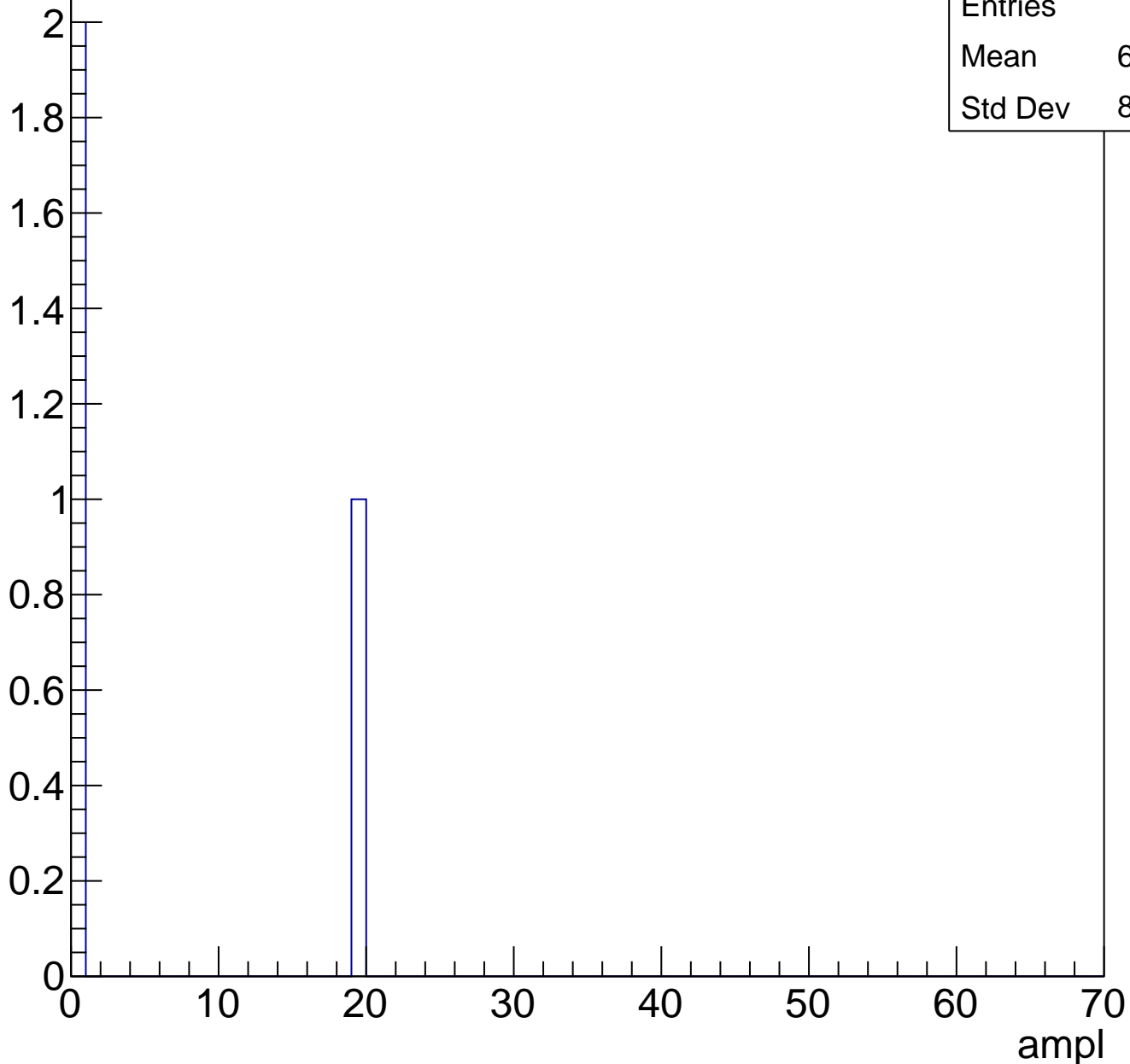




# B0L101S, U16-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 6.333 |
| Std Dev | 8.957 |

# B0L101S, U16-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 159   |
| Mean    | 27.89 |
| Std Dev | 5.533 |

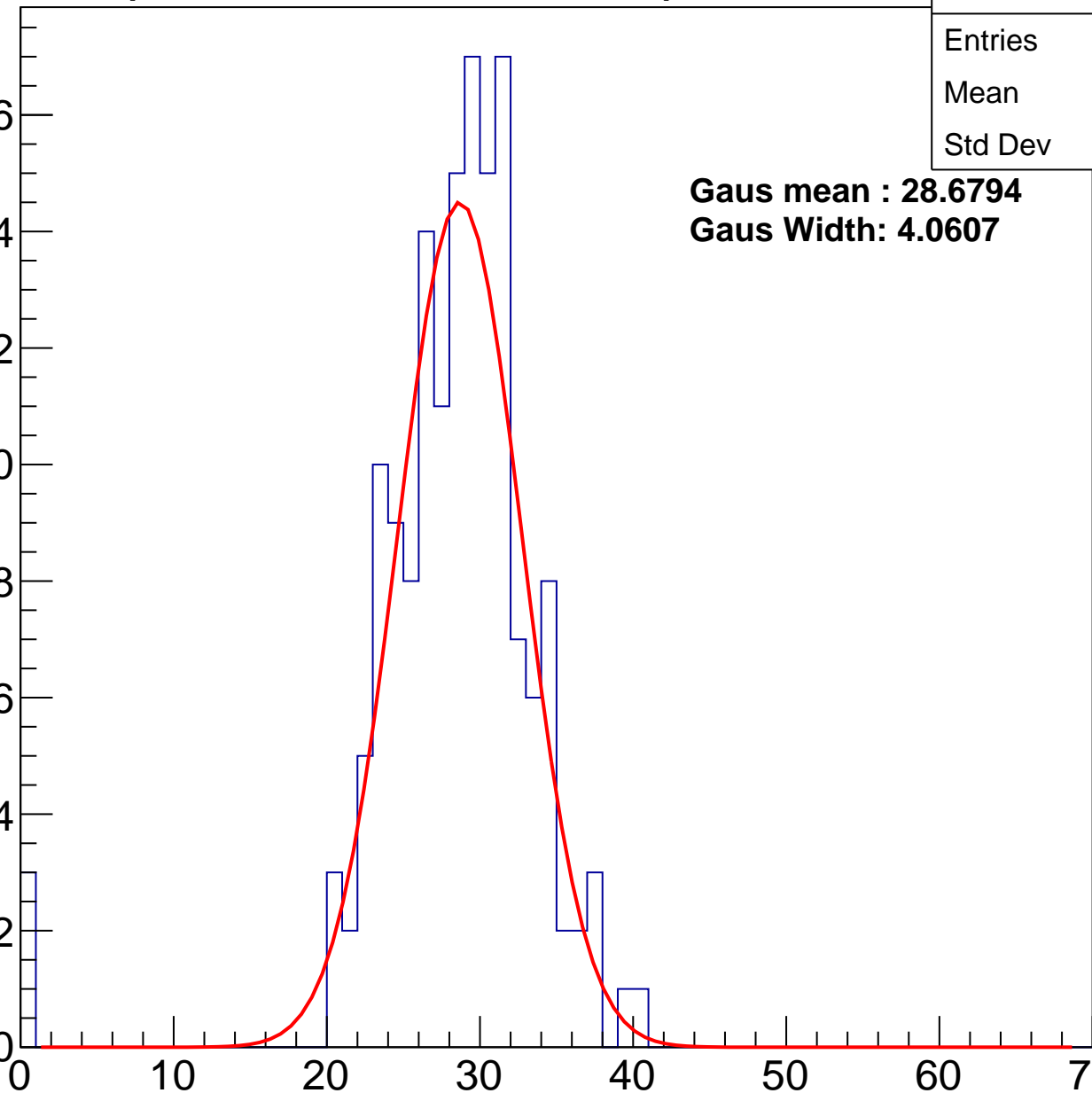
**Gaus mean : 28.6794**

**Gaus Width: 4.0607**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



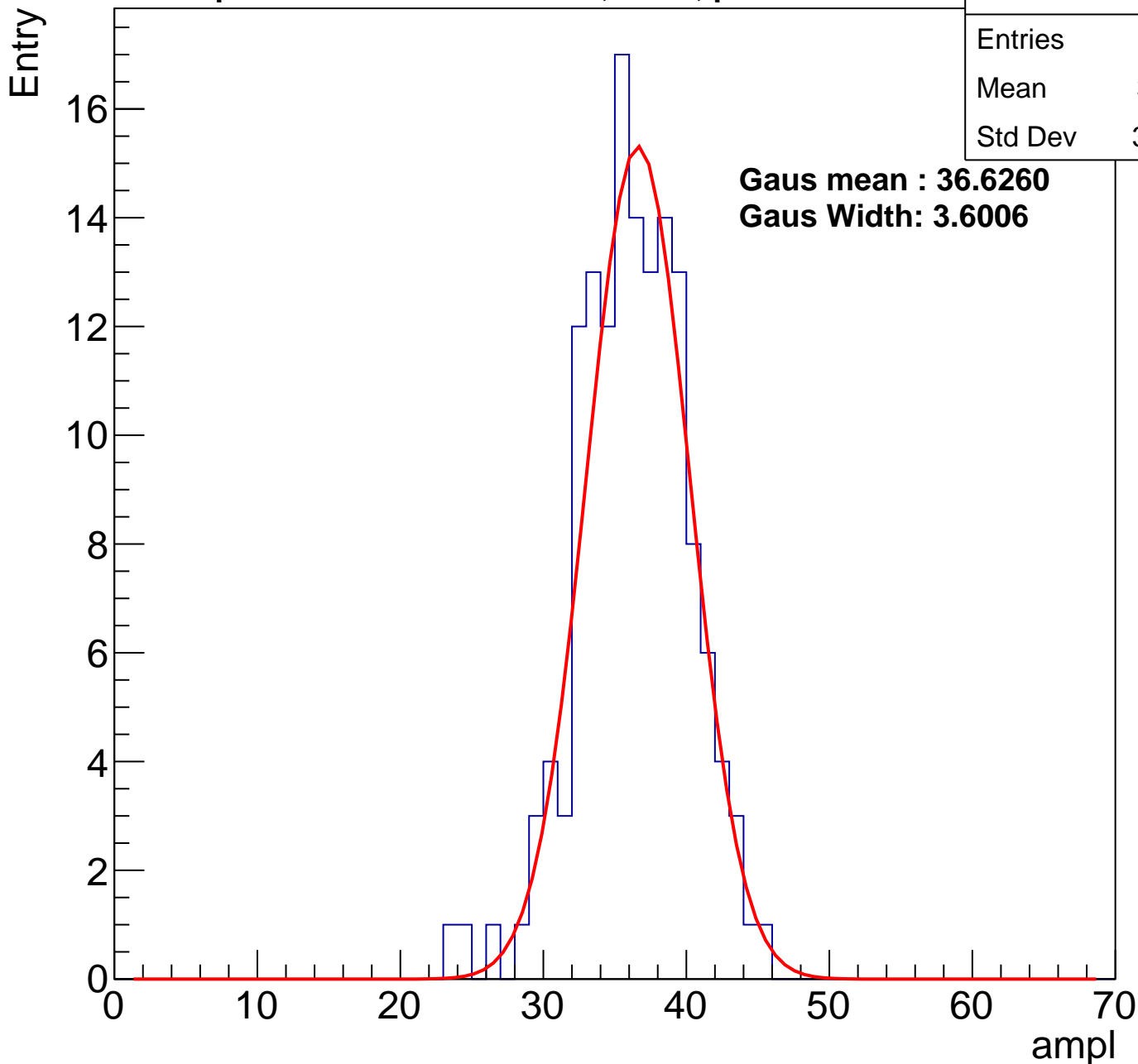
# B0L101S, U16-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 145   |
| Mean    | 35.81 |
| Std Dev | 3.816 |

**Gaus mean : 36.6260**

**Gaus Width: 3.6006**



# B0L101S, U16-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

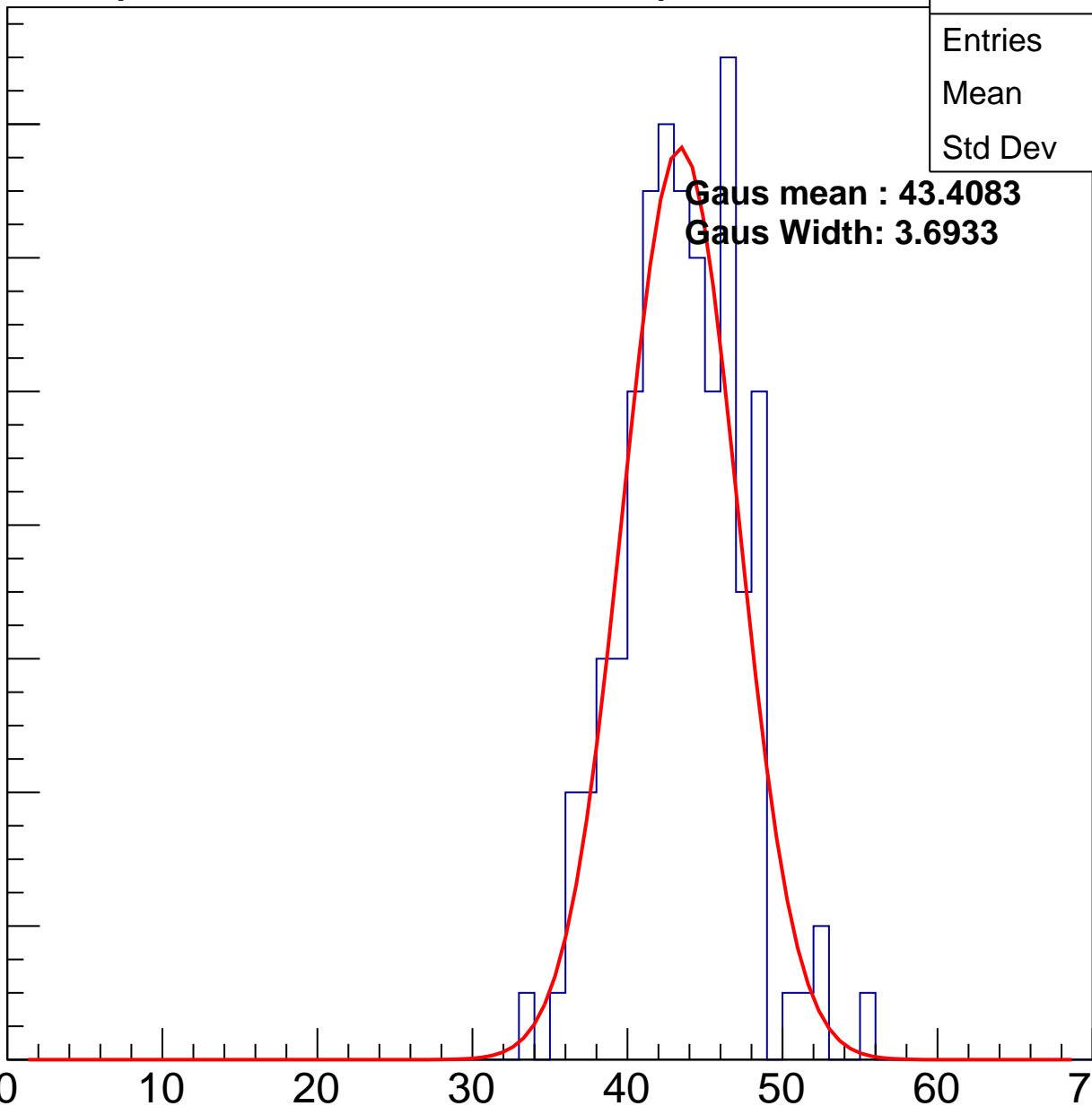
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 43.05 |
| Std Dev | 3.782 |

**Gaus mean : 43.4083**

**Gaus Width: 3.6933**

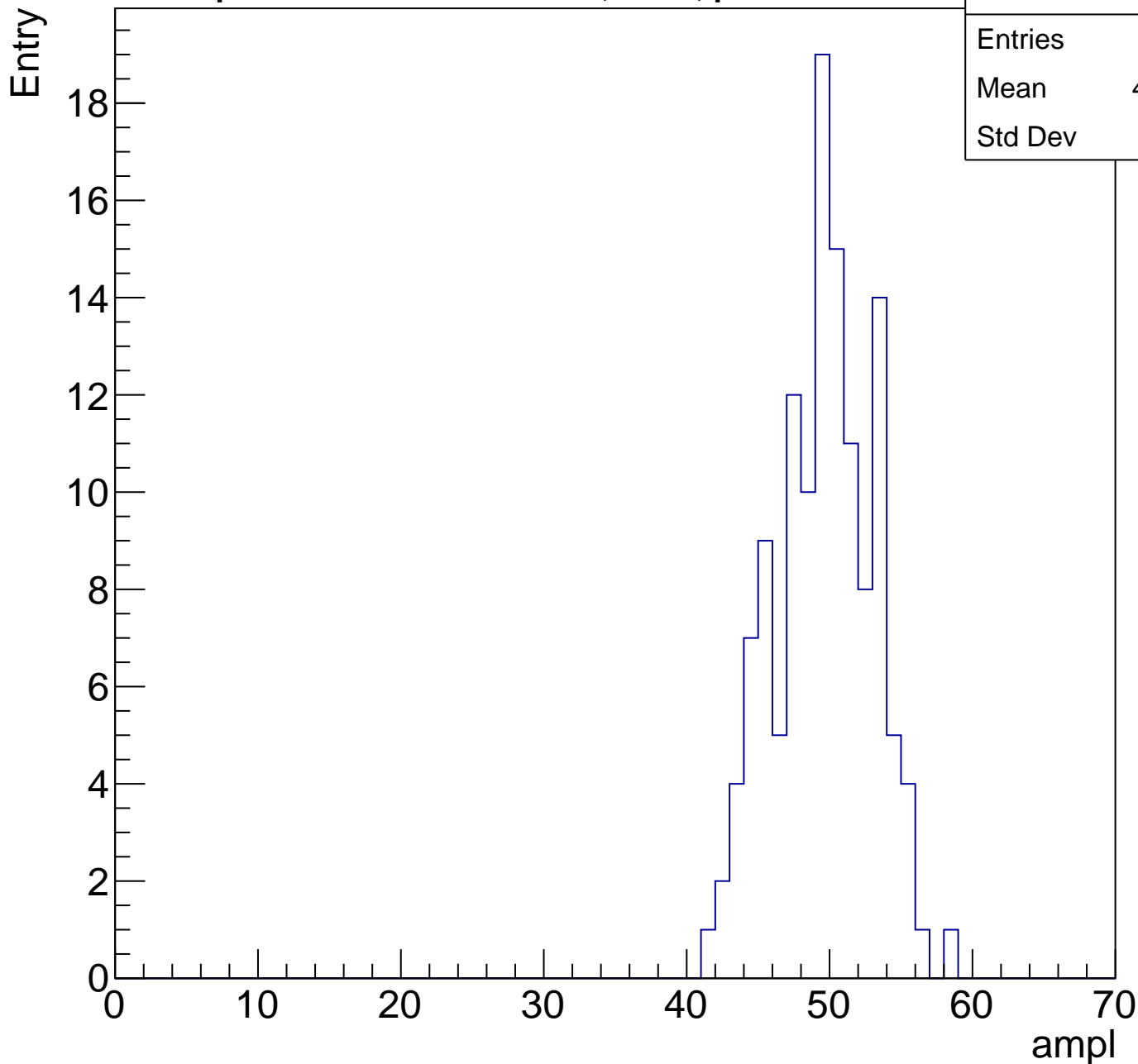
ampl



# B0L101S, U16-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

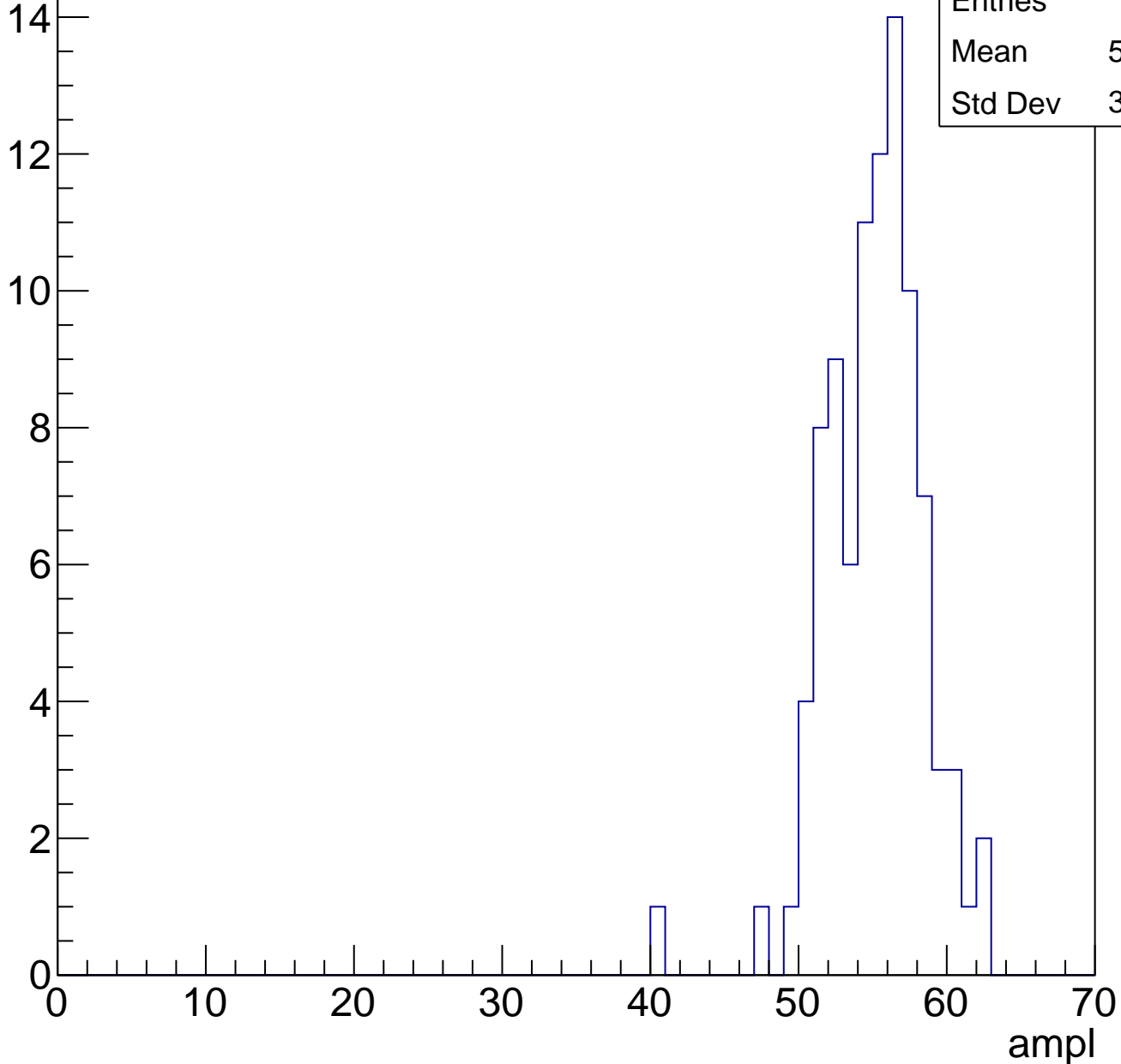
|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 49.12 |
| Std Dev | 3.4   |



# B0L101S, U16-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

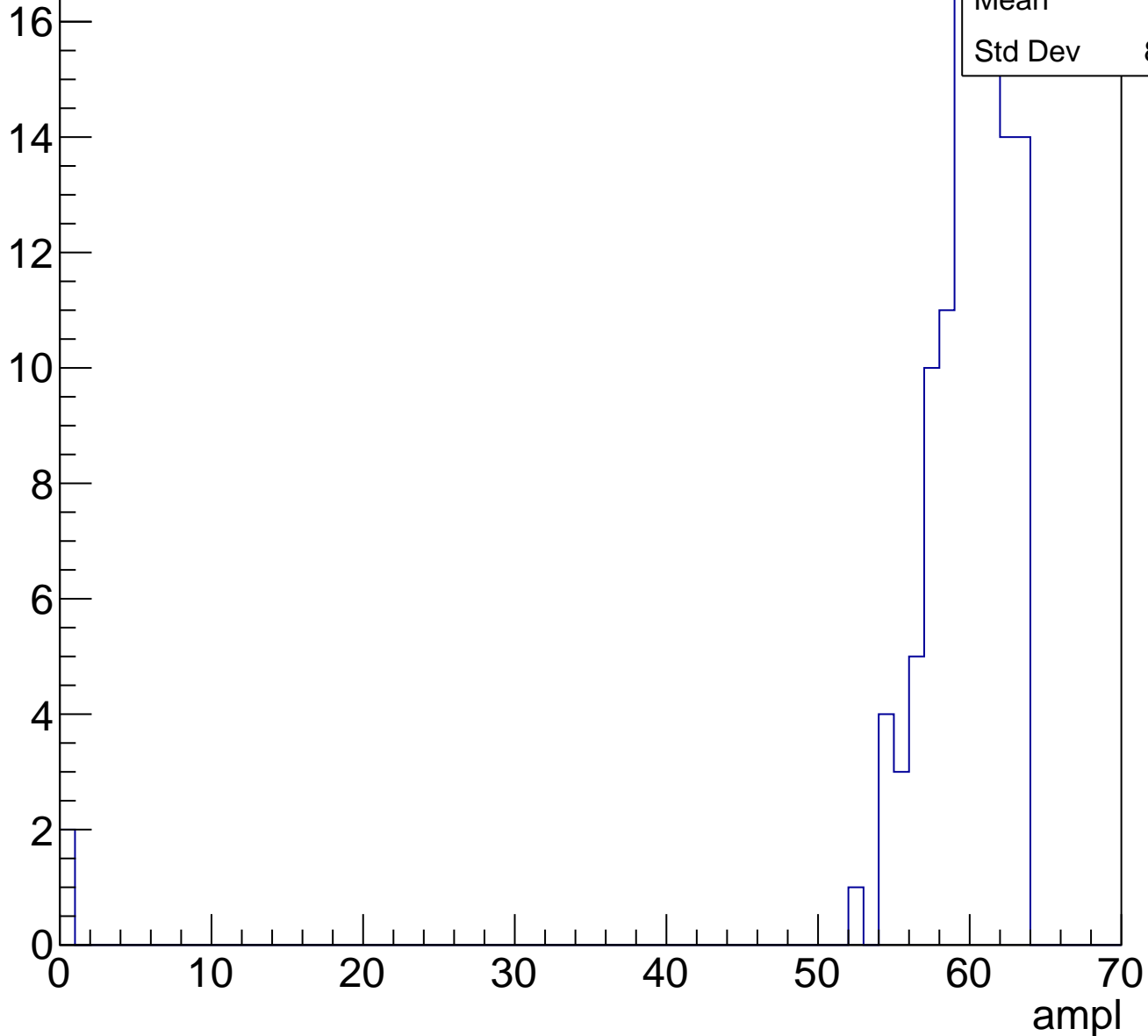
Entry



# B0L101S, U16-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

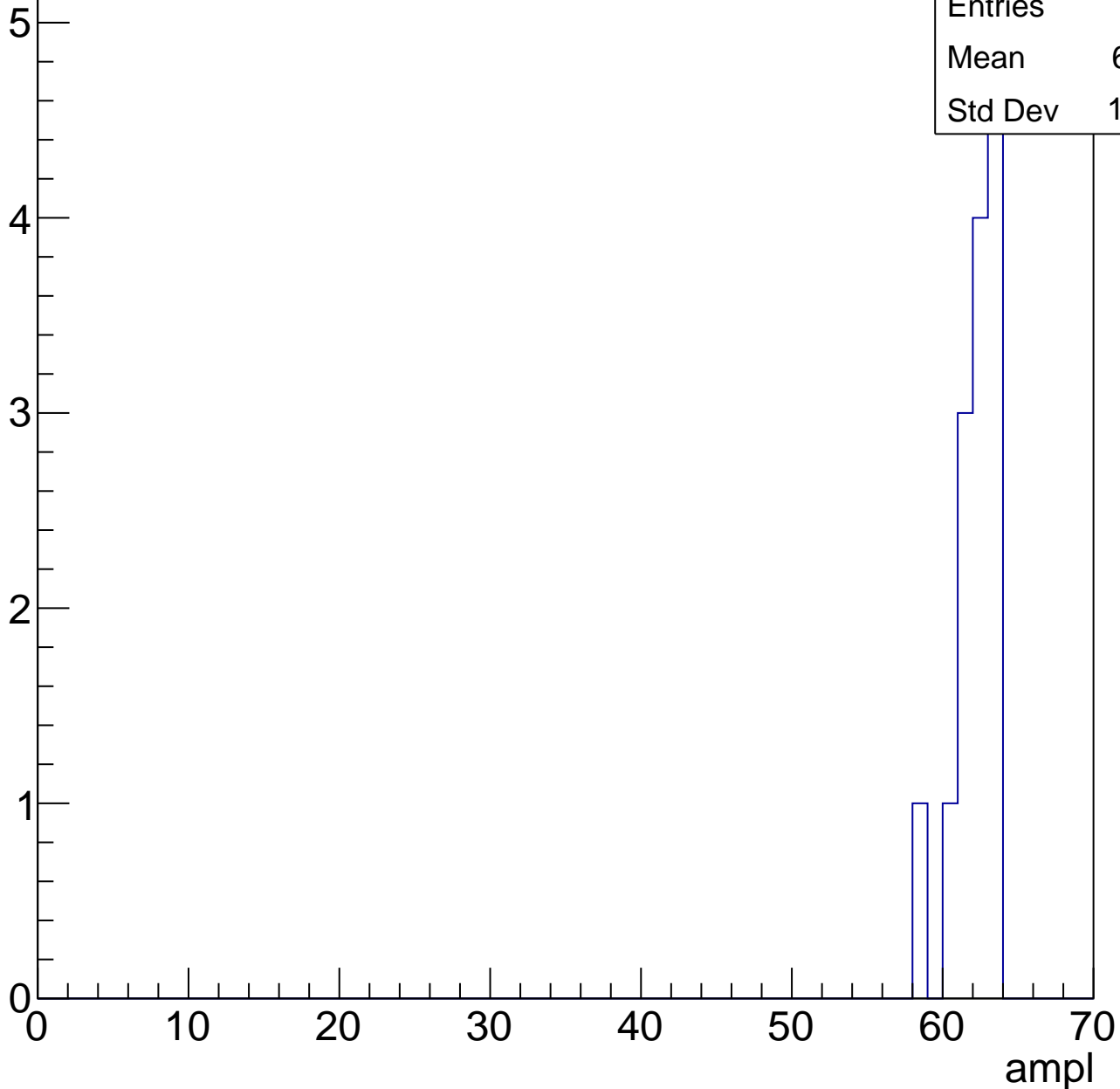


# B0L101S, U16-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 61.71 |
| Std Dev | 1.385 |

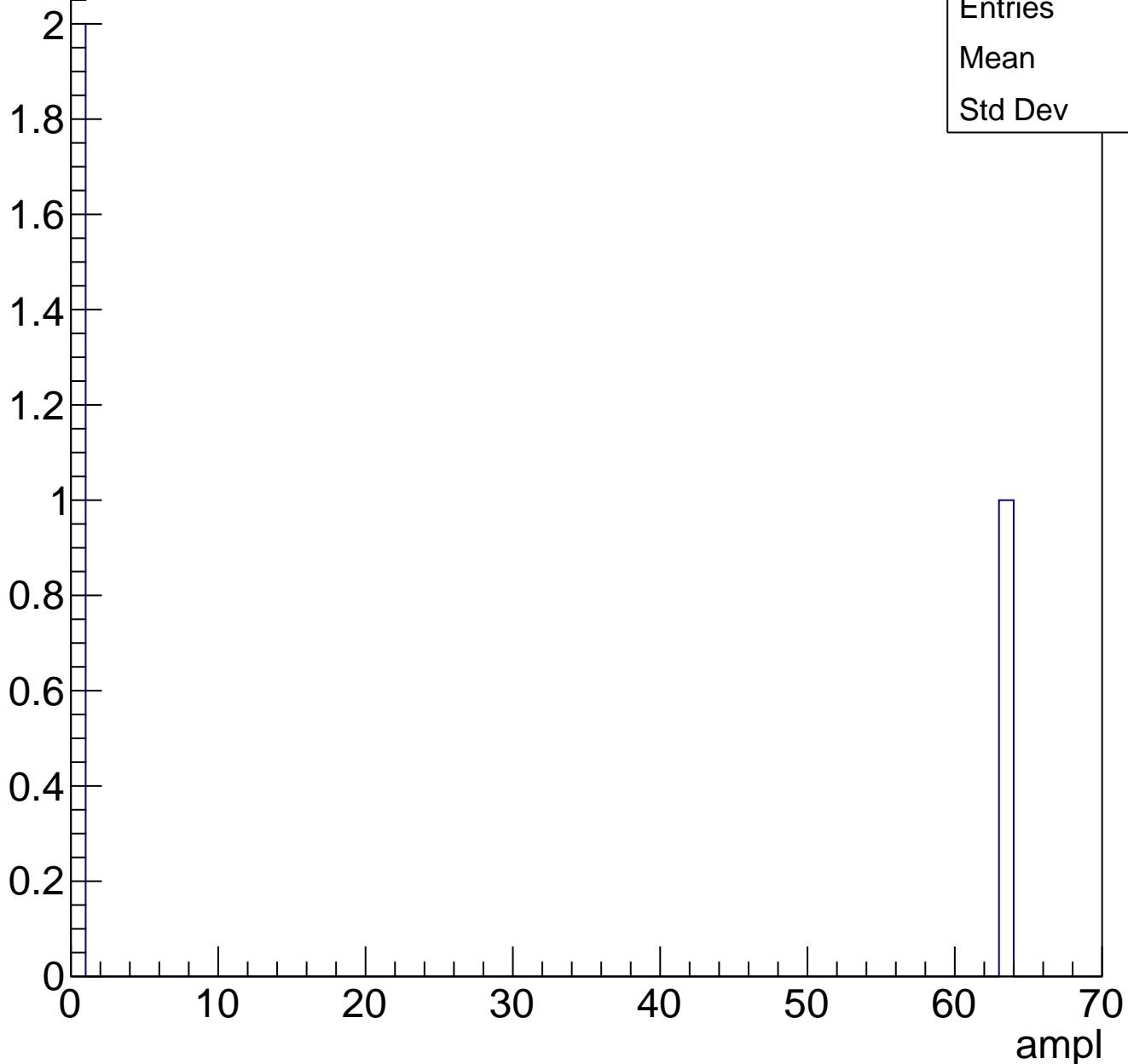




# B0L101S, U16-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |      |
|---------|------|
| Entries | 3    |
| Mean    | 21   |
| Std Dev | 29.7 |

# B0L101S, U16-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 169   |
| Mean    | 28.86 |
| Std Dev | 4.587 |

**Gaus mean : 29.8263**

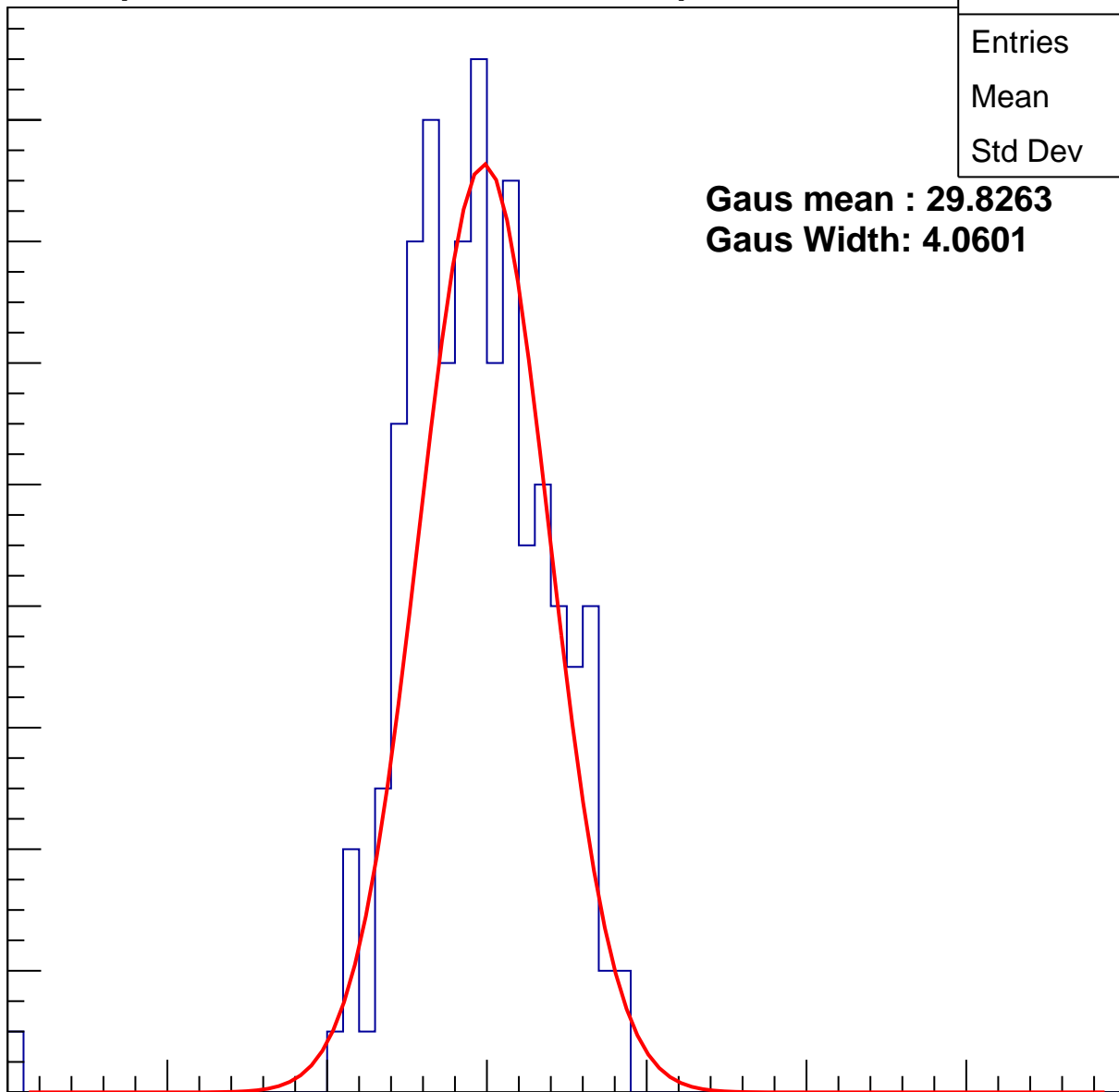
**Gaus Width: 4.0601**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 36.22 |
| Std Dev | 4.718 |

**Gaus mean : 37.0751**

**Gaus Width: 3.7577**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

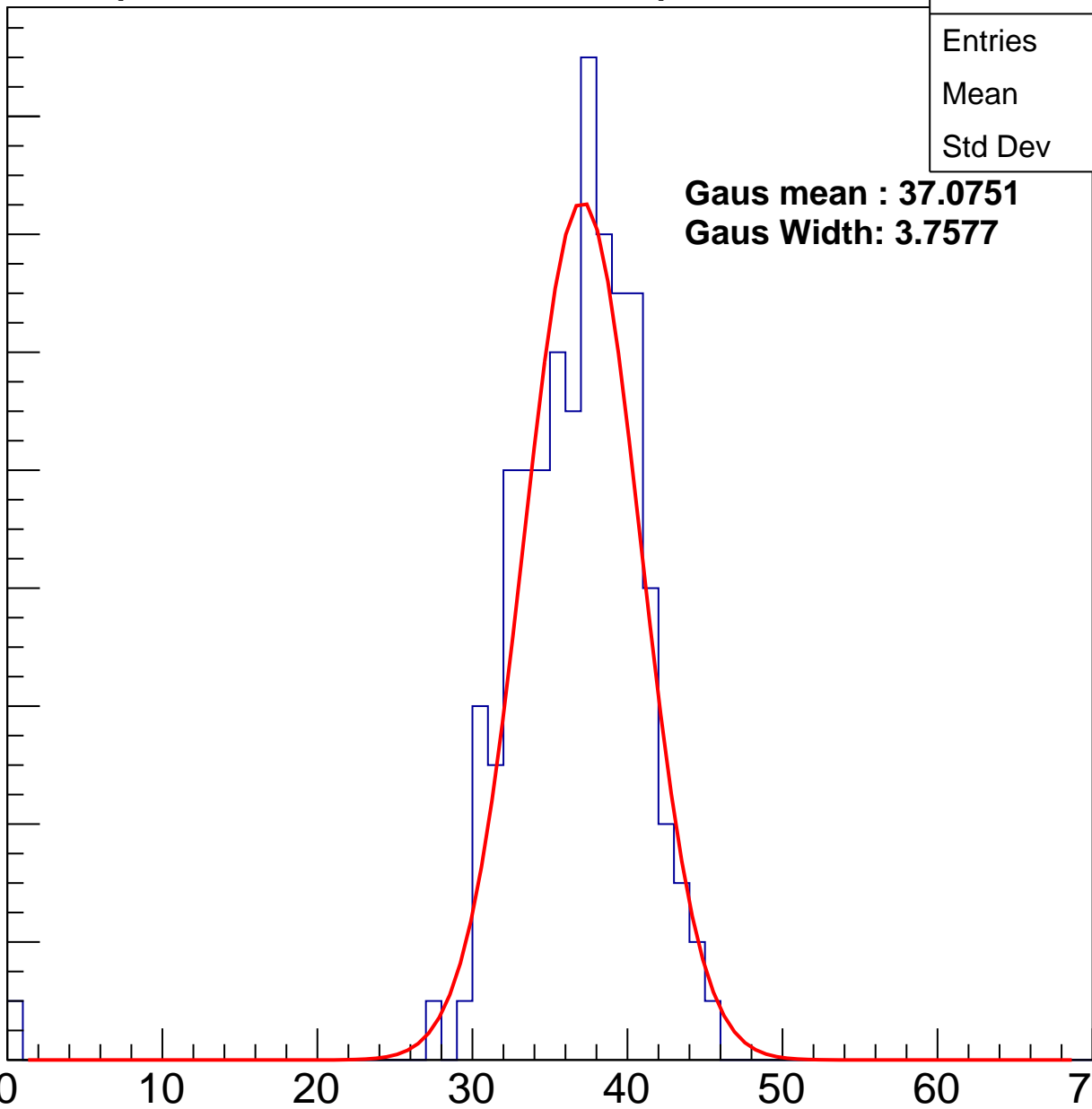
40

50

60

70

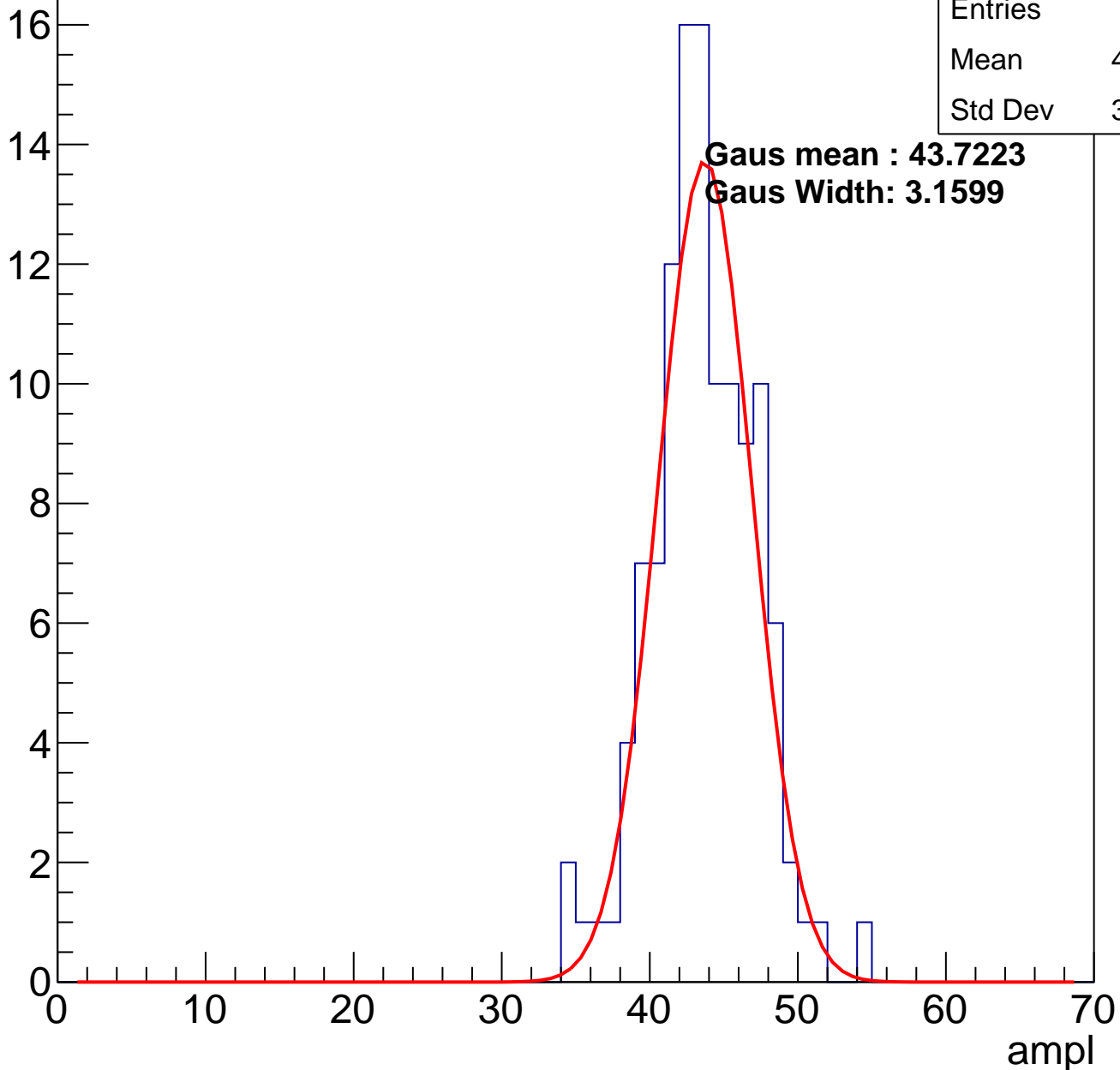
ampl



# B0L101S, U16-ch69, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

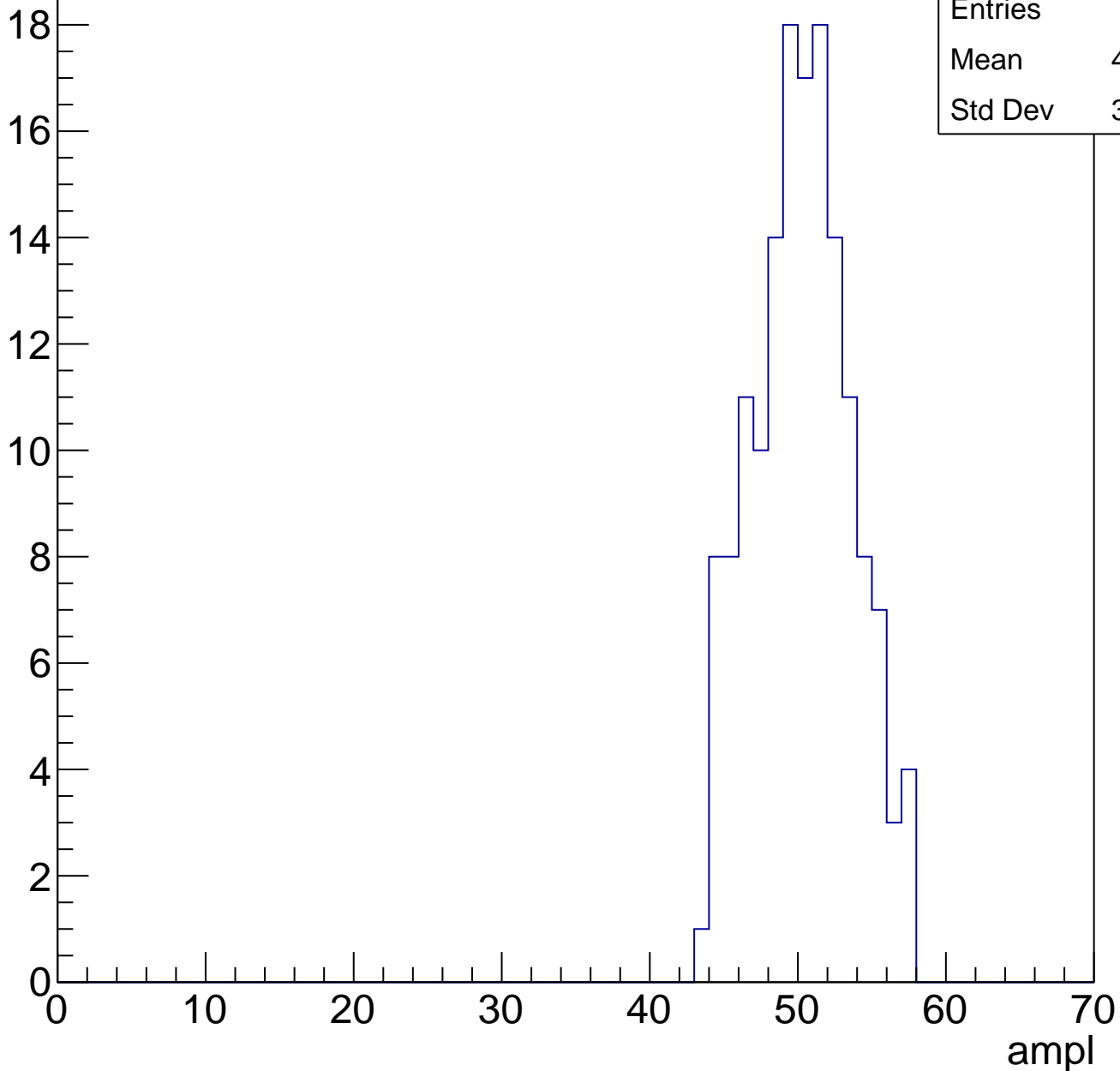
Entry



# B0L101S, U16-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

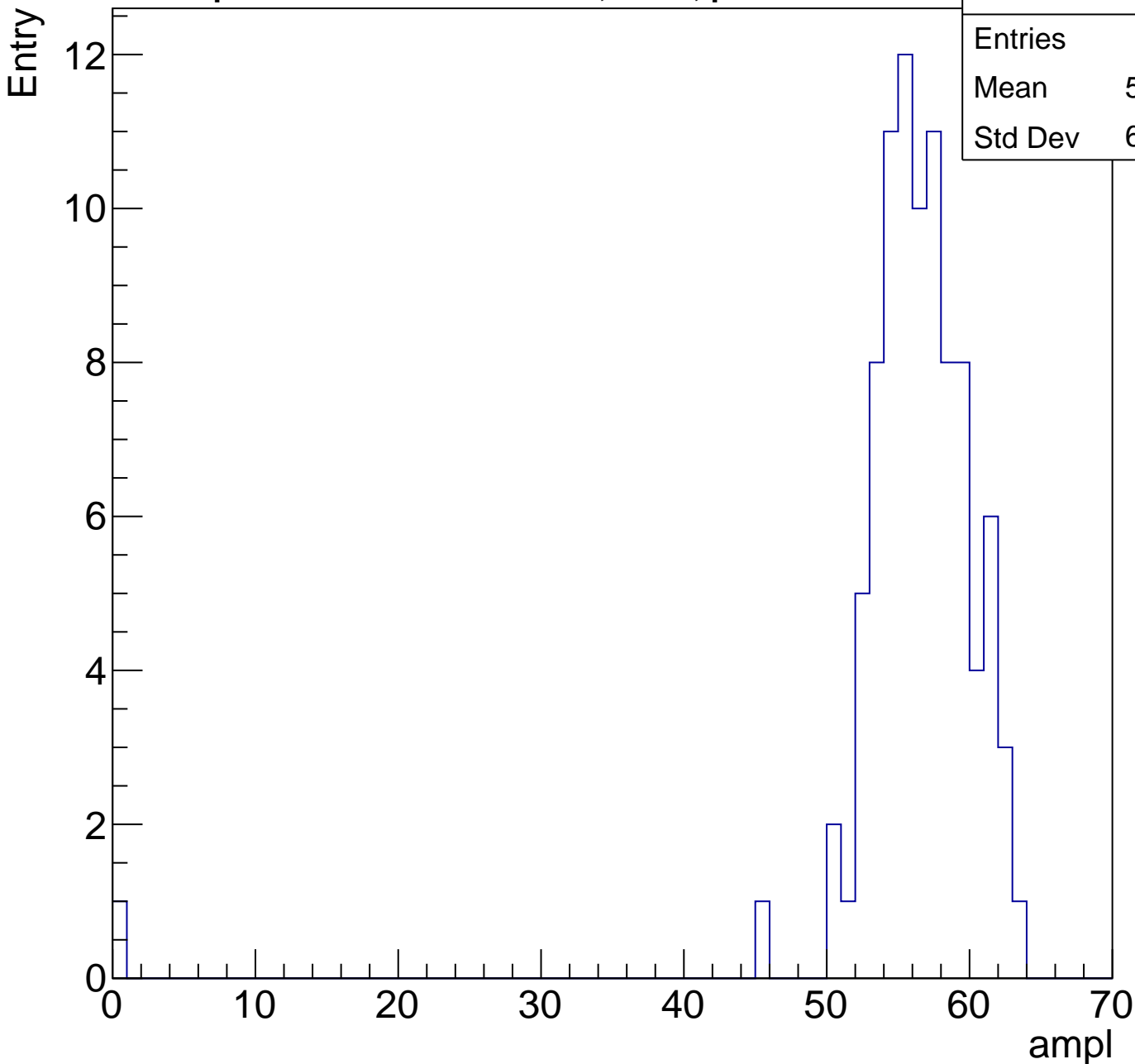
Entry



# B0L101S, U16-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 55.57 |
| Std Dev | 6.619 |



# B0L101S, U16-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

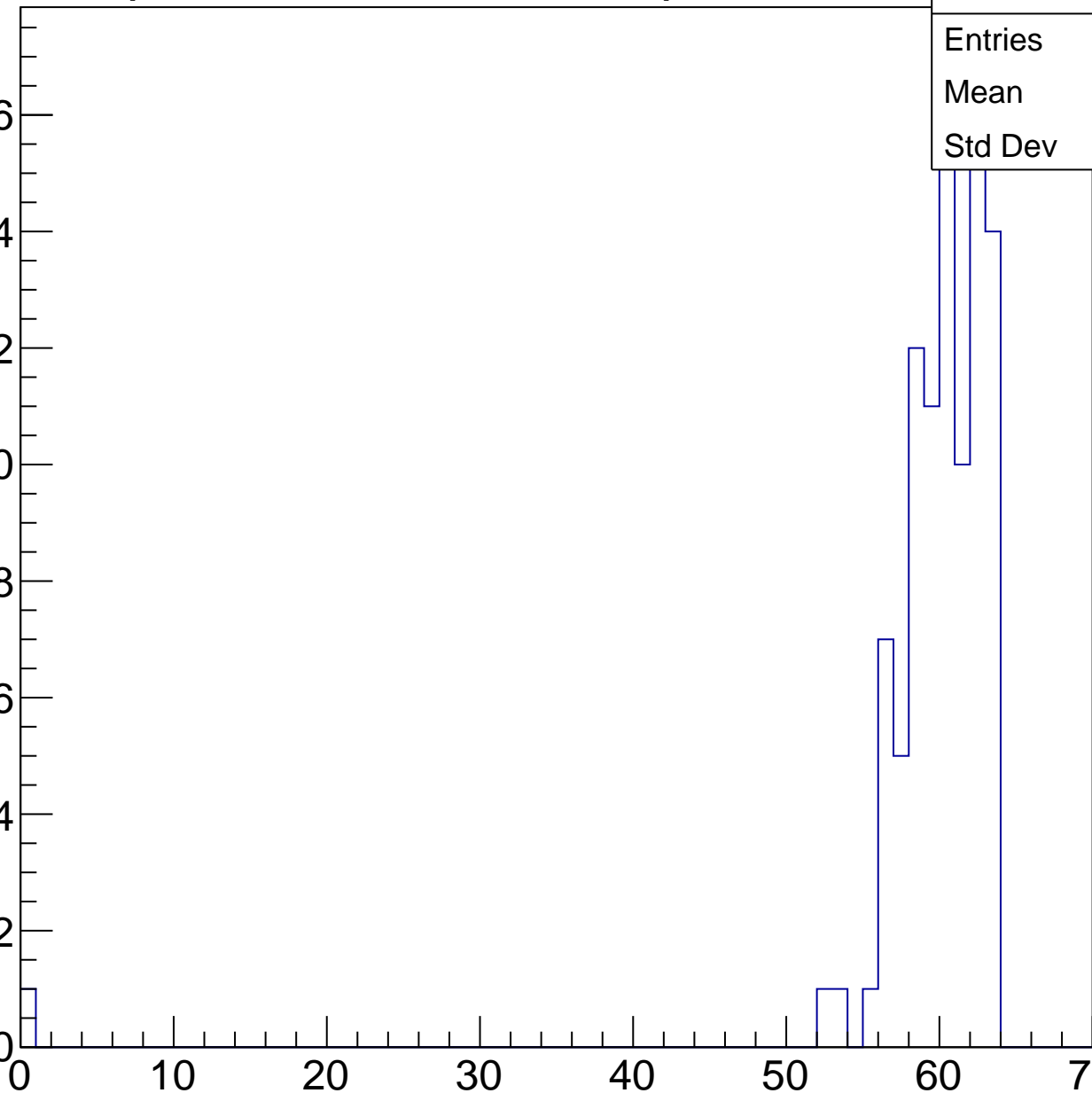
4

2

0

|         |       |
|---------|-------|
| Entries | 96    |
| Mean    | 59.25 |
| Std Dev | 6.538 |

ampl

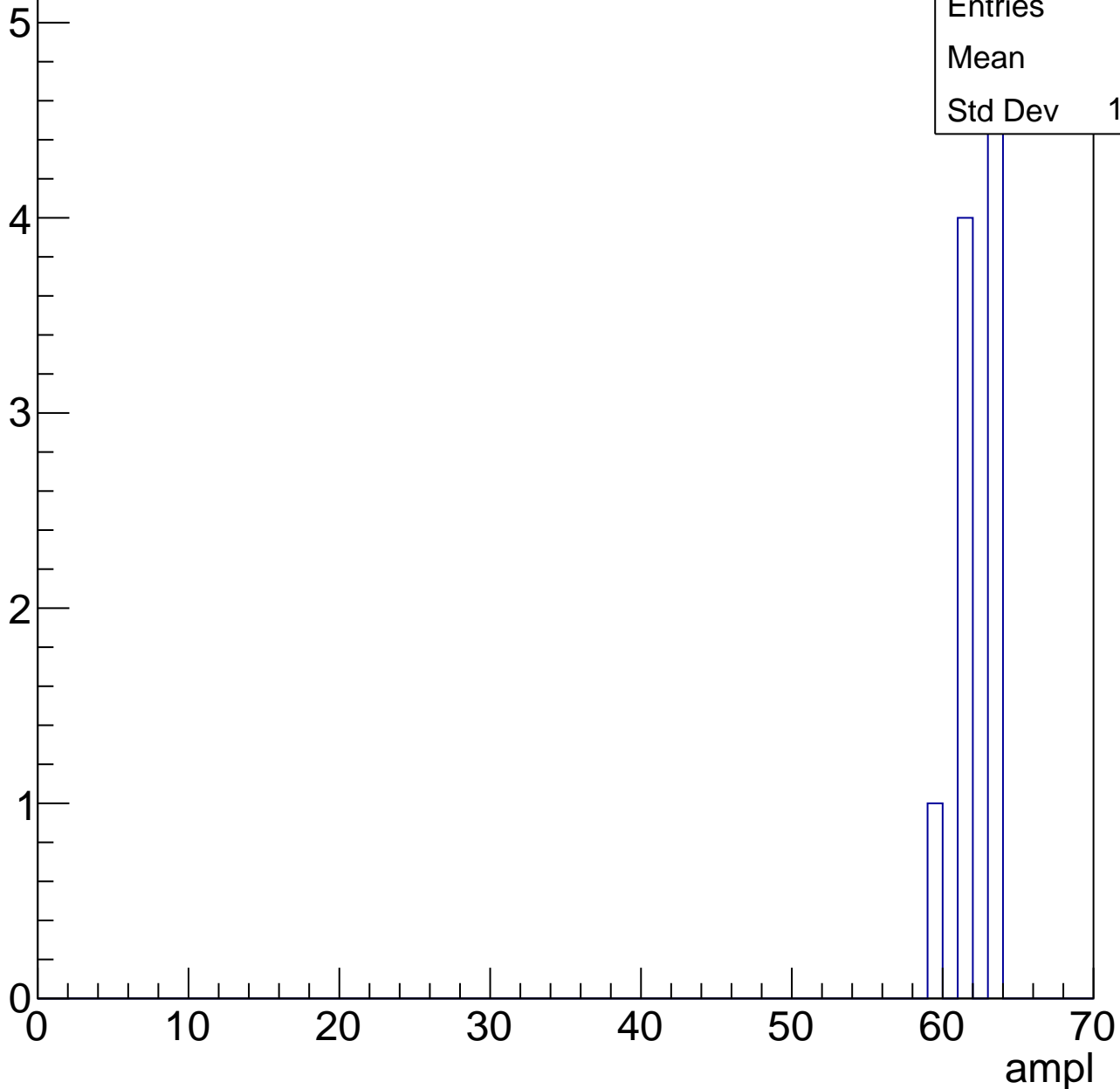


# B0L101S, U16-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 10    |
| Mean    | 61.8  |
| Std Dev | 1.327 |

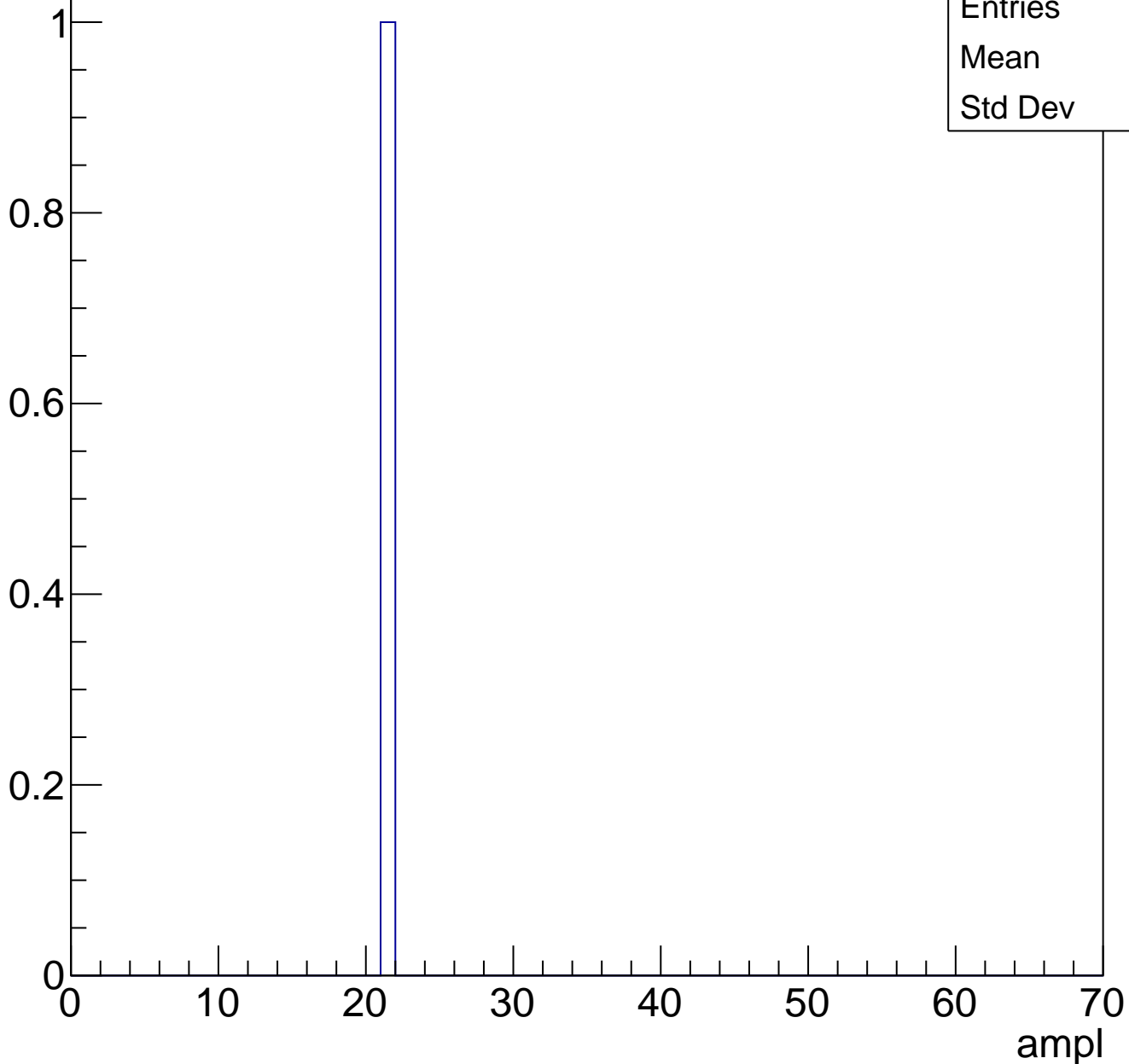




# B0L101S, U16-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



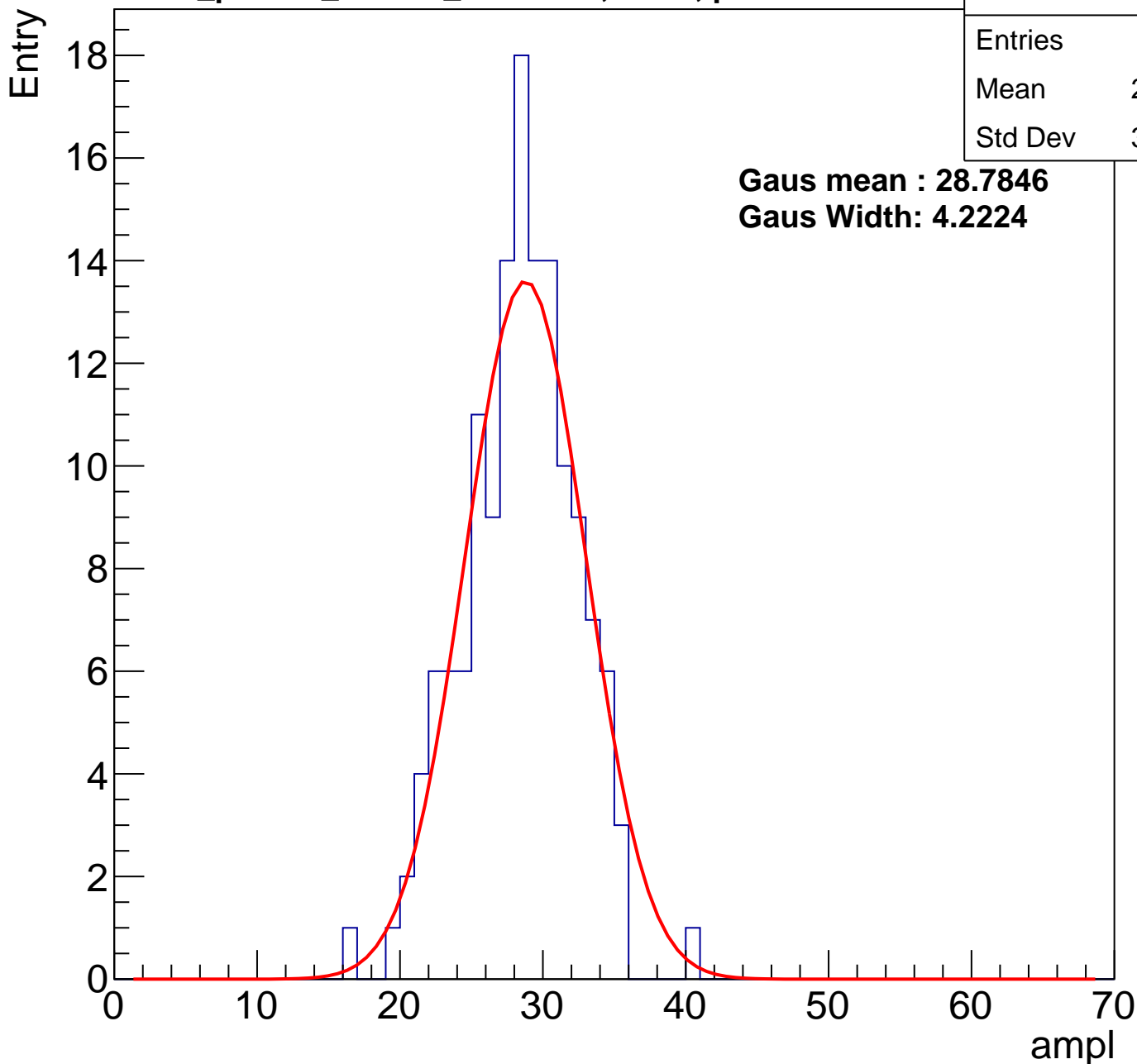
# B0L101S, U16-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 27.94 |
| Std Dev | 3.887 |

**Gaus mean : 28.7846**

**Gaus Width: 4.2224**



# B0L101S, U16-ch70, adc1

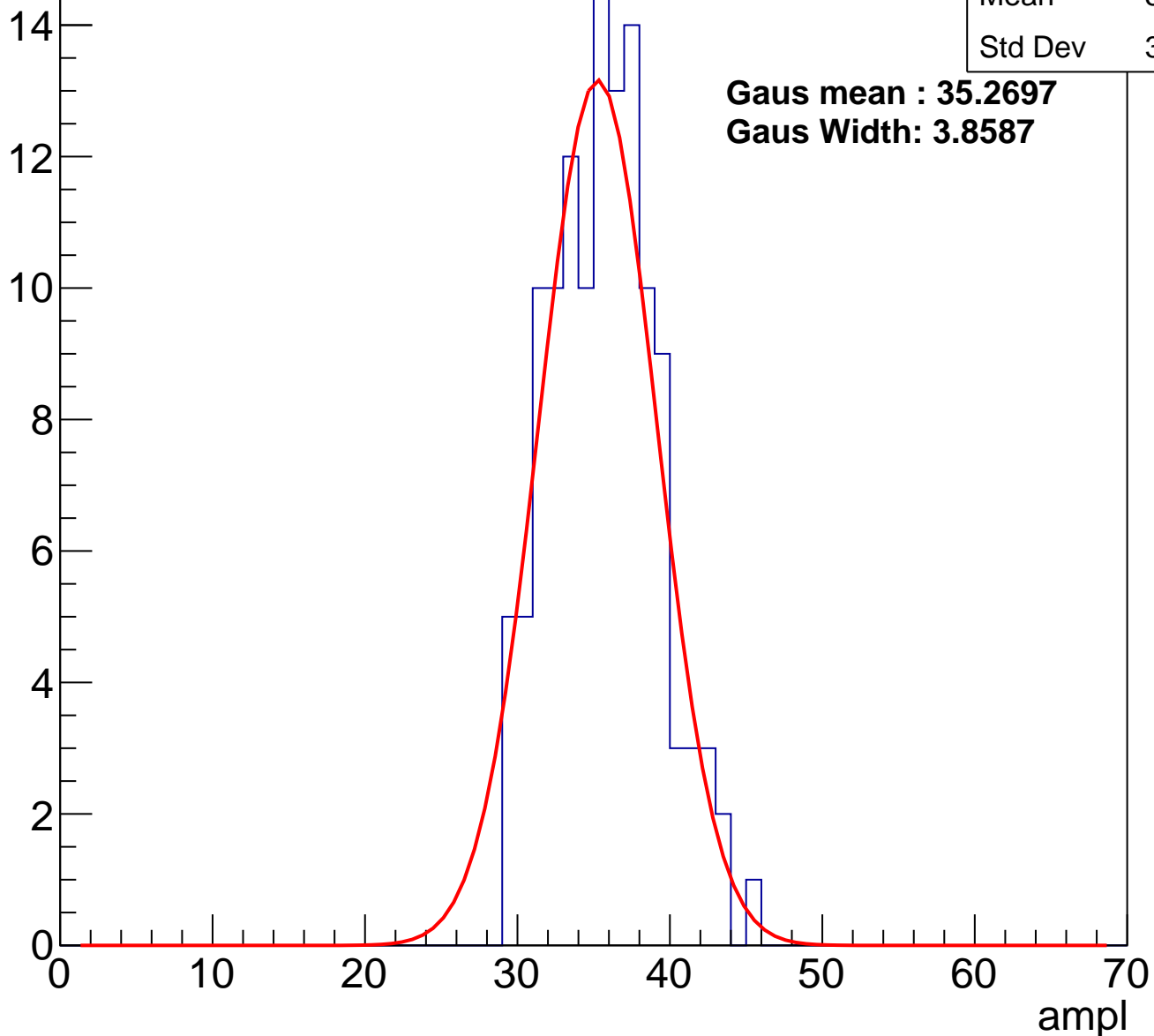
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 35.22 |
| Std Dev | 3.427 |

**Gaus mean : 35.2697**

**Gaus Width: 3.8587**

Entry



# B0L101S, U16-ch70, adc2

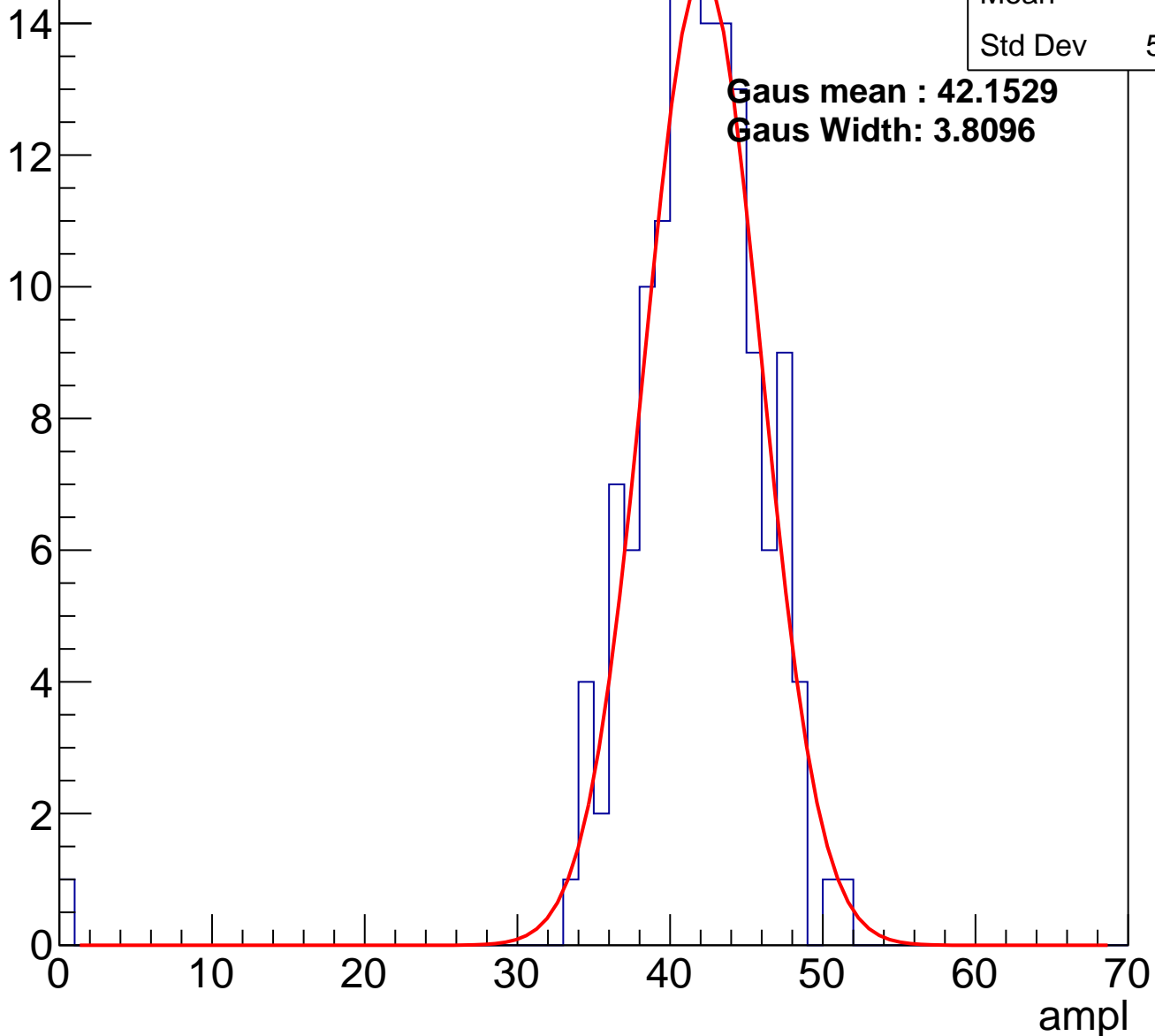
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 41.23 |
| Std Dev | 5.023 |

**Gaus mean : 42.1529**

**Gaus Width: 3.8096**

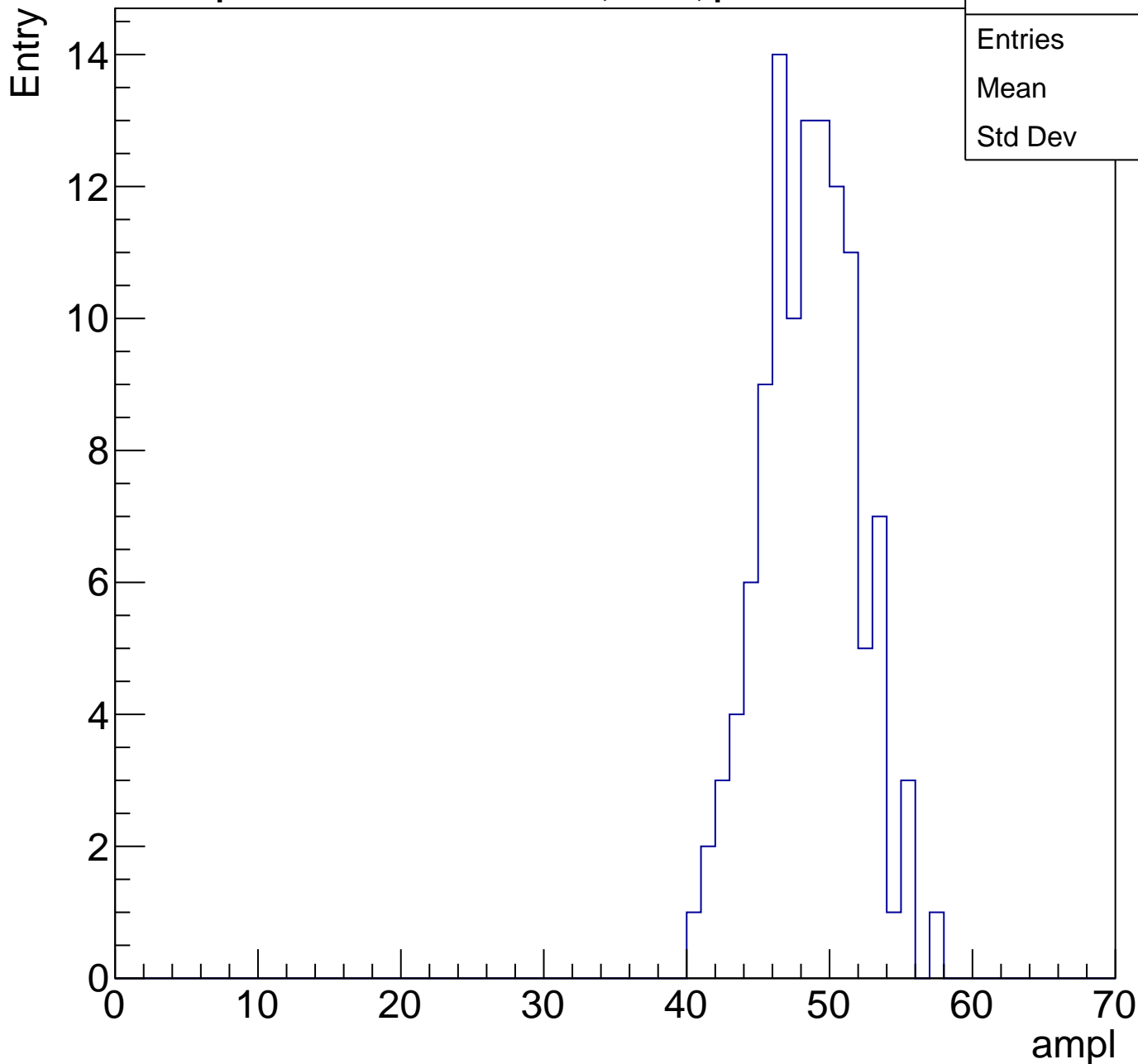
Entry



# B0L101S, U16-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |      |
|---------|------|
| Entries | 115  |
| Mean    | 48.1 |
| Std Dev | 3.35 |



# B0L101S, U16-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

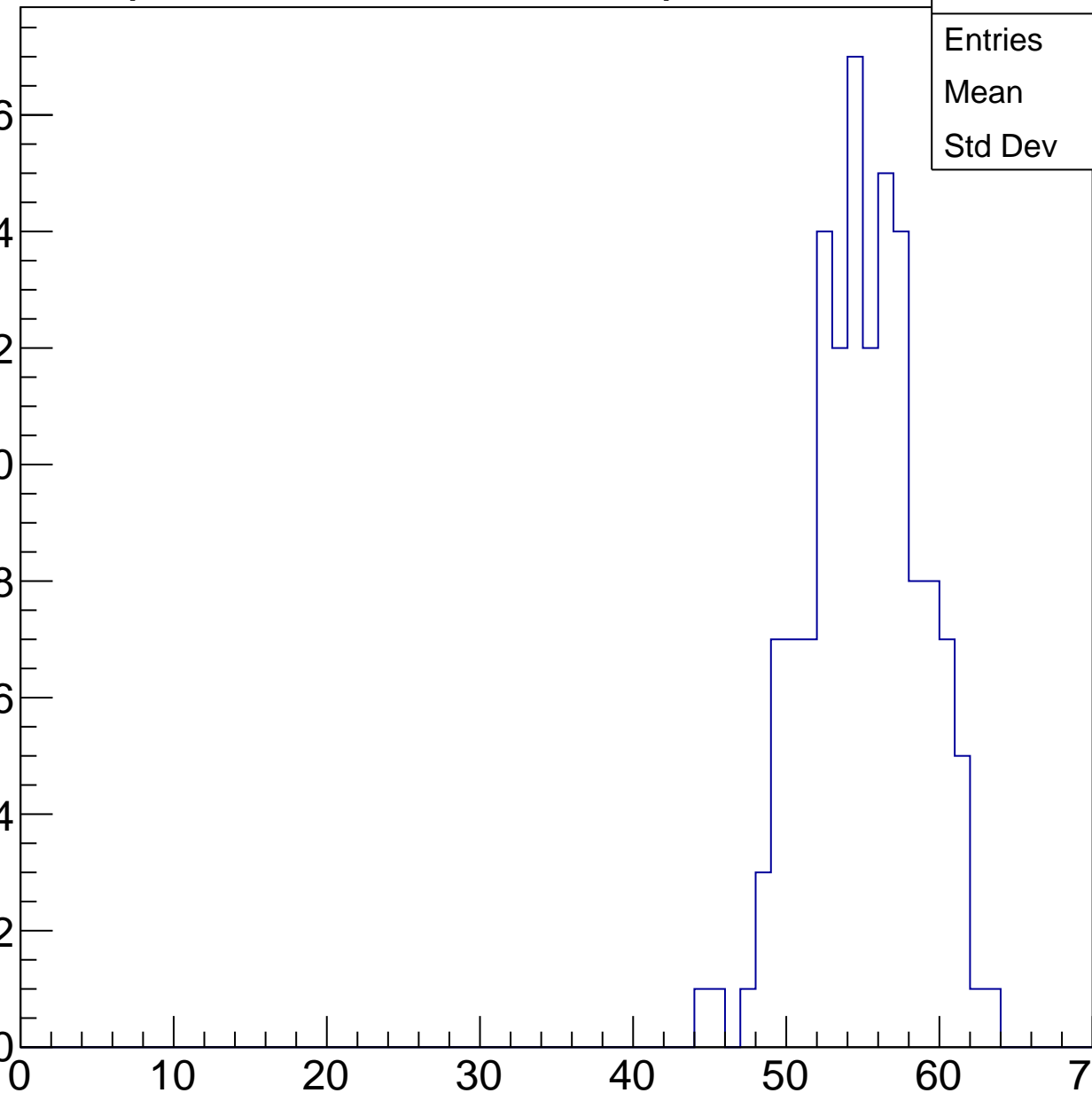
0

Entries 141

Mean 54.58

Std Dev 3.648

ampl



# B0L101S, U16-ch70, adc5

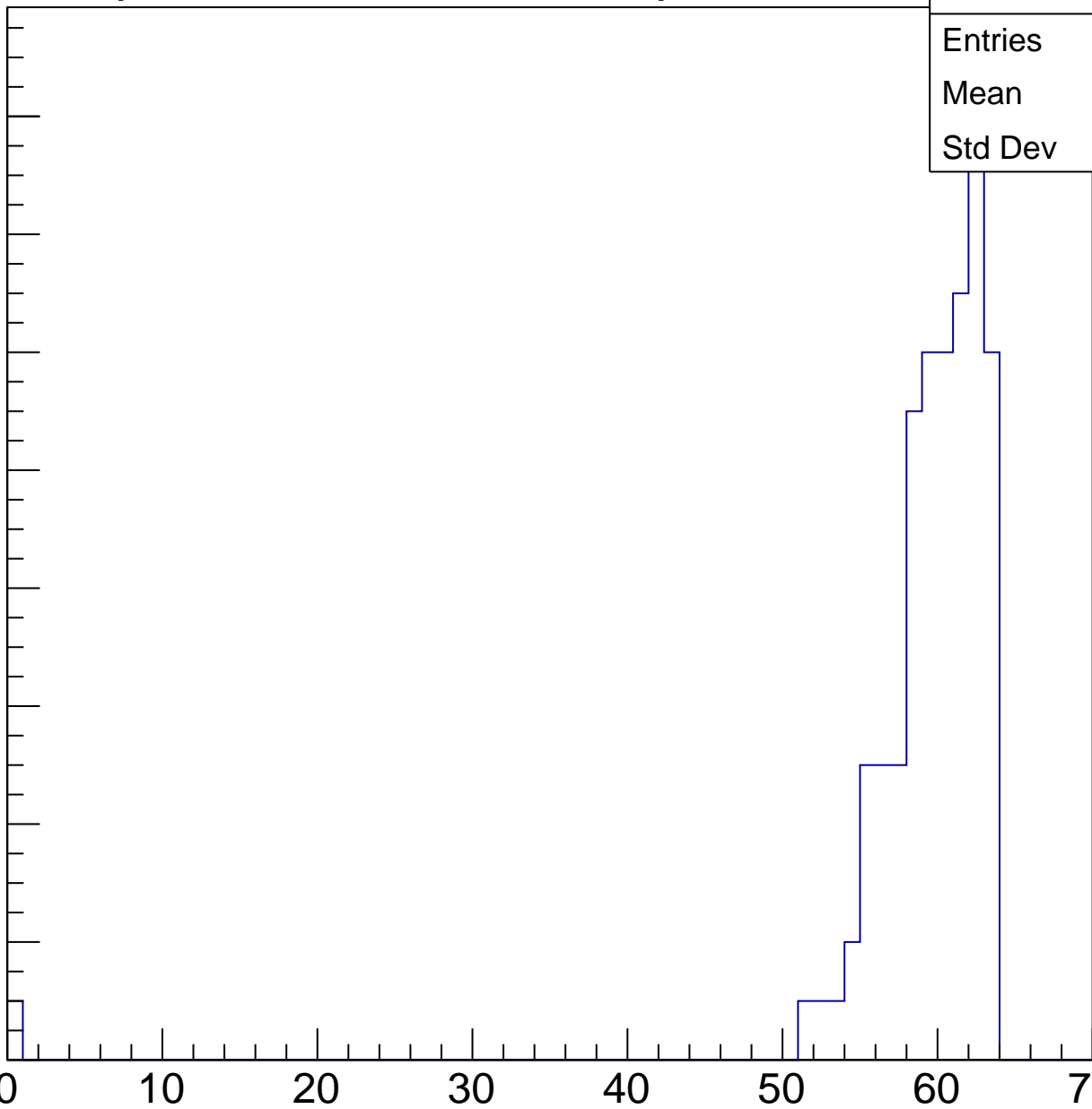
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 98    |
| Mean    | 58.91 |
| Std Dev | 6.581 |

ampl

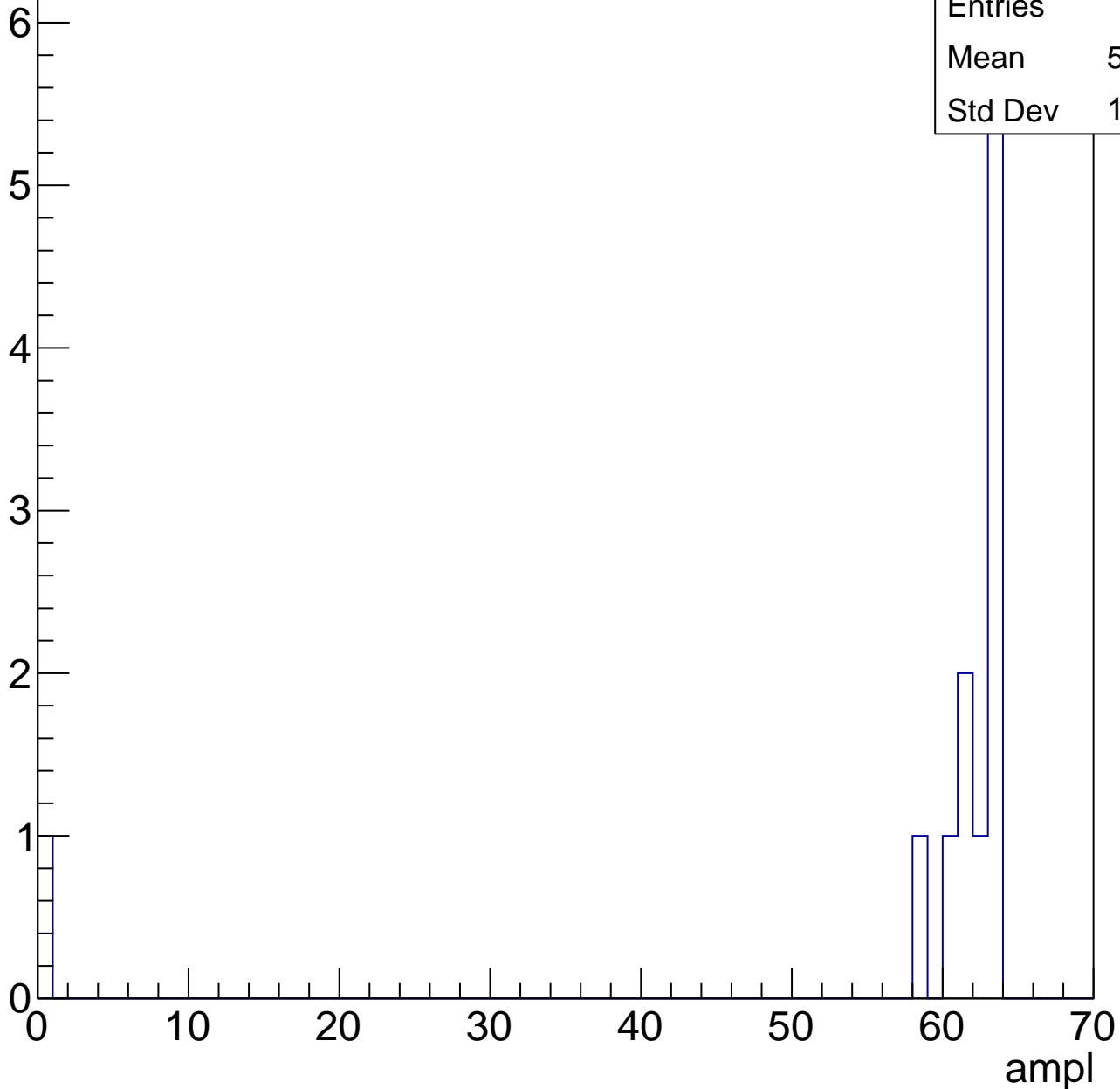


# B0L101S, U16-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 12    |
| Mean    | 56.67 |
| Std Dev | 17.15 |





# B0L101S, U16-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch71, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 29.33 |
| Std Dev | 5.18  |

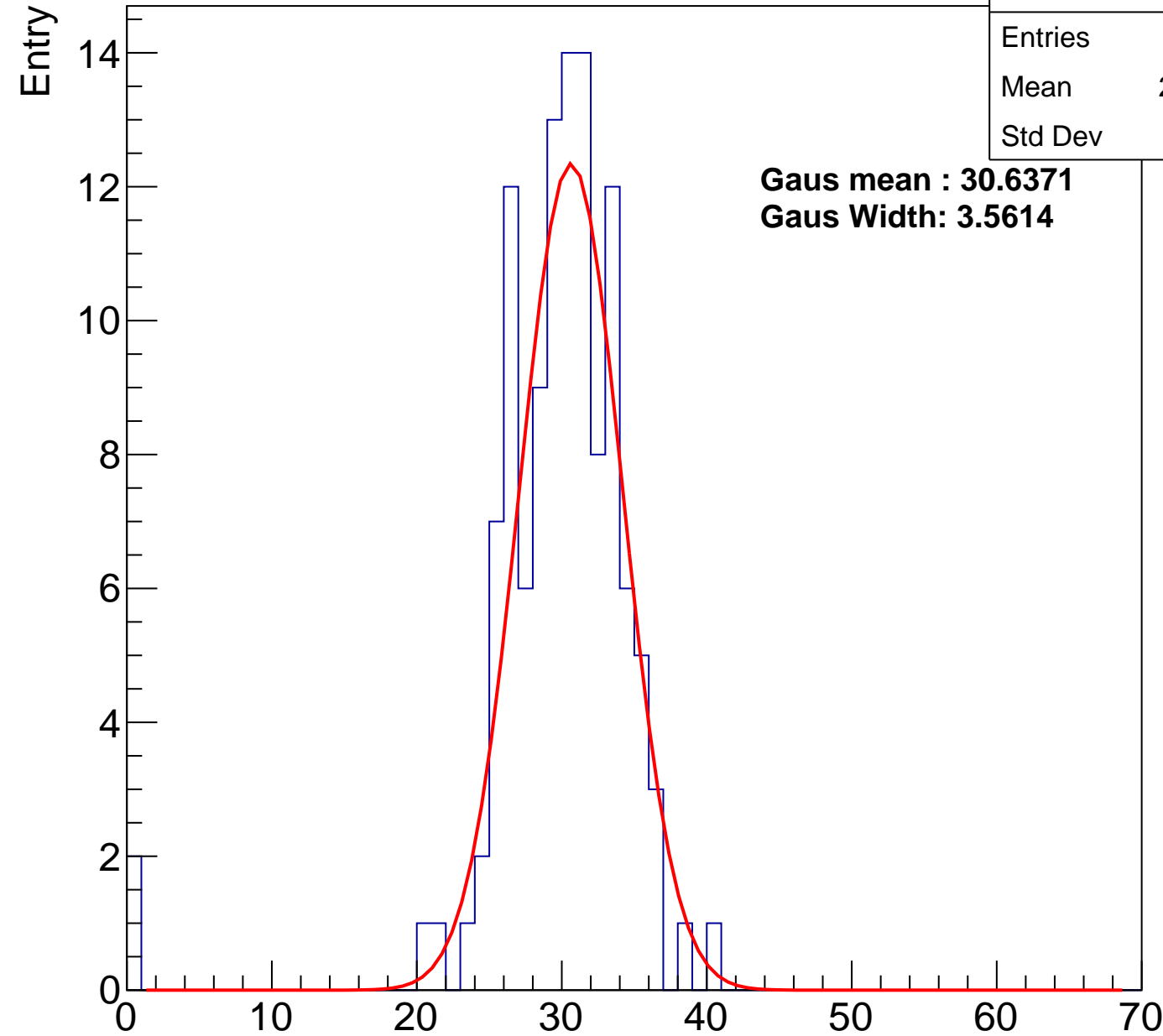
**Gaus mean : 30.6371**

**Gaus Width: 3.5614**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch71, adc1

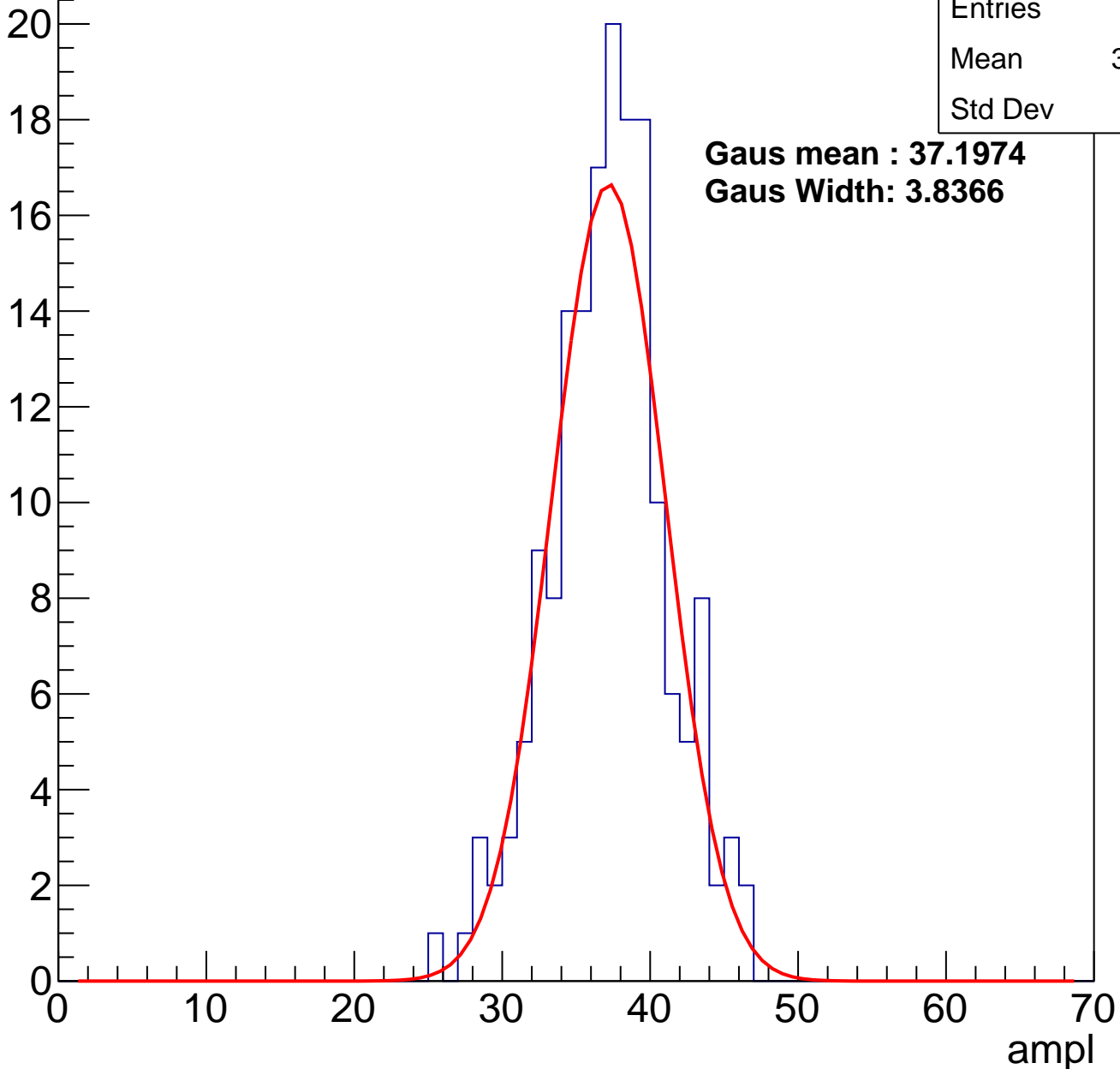
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 169   |
| Mean    | 36.75 |
| Std Dev | 3.94  |

**Gaus mean : 37.1974**

**Gaus Width: 3.8366**

Entry



# B0L101S, U16-ch71, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

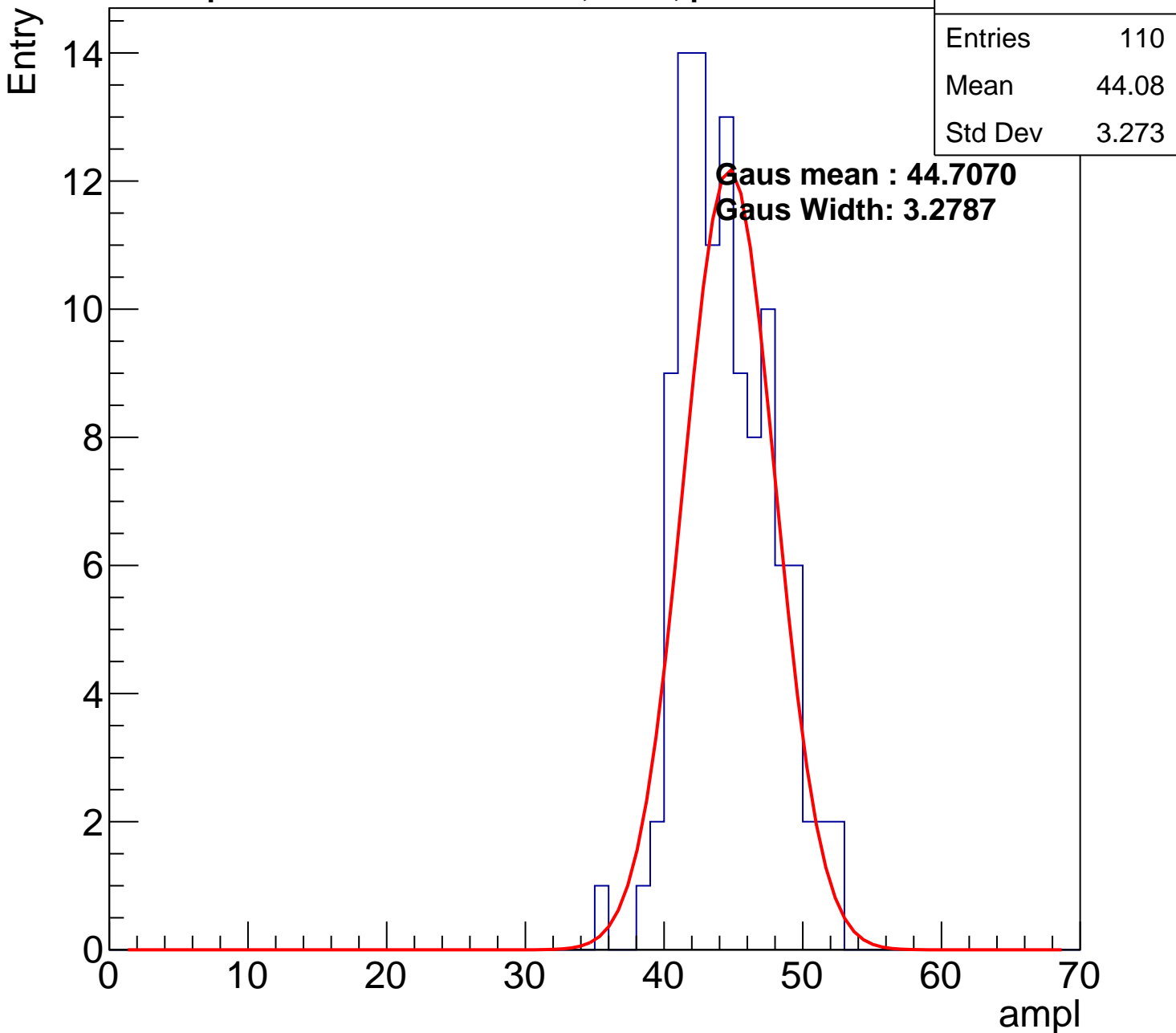
|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 44.08 |
| Std Dev | 3.273 |

**Gaus mean : 44.7070**

**Gaus Width: 3.2787**

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

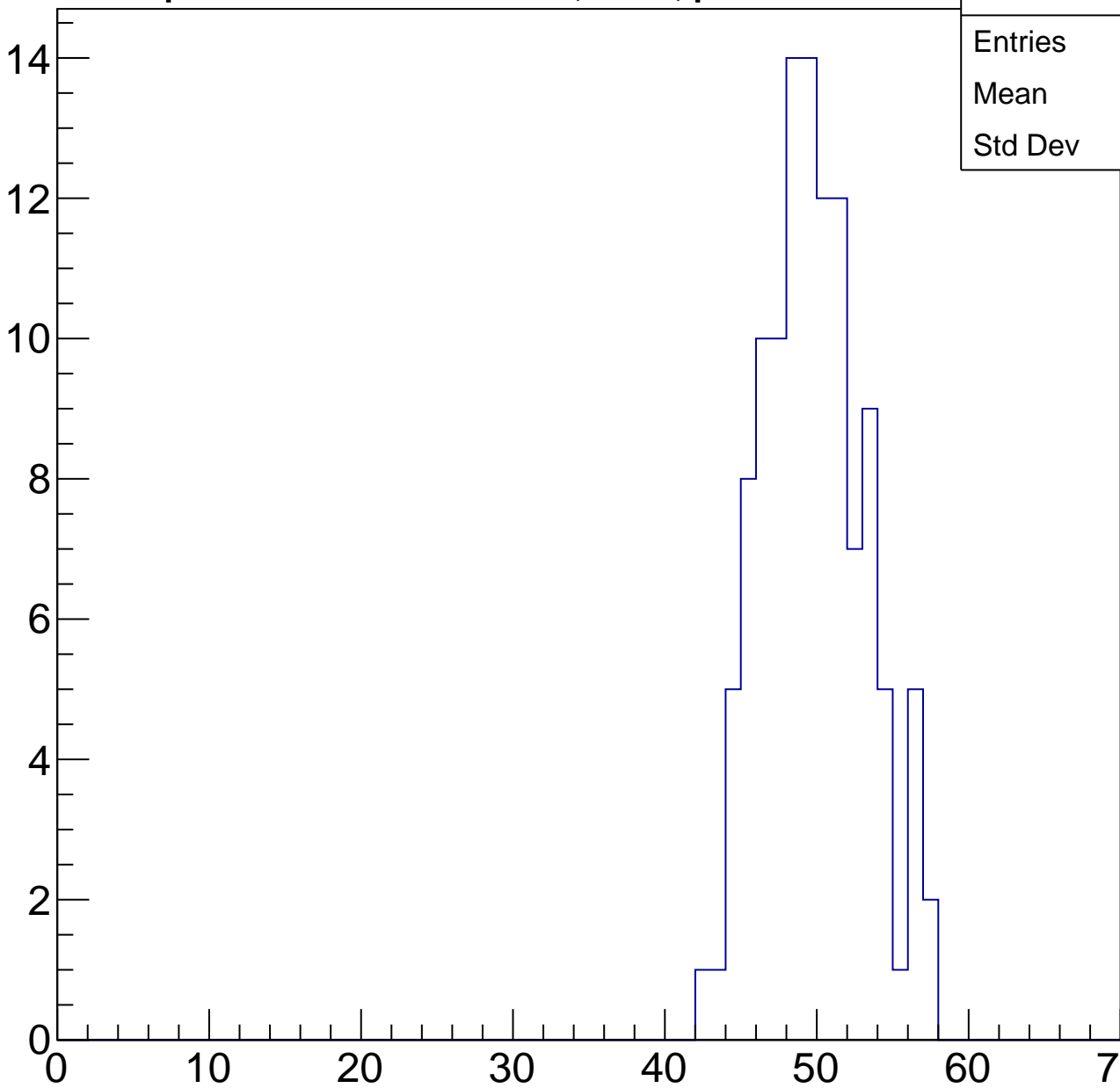
|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 49.35 |
| Std Dev | 3.307 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

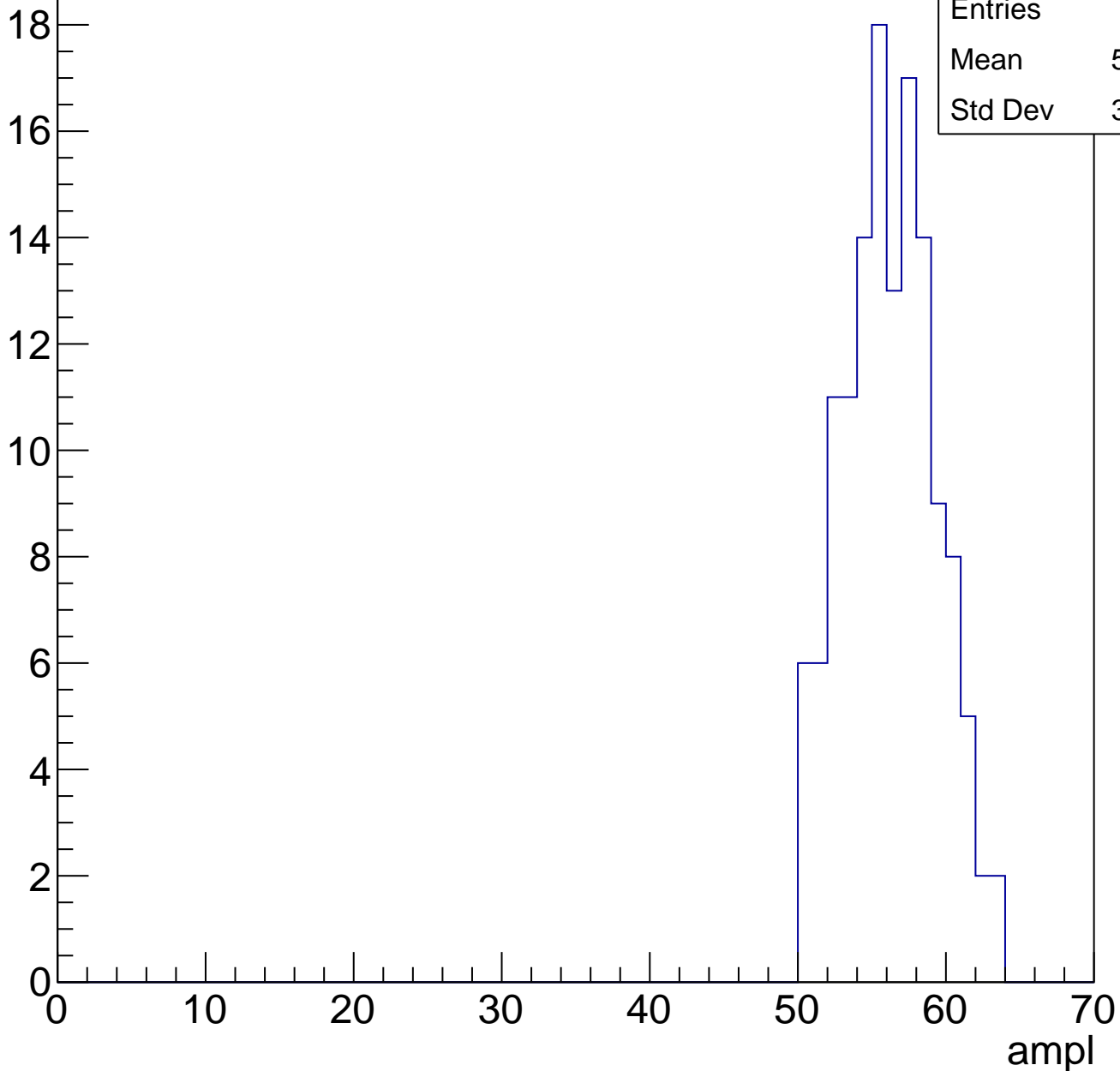
ampl



# B0L101S, U16-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch71, adc5

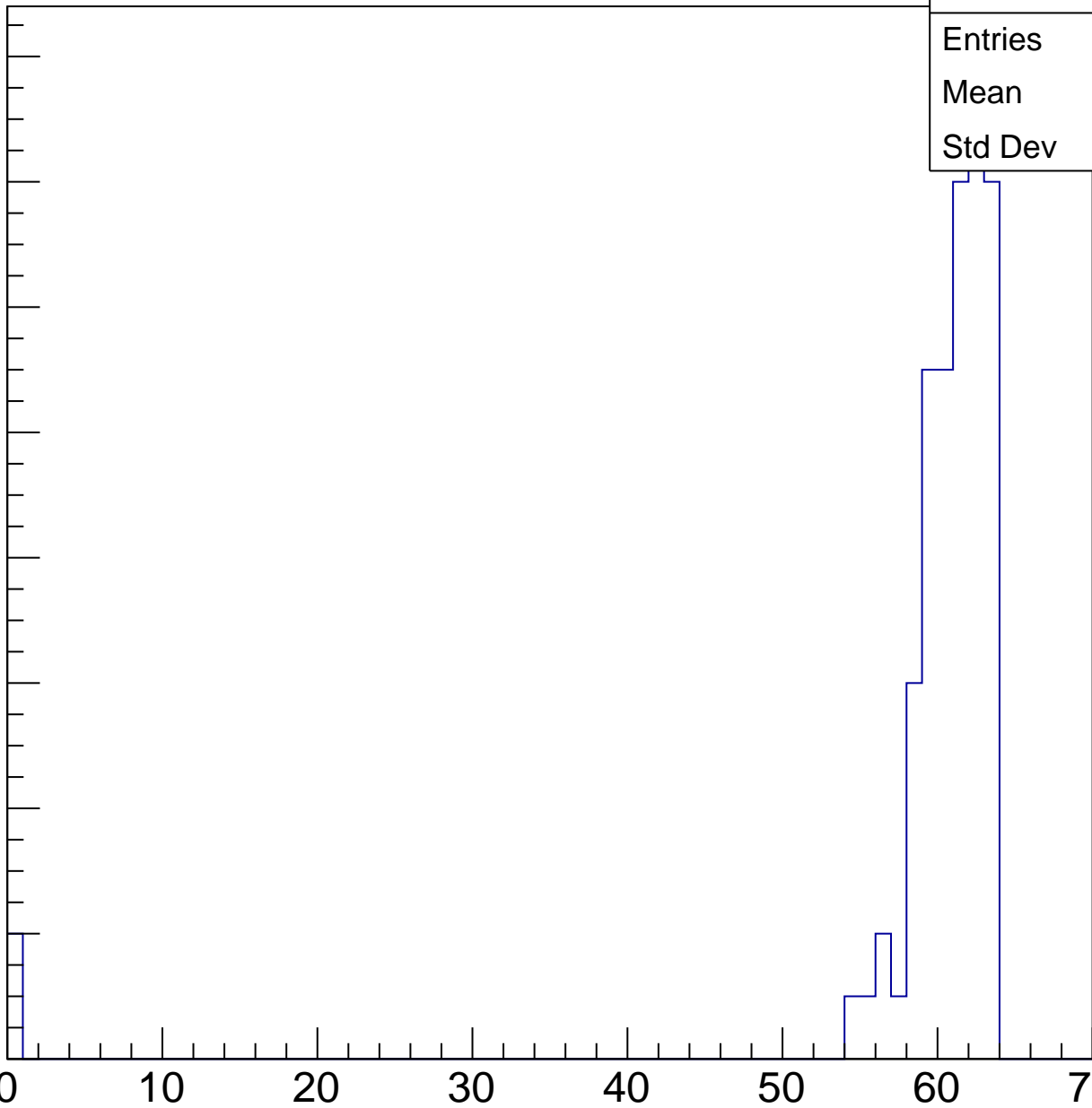
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 59.03 |
| Std Dev | 9.721 |

ampl



# B0L101S, U16-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 23 |
| Std Dev | 0  |

# B0L101S, U16-ch72, adc0

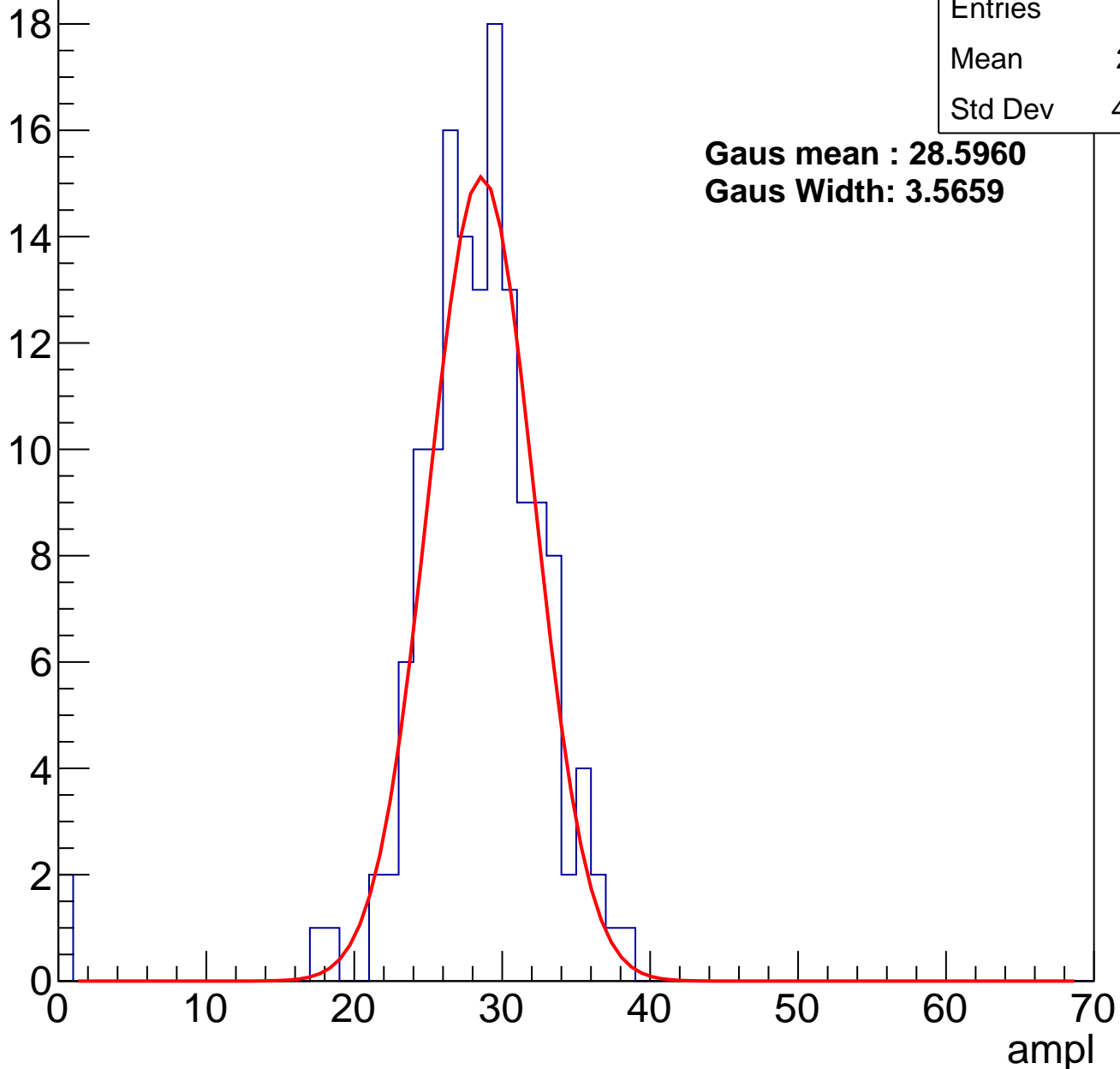
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 27.81 |
| Std Dev | 4.933 |

**Gaus mean : 28.5960**

**Gaus Width: 3.5659**

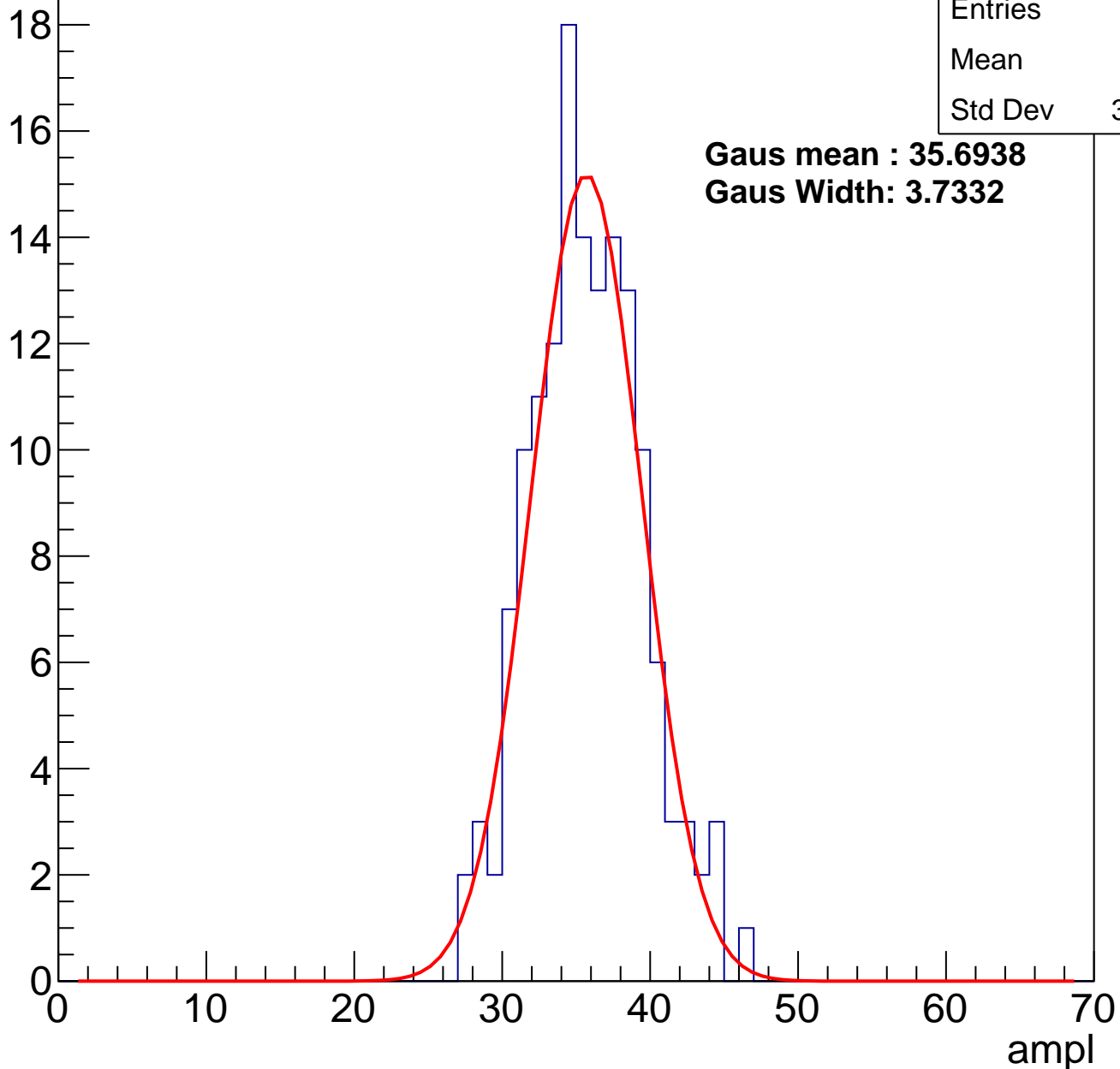
Entry



# B0L101S, U16-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch72, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 42.41 |
| Std Dev | 3.606 |

**Gaus mean : 42.6984**  
**Gaus Width: 3.8251**

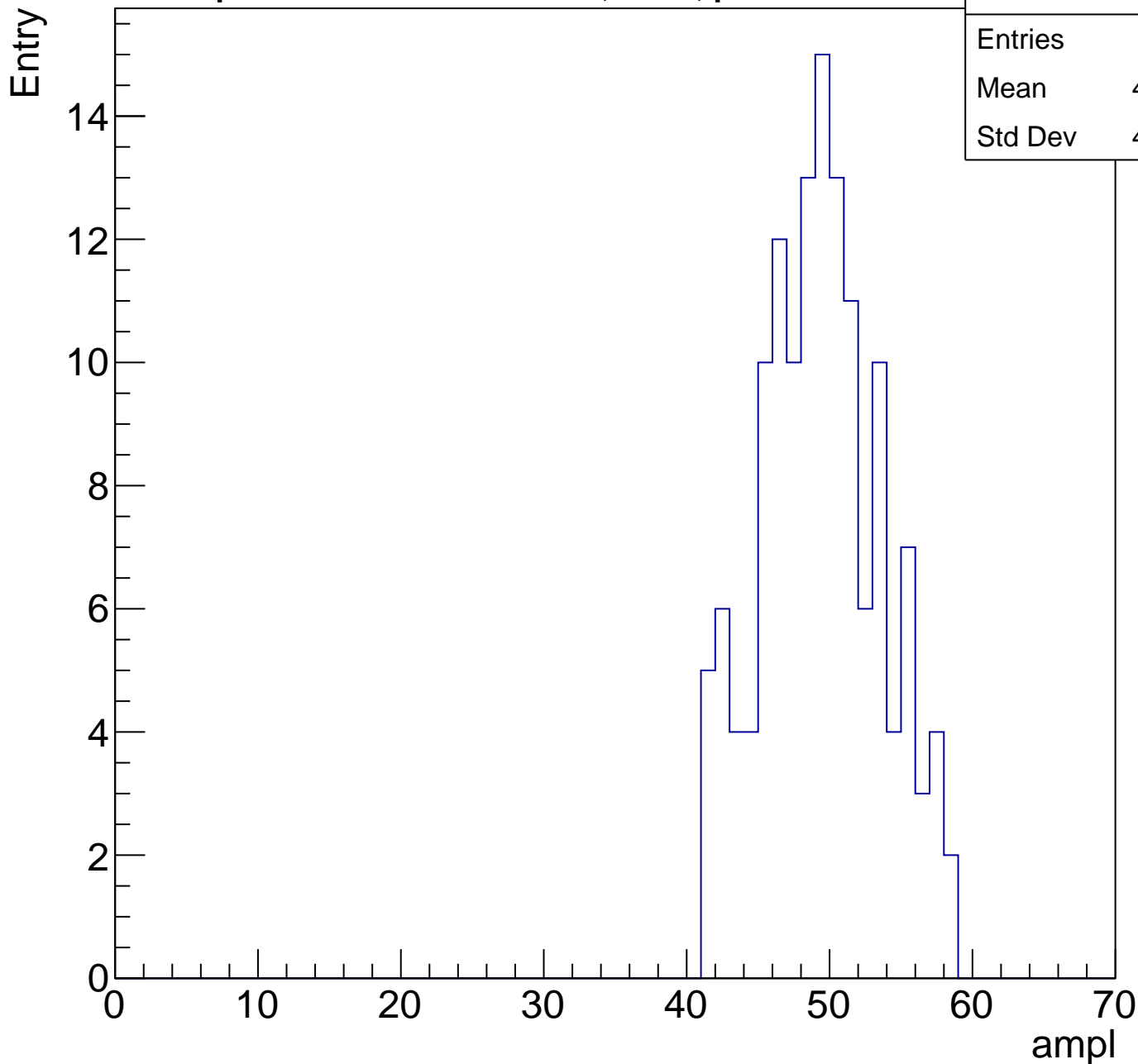
ampl

0 10 20 30 40 50 60 70

# B0L101S, U16-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

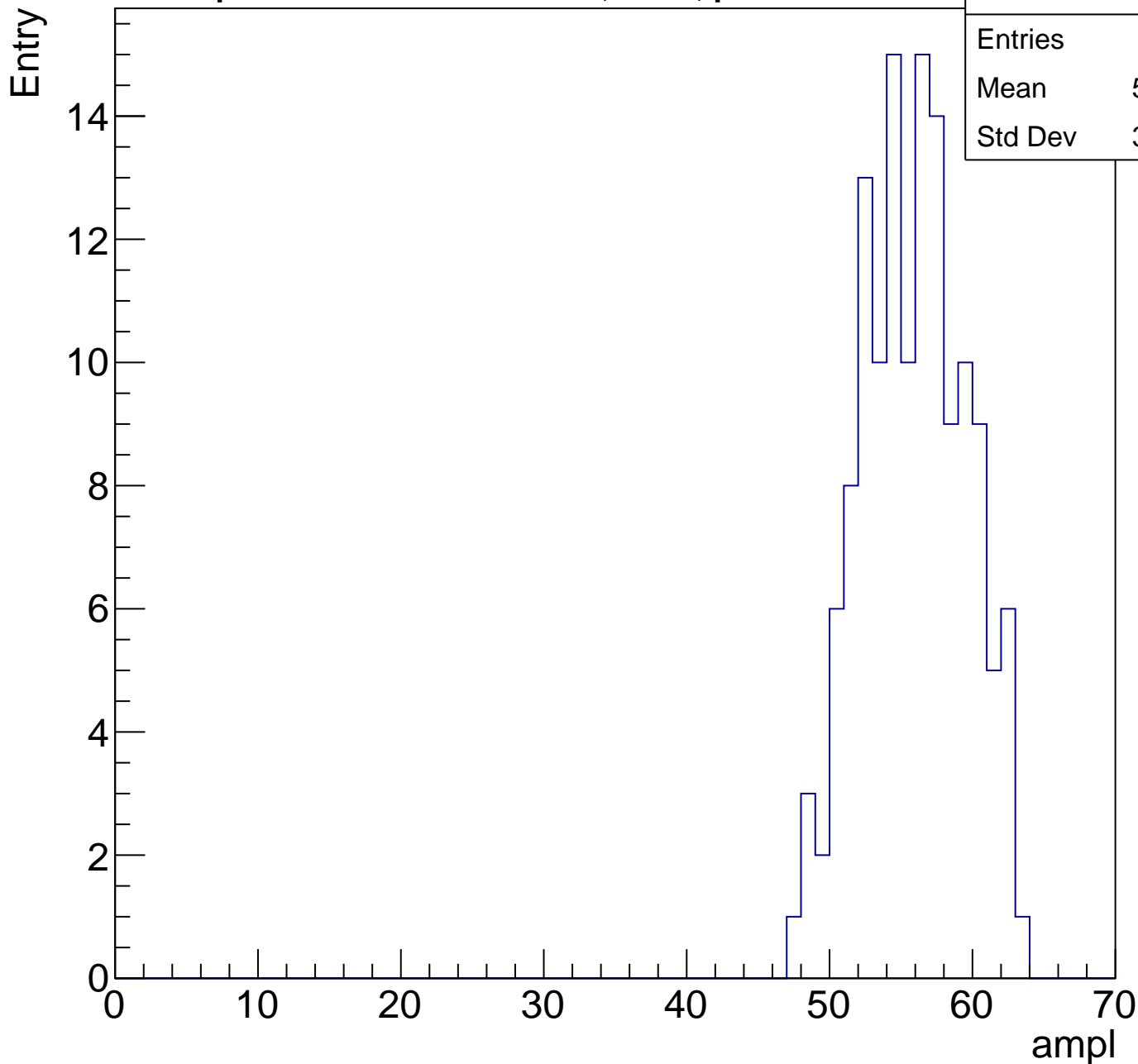
|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 48.94 |
| Std Dev | 4.126 |



# B0L101S, U16-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 55.42 |
| Std Dev | 3.608 |



# B0L101S, U16-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

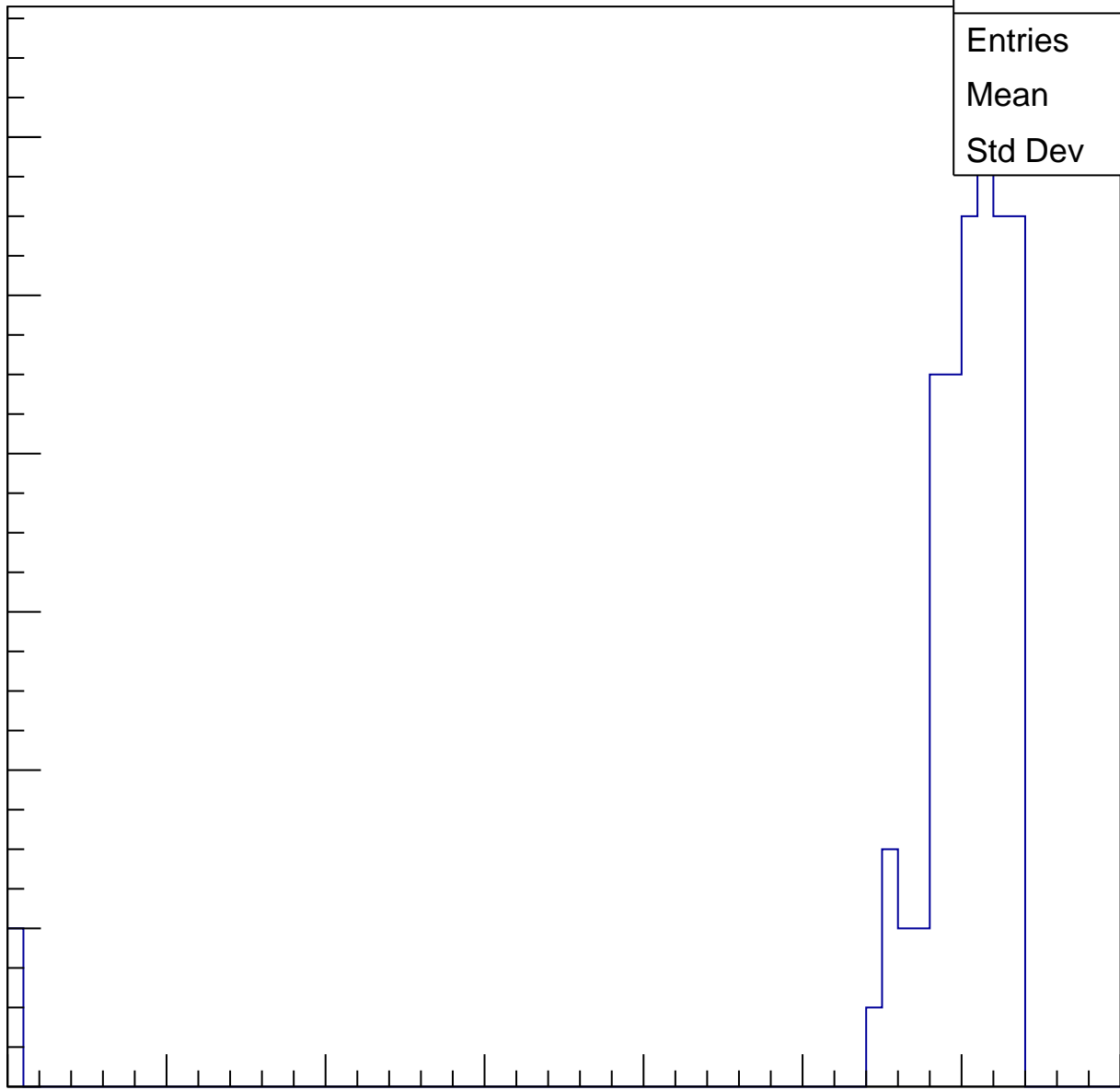
2

0

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 58.46 |
| Std Dev | 9.991 |

ampl

0 10 20 30 40 50 60 70

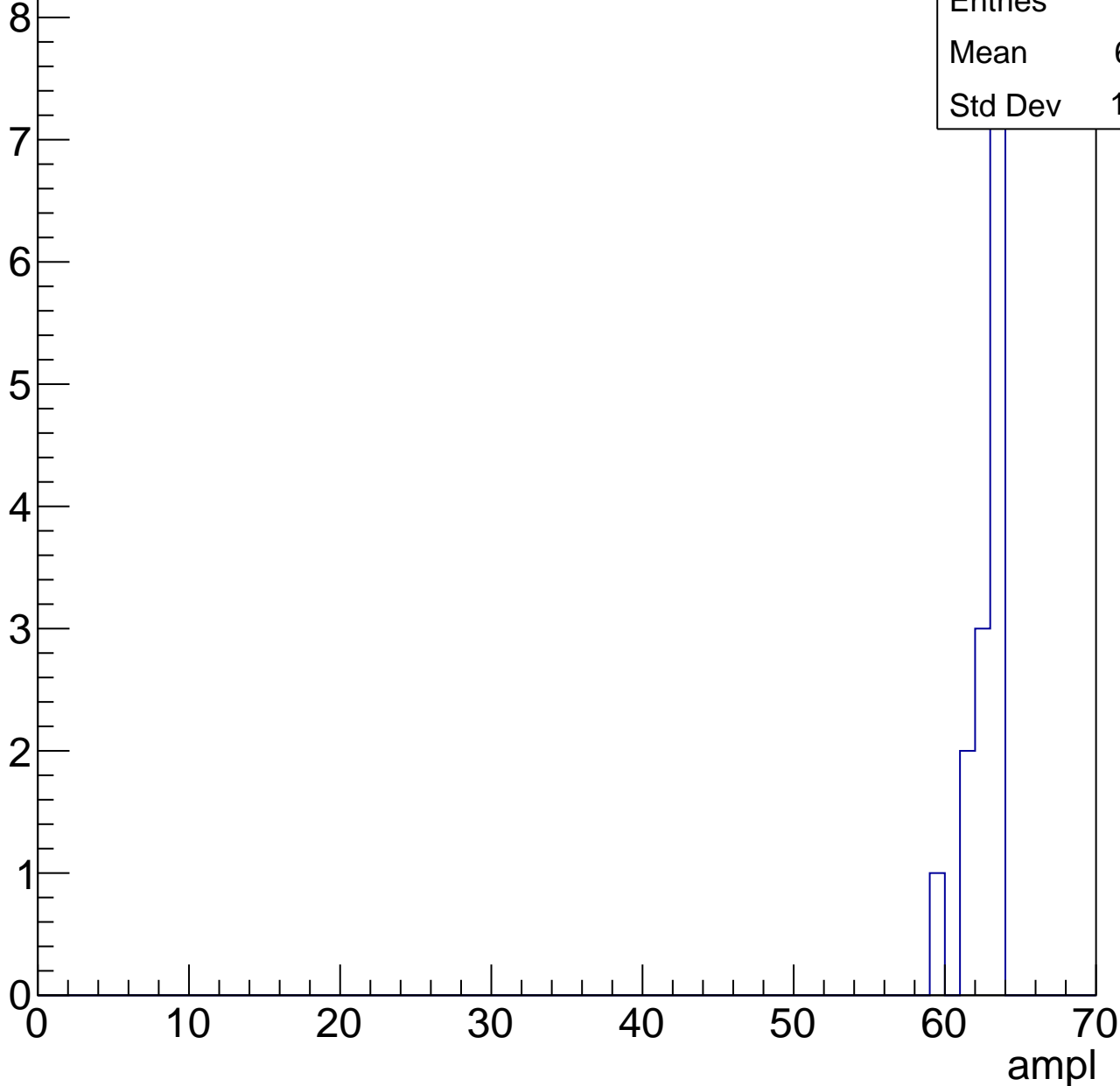


# B0L101S, U16-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 62.21 |
| Std Dev | 1.145 |





# B0L101S, U16-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 11.5 |
| Std Dev | 11.5 |

# B0L101S, U16-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 29.95 |
| Std Dev | 3.219 |

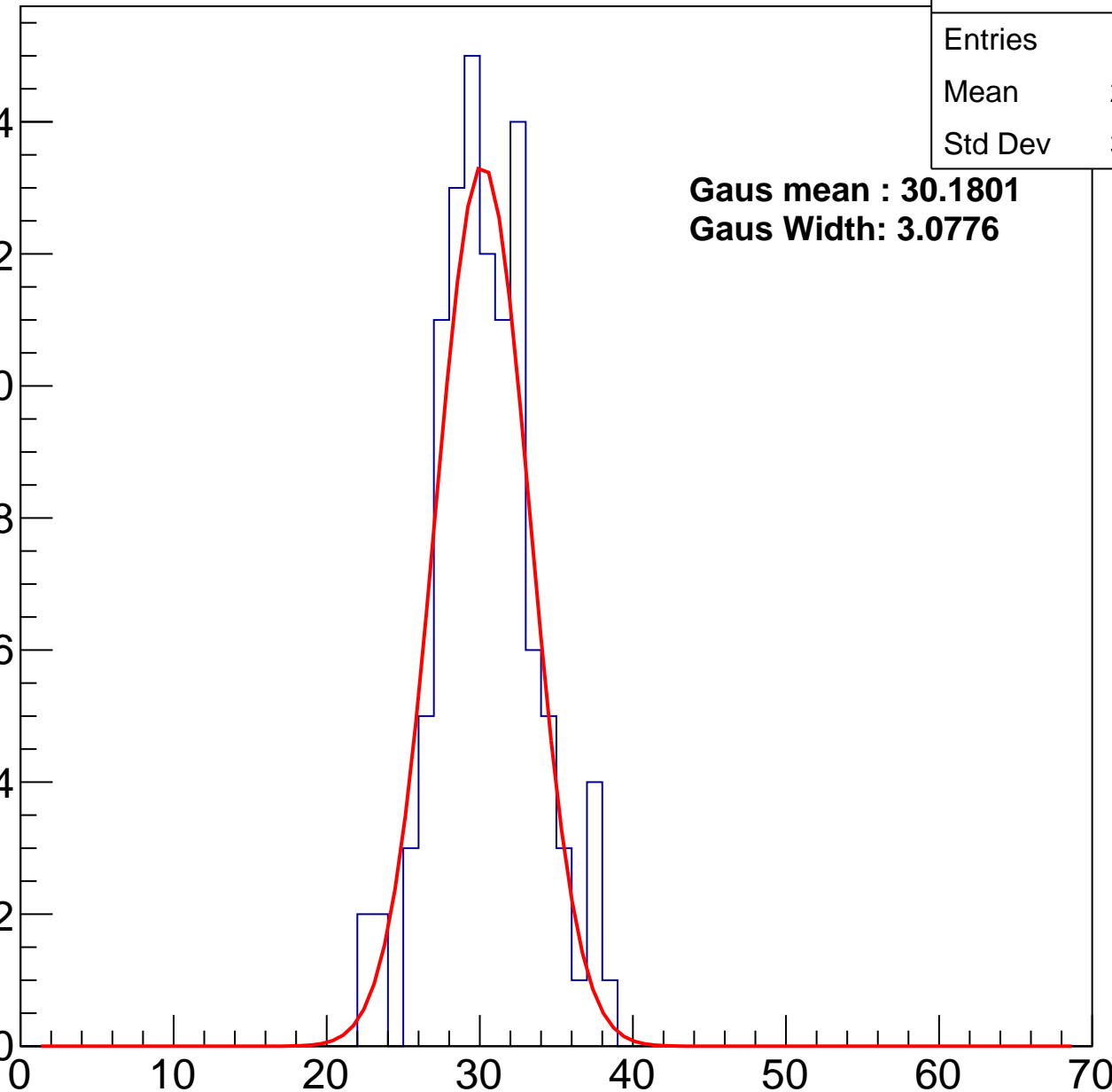
**Gaus mean : 30.1801**

**Gaus Width: 3.0776**

Entry

14  
12  
10  
8  
6  
4  
2  
0

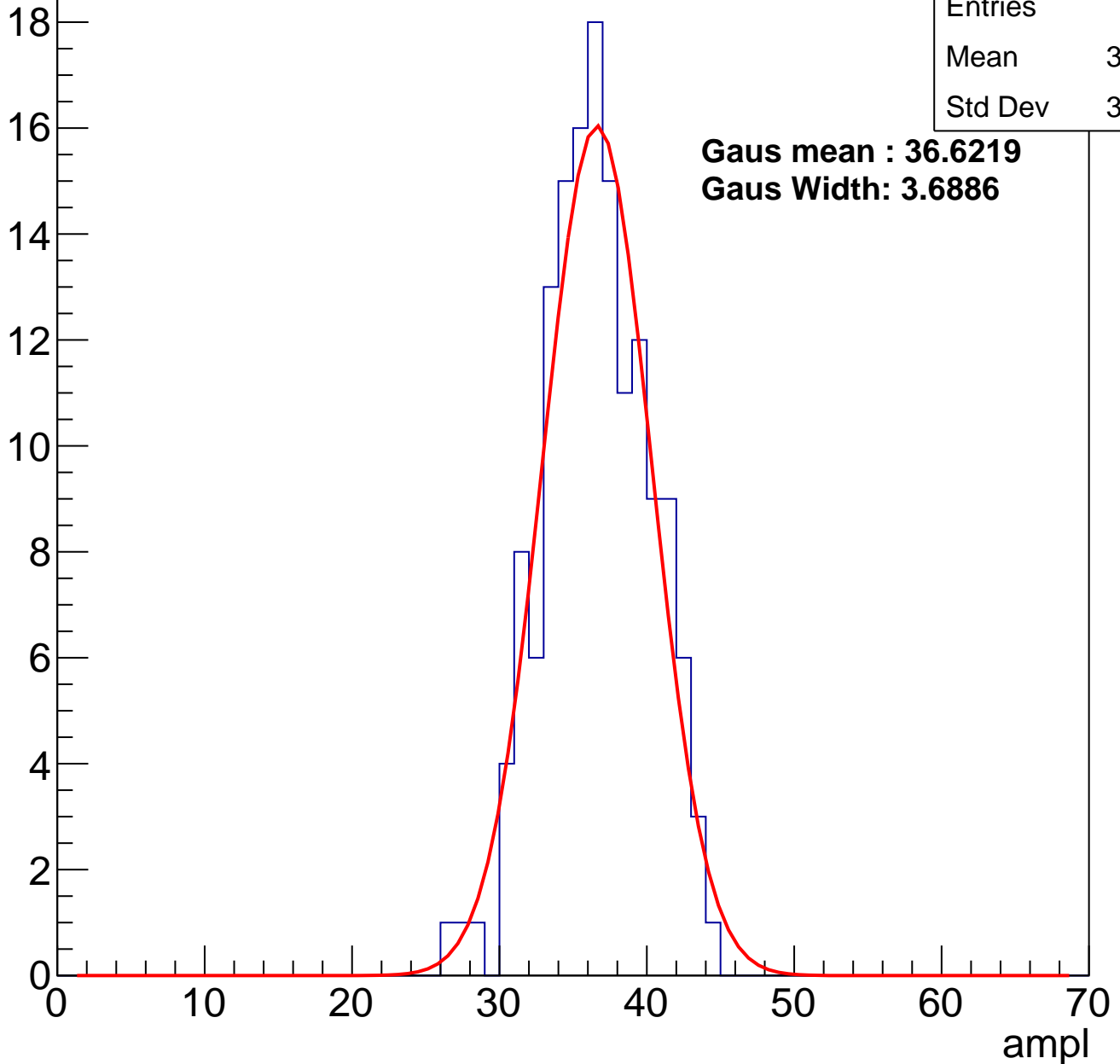
ampl



# B0L101S, U16-ch73, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

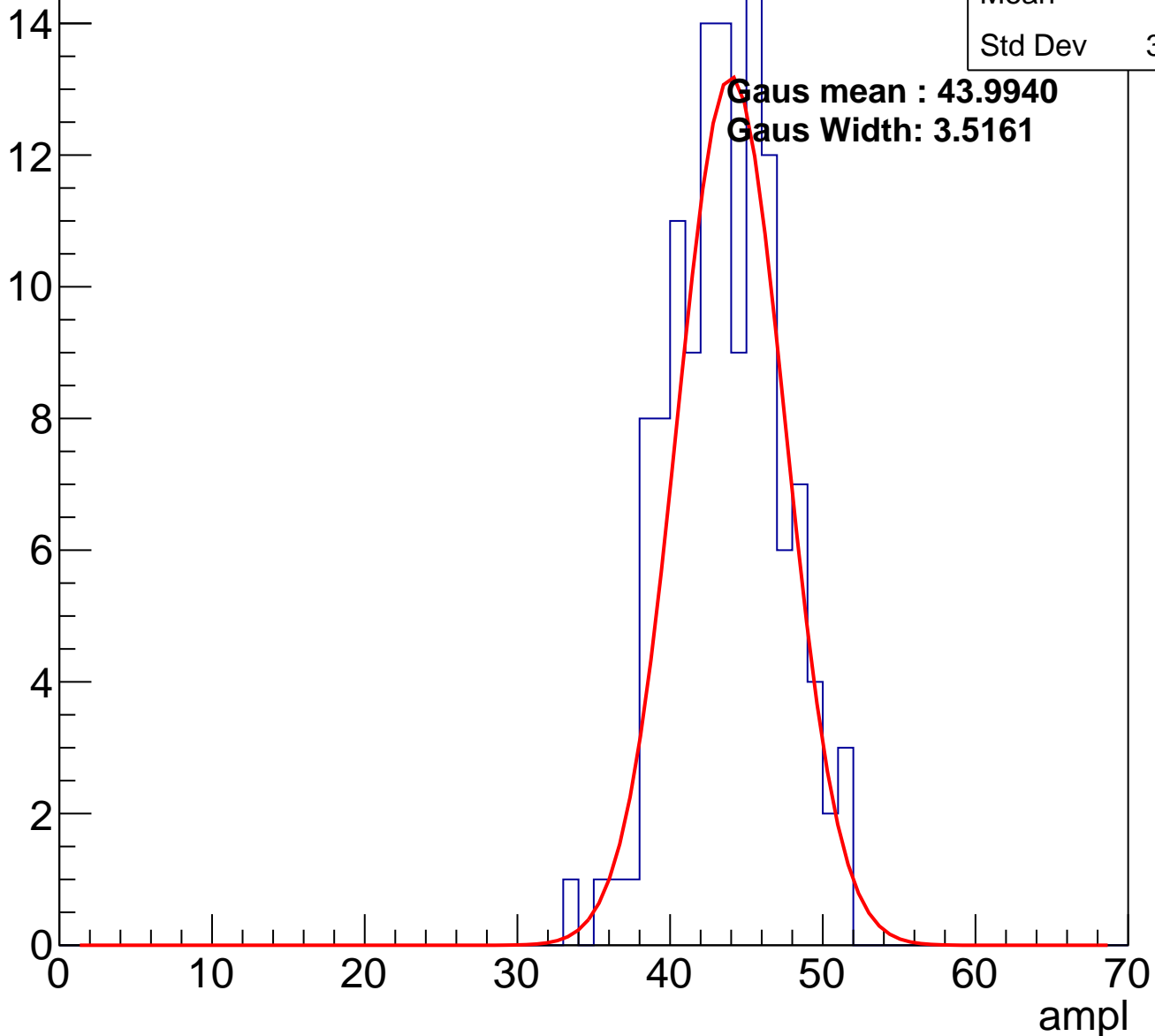
Entry



# B0L101S, U16-ch73, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

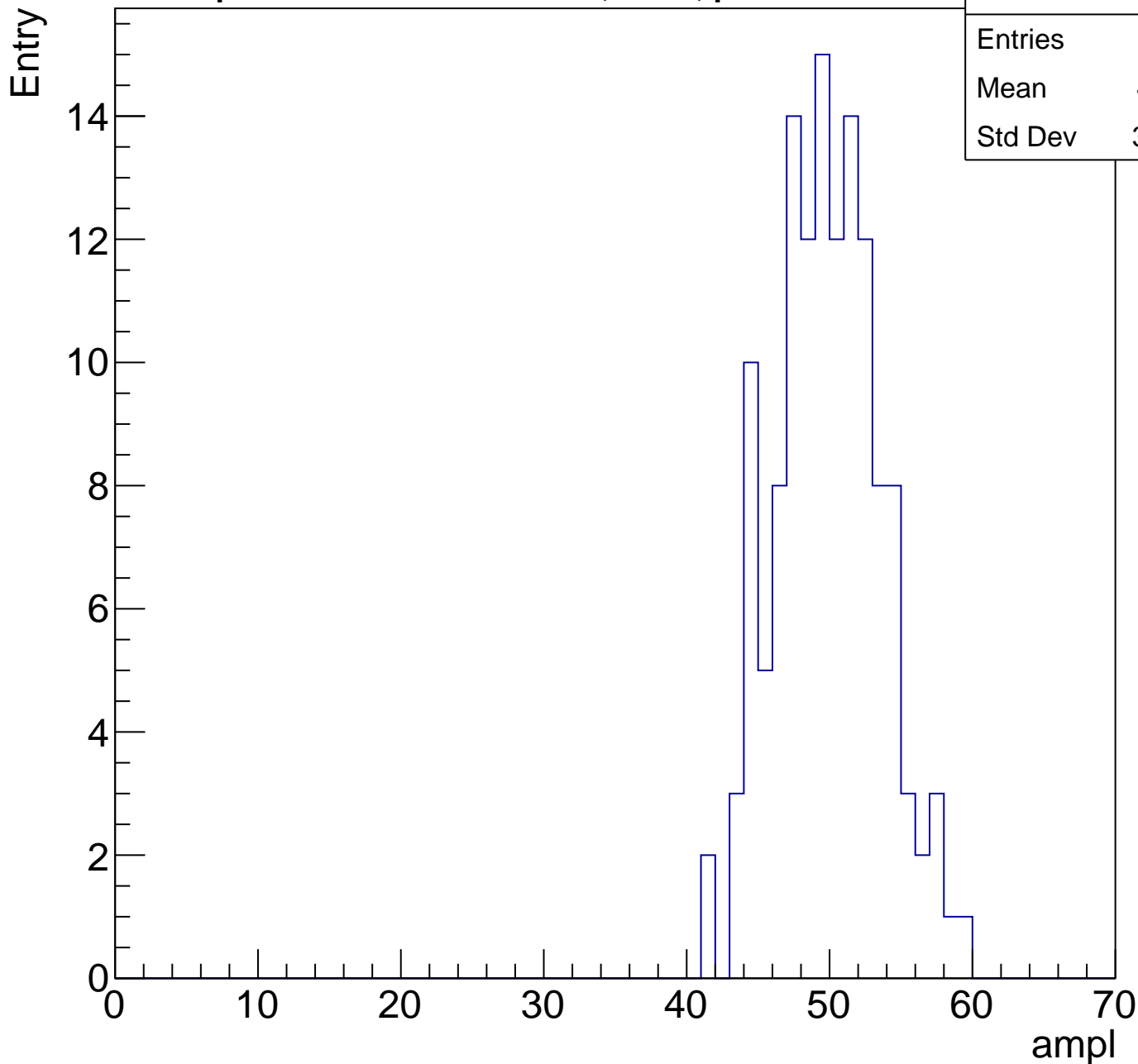
Entry



# B0L101S, U16-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 49.41 |
| Std Dev | 3.644 |



# B0L101S, U16-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 56.01 |
| Std Dev | 5.857 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch73, adc5

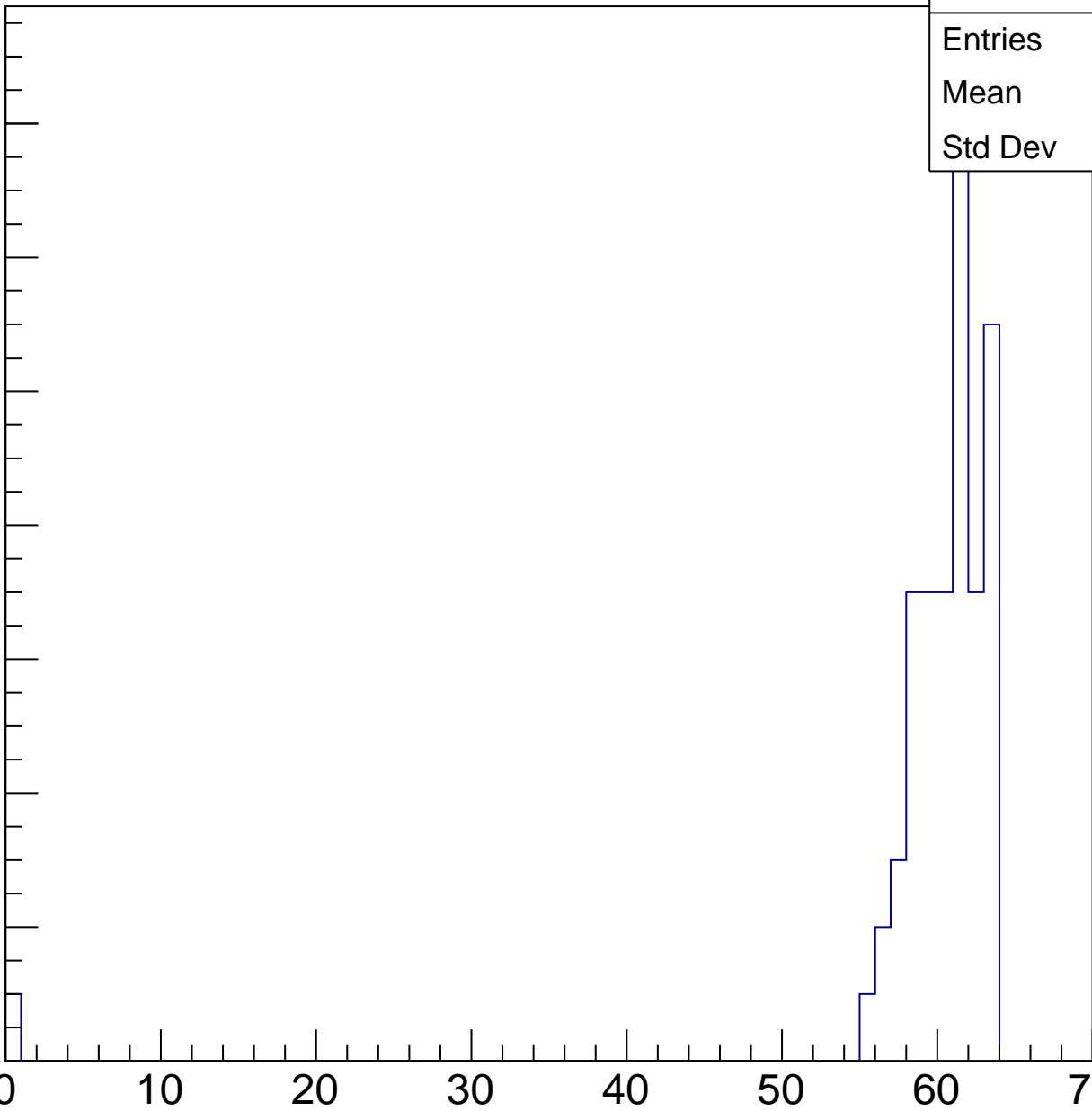
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 59.33 |
| Std Dev | 7.927 |

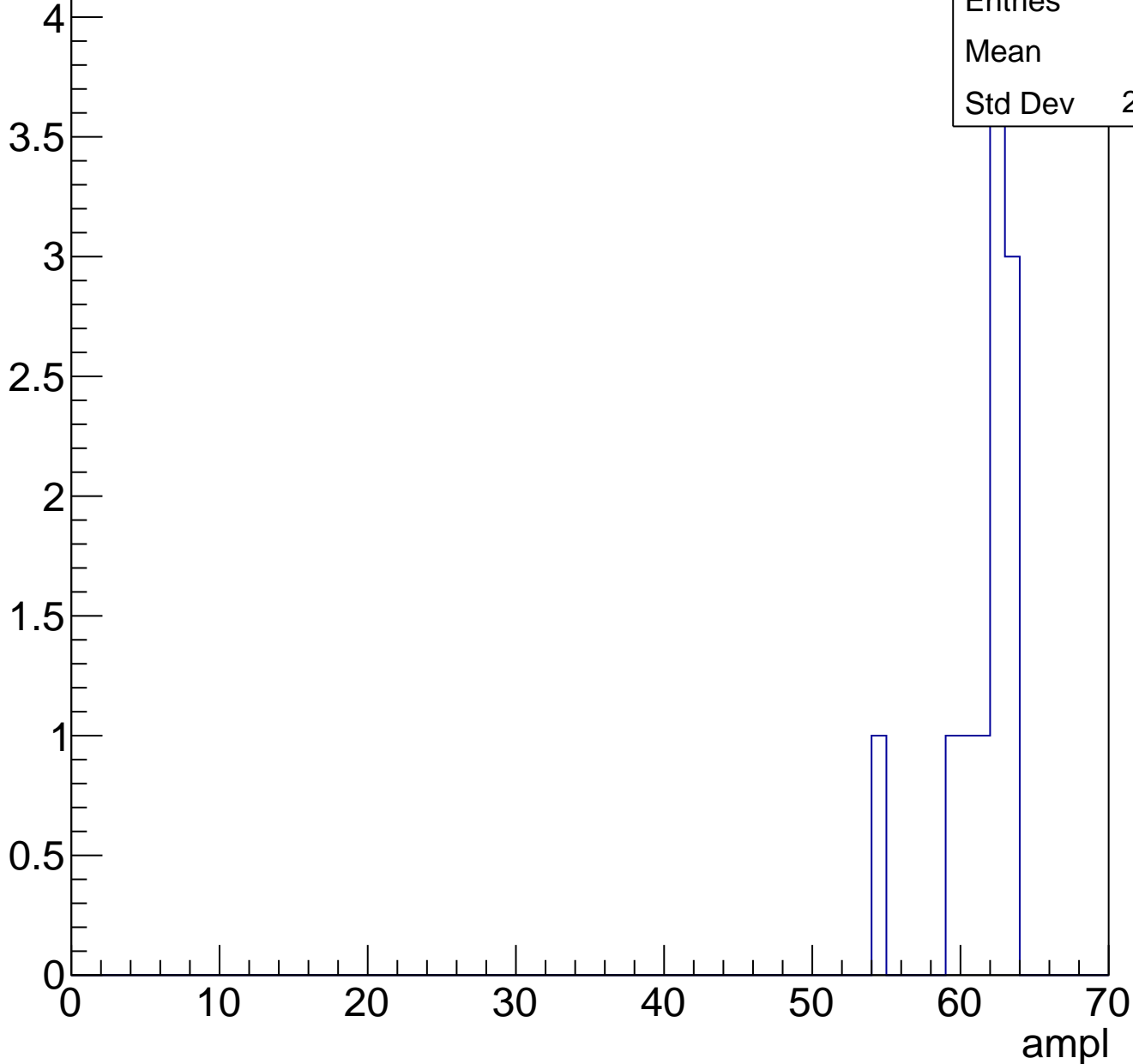
ampl



# B0L101S, U16-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch74, adc0

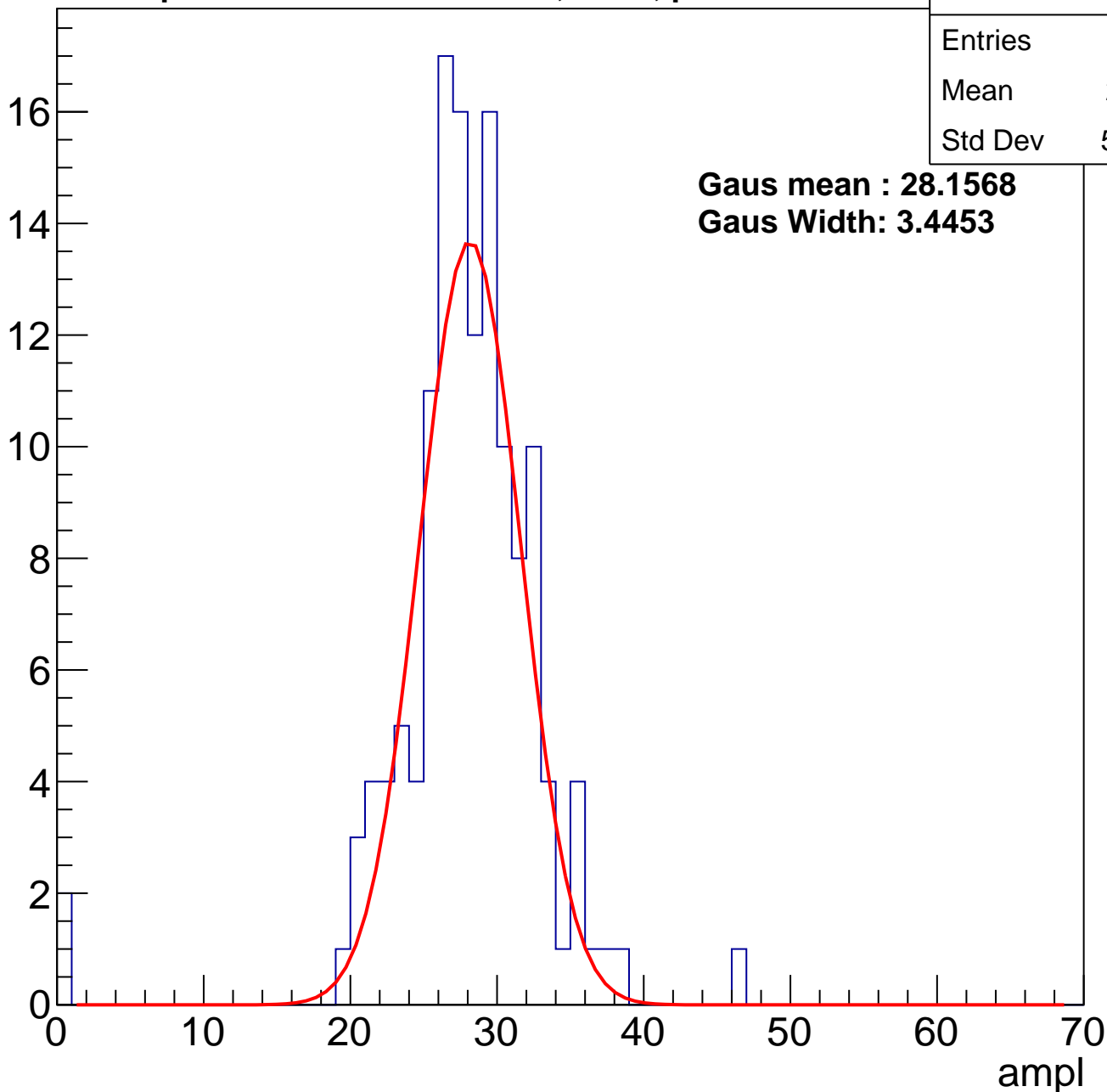
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 27.51 |
| Std Dev | 5.223 |

**Gaus mean : 28.1568**

**Gaus Width: 3.4453**

Entry



# B0L101S, U16-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

34.39

Std Dev

3.644

**Gaus mean : 35.0186**

**Gaus Width: 3.6451**

0

10

20

30

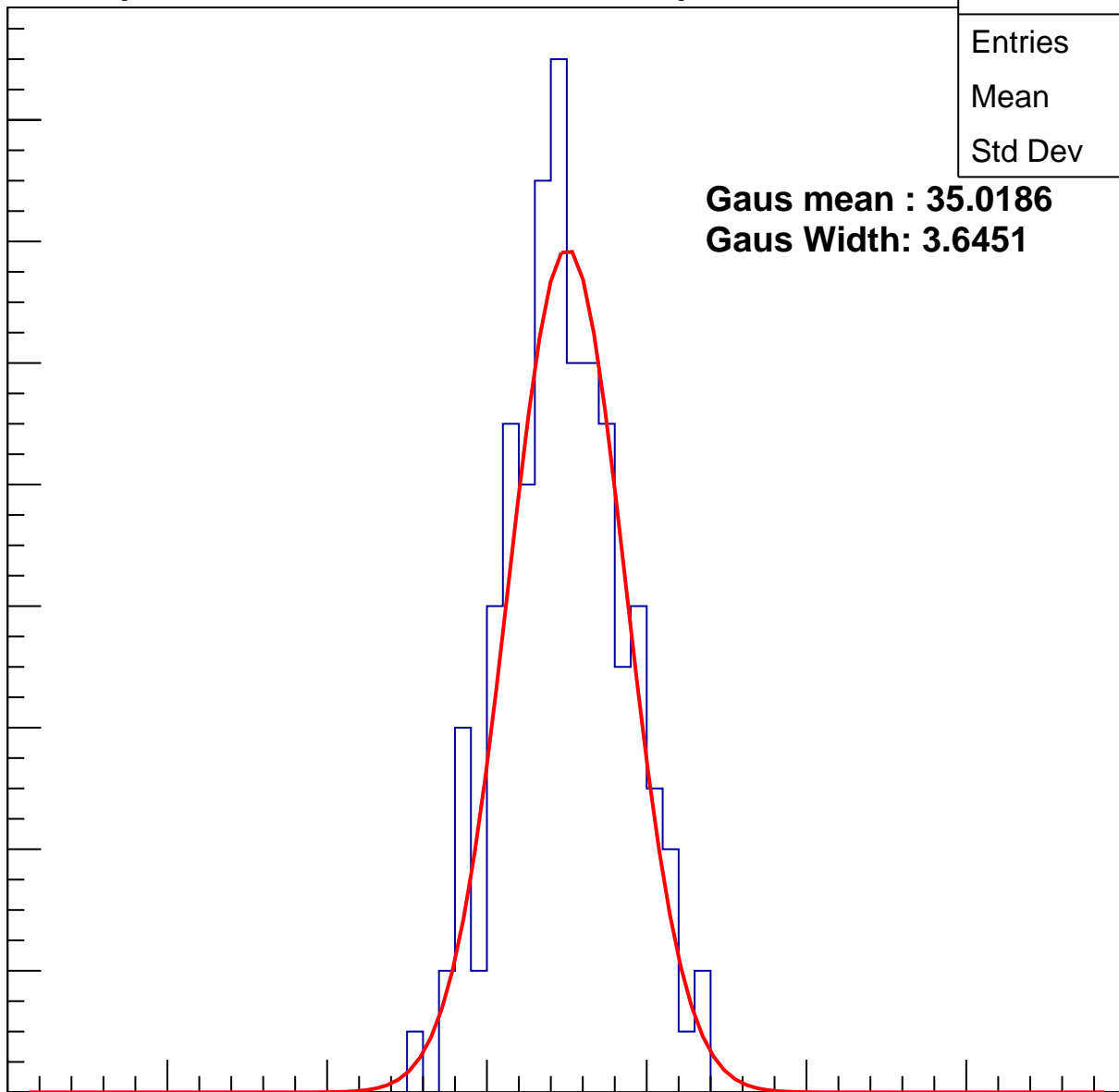
40

50

60

70

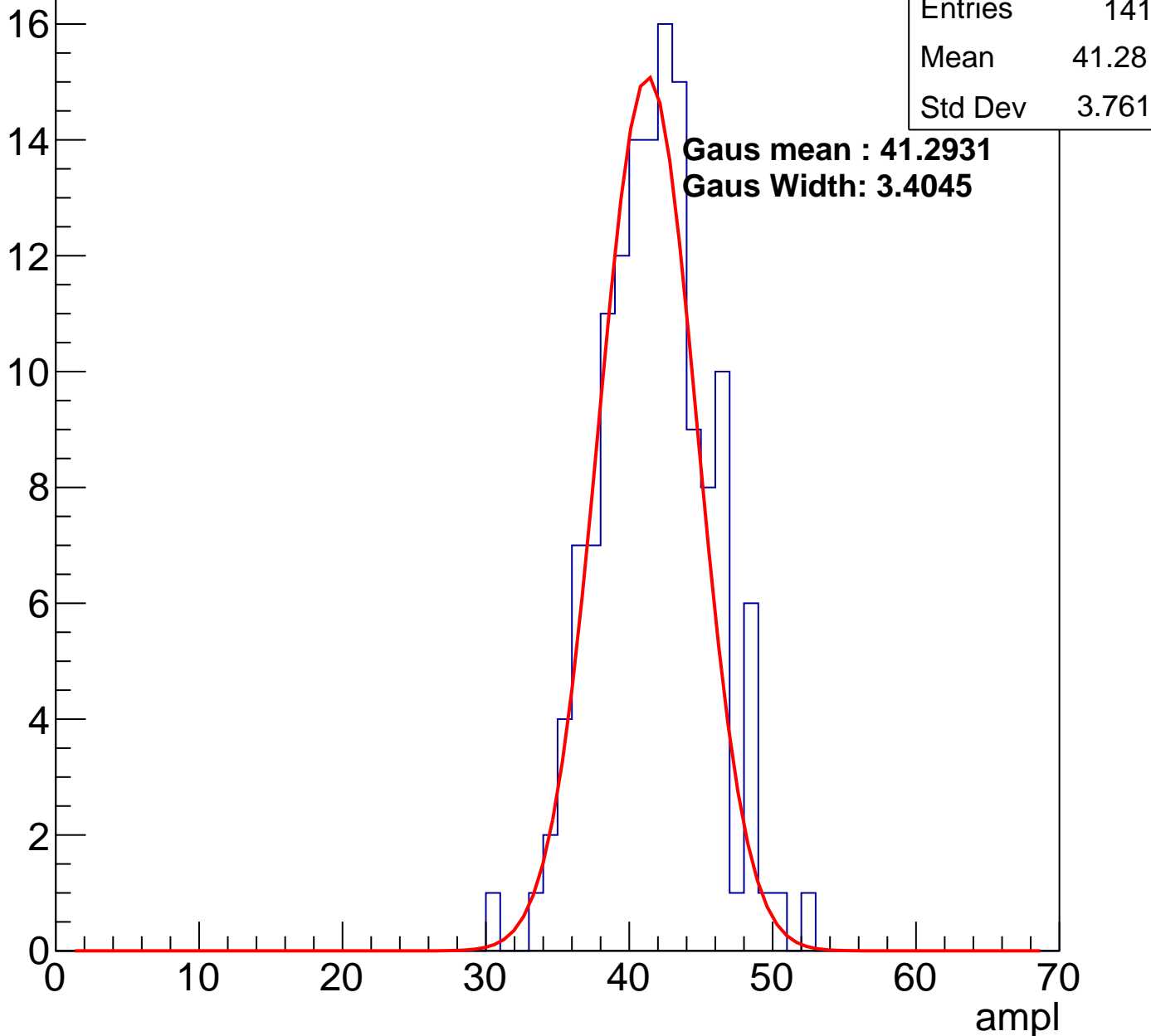
ampl



# B0L101S, U16-ch74, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

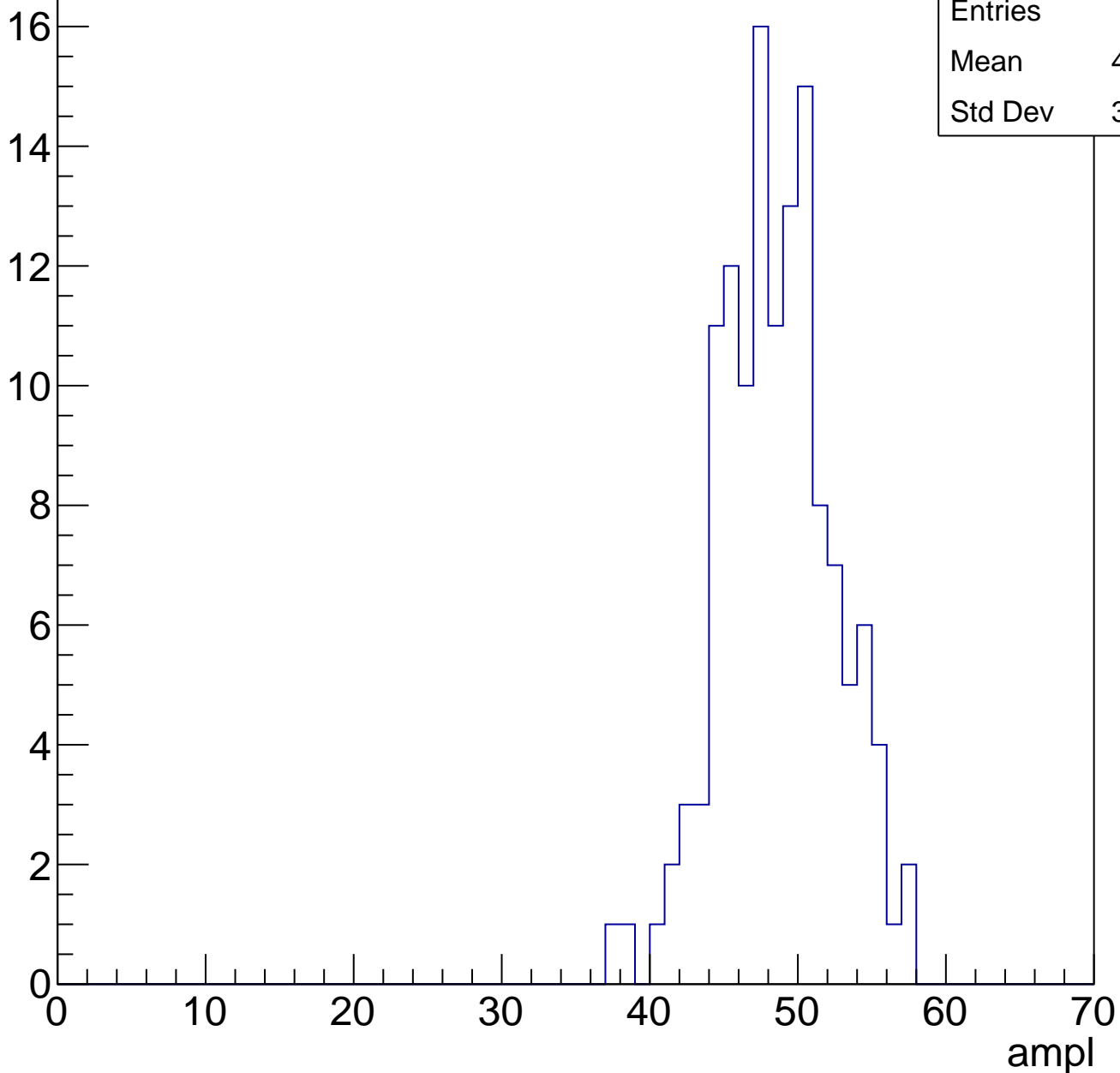
Entry



# B0L101S, U16-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 48.14 |
| Std Dev | 3.817 |

# B0L101S, U16-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 112   |
| Mean    | 53.92 |
| Std Dev | 3.314 |

Entry

12

10

8

6

4

2

0

0

10

20

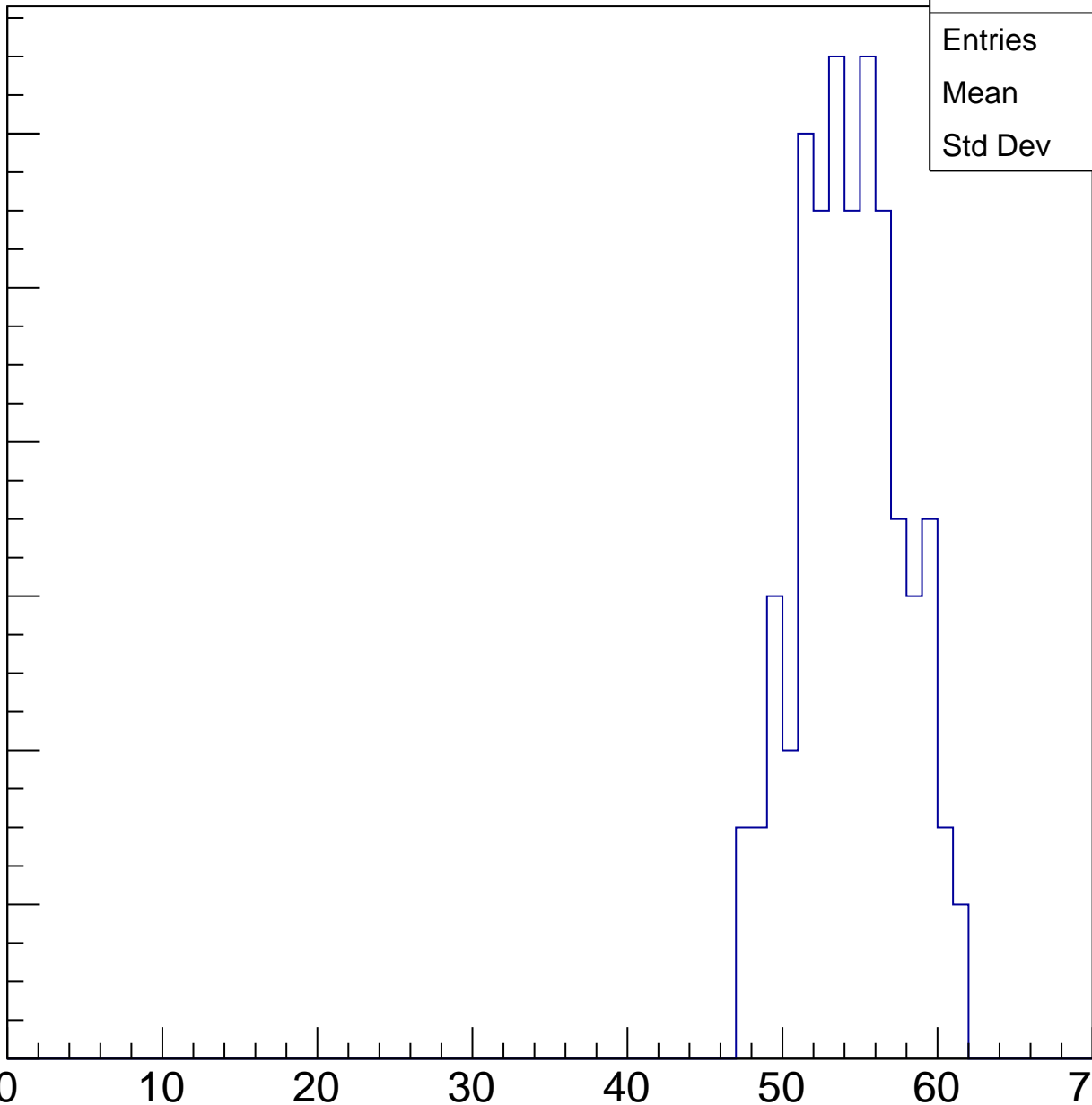
30

40

50

60

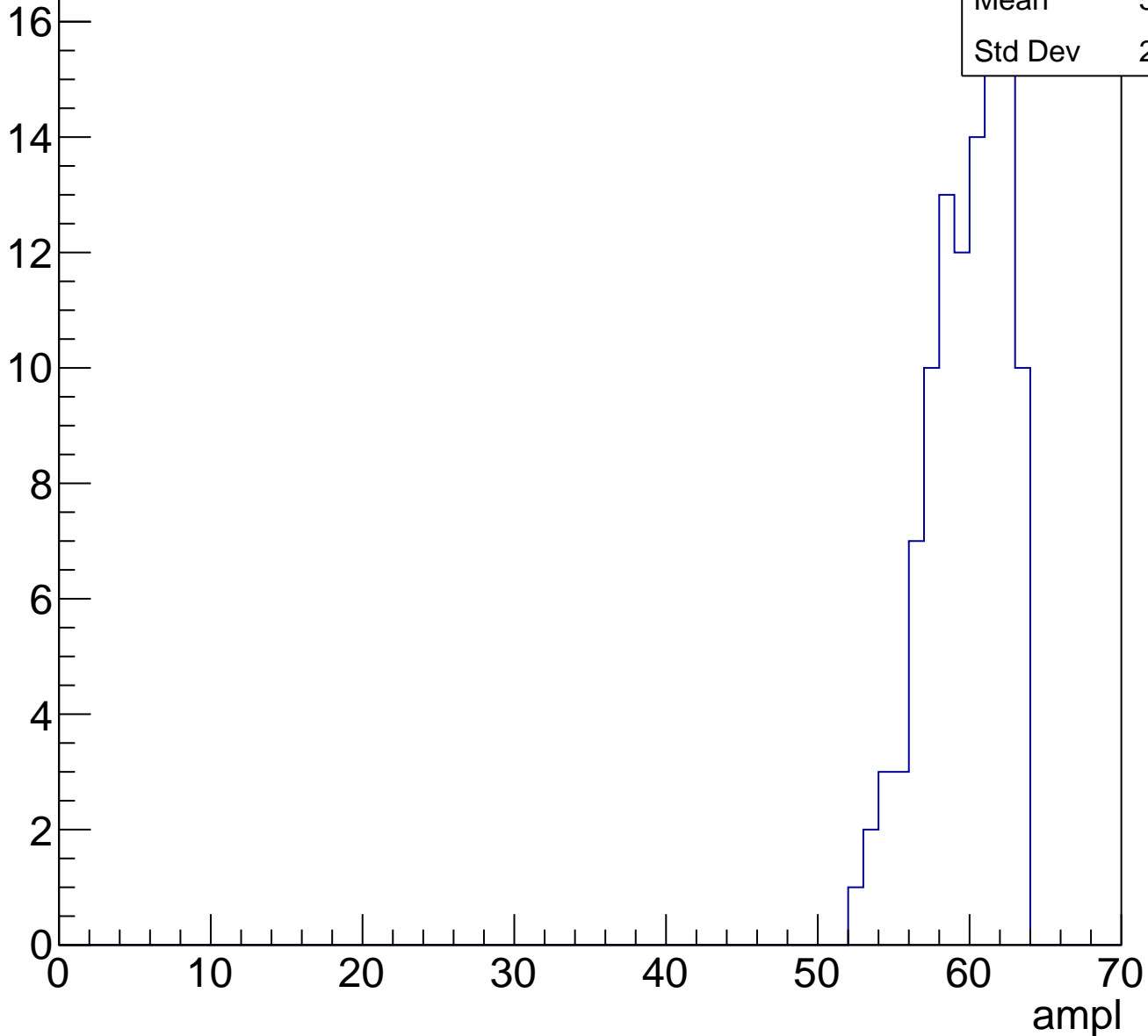
ampl



# B0L101S, U16-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

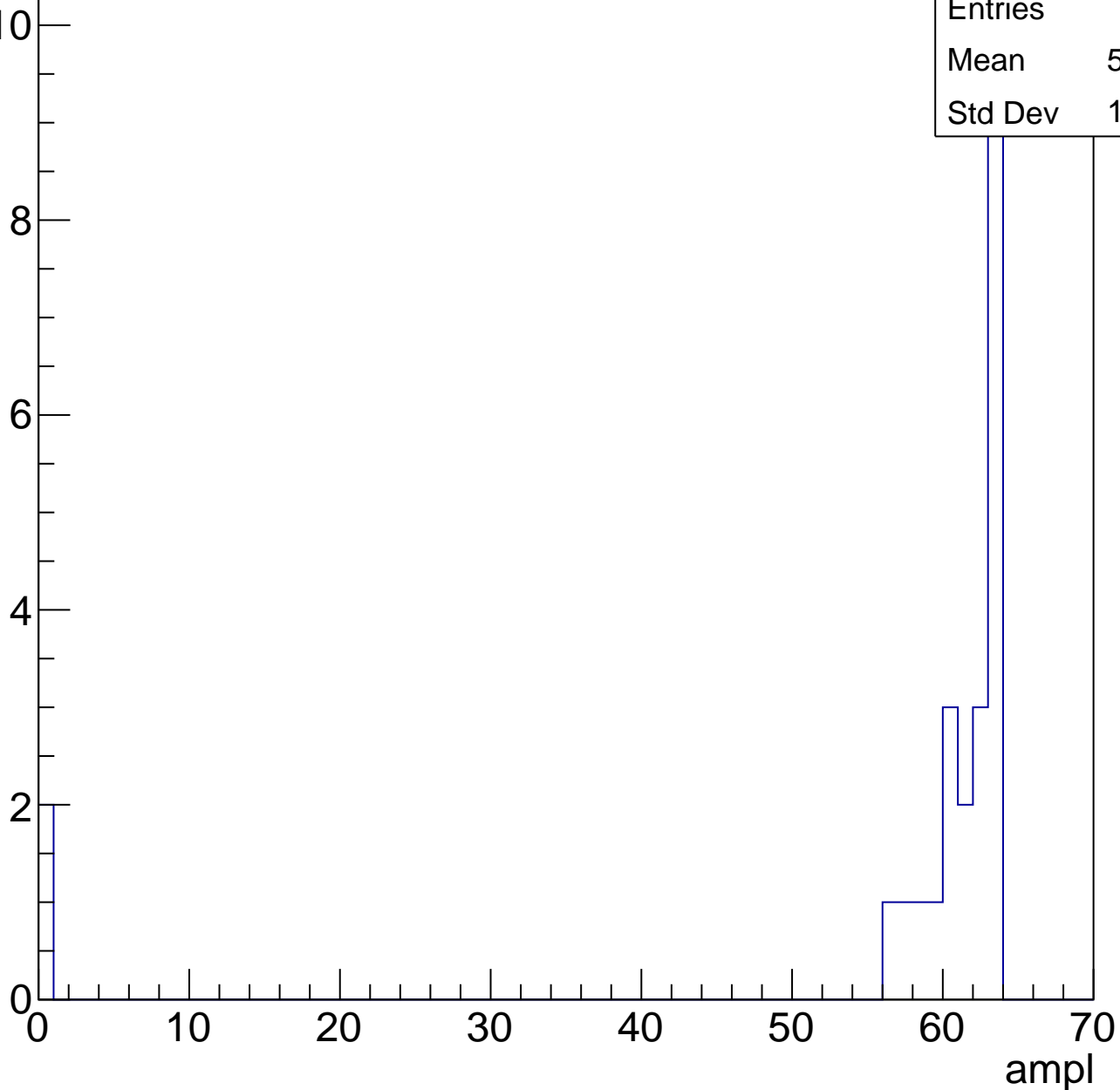


# B0L101S, U16-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 24    |
| Mean    | 56.17 |
| Std Dev | 17.06 |





# B0L101S, U16-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 27.17 |
| Std Dev | 4.71  |

**Gaus mean : 28.1149**

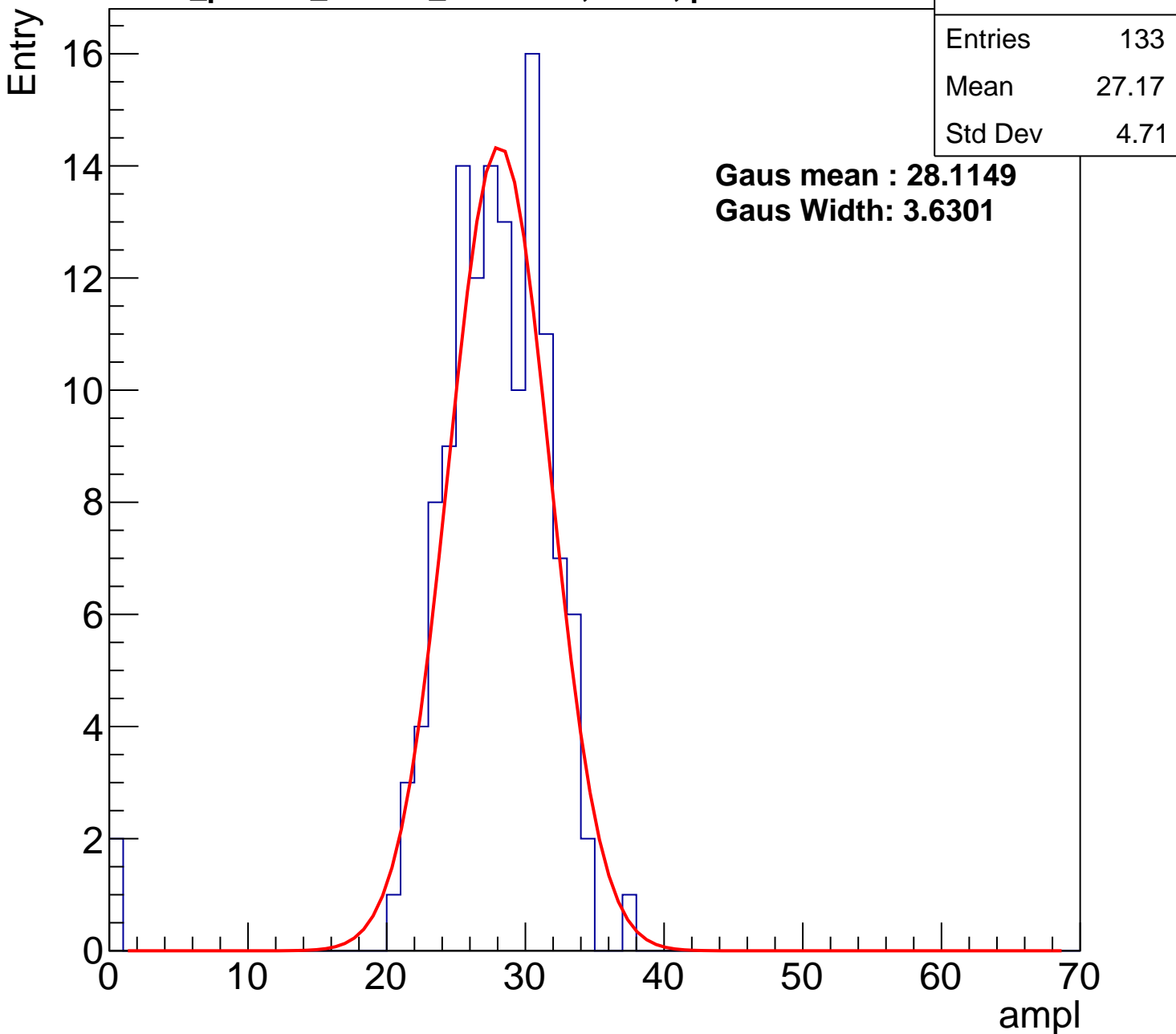
**Gaus Width: 3.6301**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 34.91 |
| Std Dev | 3.871 |

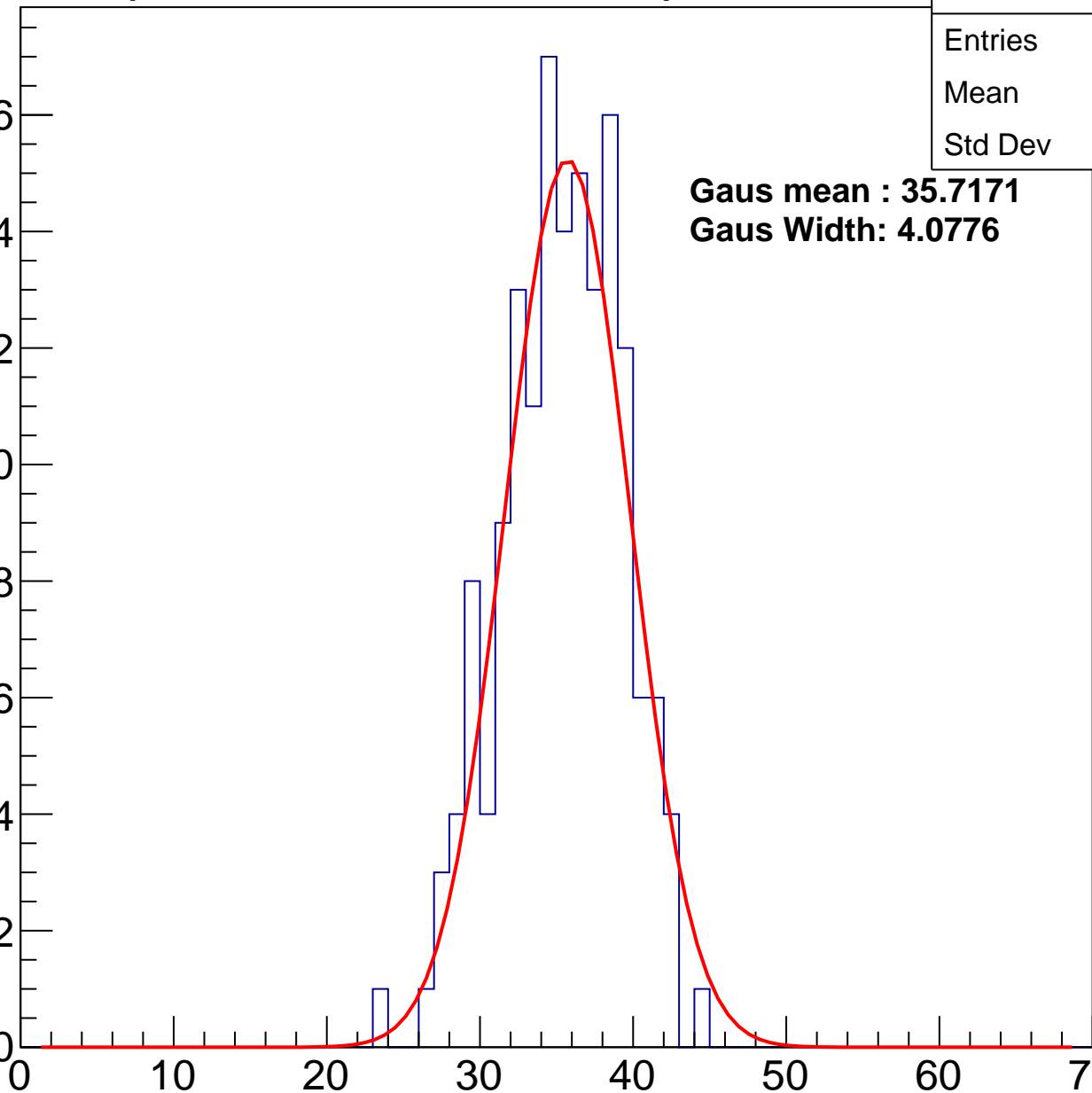
**Gaus mean : 35.7171**

**Gaus Width: 4.0776**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch75, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 41.06 |
| Std Dev | 3.611 |

**Gaus mean : 41.9049**

**Gaus Width: 3.5269**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

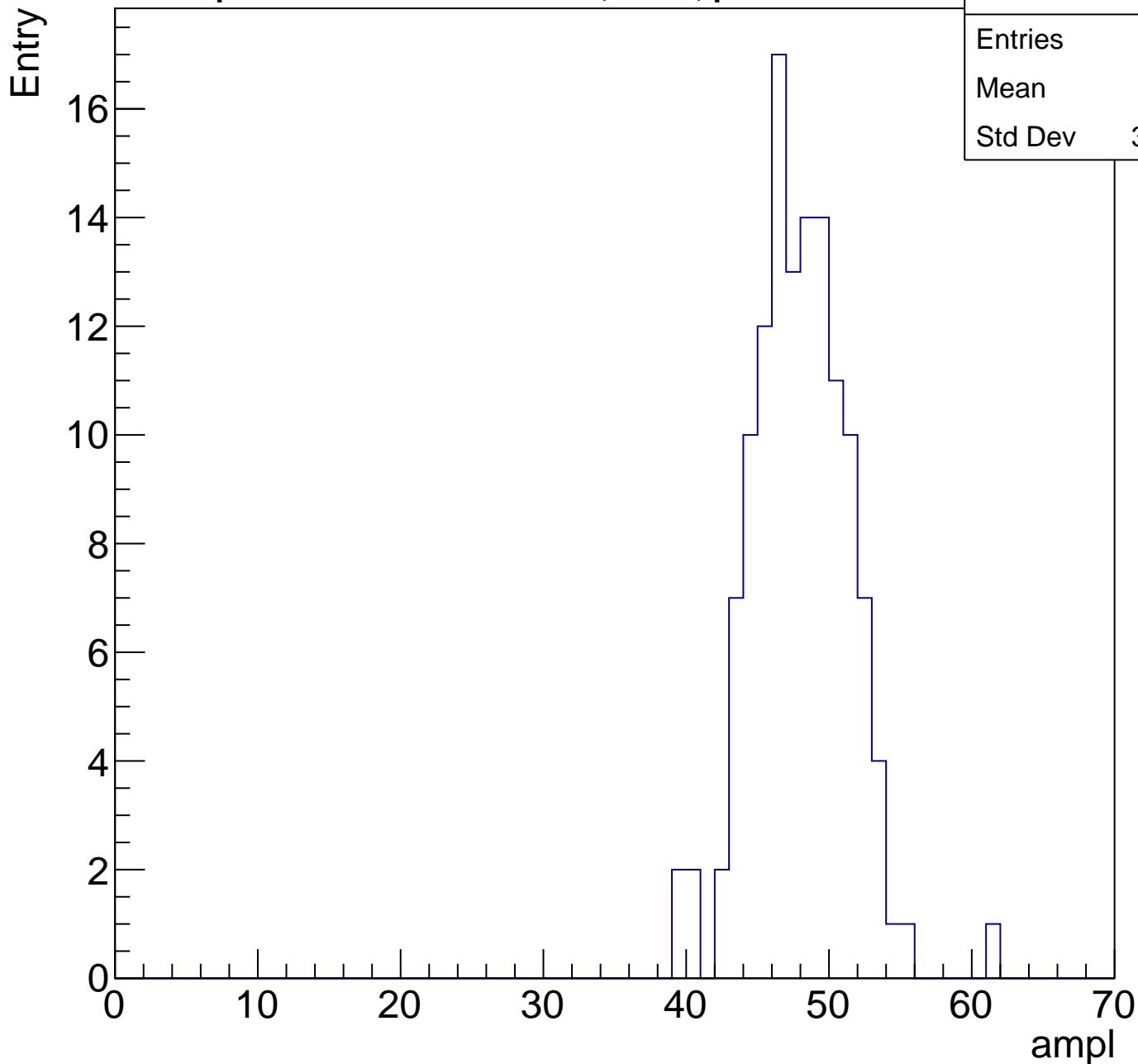
60

70

# B0L101S, U16-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

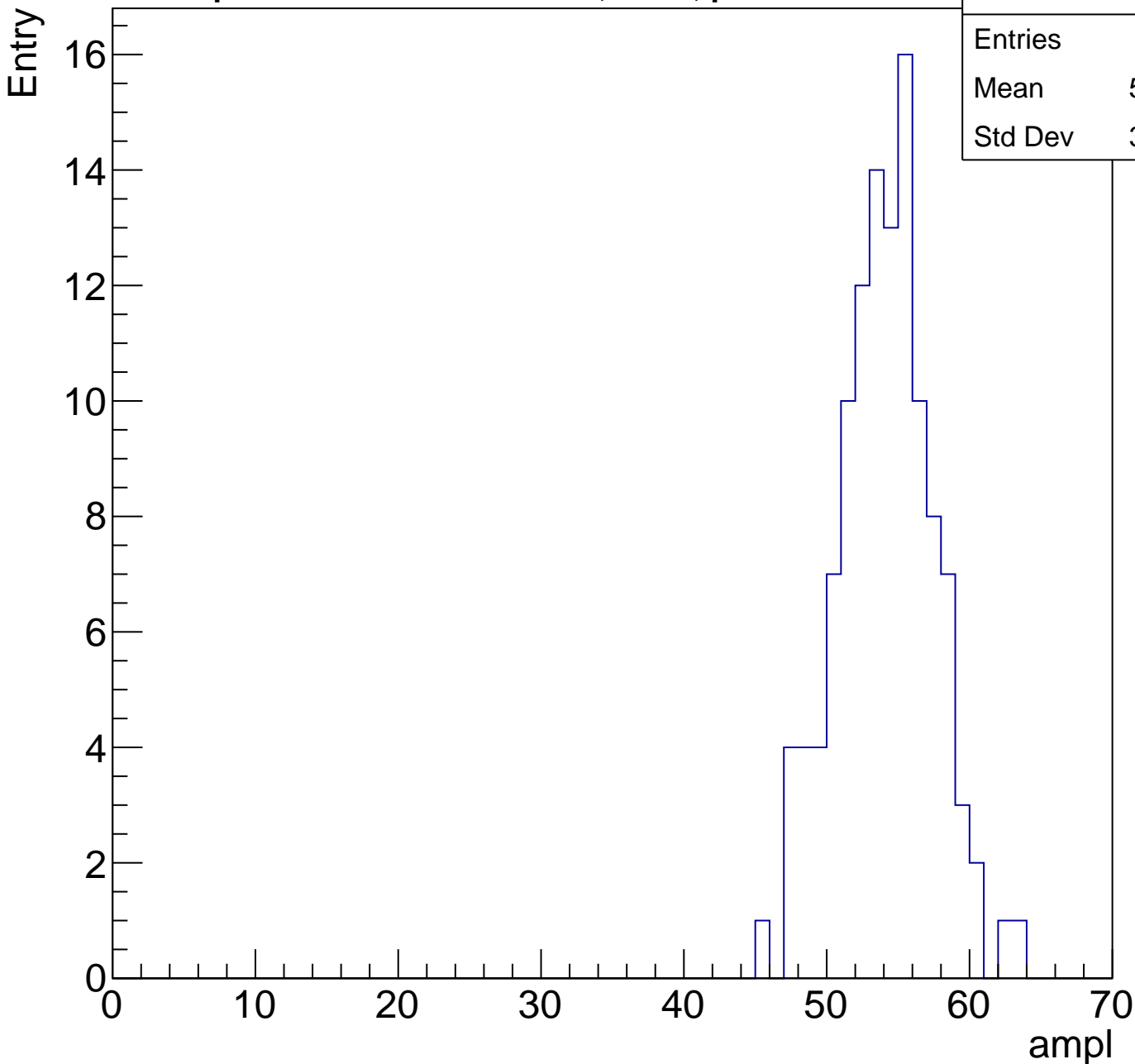
|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 47.5  |
| Std Dev | 3.387 |



# B0L101S, U16-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 53.62 |
| Std Dev | 3.333 |



# B0L101S, U16-ch75, adc5

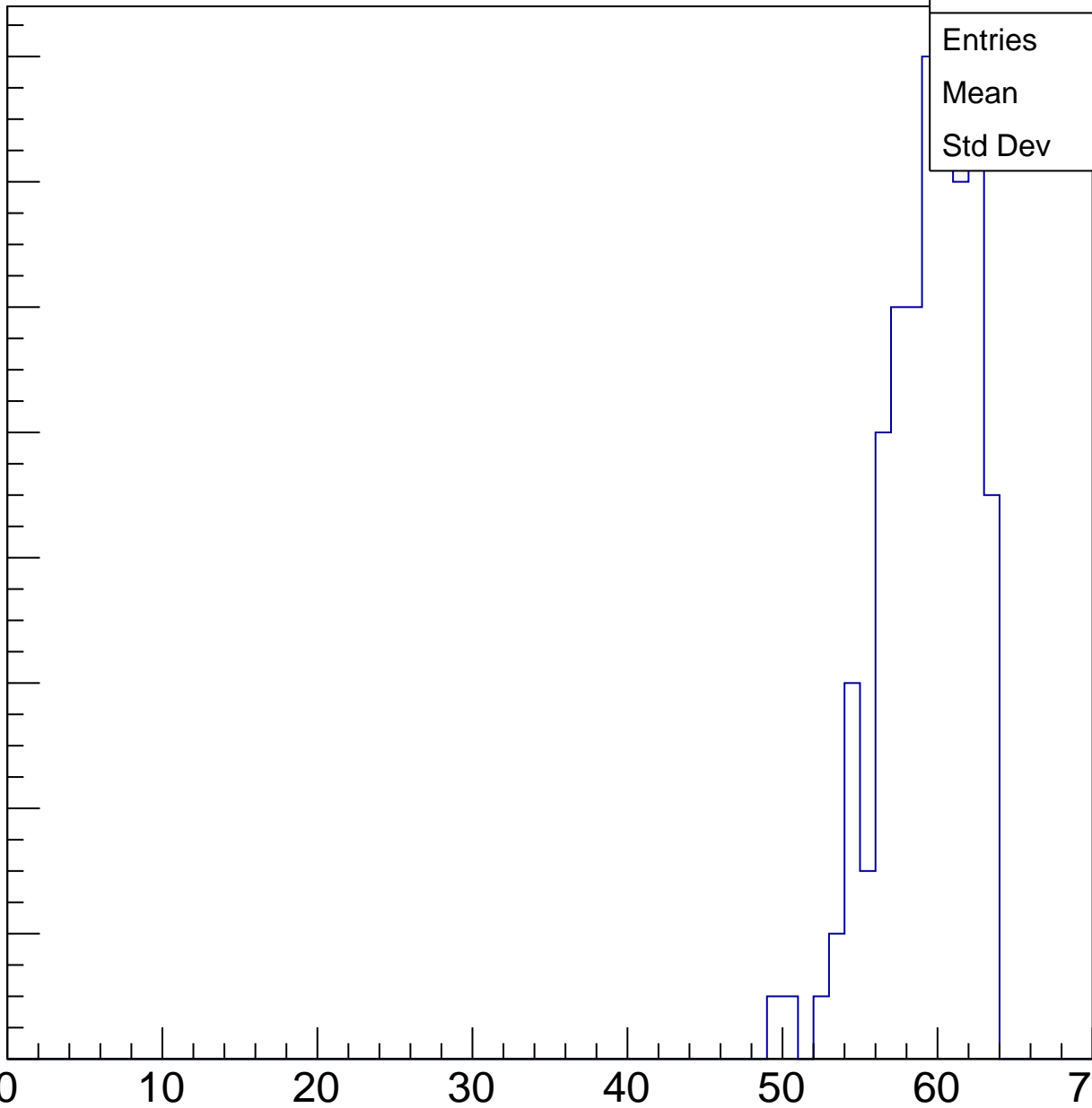
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 58.85 |
| Std Dev | 2.909 |

ampl



# B0L101S, U16-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 24    |
| Mean    | 56.58 |
| Std Dev | 17.12 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

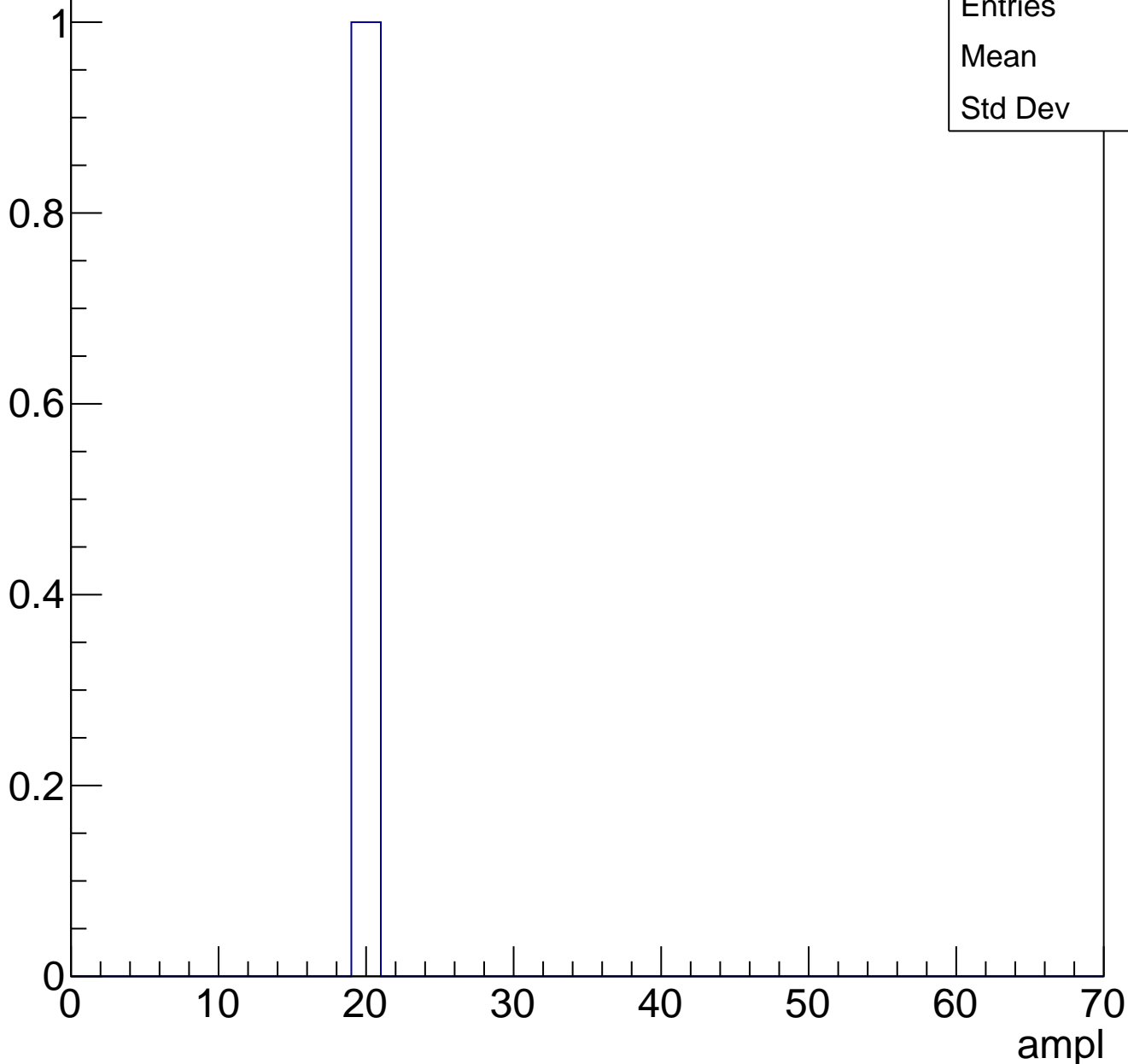
10



# B0L101S, U16-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch76, adc0

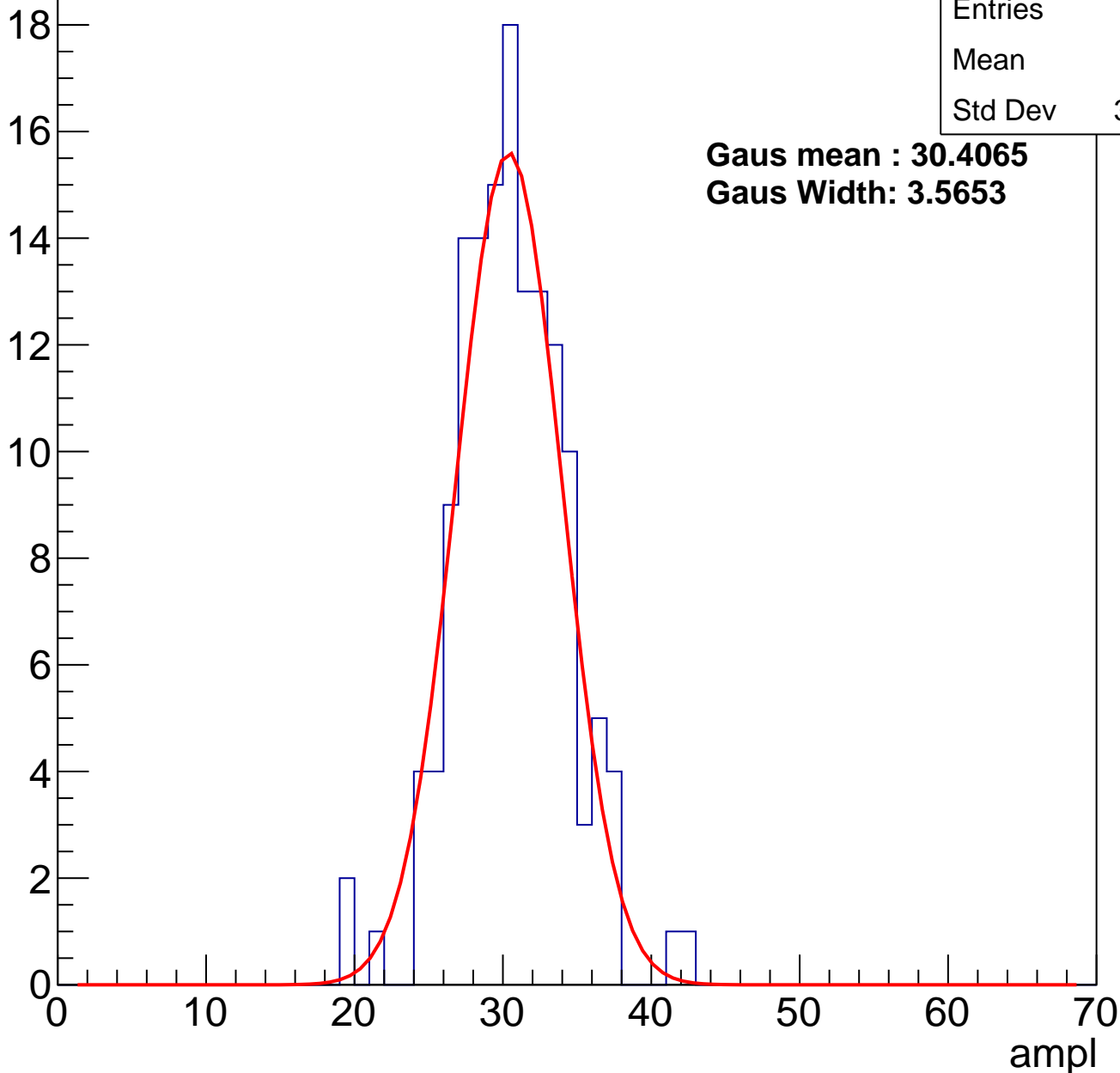
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 30.1  |
| Std Dev | 3.698 |

**Gaus mean : 30.4065**

**Gaus Width: 3.5653**

Entry



# B0L101S, U16-ch76, adc1

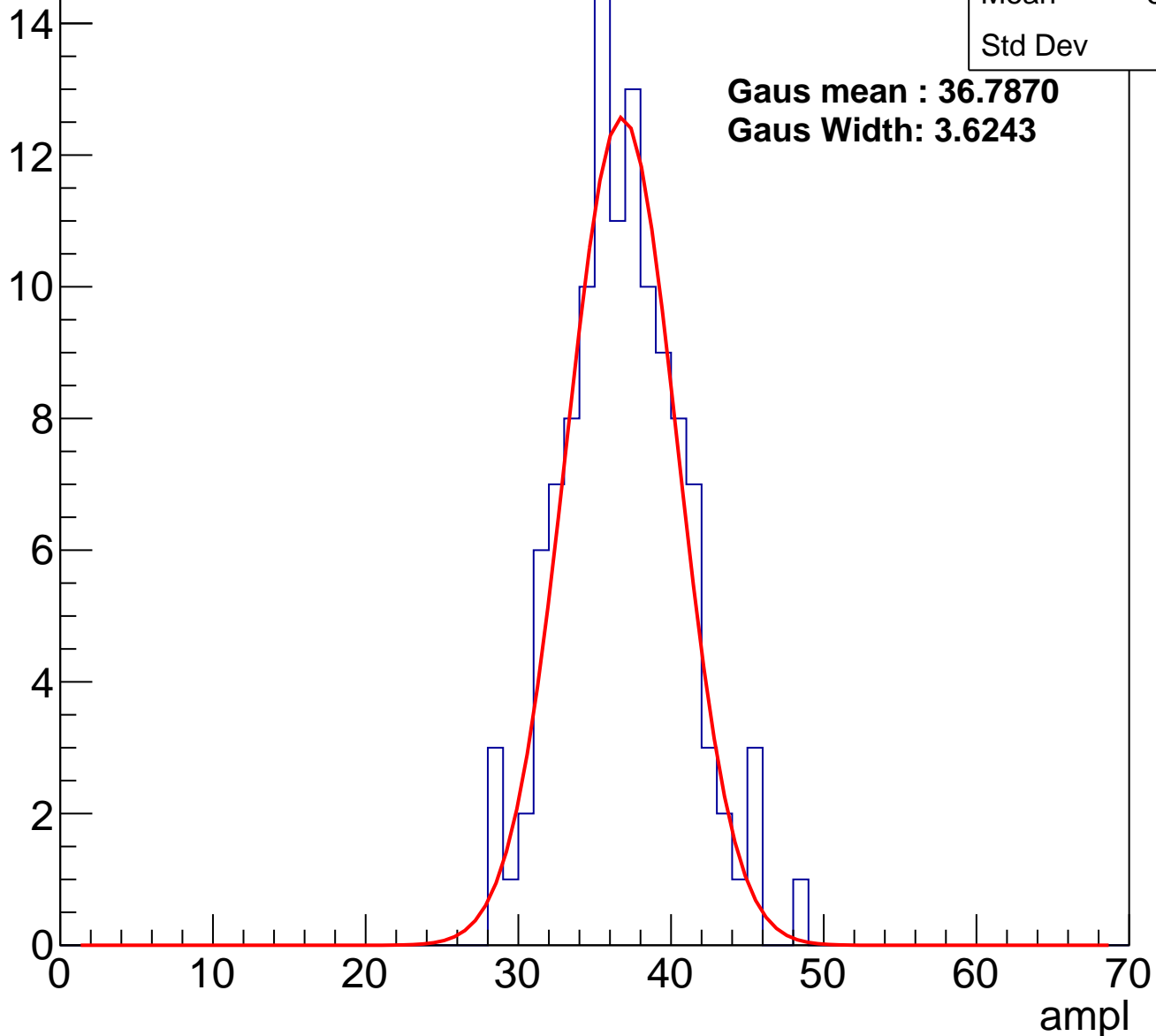
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 120   |
| Mean    | 36.38 |
| Std Dev | 3.85  |

**Gaus mean : 36.7870**

**Gaus Width: 3.6243**

Entry



# B0L101S, U16-ch76, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

134

Mean

42.61

Std Dev

3.765

Gaus mean : 42.8826

Gaus Width: 3.9812

0

10

20

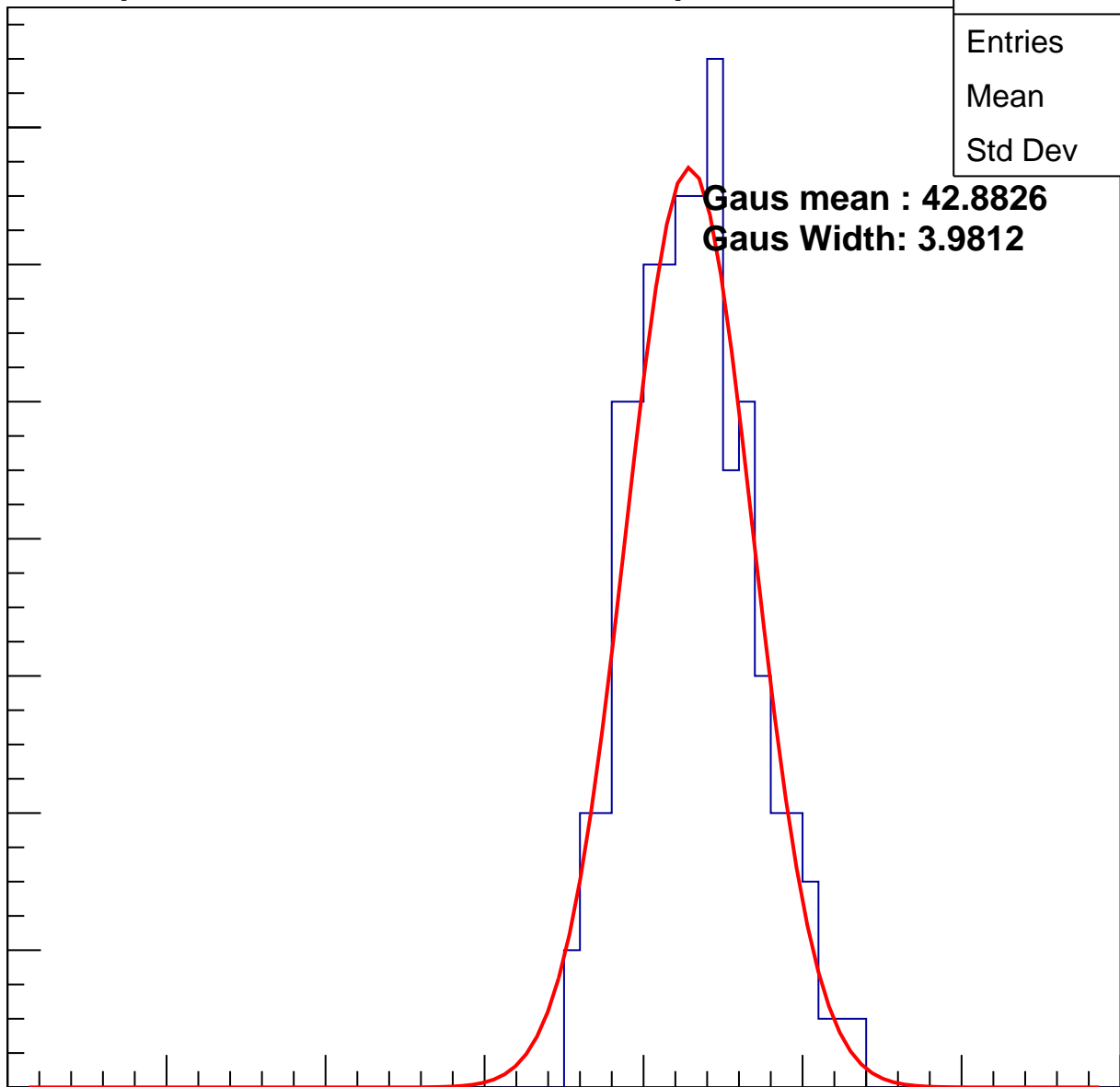
30

40

50

60

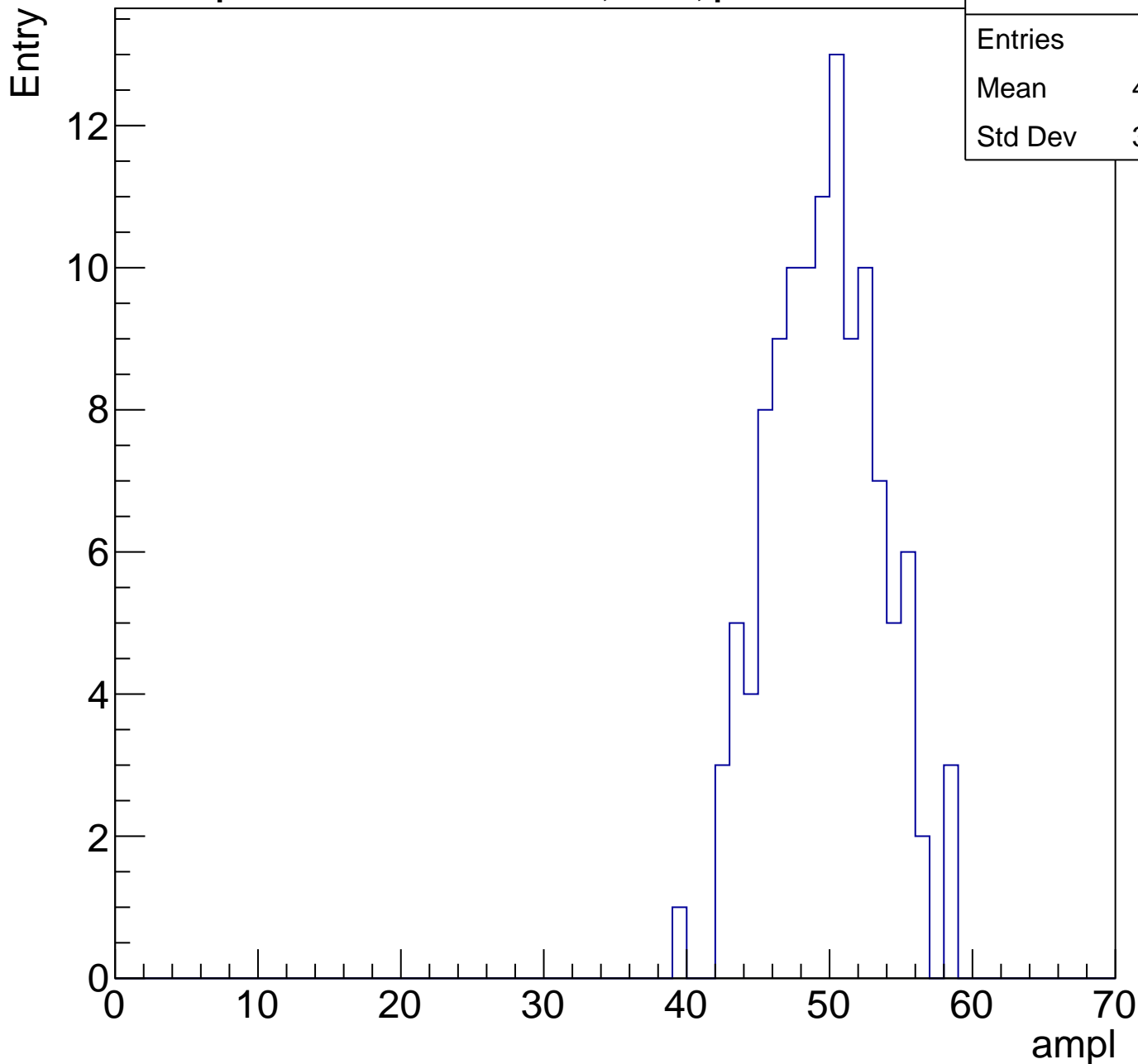
ampl



# B0L101S, U16-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

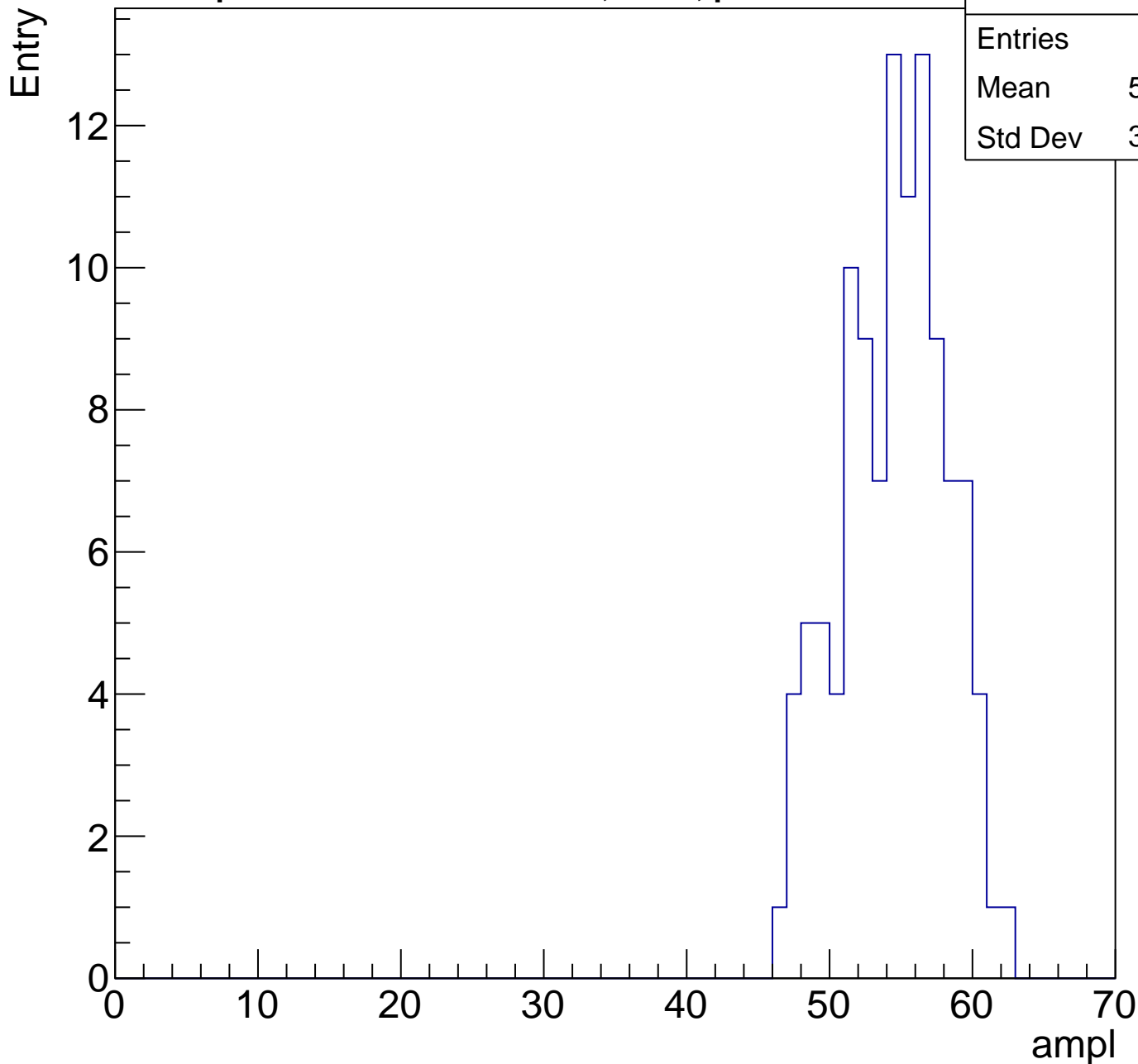
|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 49.18 |
| Std Dev | 3.845 |



# B0L101S, U16-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 54.04 |
| Std Dev | 3.614 |



# B0L101S, U16-ch76, adc5

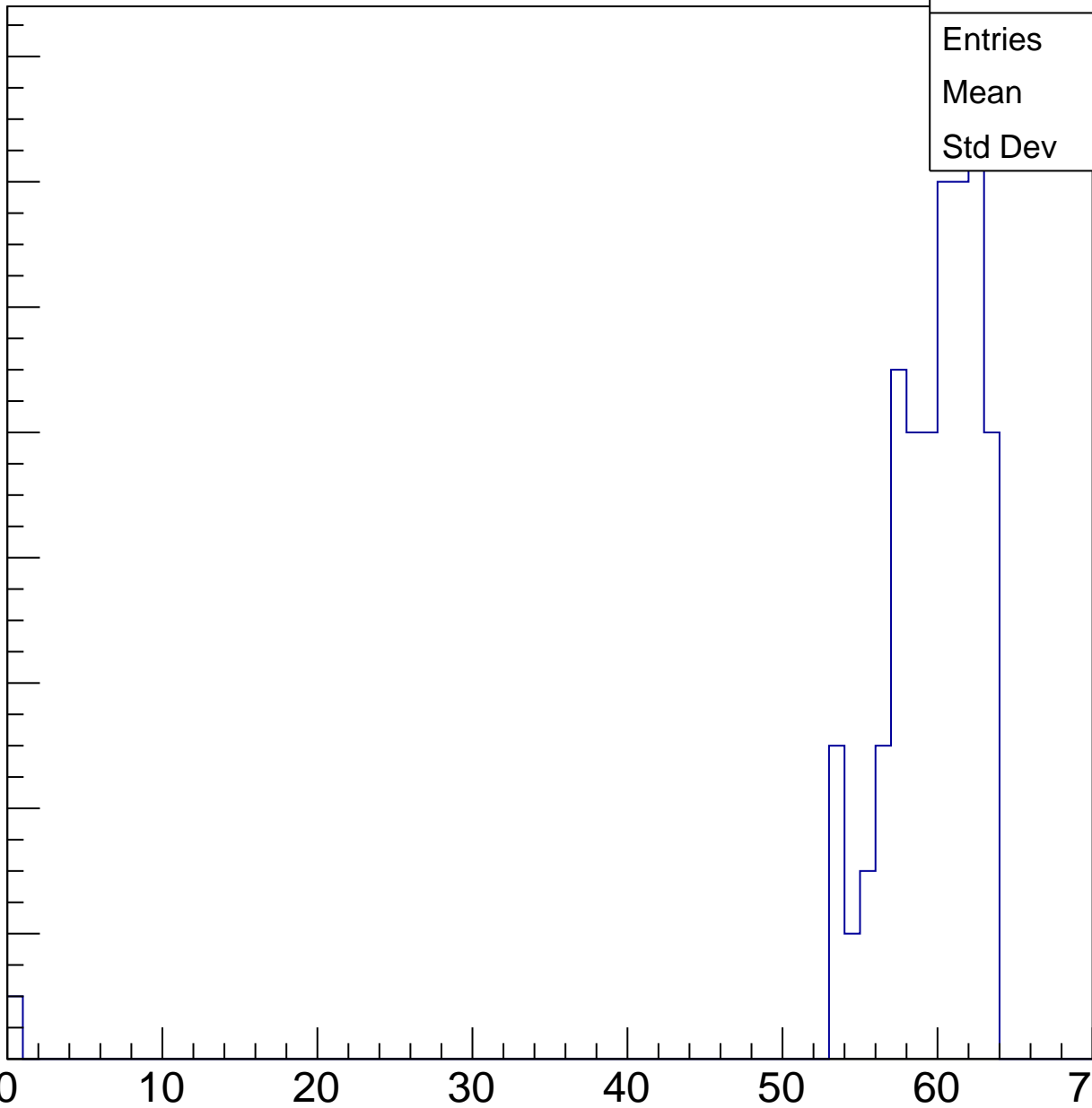
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 58.72 |
| Std Dev | 6.467 |

ampl

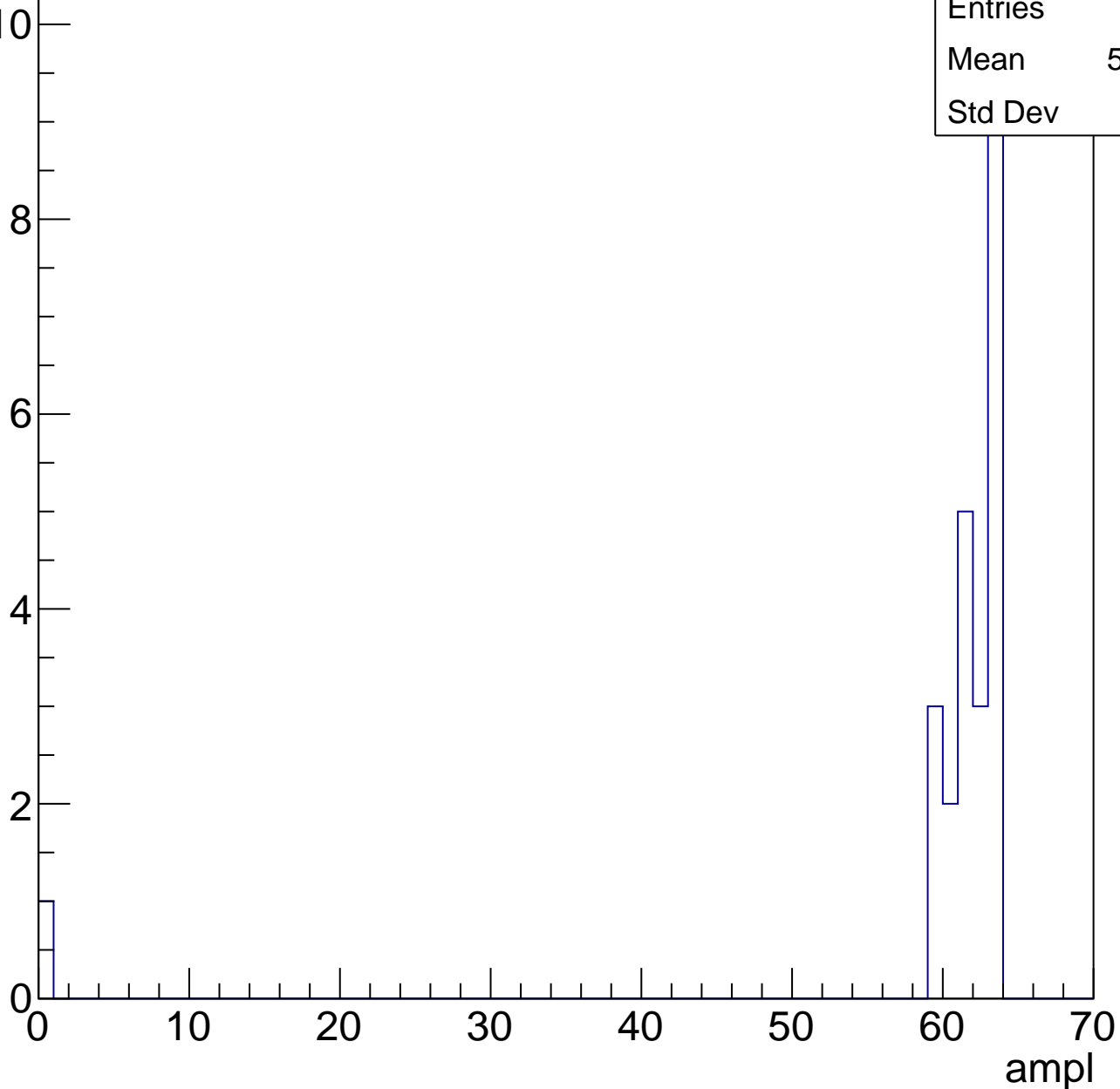


# B0L101S, U16-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 24    |
| Mean    | 59.08 |
| Std Dev | 12.4  |





# B0L101S, U16-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

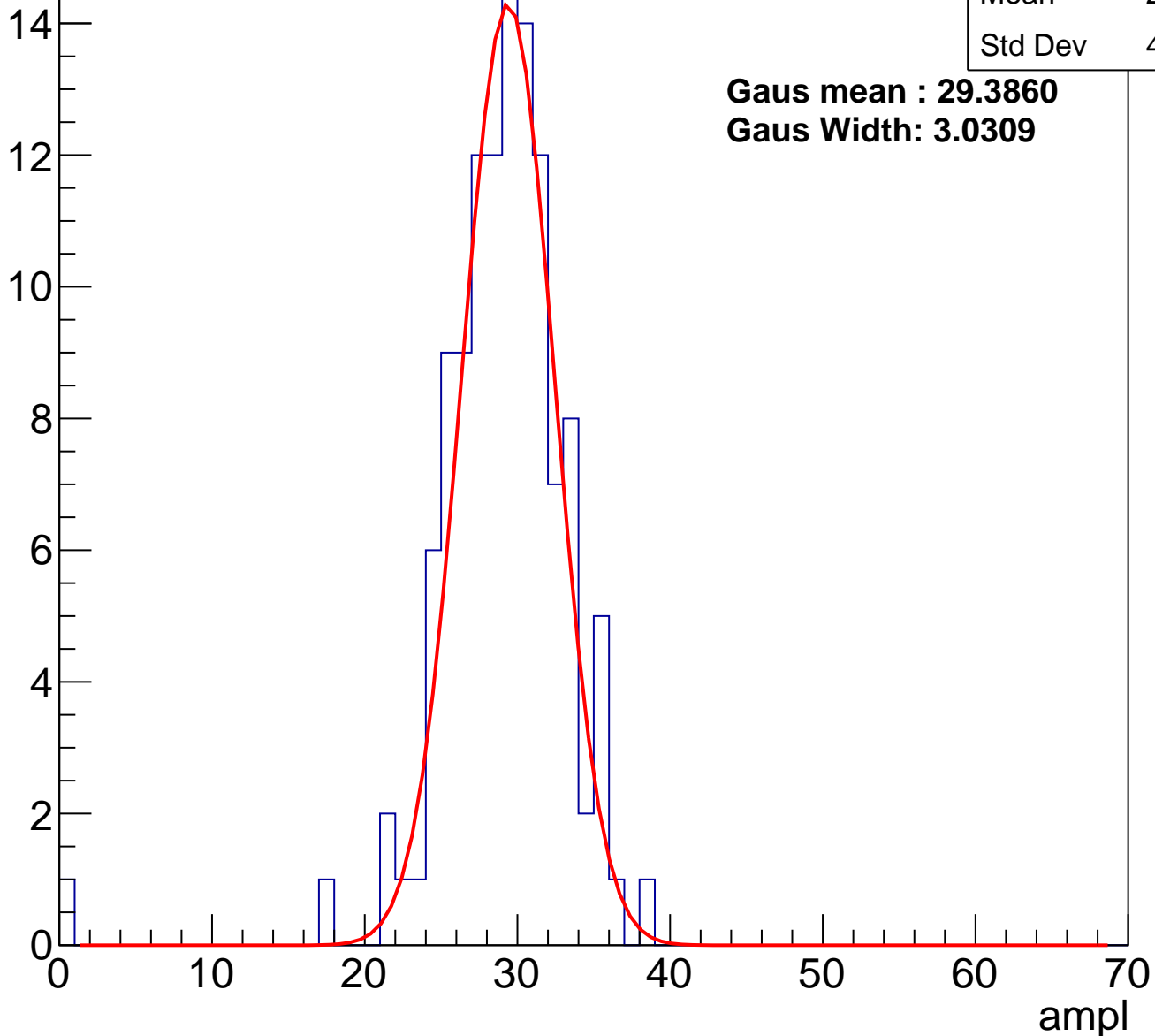


|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch77, adc1

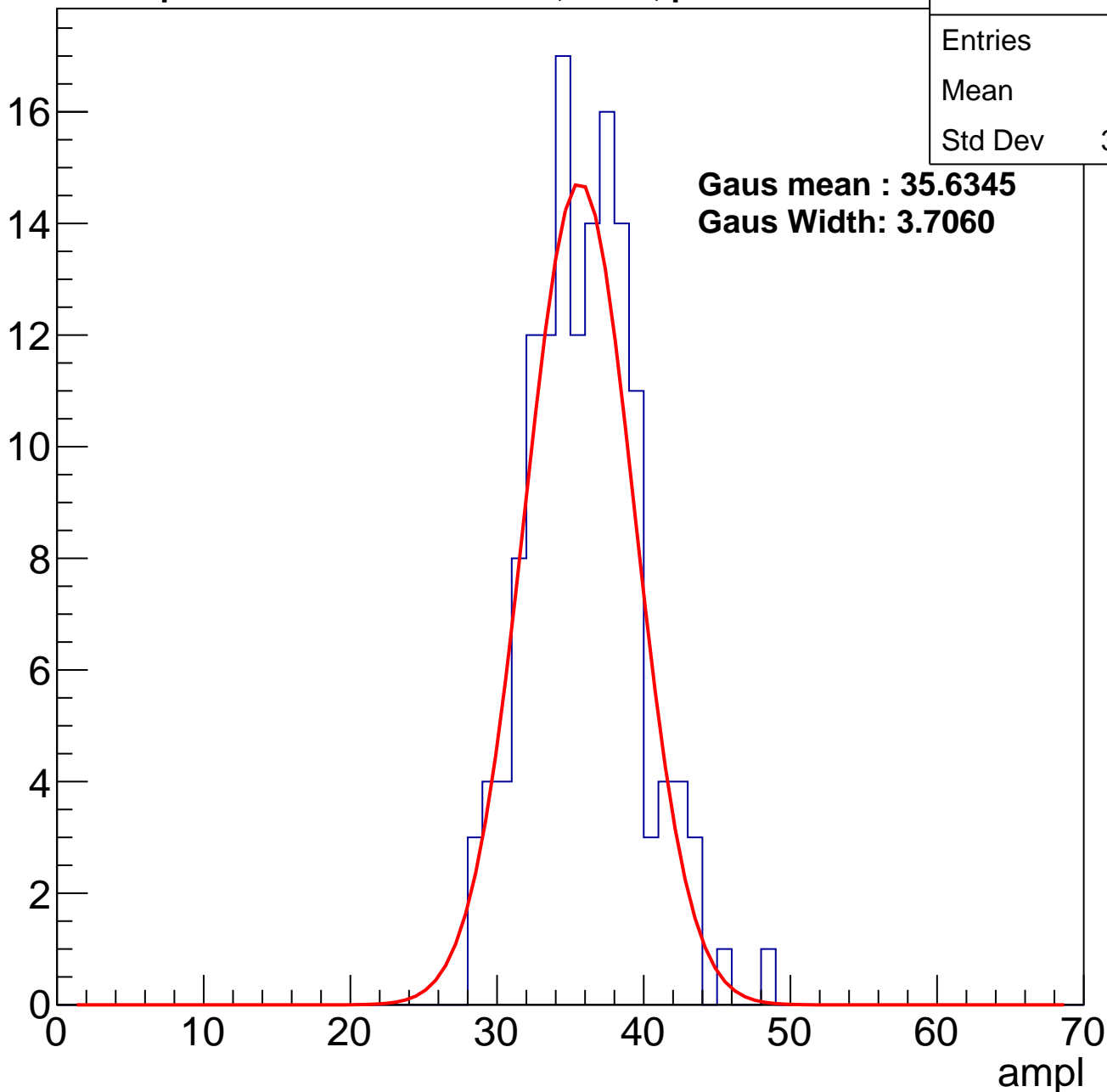
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 35.5  |
| Std Dev | 3.656 |

**Gaus mean : 35.6345**

**Gaus Width: 3.7060**

Entry



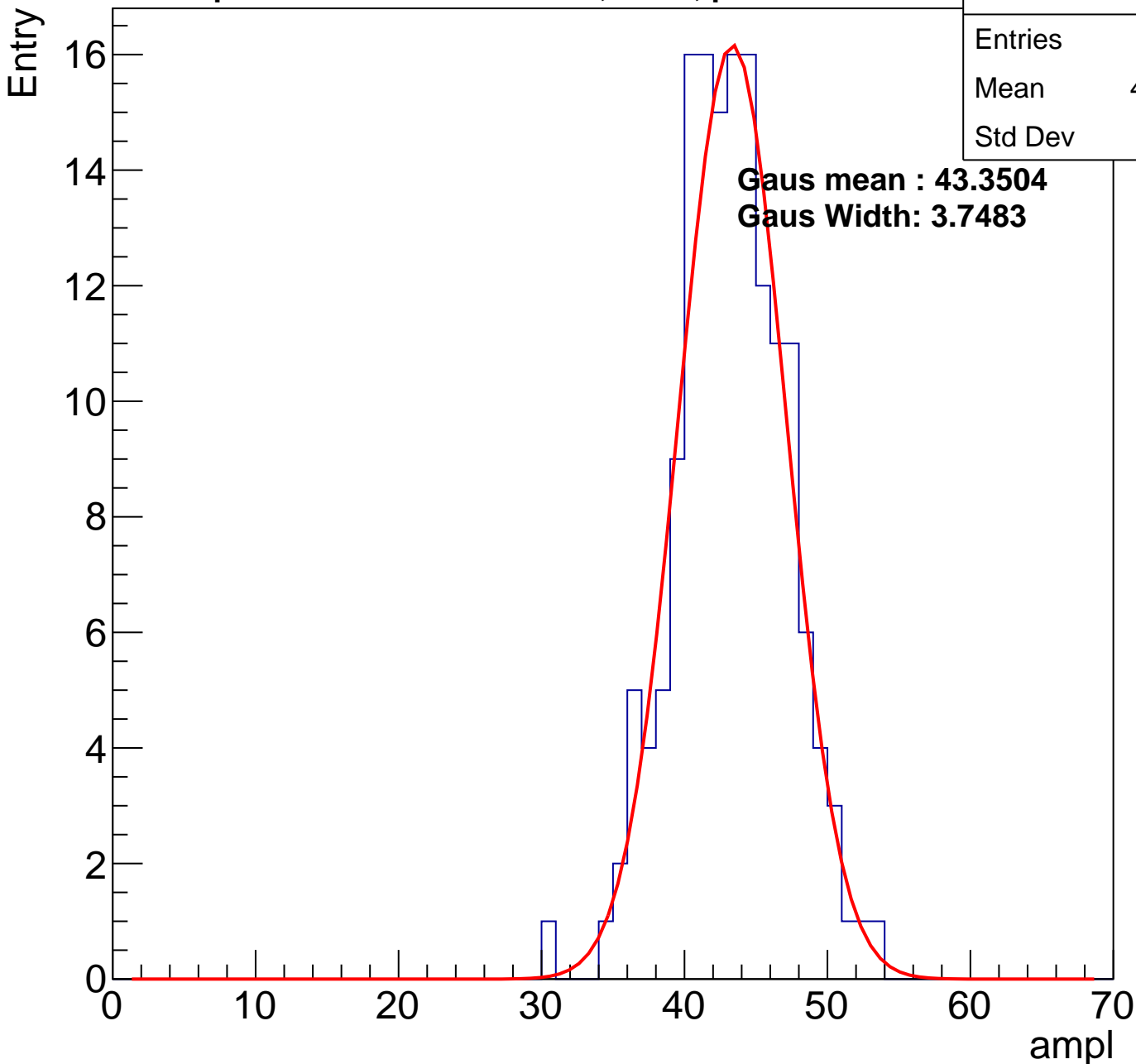
# B0L101S, U16-ch77, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 42.78 |
| Std Dev | 3.82  |

**Gaus mean : 43.3504**

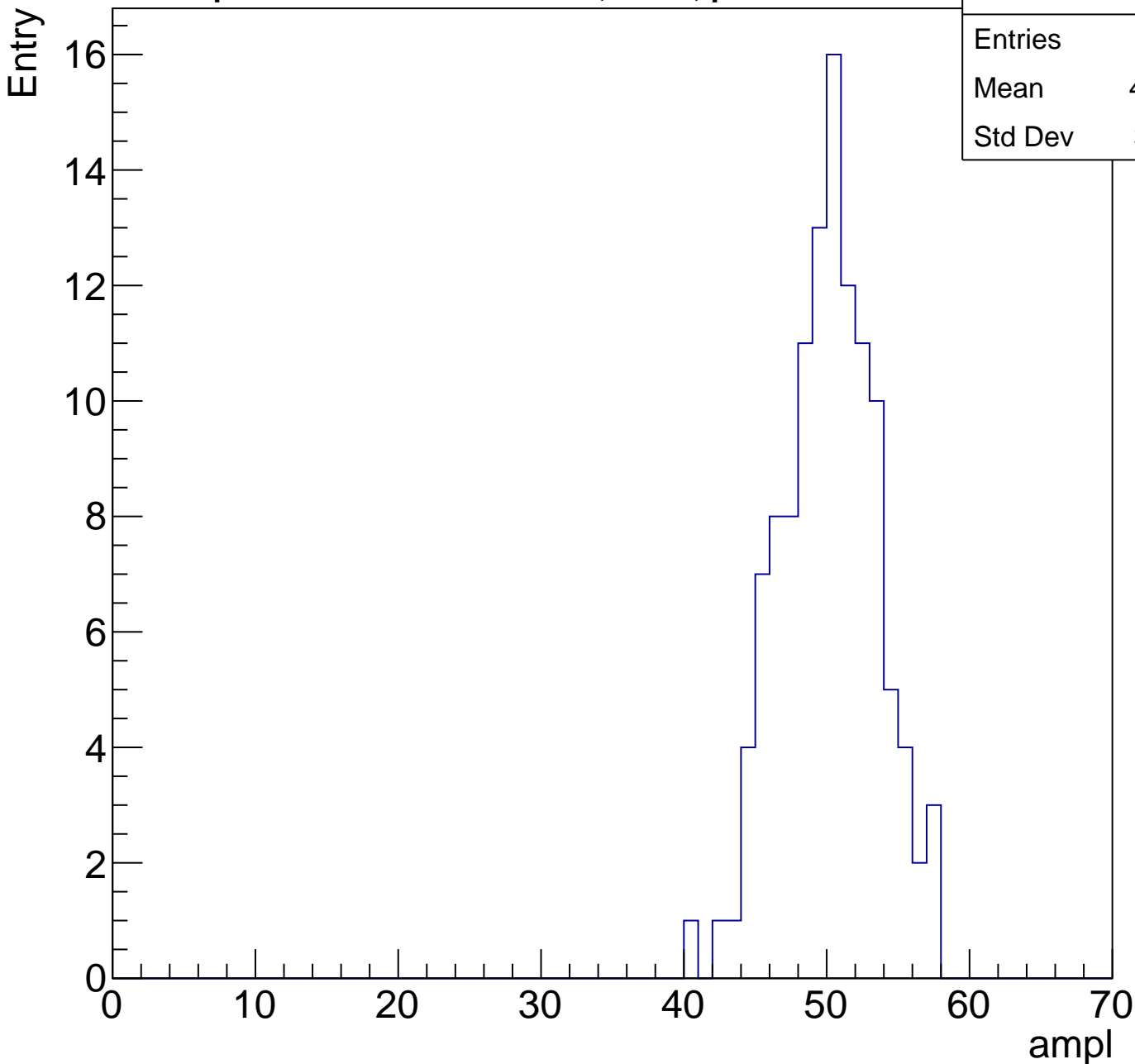
**Gaus Width: 3.7483**



# B0L101S, U16-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

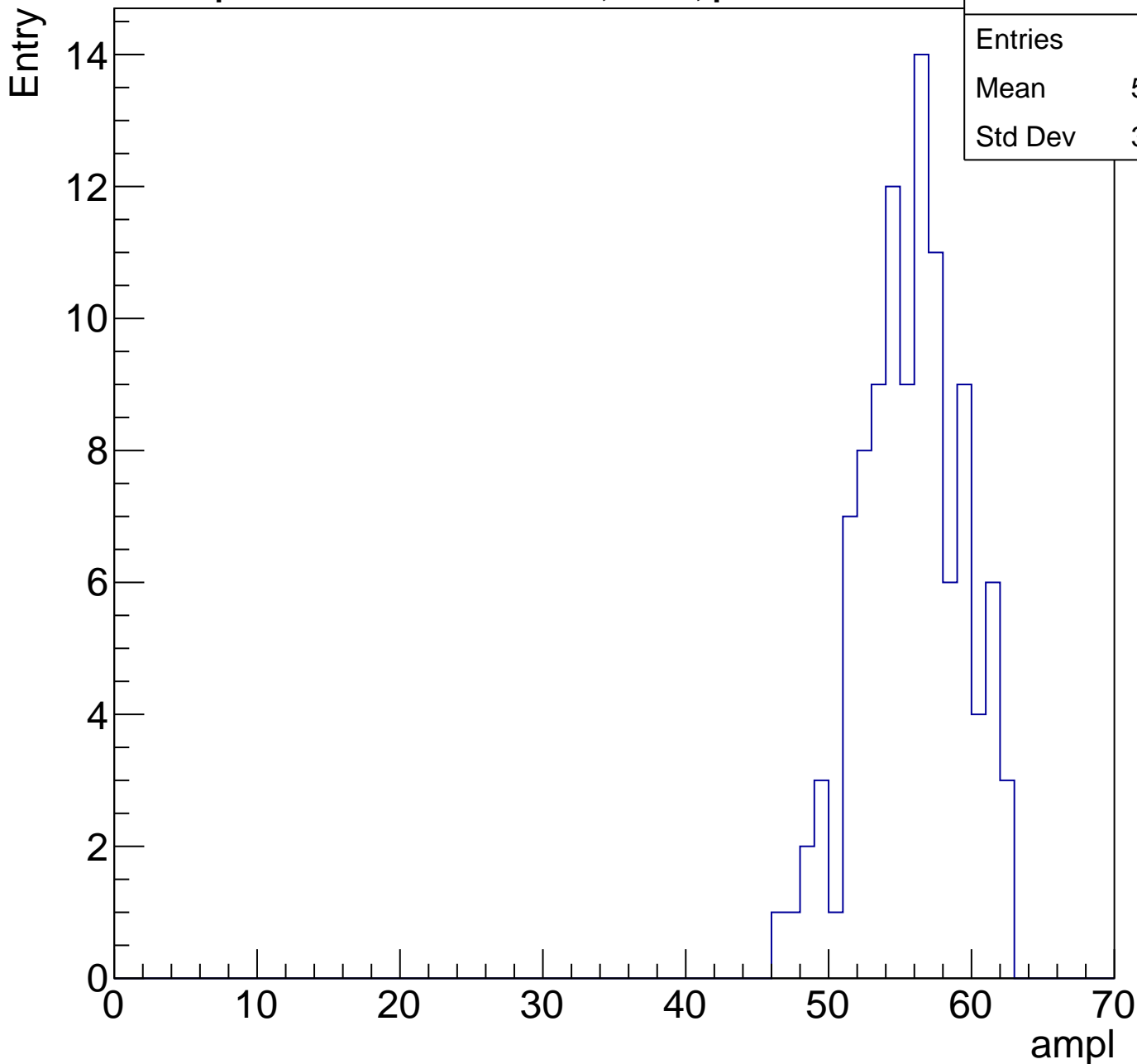
|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 49.68 |
| Std Dev | 3.361 |



# B0L101S, U16-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

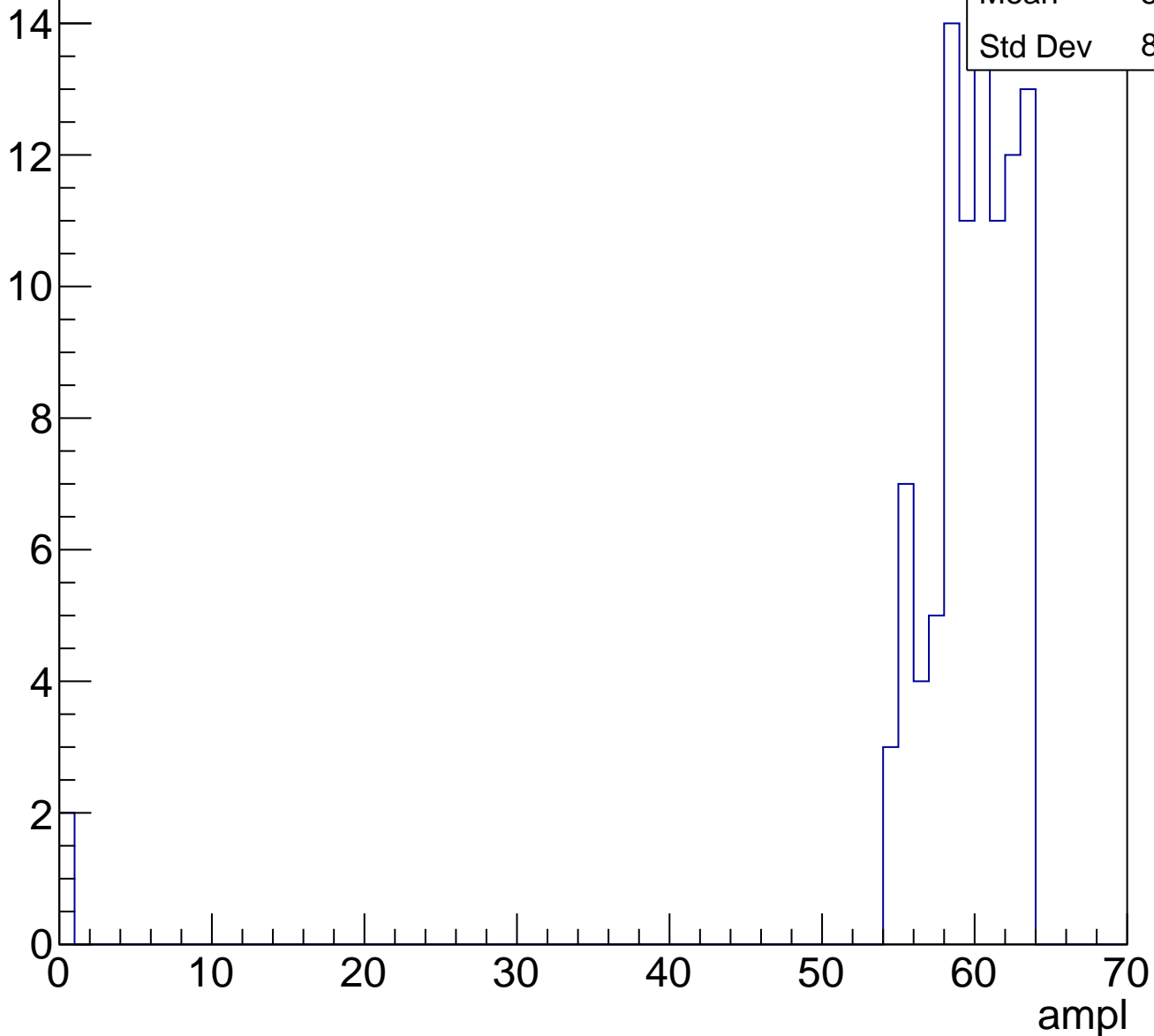
|         |       |
|---------|-------|
| Entries | 106   |
| Mean    | 55.29 |
| Std Dev | 3.526 |



# B0L101S, U16-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

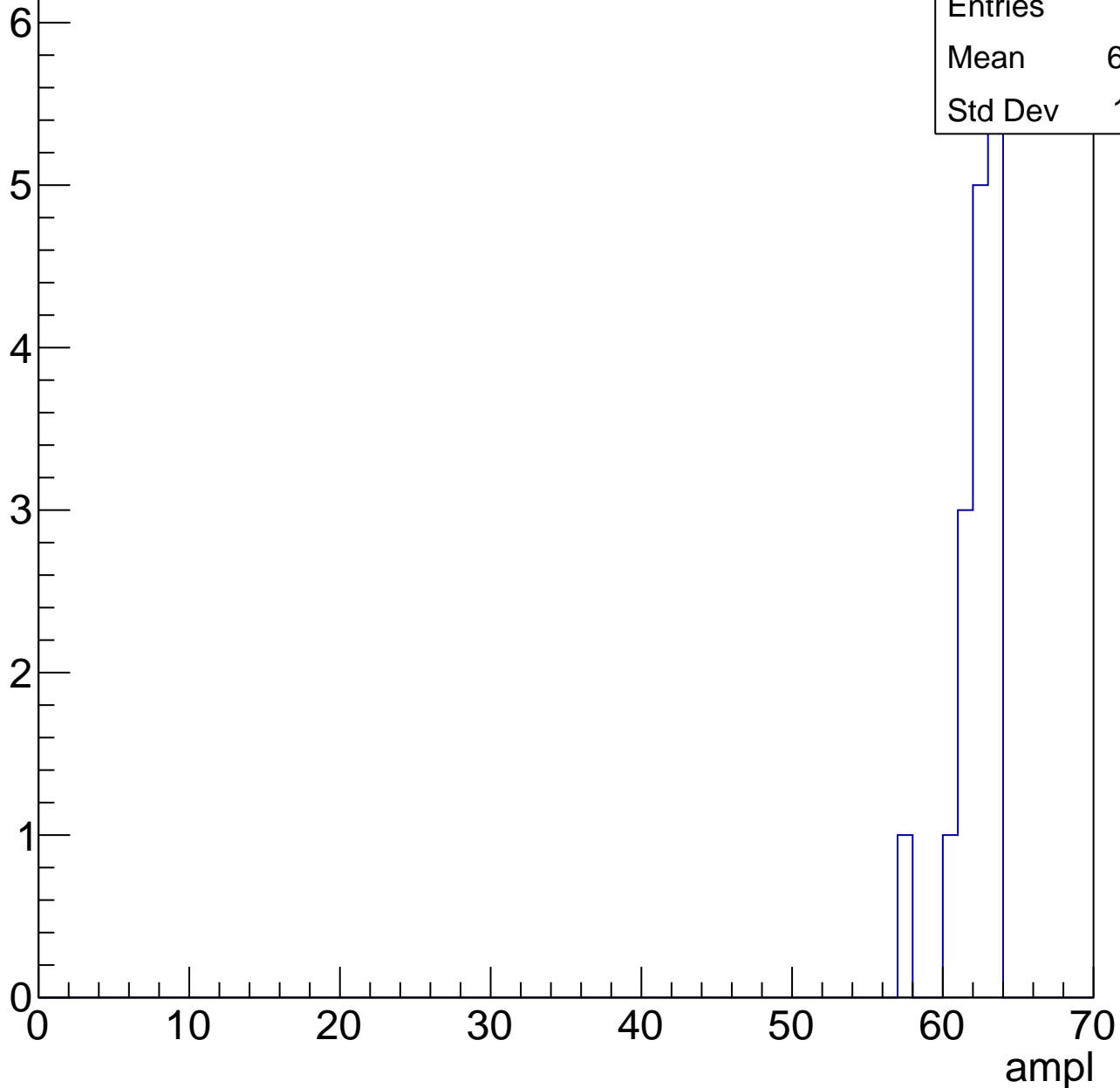


# B0L101S, U16-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 16    |
| Mean    | 61.75 |
| Std Dev | 1.521 |





# B0L101S, U16-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch78, adc0

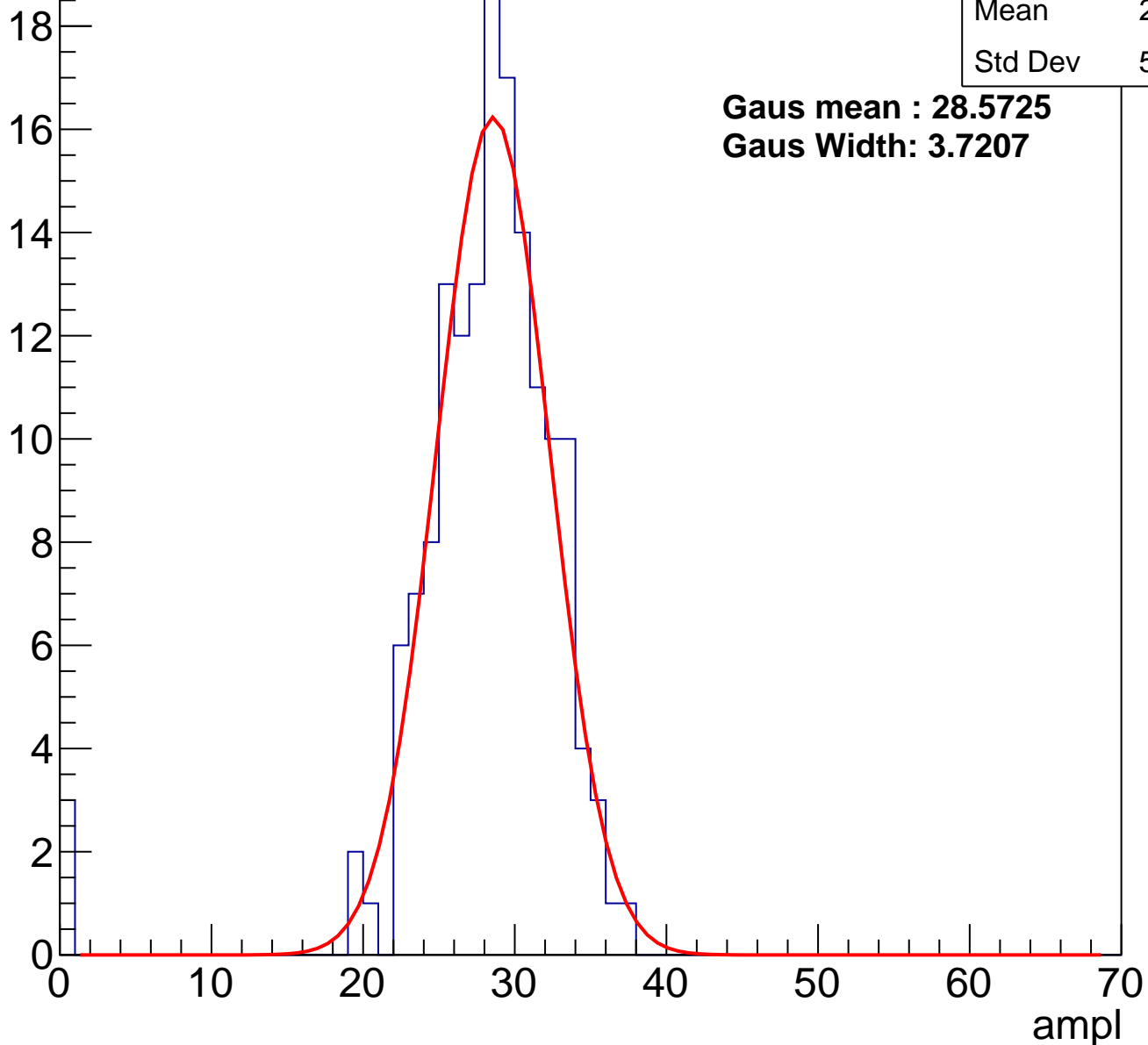
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 155   |
| Mean    | 27.62 |
| Std Dev | 5.249 |

**Gaus mean : 28.5725**

**Gaus Width: 3.7207**

Entry

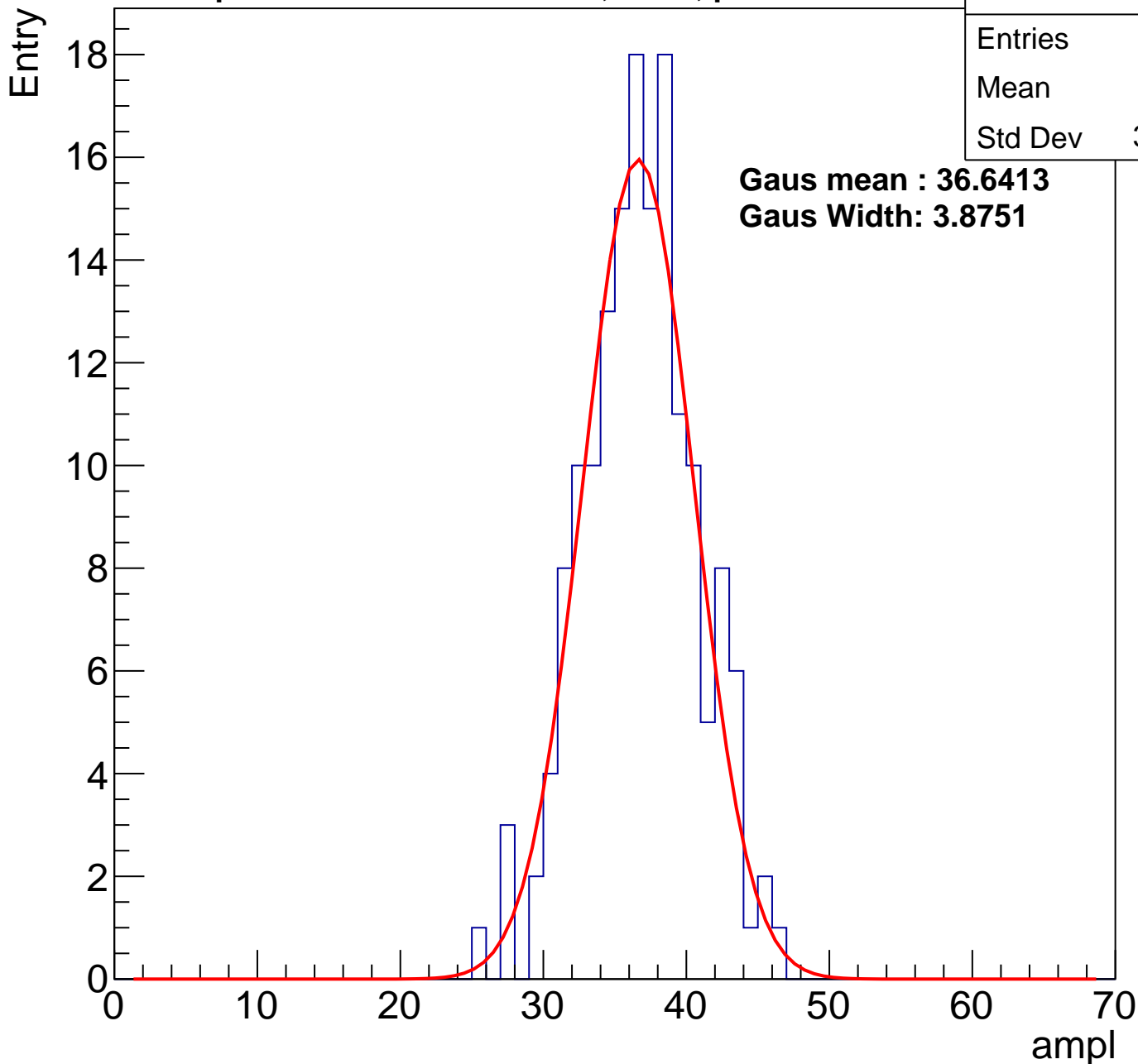


# B0L101S, U16-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 161   |
| Mean    | 36.3  |
| Std Dev | 3.931 |

**Gaus mean : 36.6413**  
**Gaus Width: 3.8751**



# B0L101S, U16-ch78, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

119

Mean

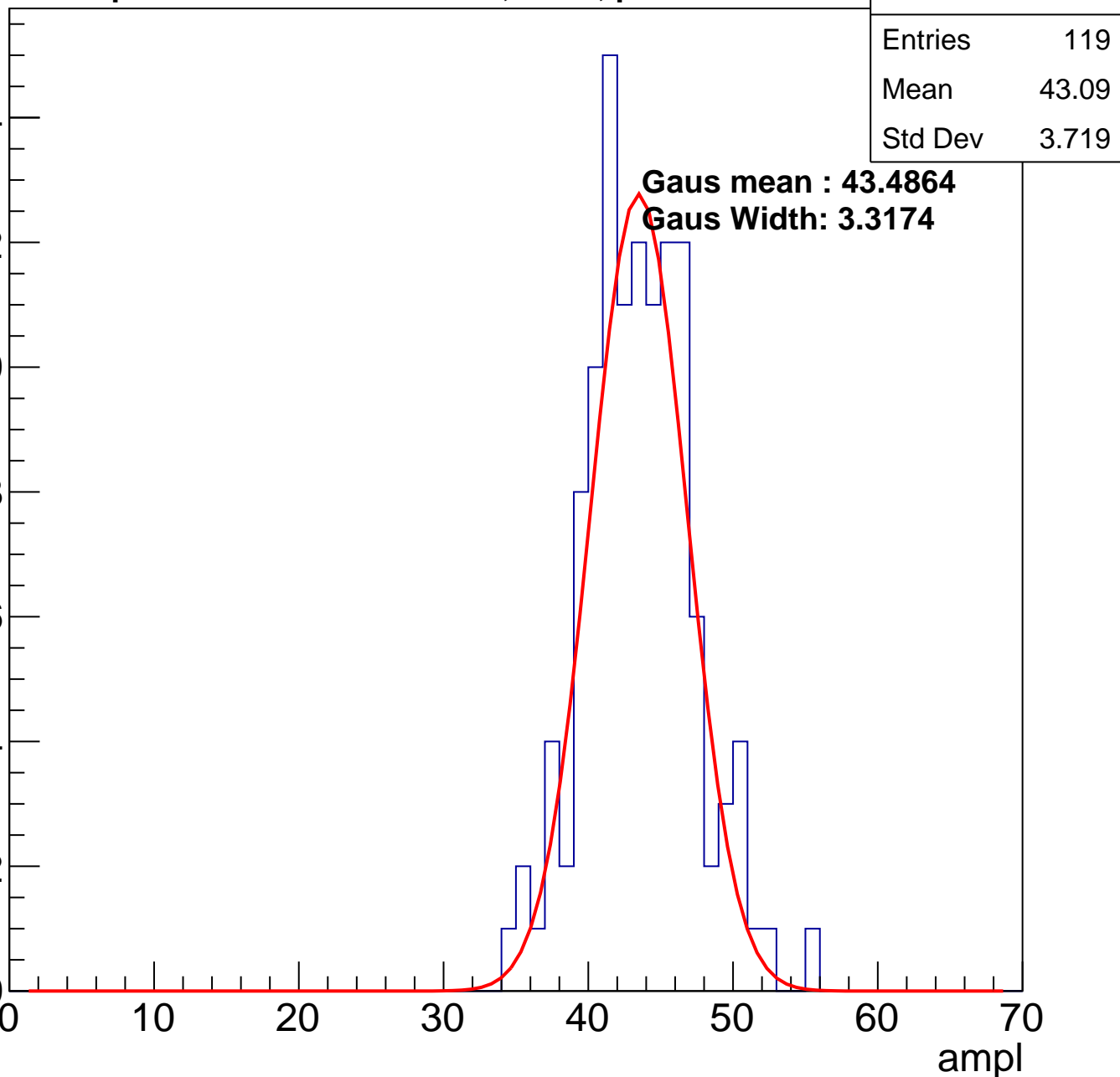
43.09

Std Dev

3.719

**Gaus mean : 43.4864**

**Gaus Width: 3.3174**



# B0L101S, U16-ch78, adc3

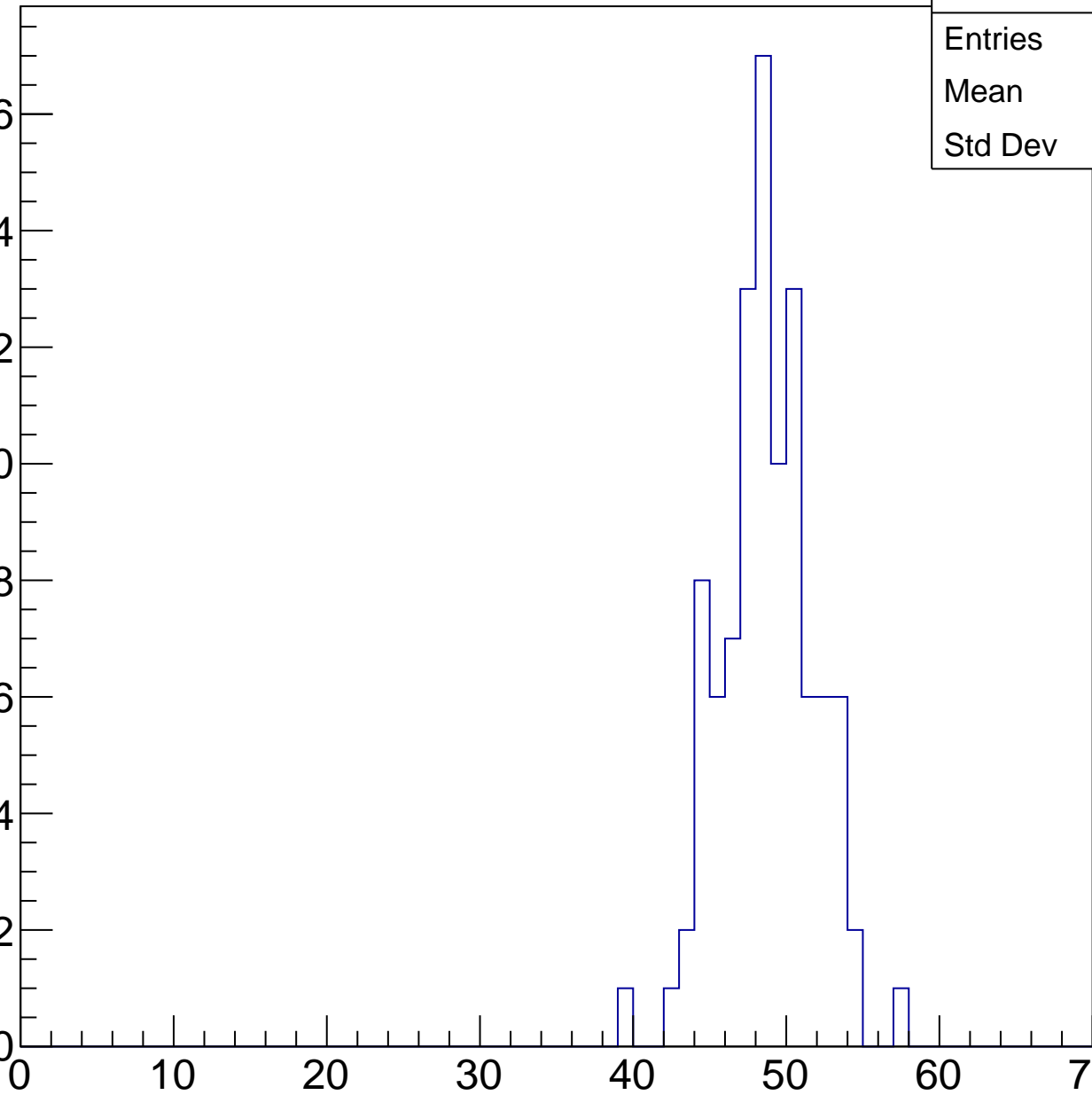
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 99    |
| Mean    | 48.27 |
| Std Dev | 3.011 |

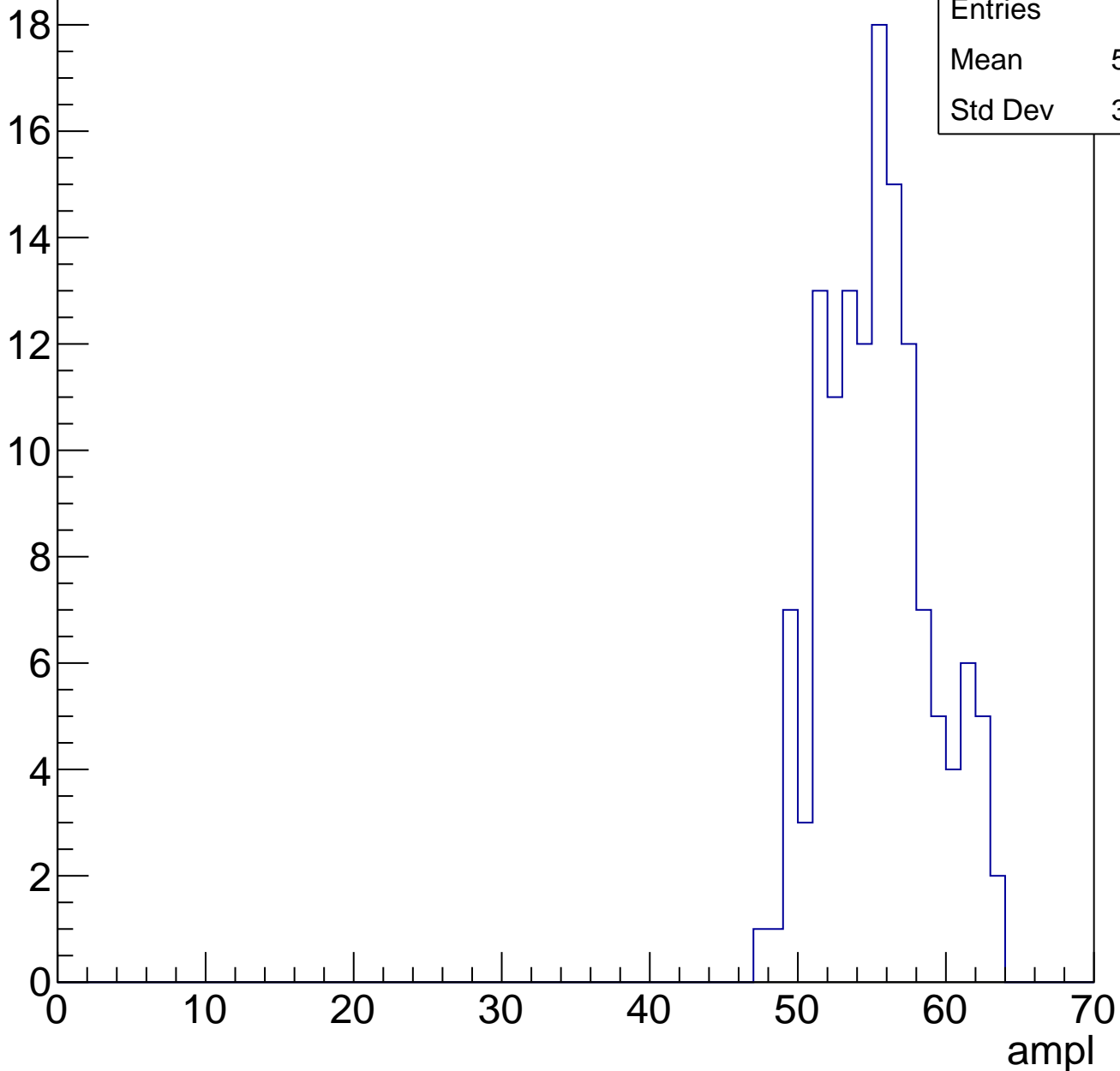
ampl



# B0L101S, U16-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch78, adc5

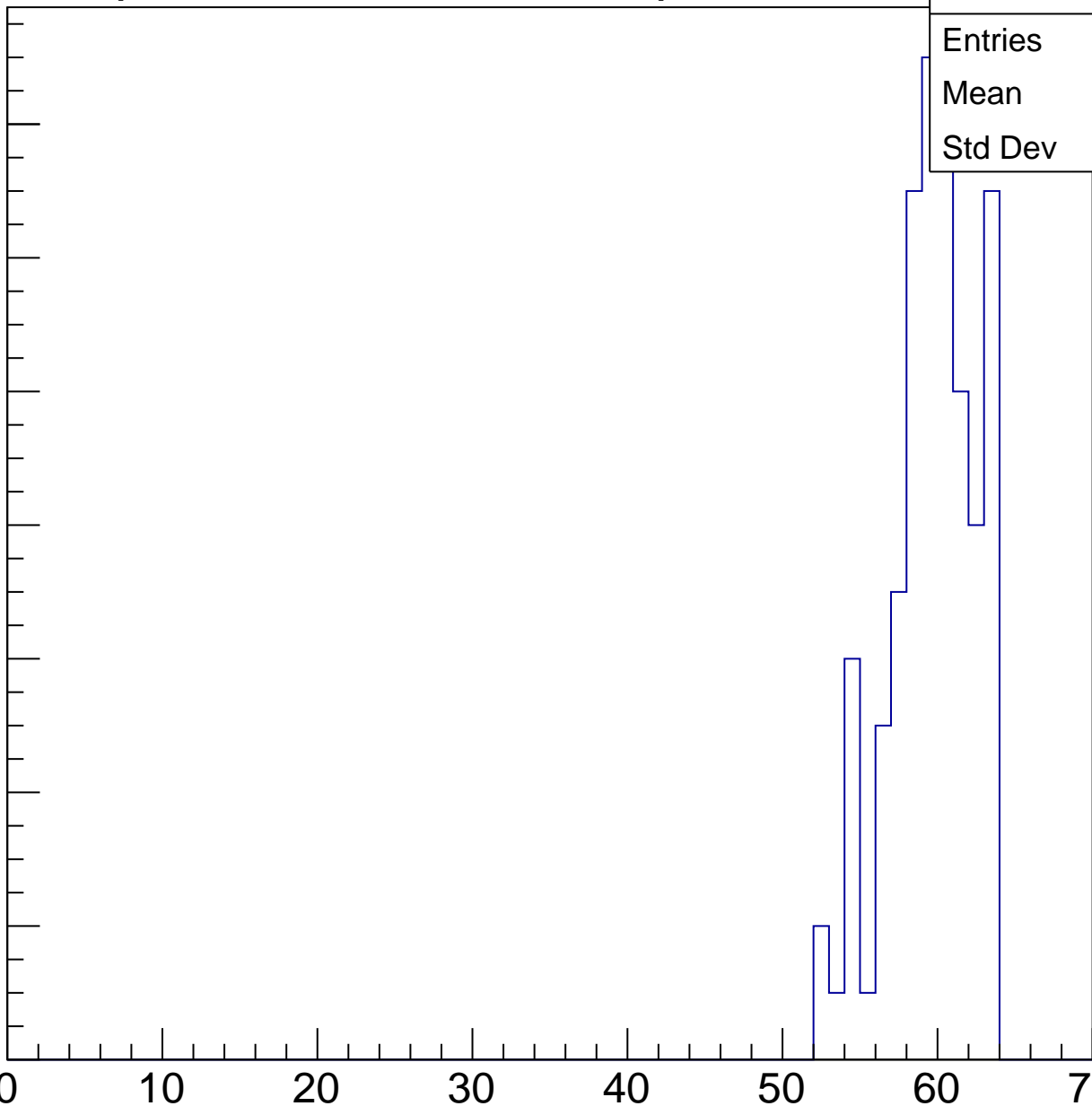
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 59.15 |
| Std Dev | 2.749 |

ampl

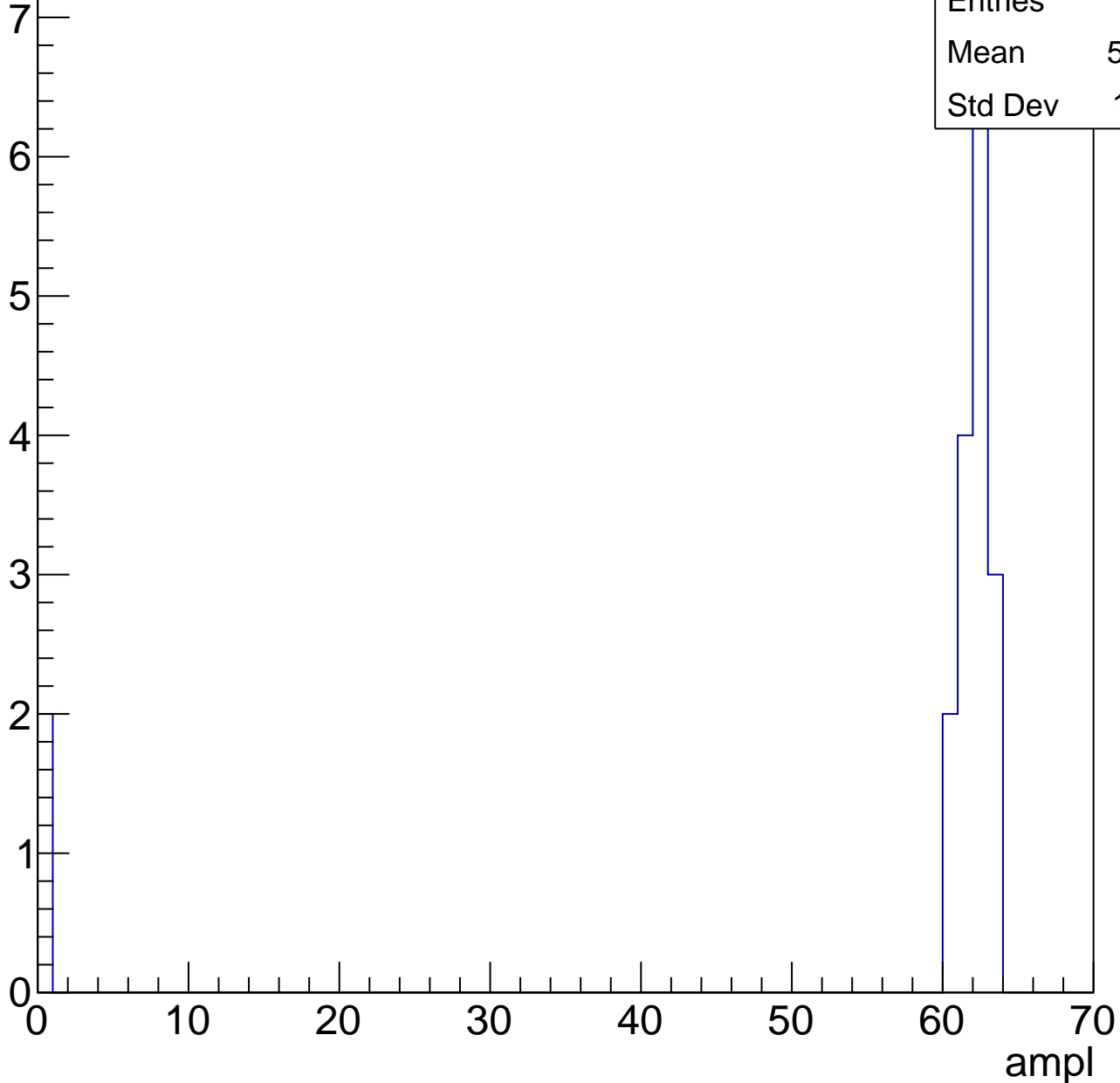


# B0L101S, U16-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 18    |
| Mean    | 54.83 |
| Std Dev | 19.41 |





# B0L101S, U16-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch79, adc0

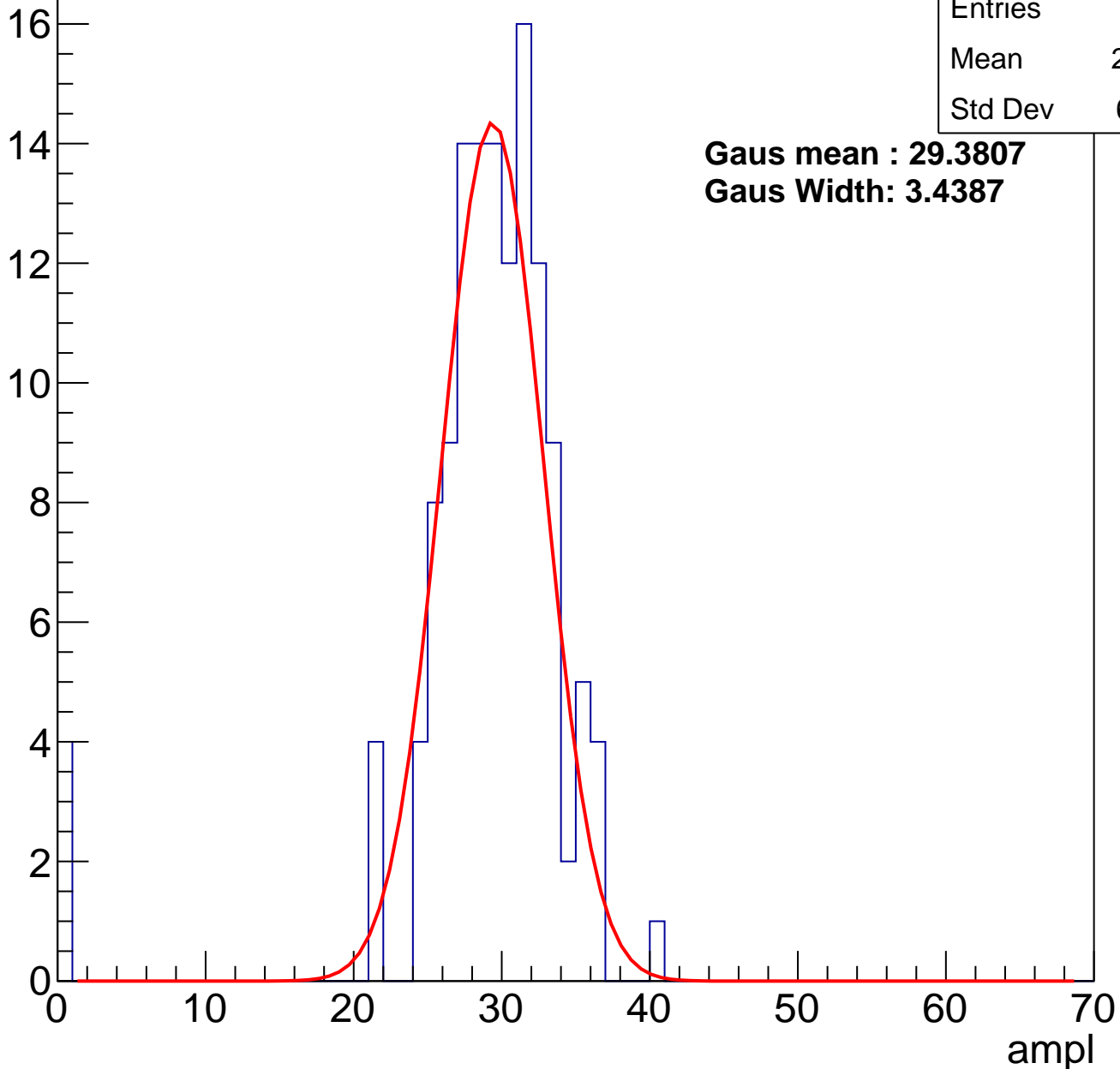
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 28.44 |
| Std Dev | 6.051 |

**Gaus mean : 29.3807**

**Gaus Width: 3.4387**

Entry



# B0L101S, U16-ch79, adc1

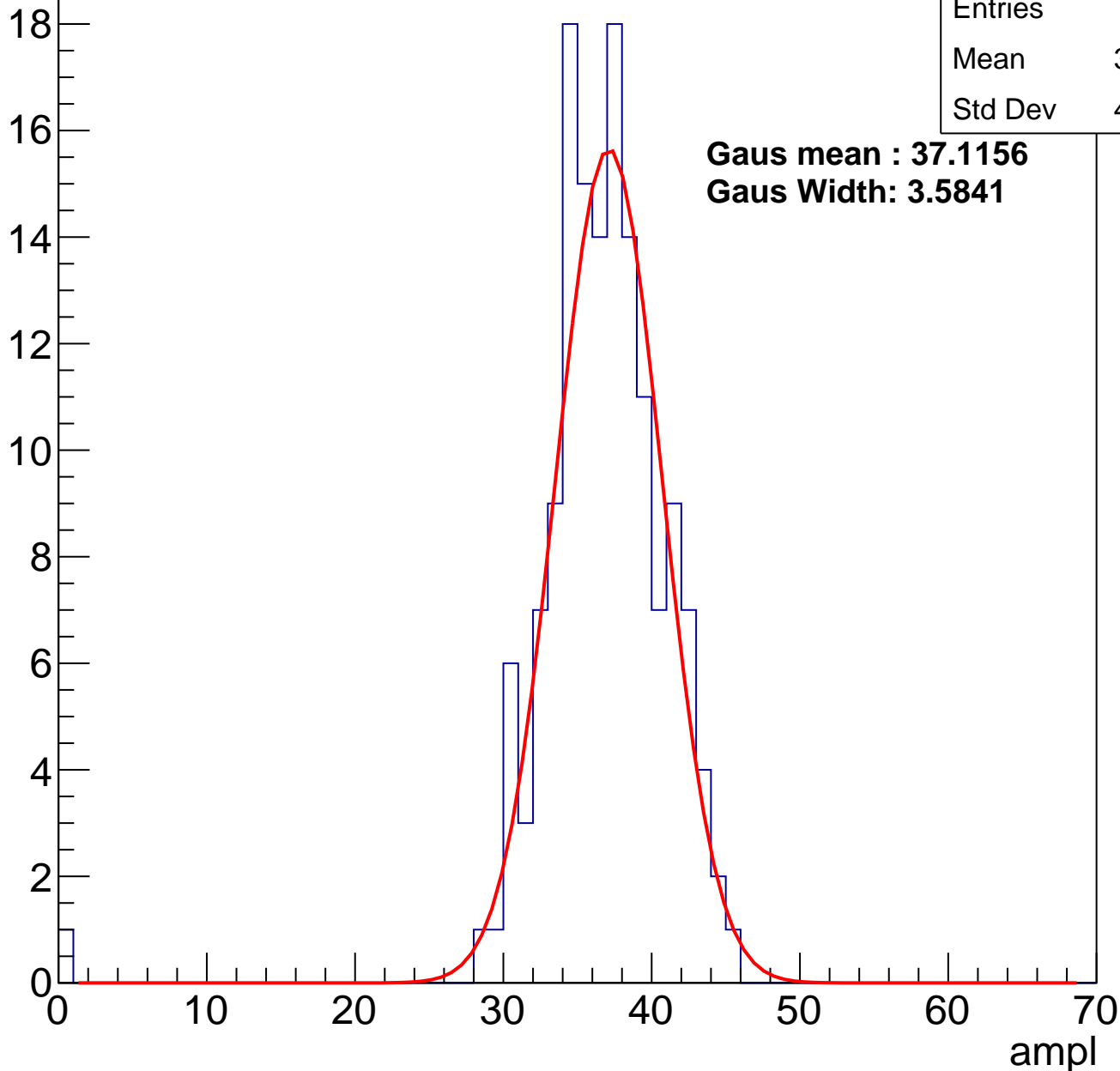
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 36.26 |
| Std Dev | 4.604 |

**Gaus mean : 37.1156**

**Gaus Width: 3.5841**

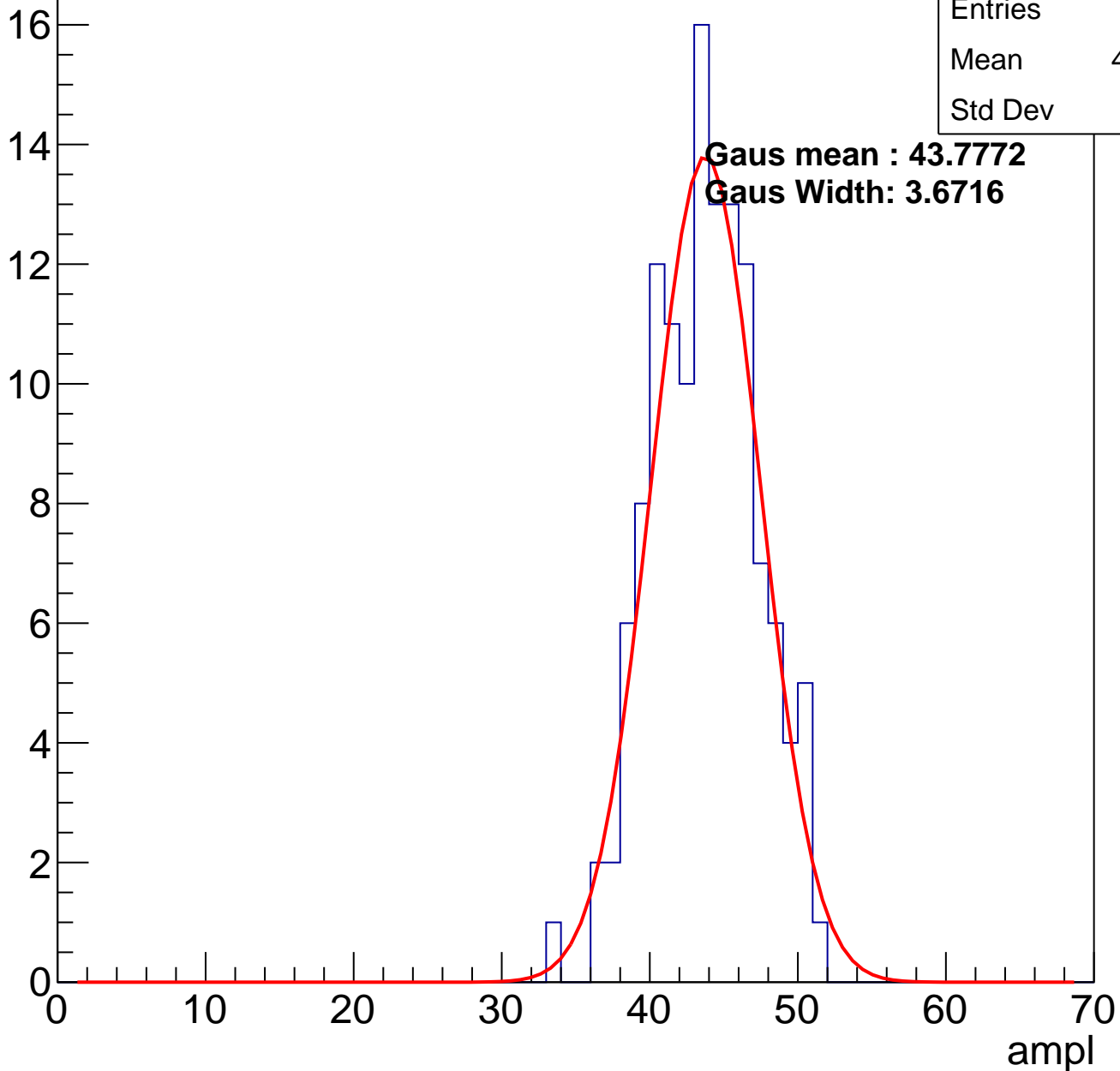
Entry



# B0L101S, U16-ch79, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

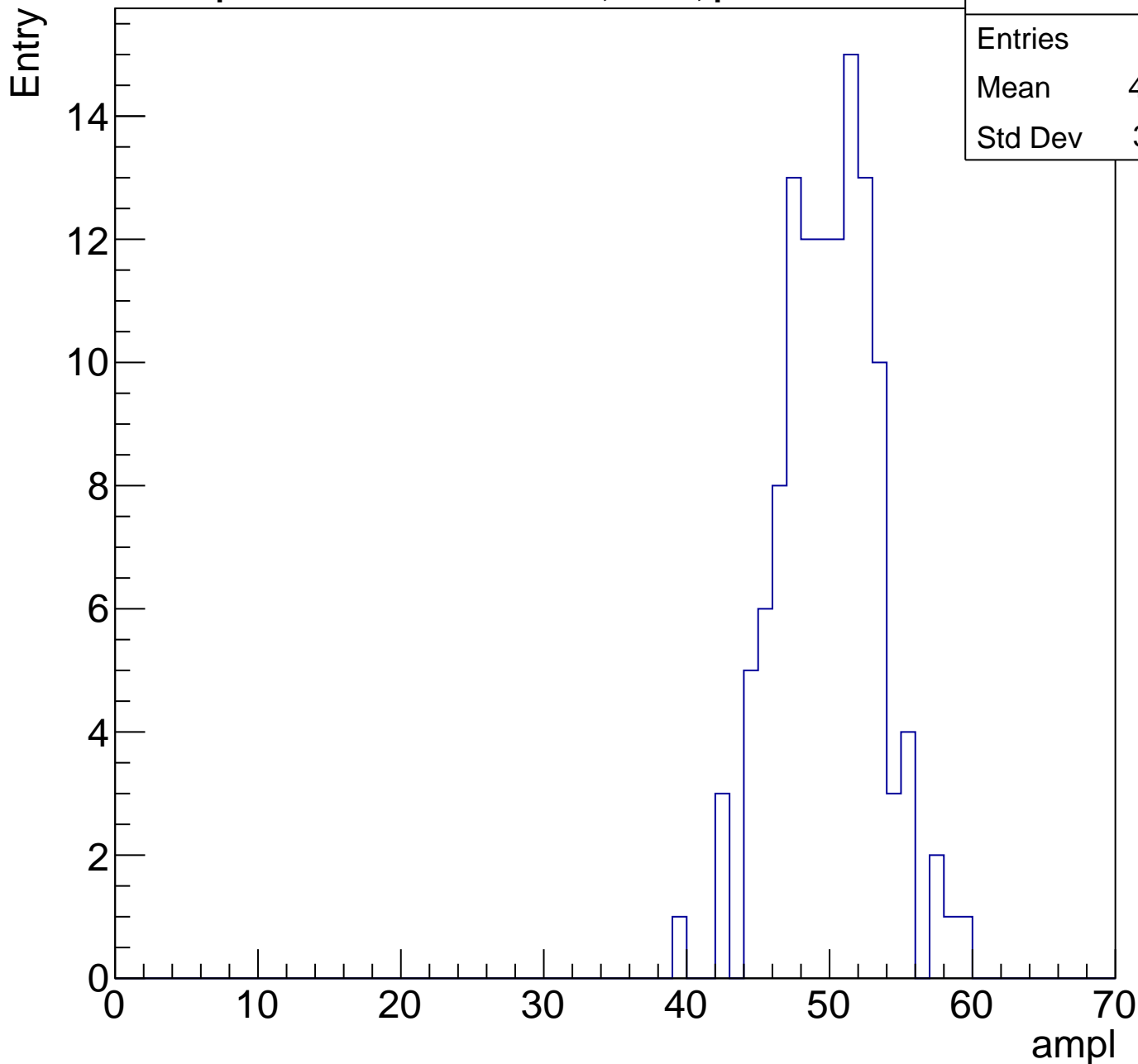
Entry



# B0L101S, U16-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 49.44 |
| Std Dev | 3.471 |



# B0L101S, U16-ch79, adc4

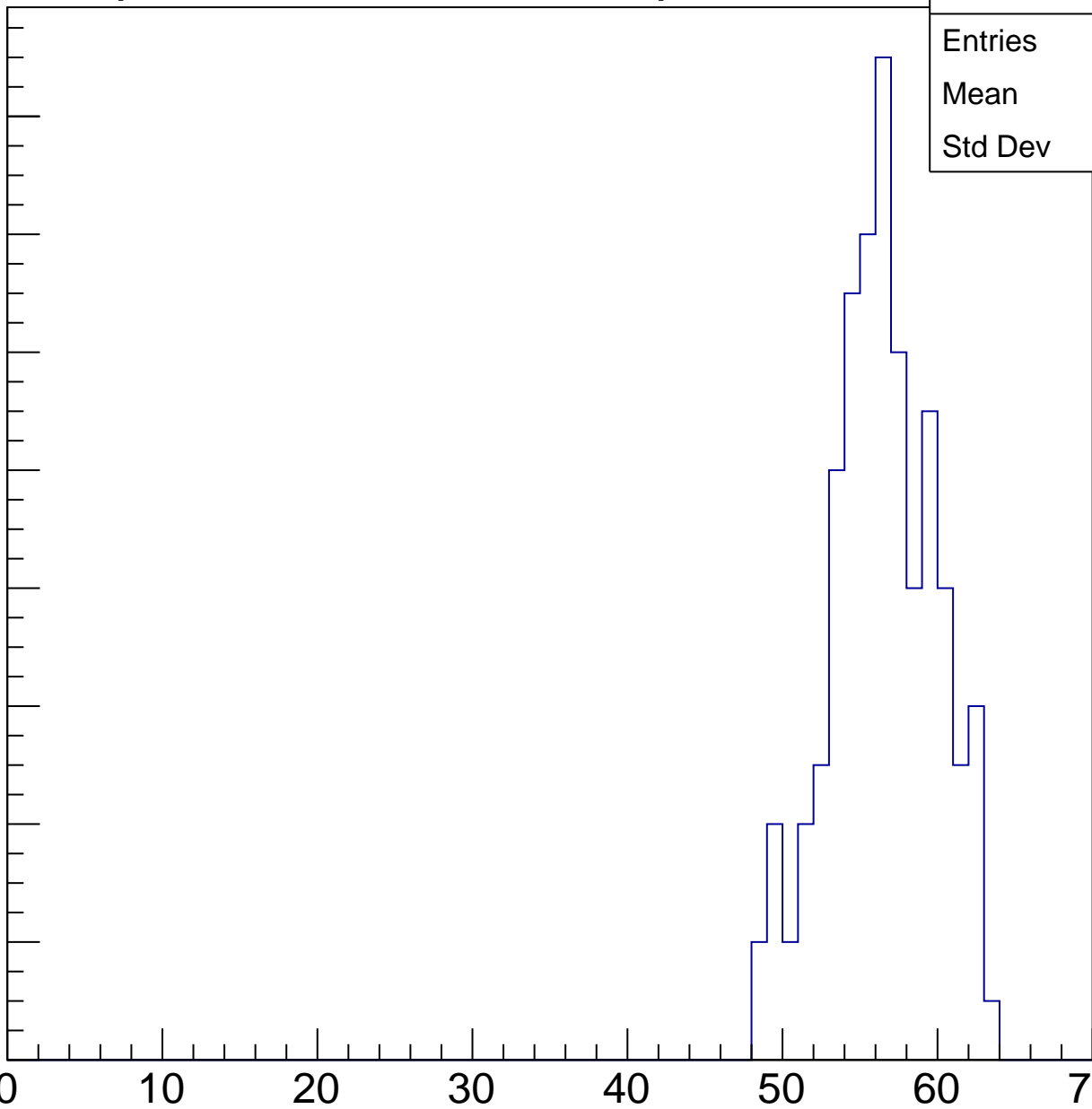
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 55.96 |
| Std Dev | 3.379 |

ampl



# B0L101S, U16-ch79, adc5

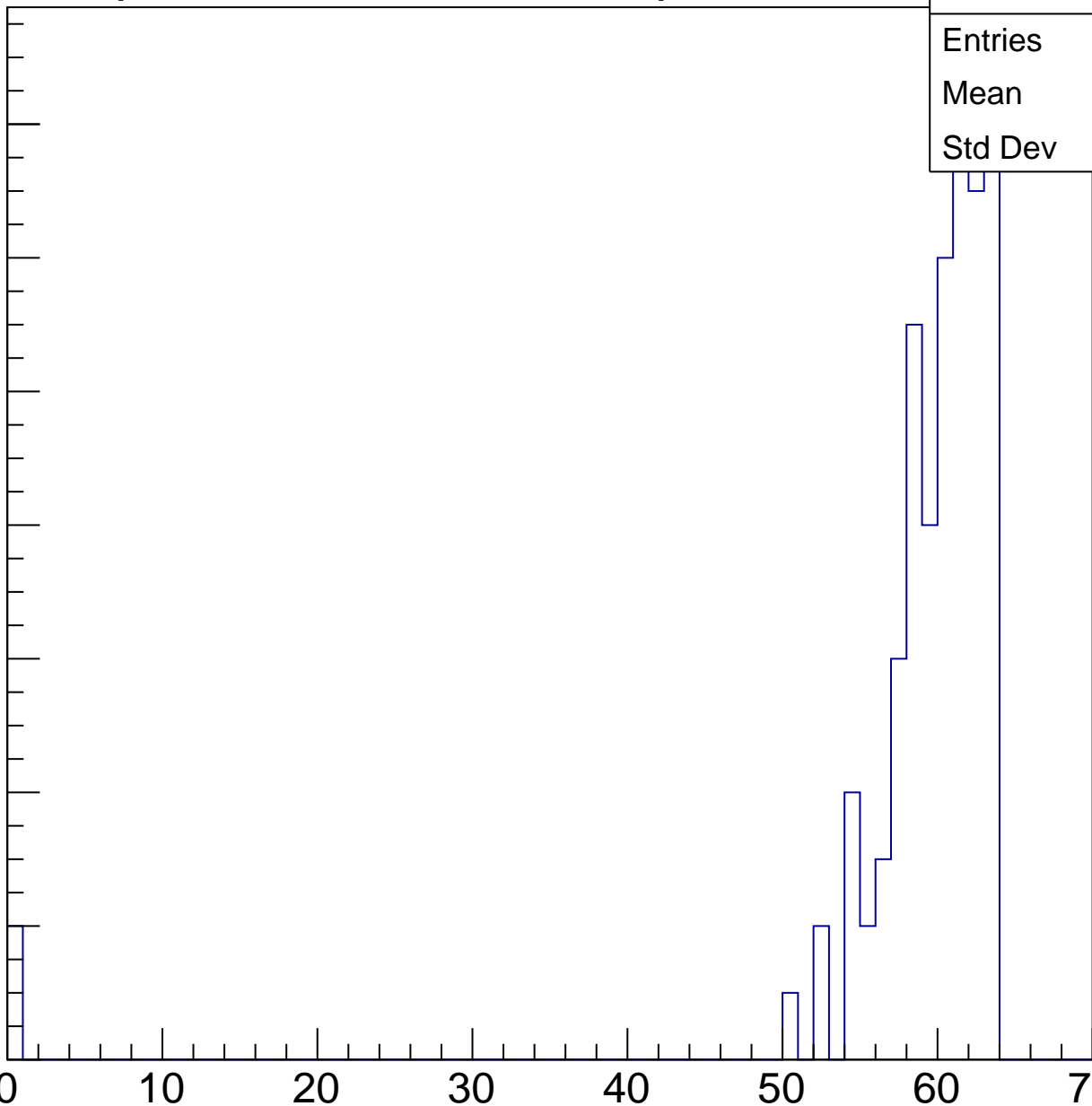
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 93    |
| Mean    | 58.33 |
| Std Dev | 9.11  |

ampl



# B0L101S, U16-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 28.21 |
| Std Dev | 4.526 |

**Gaus mean : 29.1505**

**Gaus Width: 3.5817**

Entry

14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

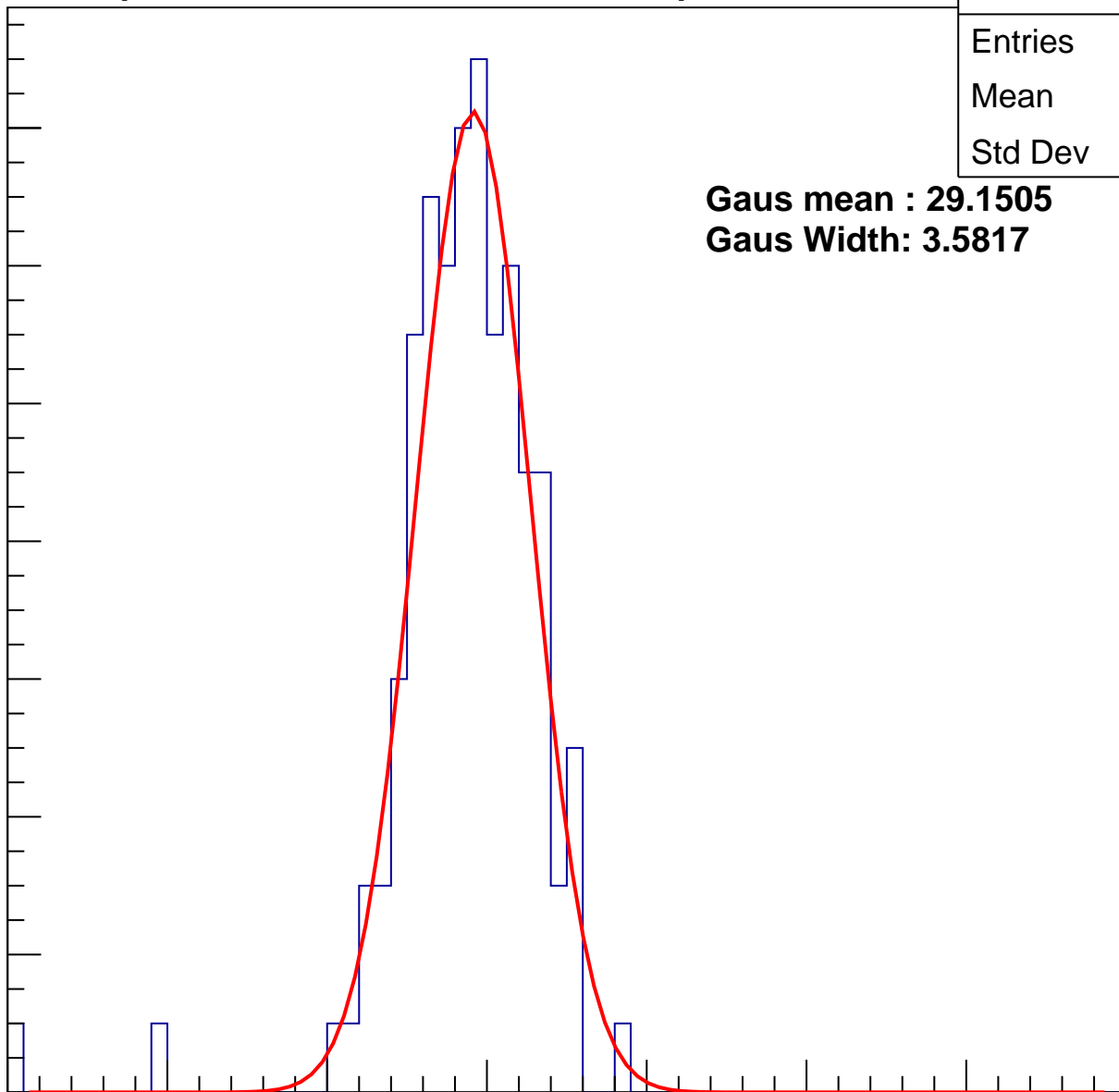
40

50

60

70

ampl



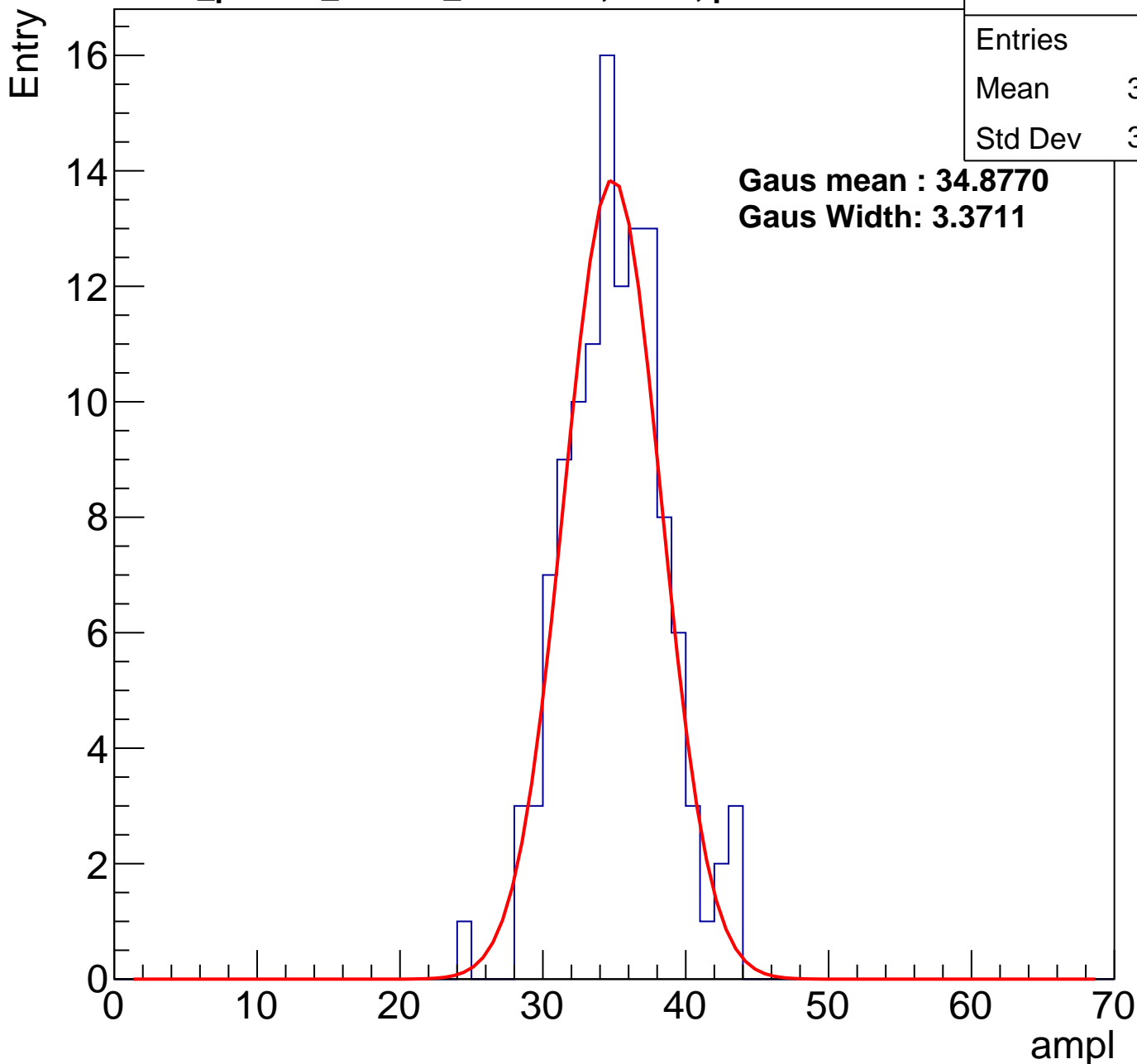
# B0L101S, U16-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 34.64 |
| Std Dev | 3.478 |

**Gaus mean : 34.8770**

**Gaus Width: 3.3711**



# B0L101S, U16-ch80, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 41.73 |
| Std Dev | 3.58  |

**Gaus mean : 42.0408**

**Gaus Width: 3.5812**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

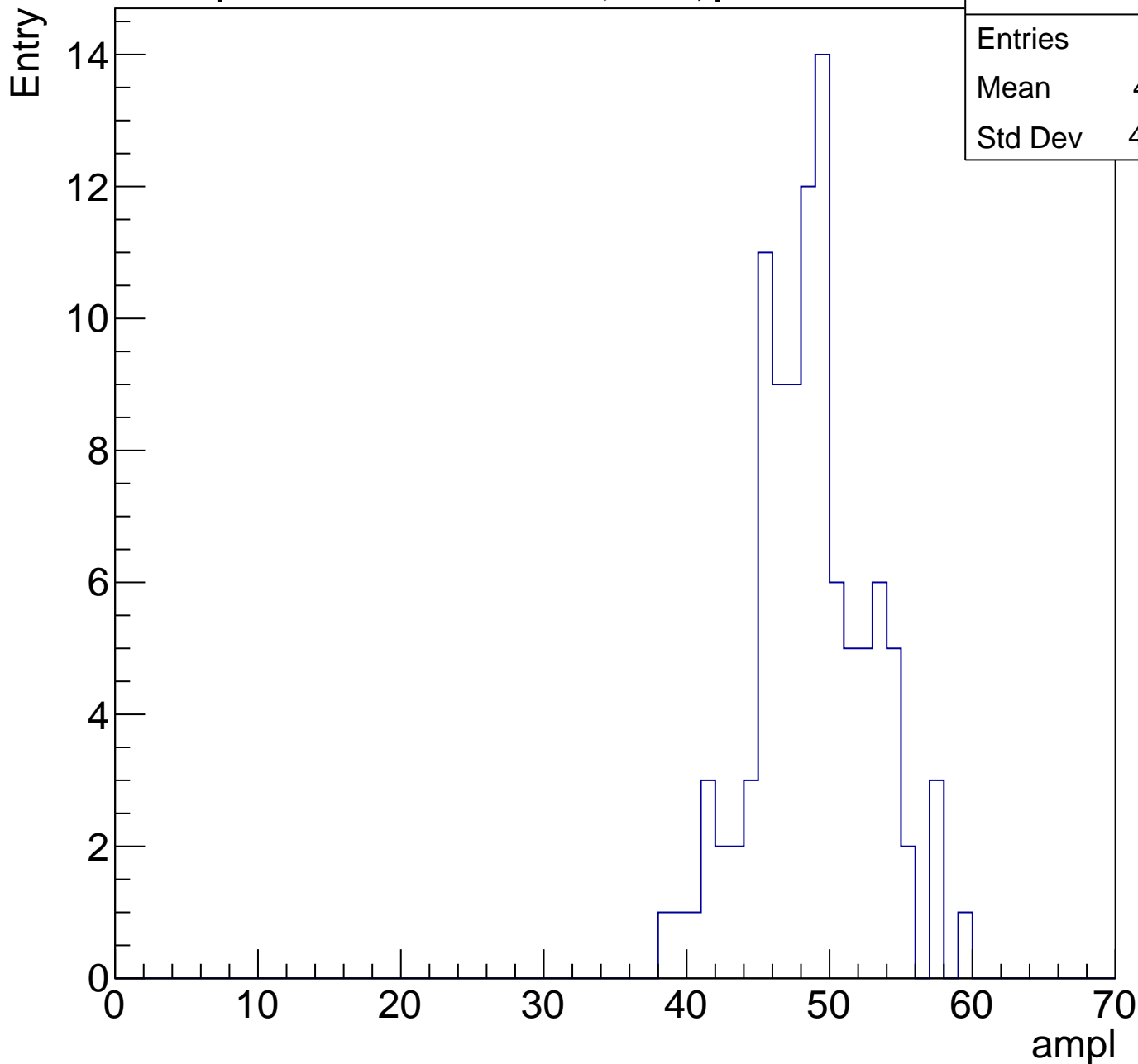
60

70

# B0L101S, U16-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 48.31 |
| Std Dev | 4.044 |



# B0L101S, U16-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

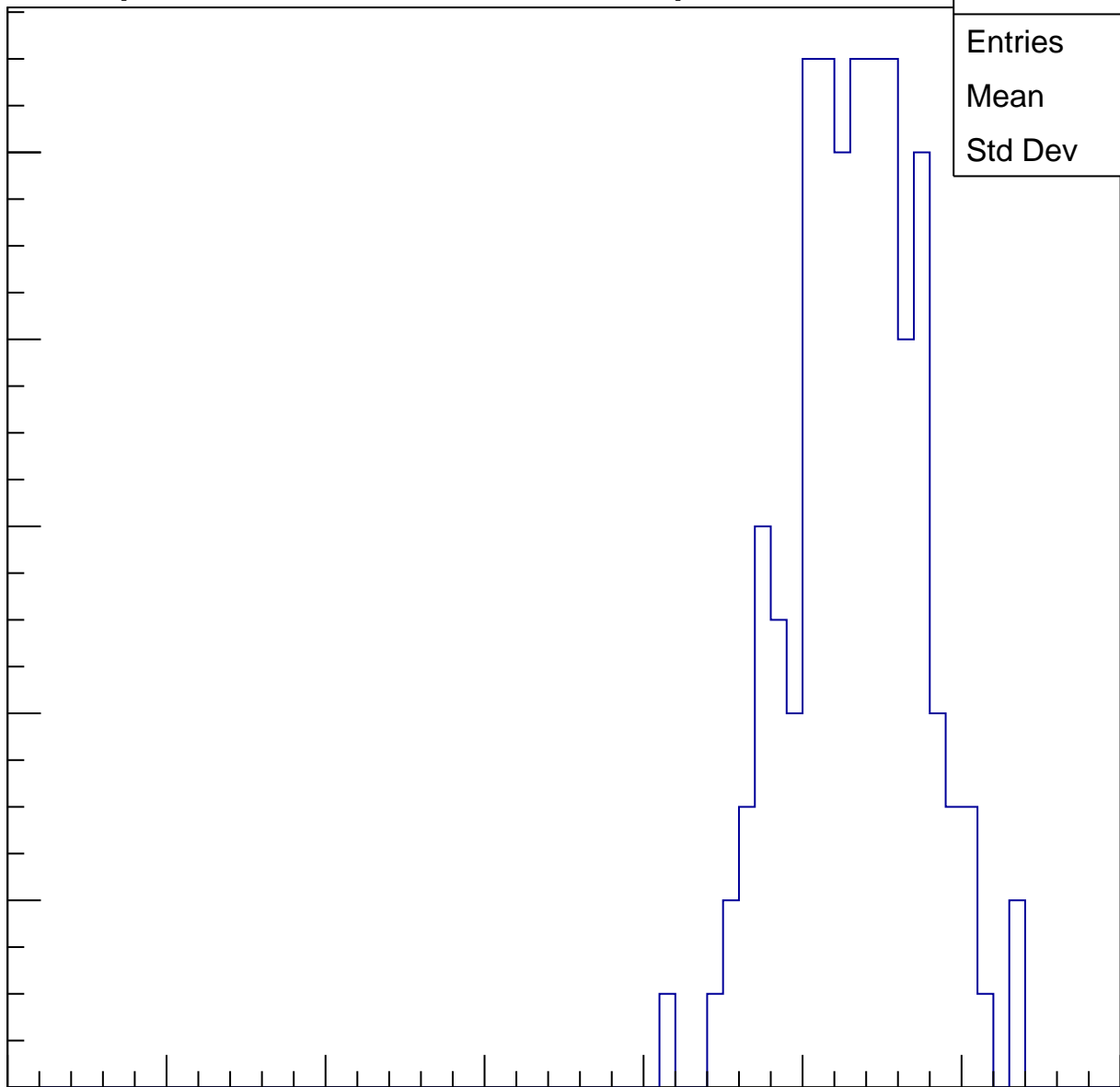
40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 52.86 |
| Std Dev | 4.077 |



# B0L101S, U16-ch80, adc5

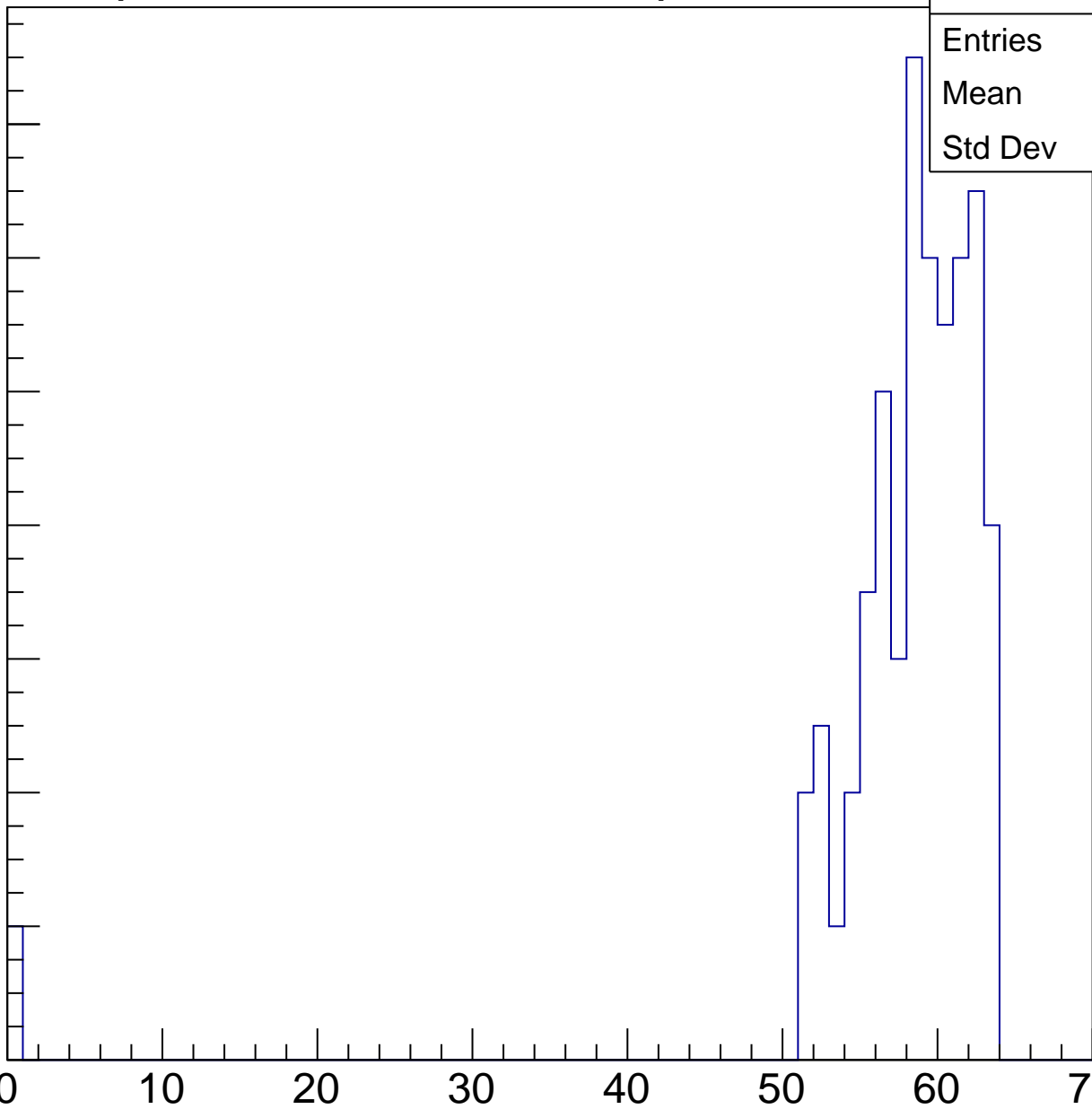
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 57.23 |
| Std Dev | 8.401 |

ampl

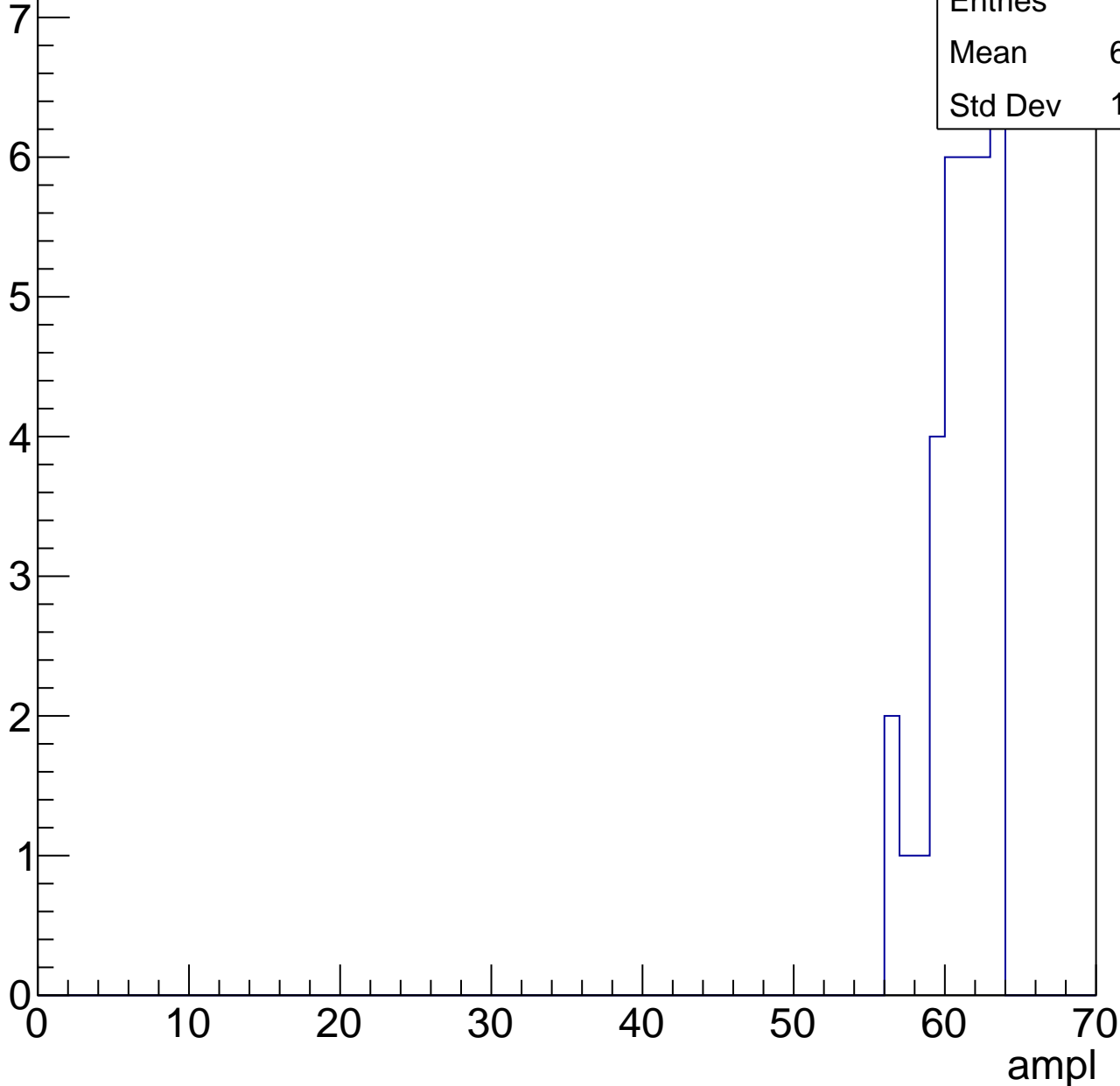


# B0L101S, U16-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 33    |
| Mean    | 60.67 |
| Std Dev | 1.964 |

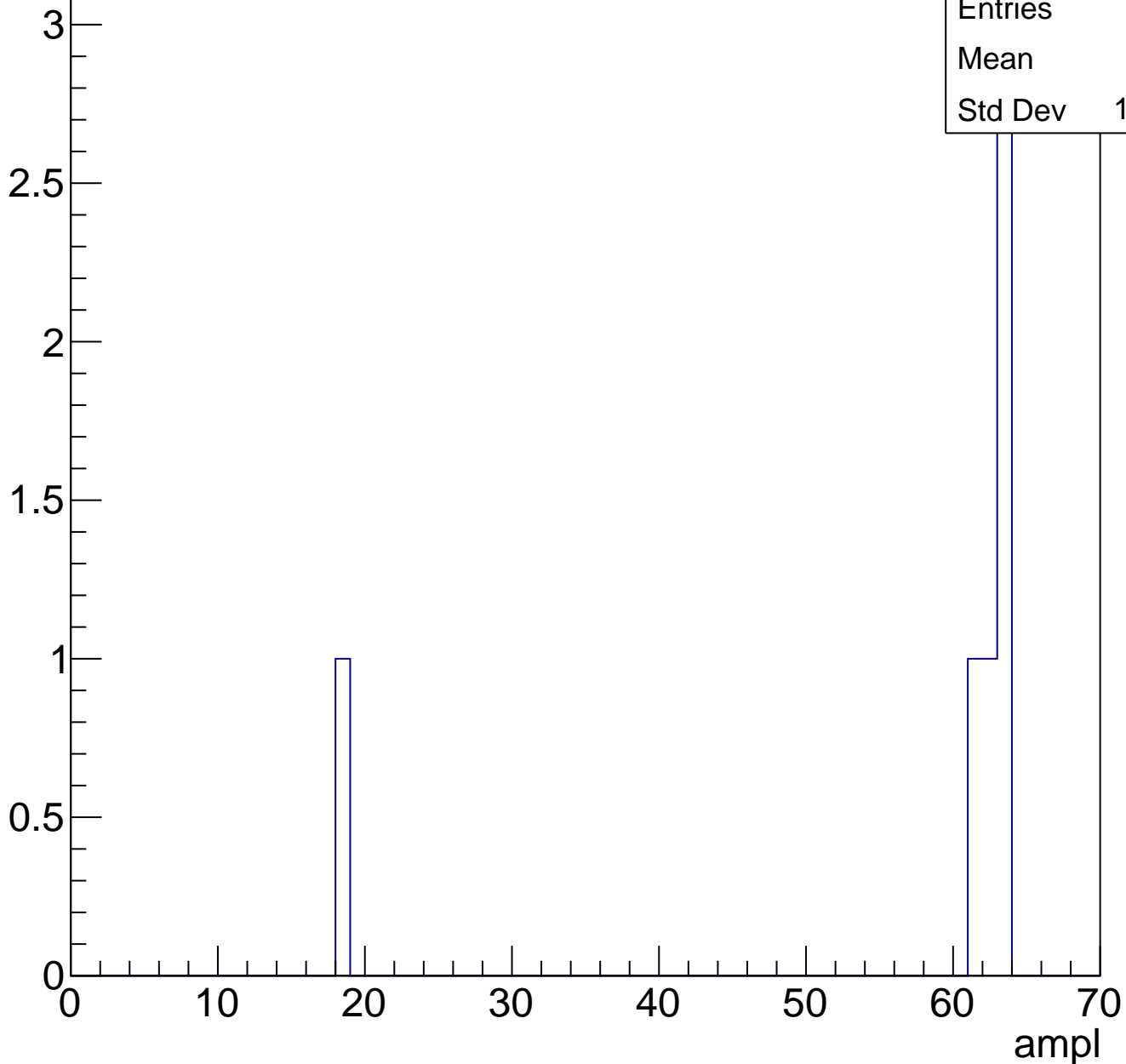




# B0L101S, U16-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch81, adc0

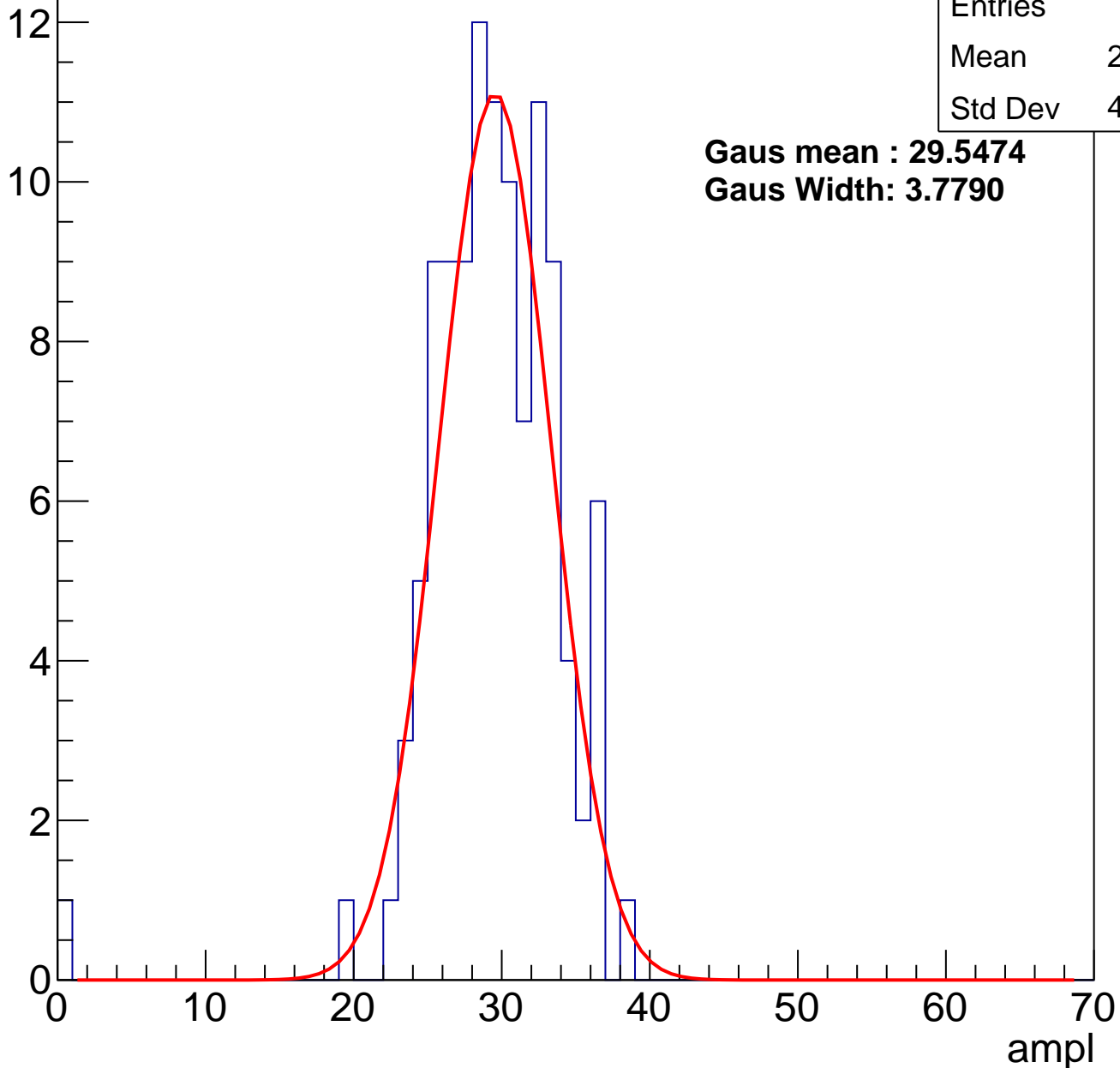
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 28.95 |
| Std Dev | 4.575 |

**Gaus mean : 29.5474**

**Gaus Width: 3.7790**

Entry



# B0L101S, U16-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 35.51 |
| Std Dev | 3.68  |

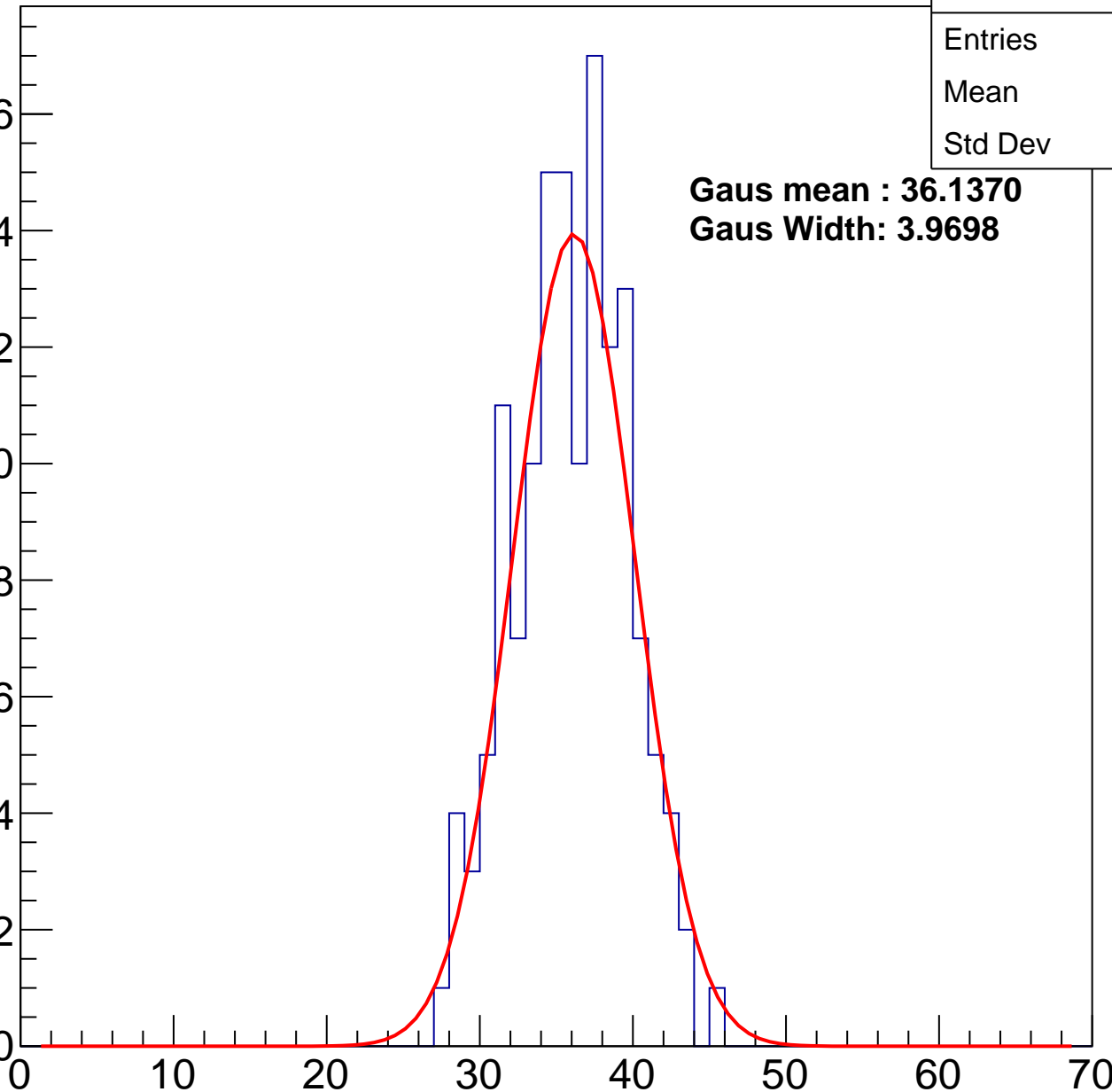
**Gaus mean : 36.1370**

**Gaus Width: 3.9698**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch81, adc2

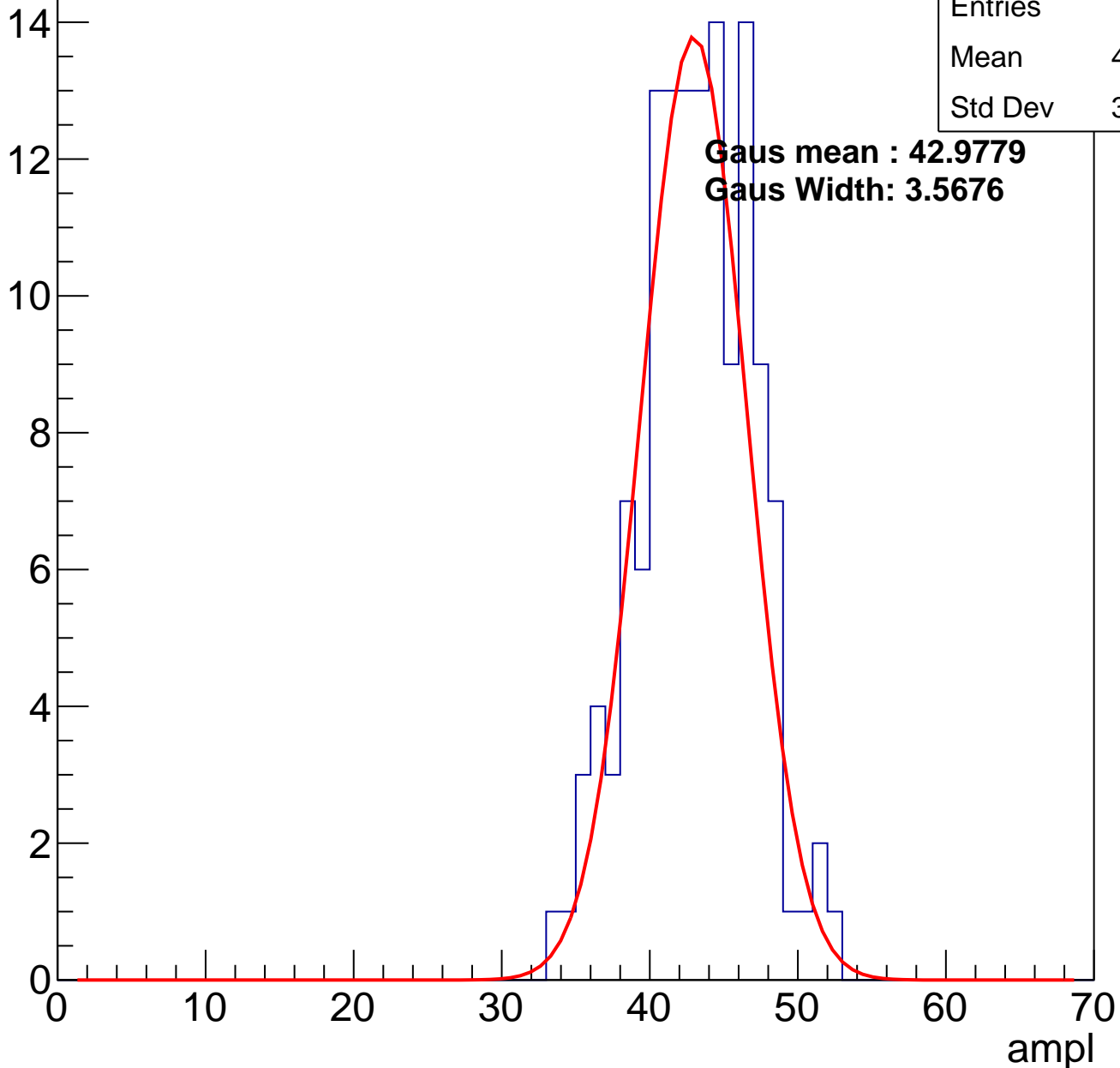
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 42.68 |
| Std Dev | 3.757 |

**Gaus mean : 42.9779**

**Gaus Width: 3.5676**

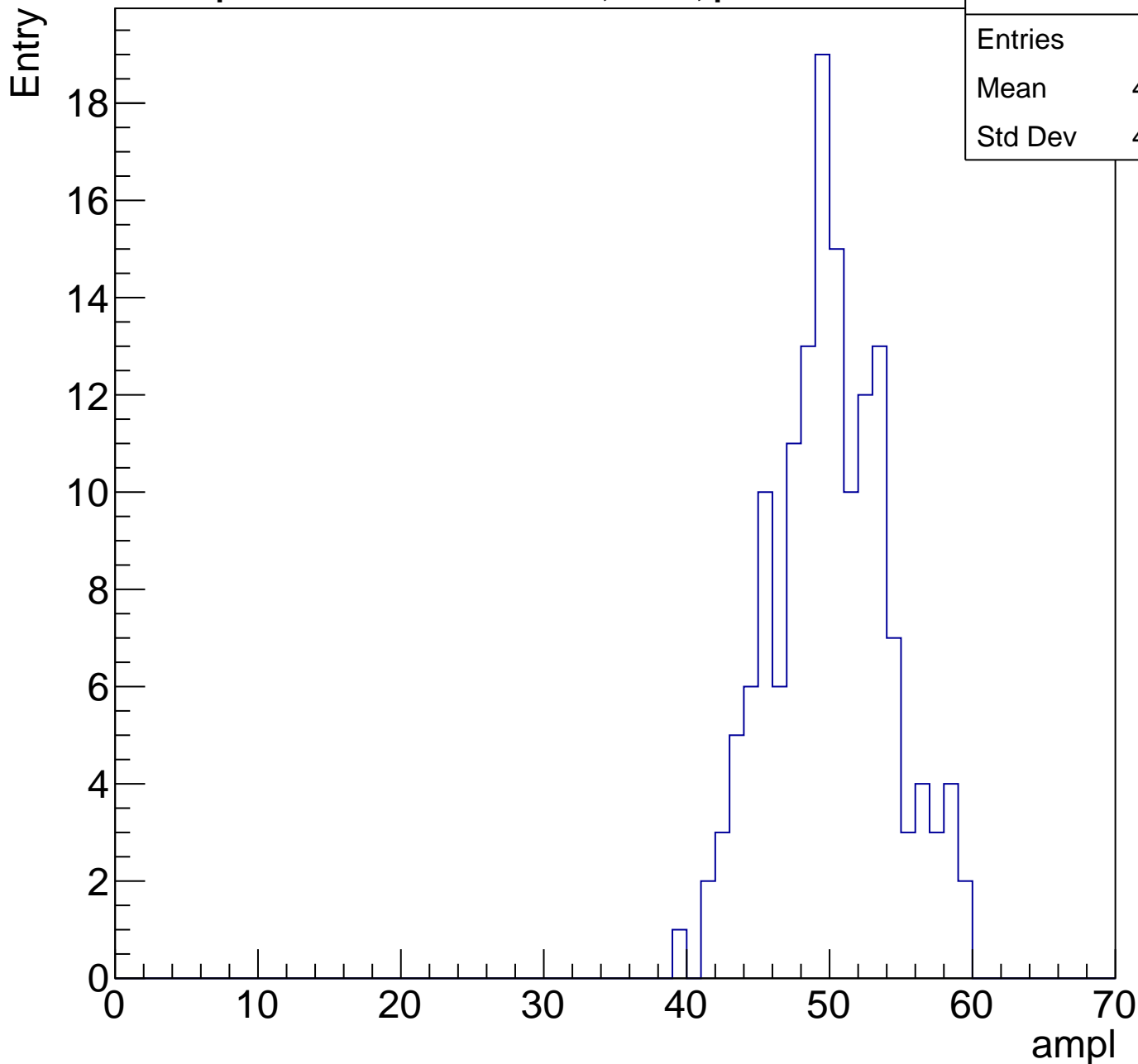
Entry



# B0L101S, U16-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 49.56 |
| Std Dev | 4.092 |

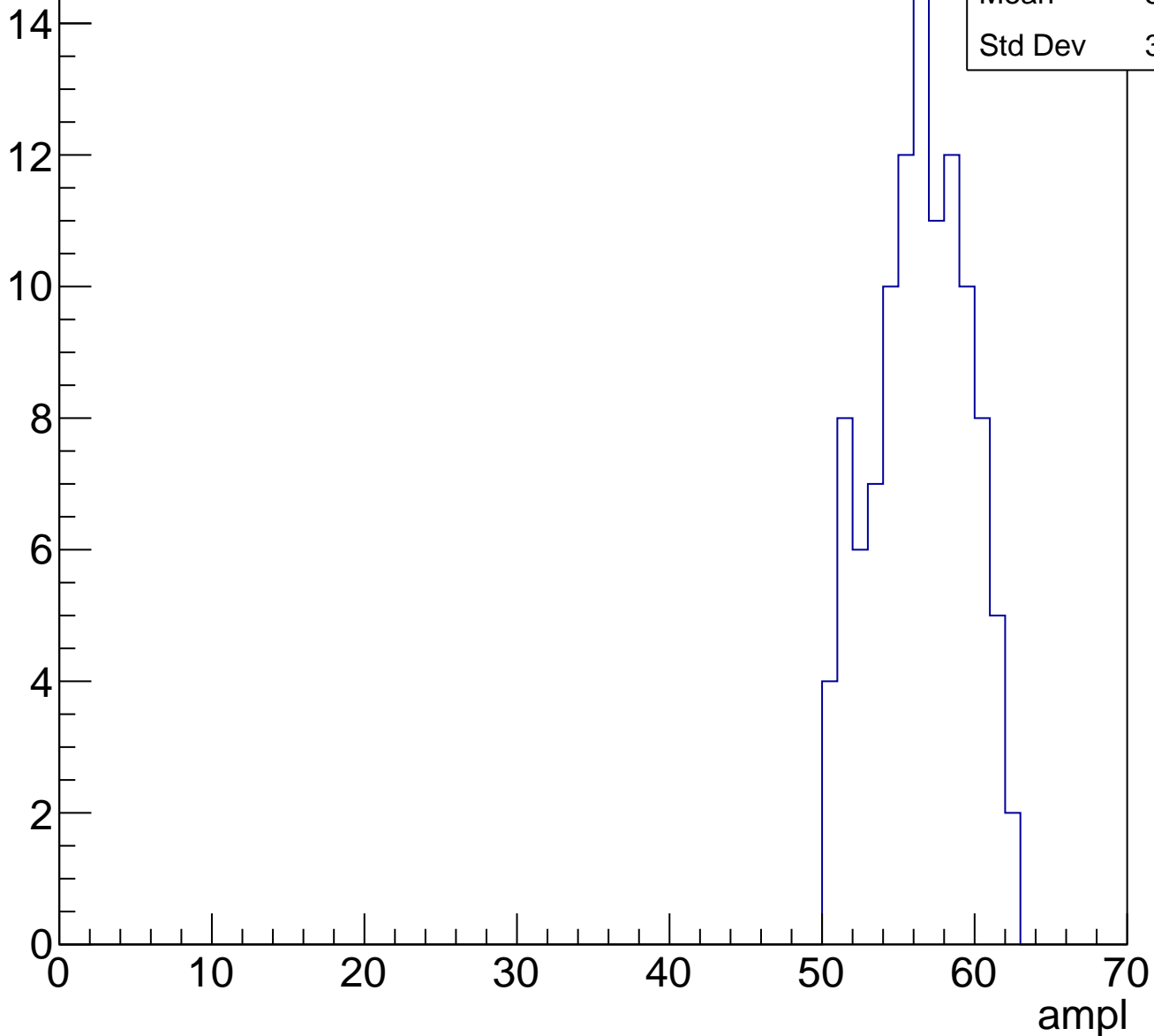


# B0L101S, U16-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 55.94 |
| Std Dev | 3.058 |

Entry



# B0L101S, U16-ch81, adc5

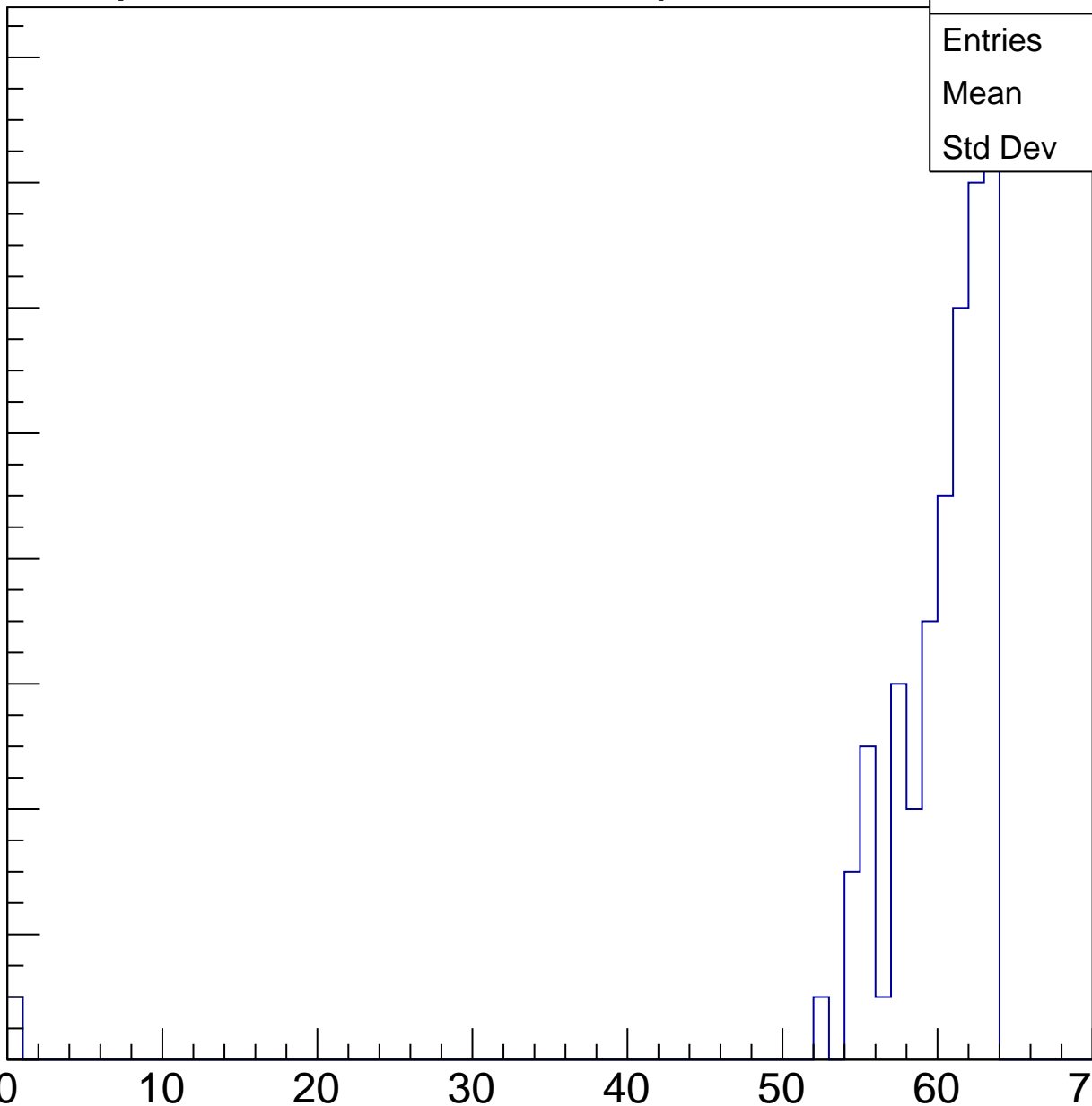
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 59.24 |
| Std Dev | 7.253 |

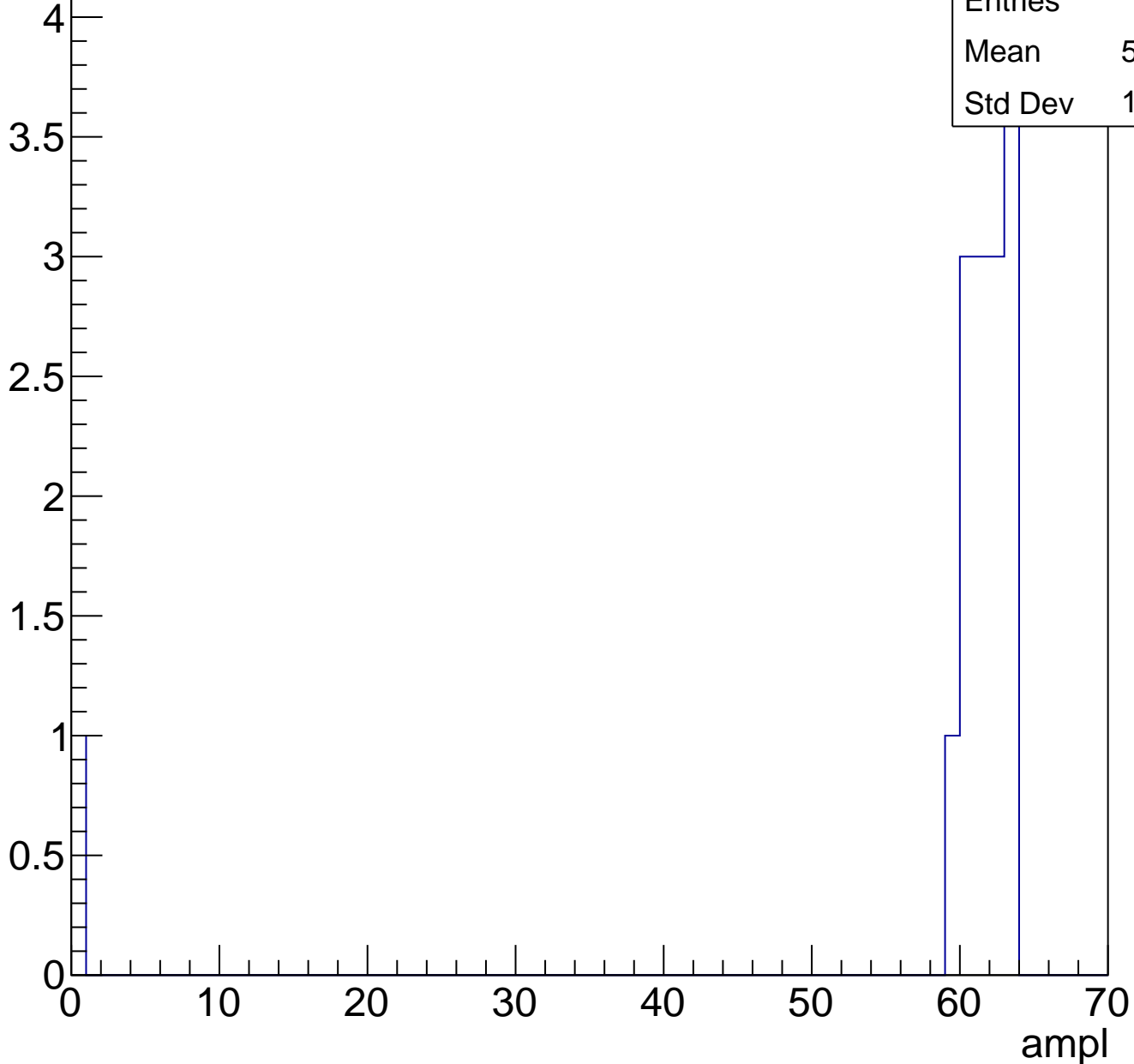
ampl



# B0L101S, U16-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

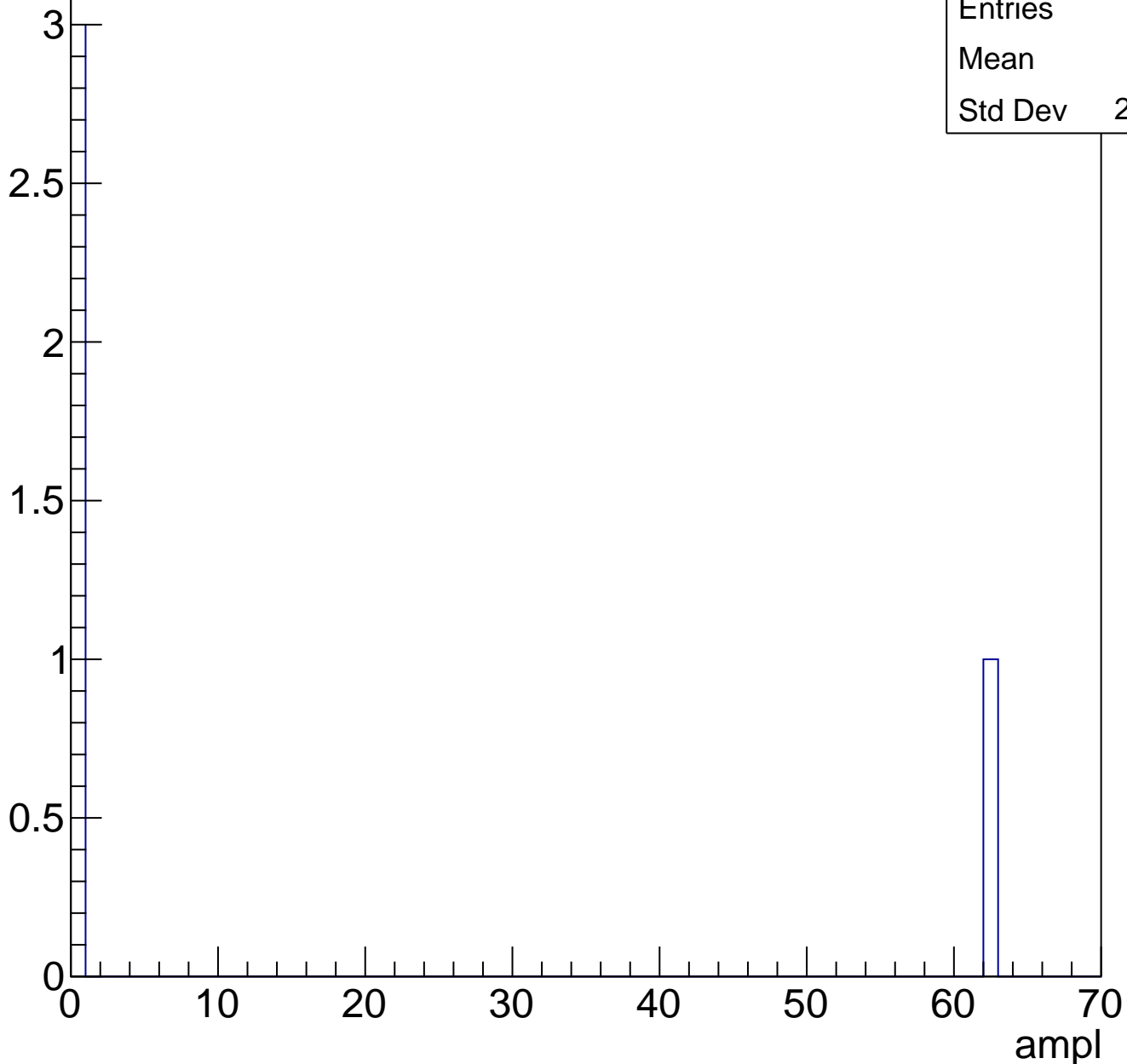




# B0L101S, U16-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 30.2  |
| Std Dev | 4.559 |

**Gaus mean : 30.9648**

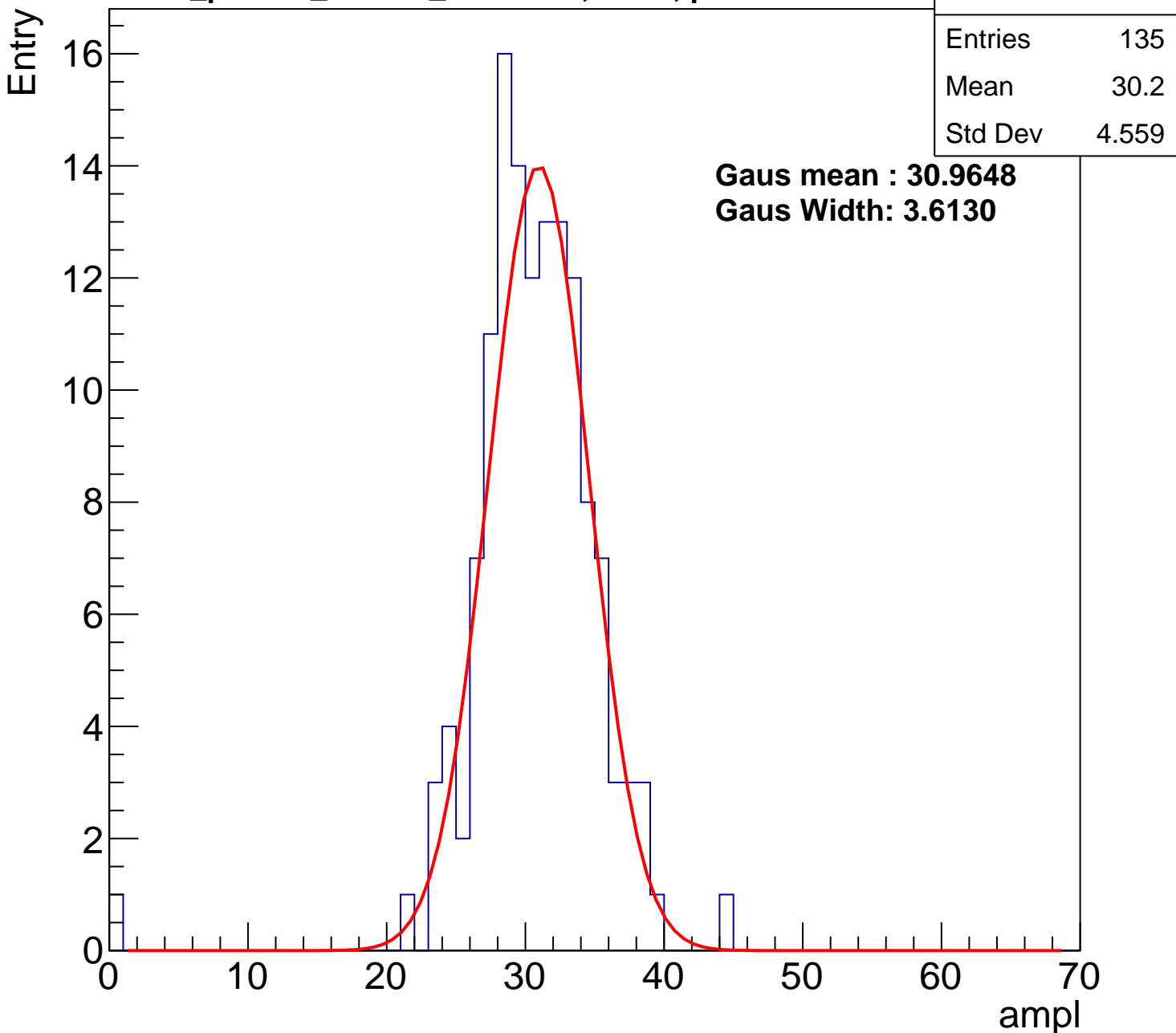
**Gaus Width: 3.6130**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



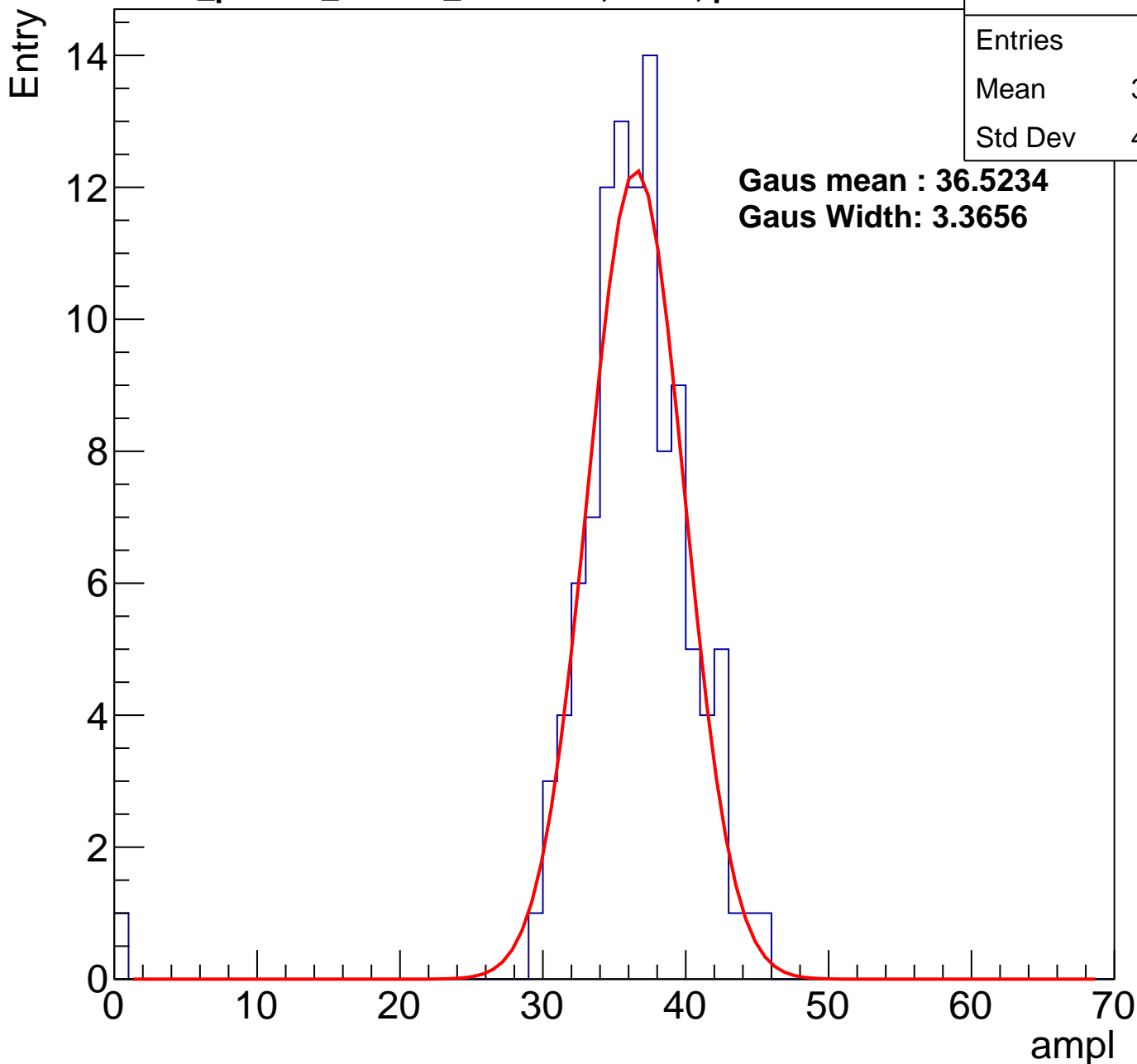
# B0L101S, U16-ch82, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 35.89 |
| Std Dev | 4.788 |

**Gaus mean : 36.5234**

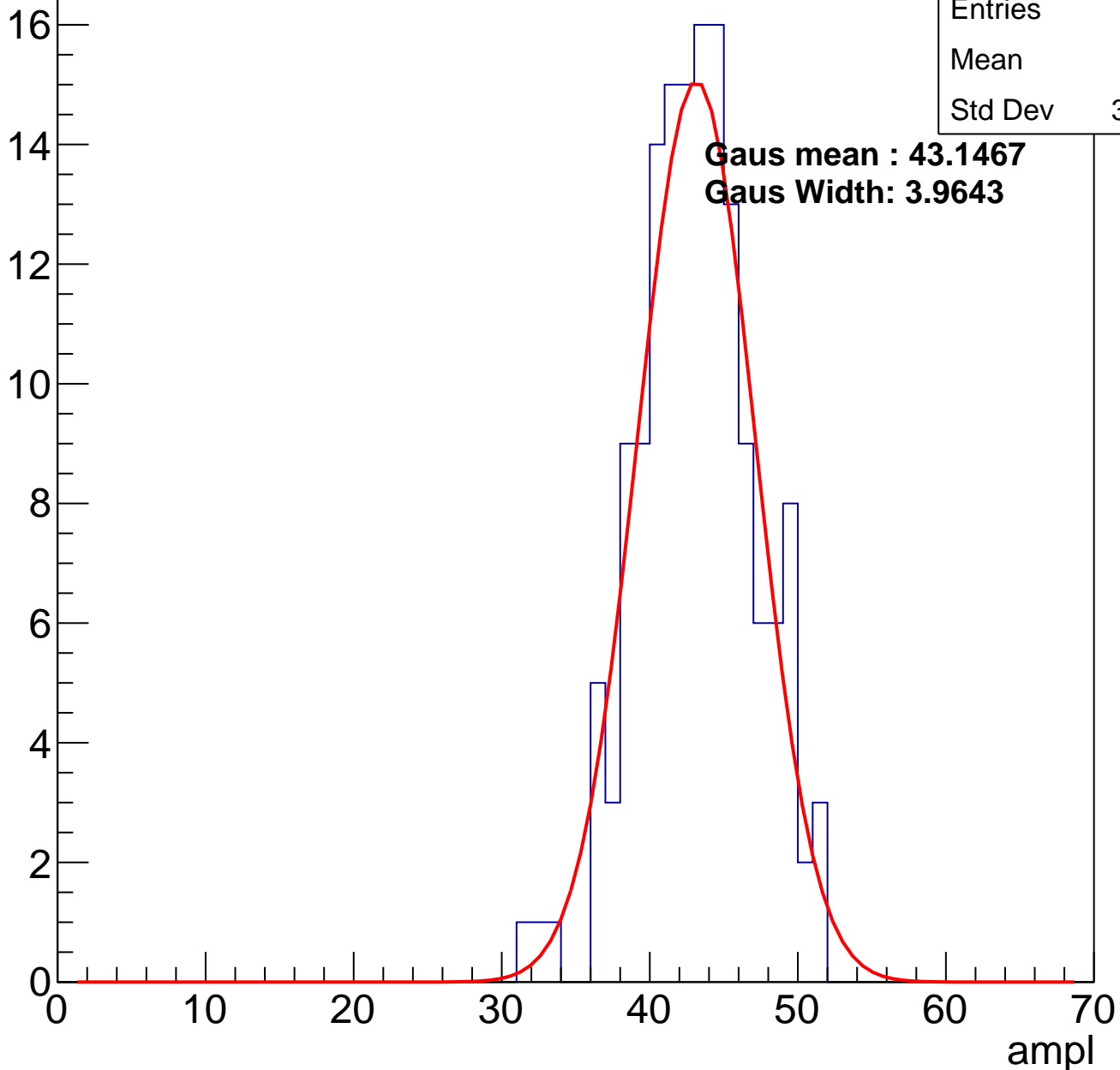
**Gaus Width: 3.3656**



# B0L101S, U16-ch82, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

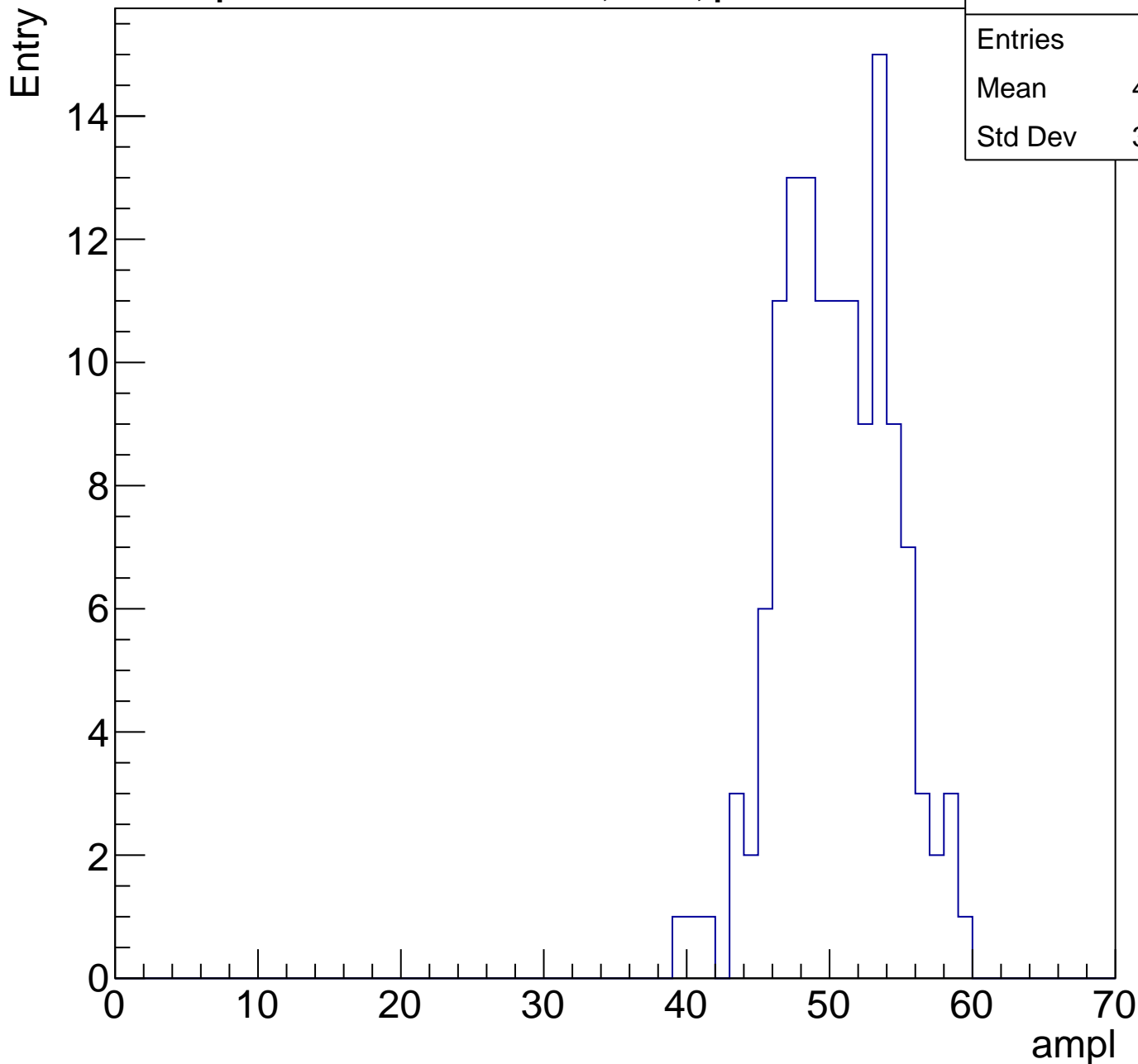
Entry



# B0L101S, U16-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 49.98 |
| Std Dev | 3.885 |

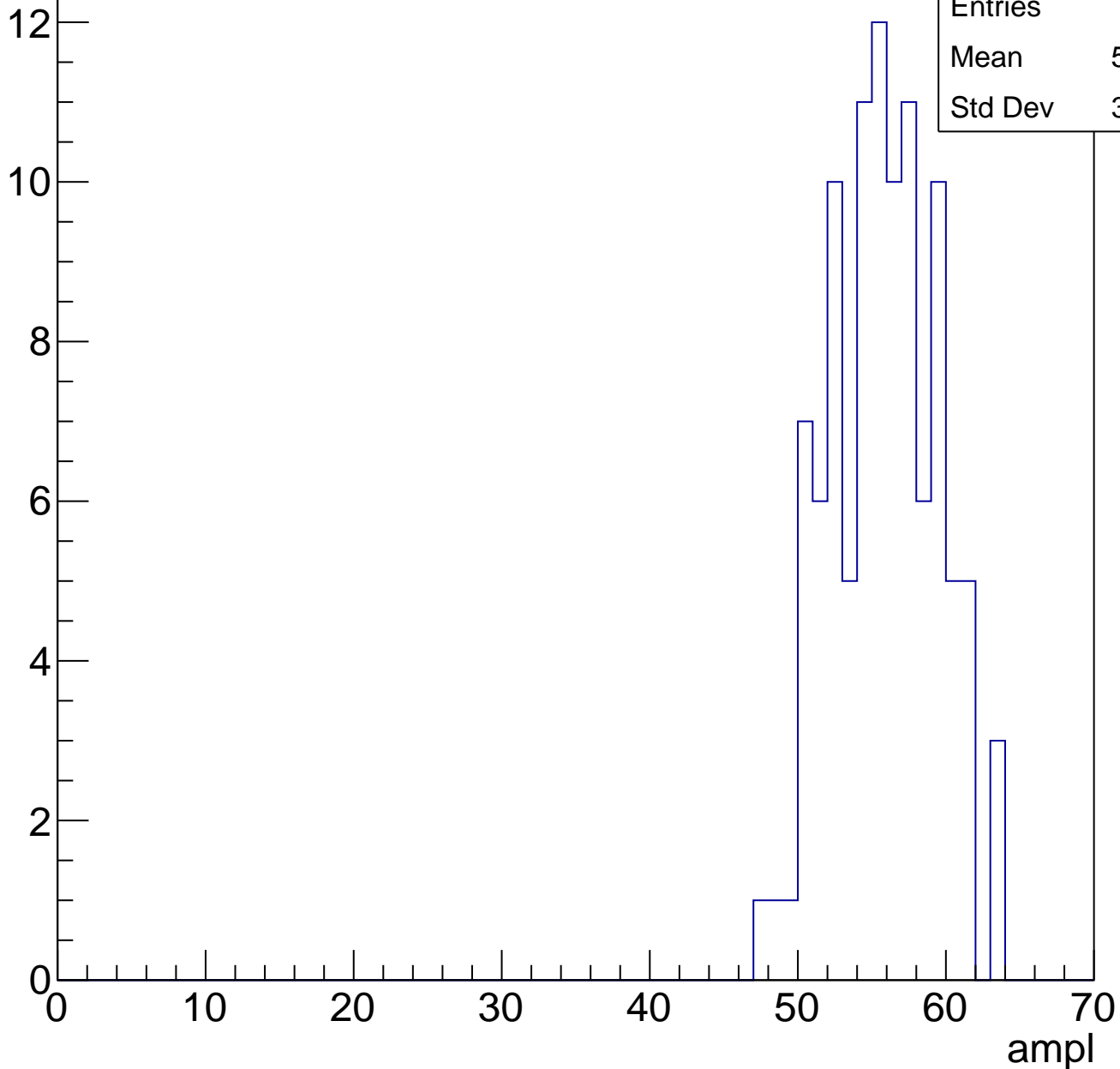


# B0L101S, U16-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 55.37 |
| Std Dev | 3.525 |

Entry



# B0L101S, U16-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

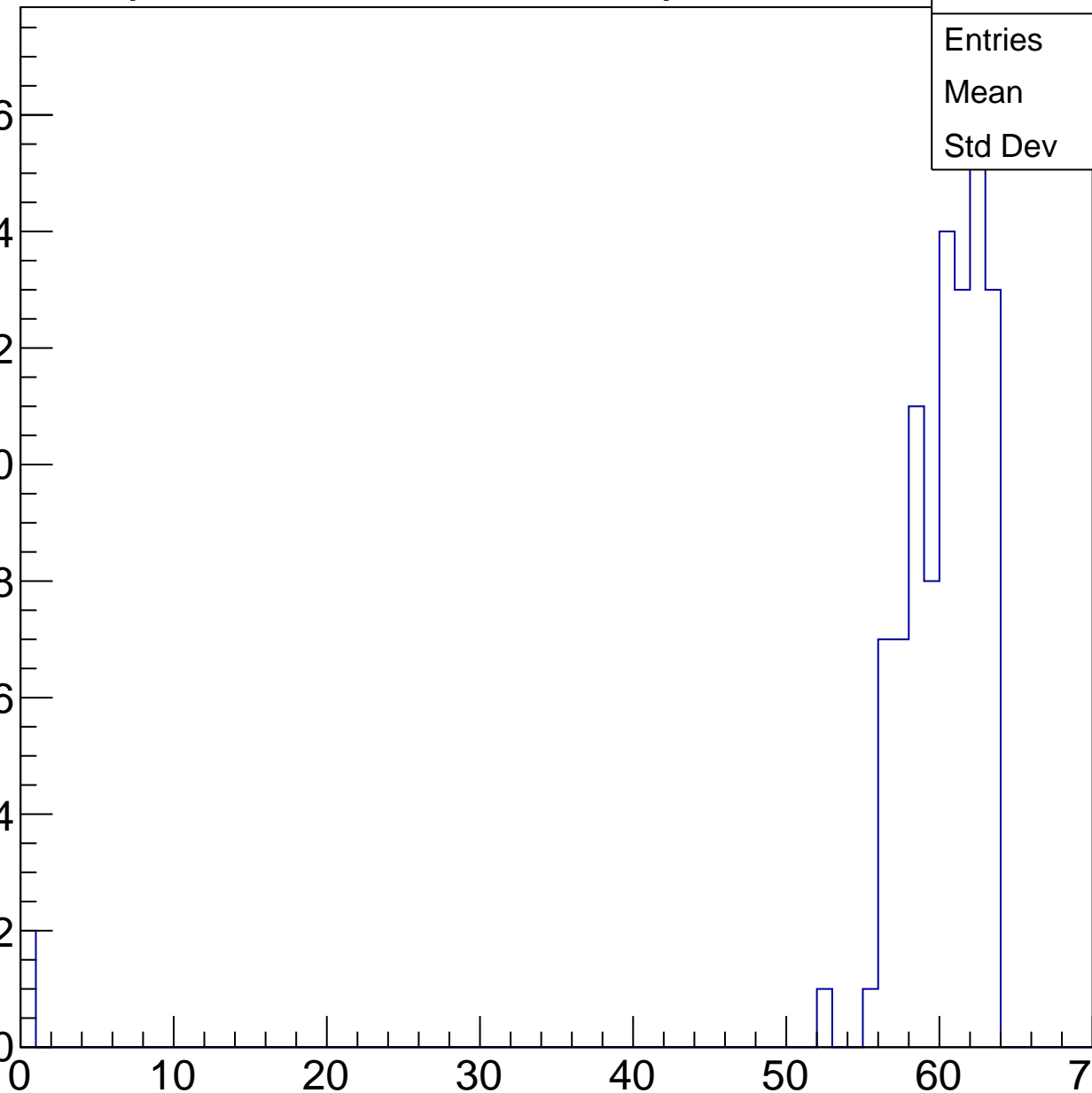
4

2

0

|         |       |
|---------|-------|
| Entries | 94    |
| Mean    | 58.66 |
| Std Dev | 8.959 |

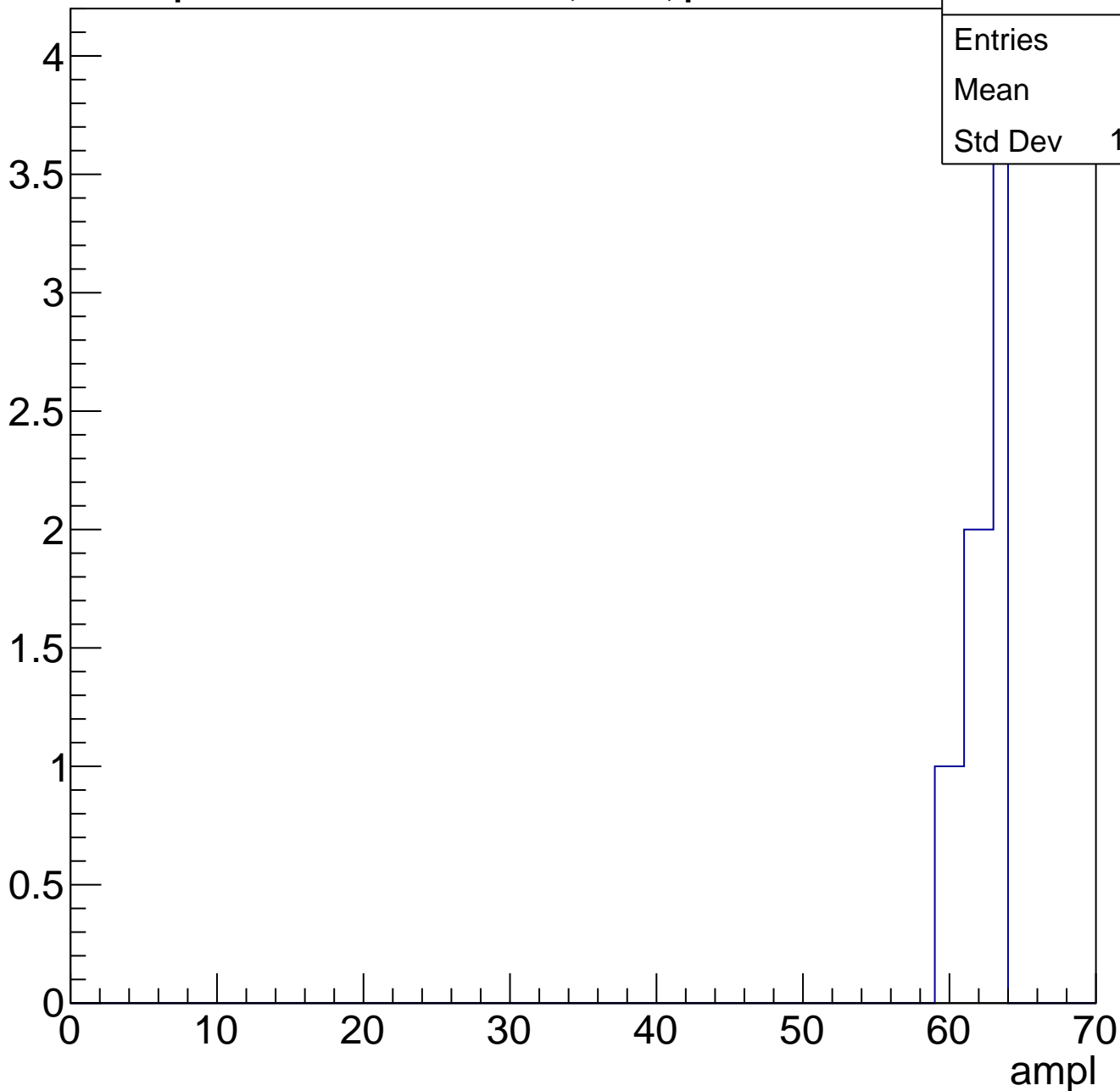
ampl



# B0L101S, U16-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch83, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 154   |
| Mean    | 28.23 |
| Std Dev | 4.78  |

**Gaus mean : 29.0976**

**Gaus Width: 3.6057**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

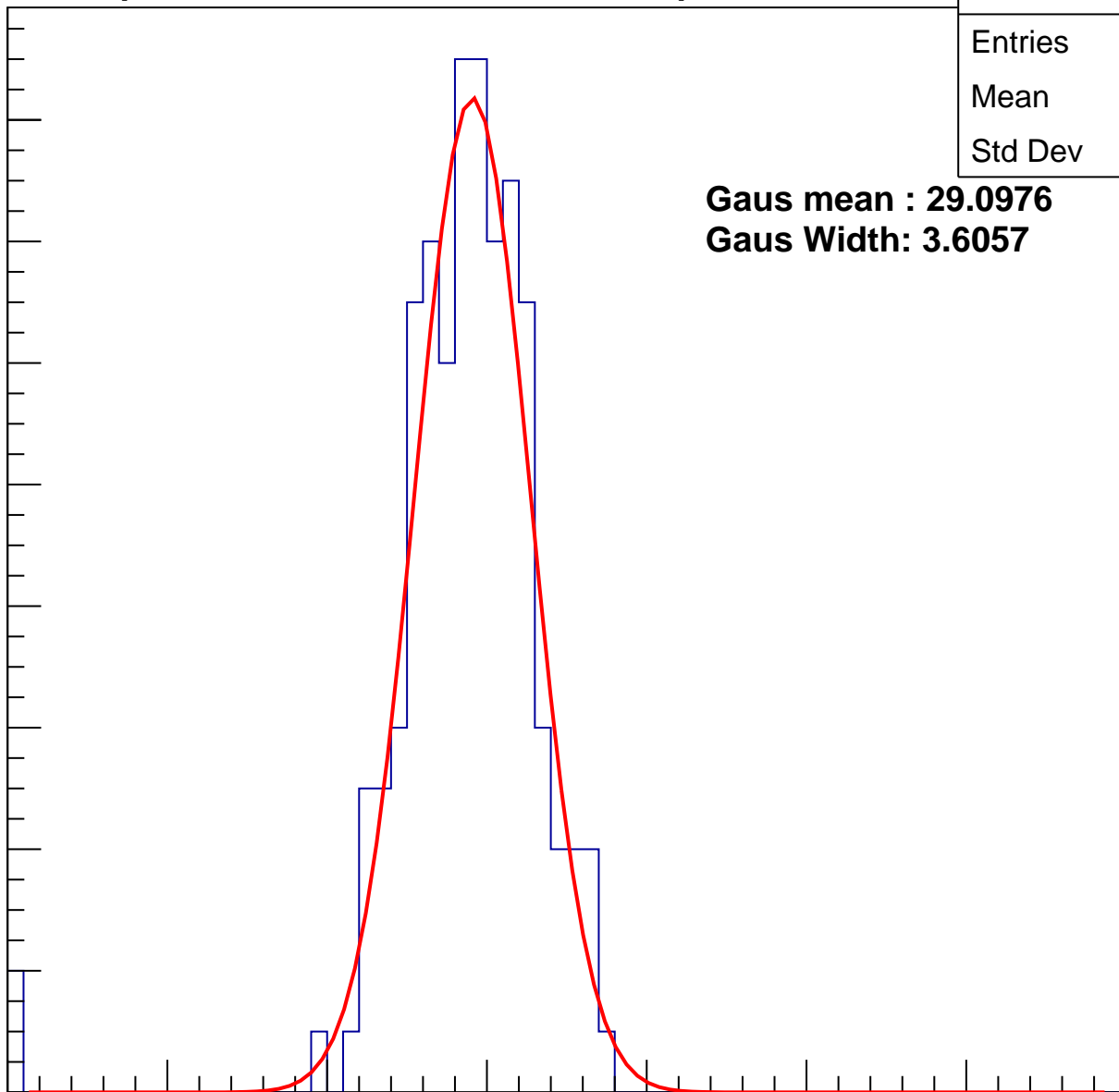
40

50

60

70

ampl



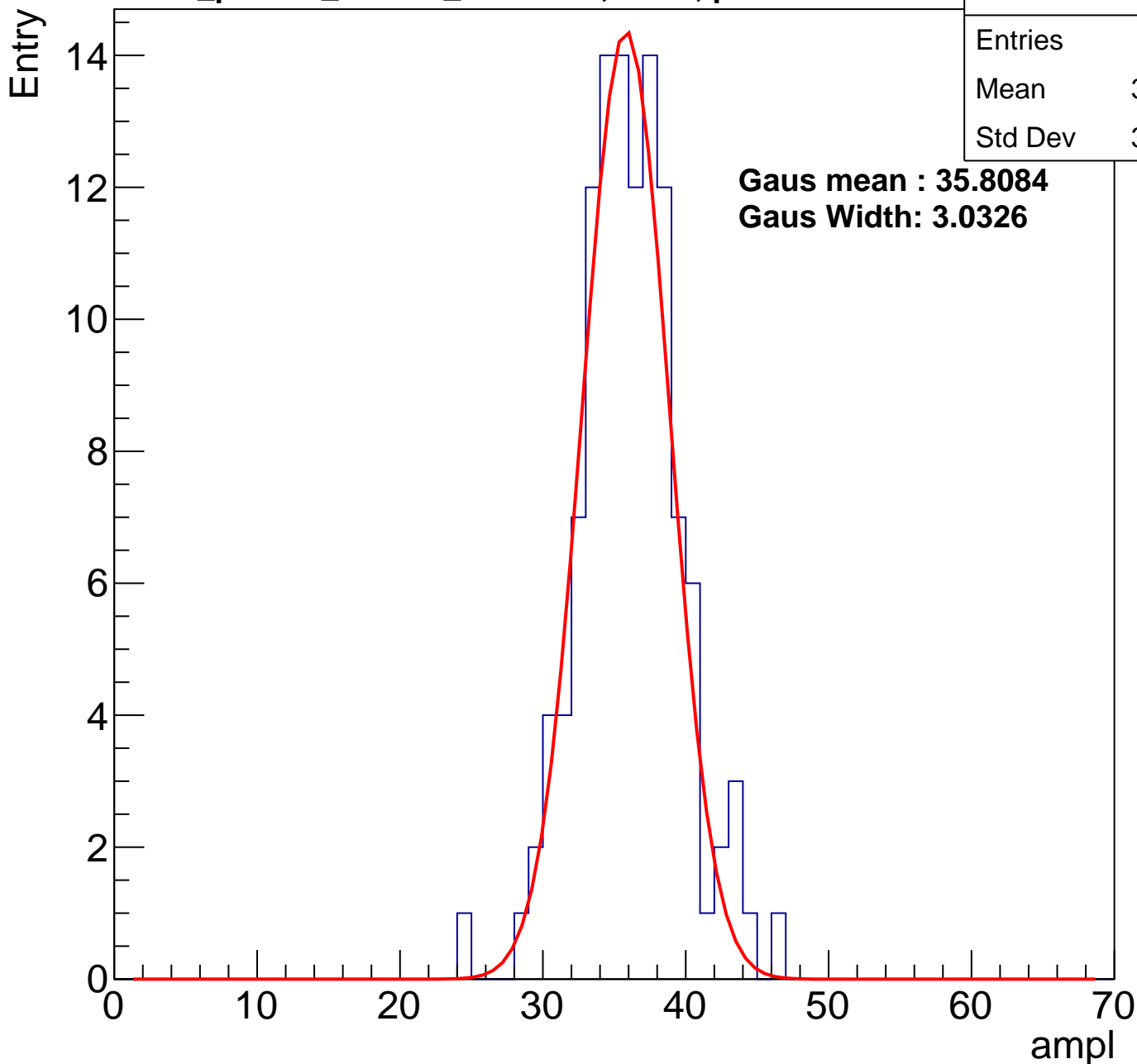
# B0L101S, U16-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 35.62 |
| Std Dev | 3.508 |

**Gaus mean : 35.8084**

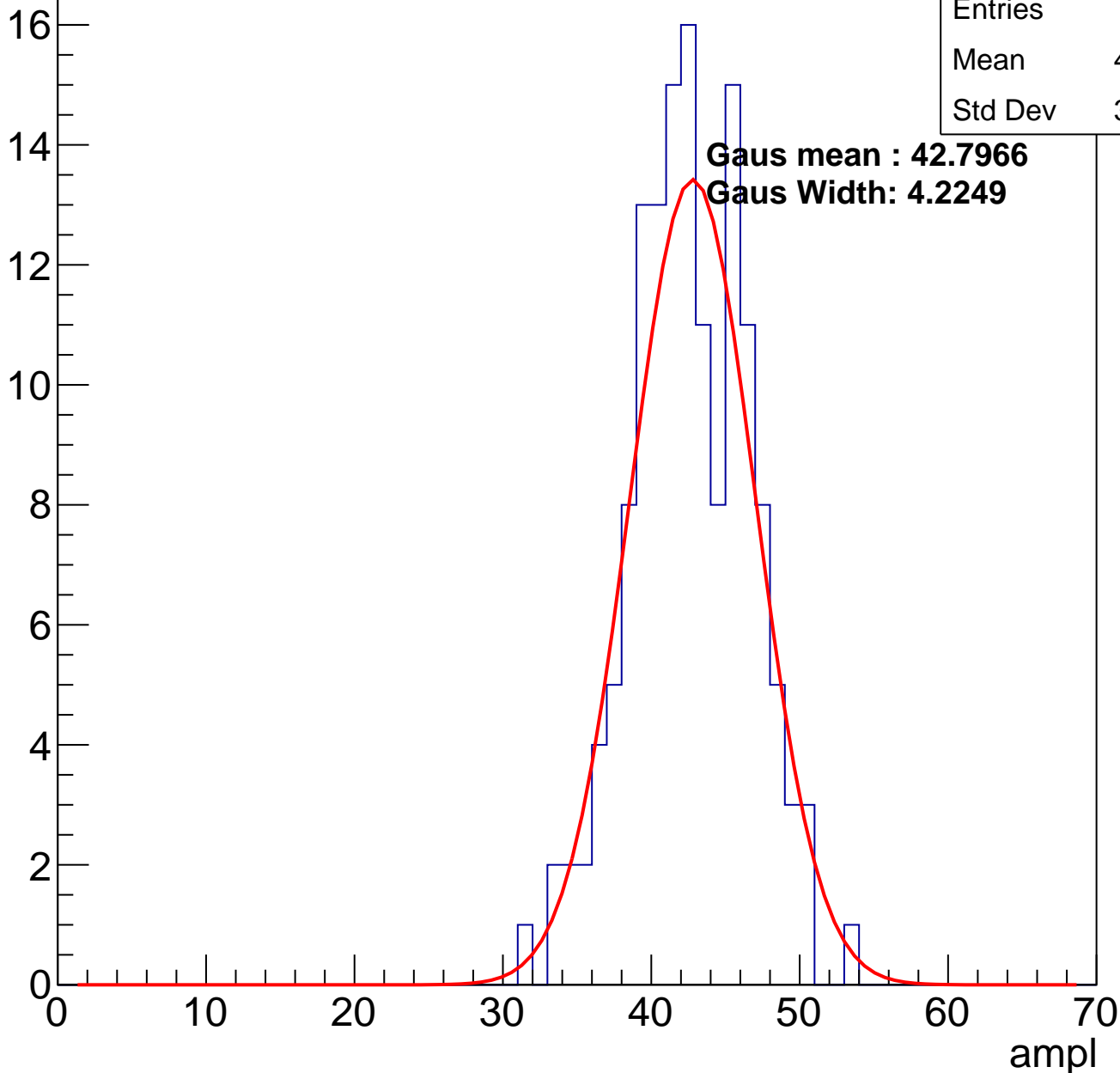
**Gaus Width: 3.0326**



# B0L101S, U16-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

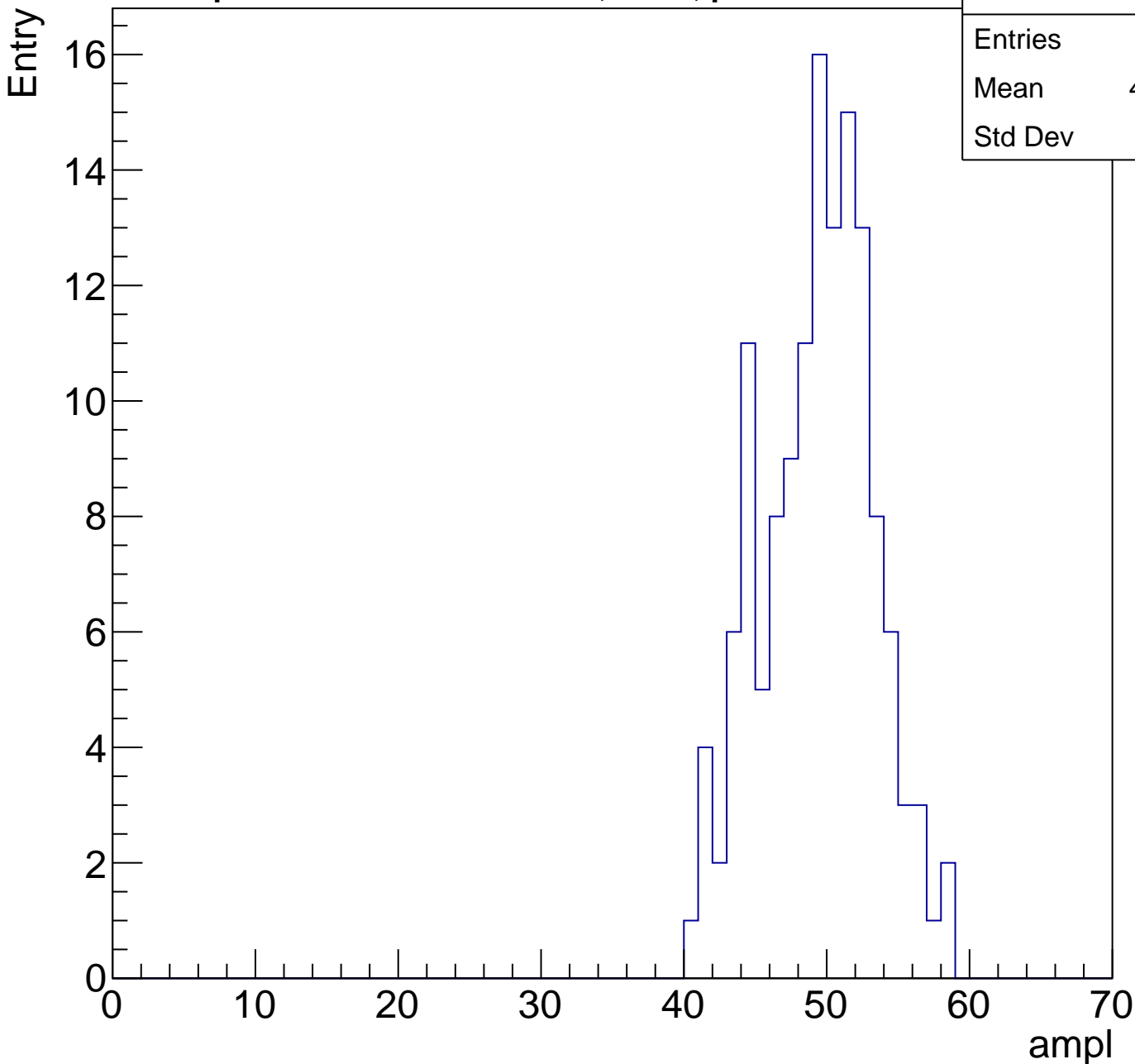
Entry



# B0L101S, U16-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

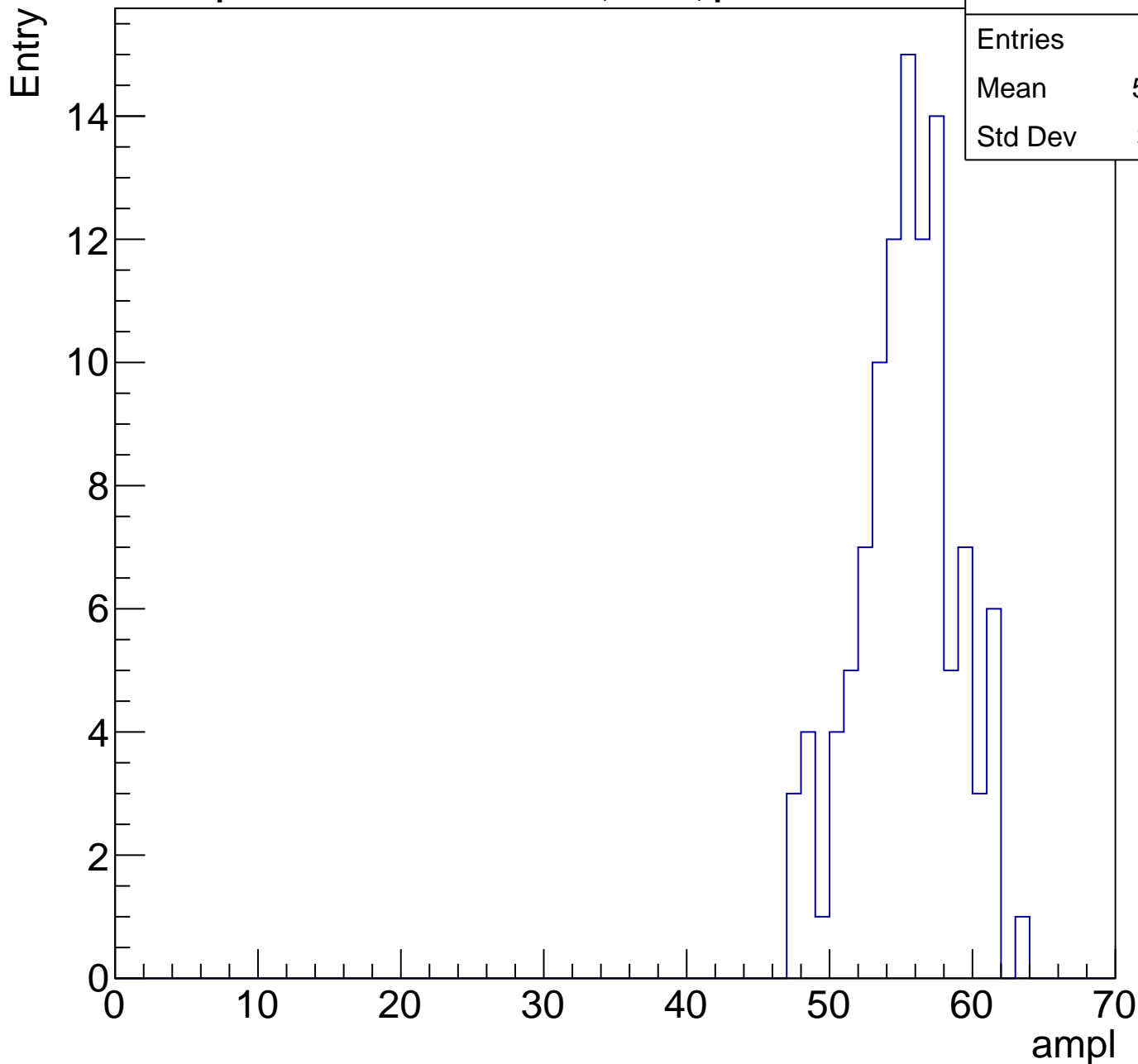
|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 48.93 |
| Std Dev | 3.89  |



# B0L101S, U16-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 54.92 |
| Std Dev | 3.451 |



# B0L101S, U16-ch83, adc5

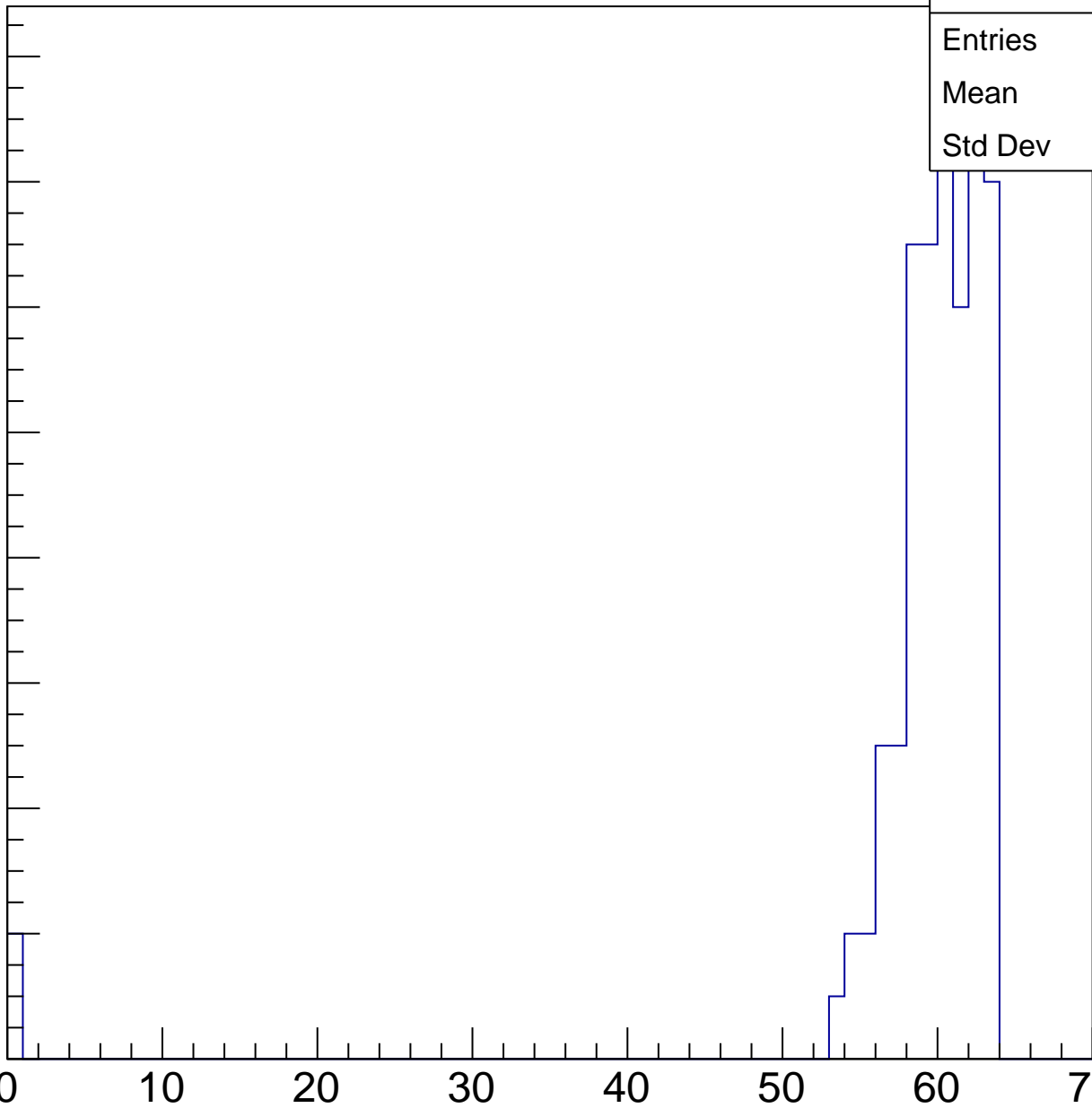
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 100   |
| Mean    | 58.61 |
| Std Dev | 8.698 |

ampl

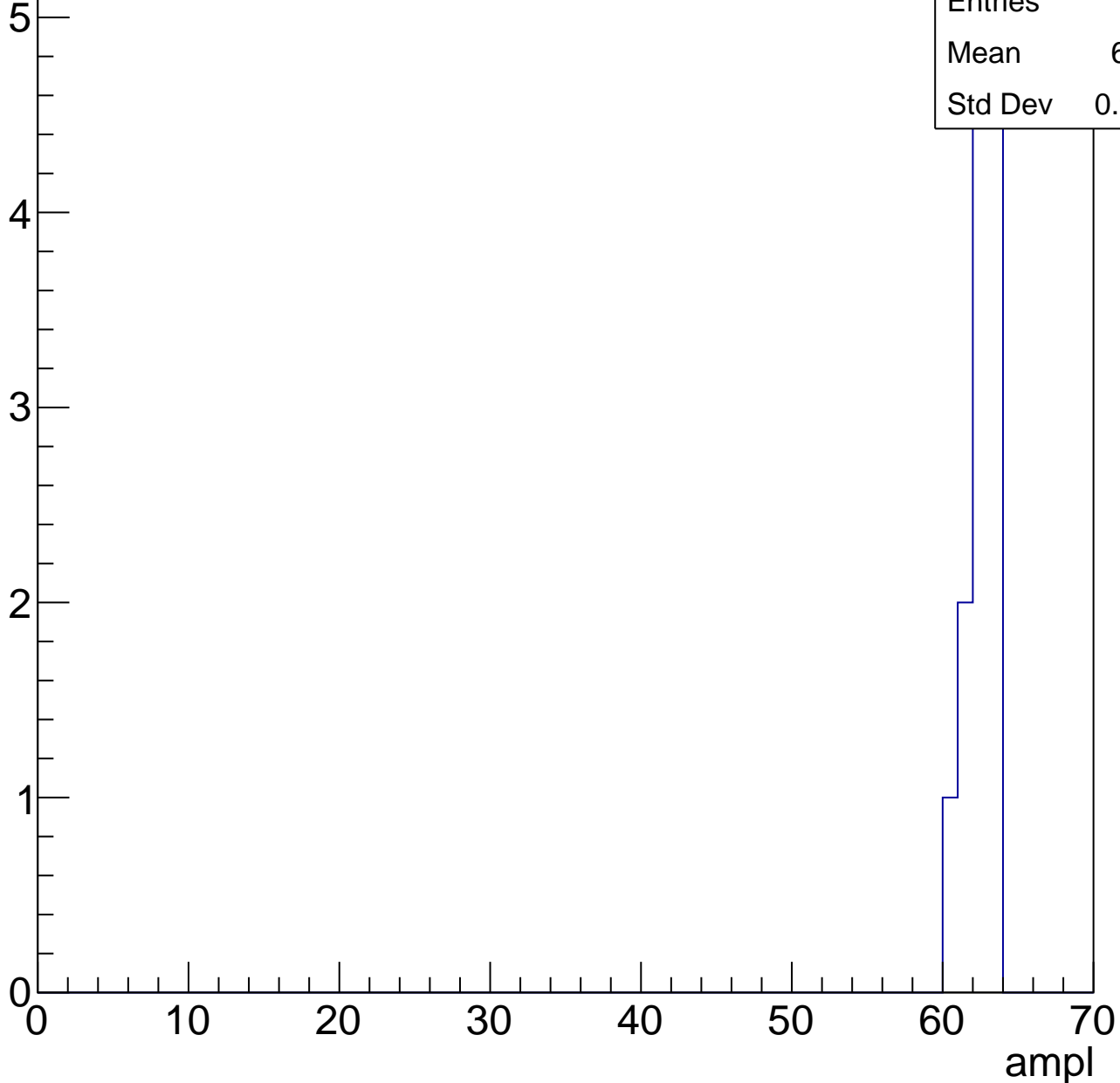


# B0L101S, U16-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 13     |
| Mean    | 62.08  |
| Std Dev | 0.9166 |





# B0L101S, U16-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

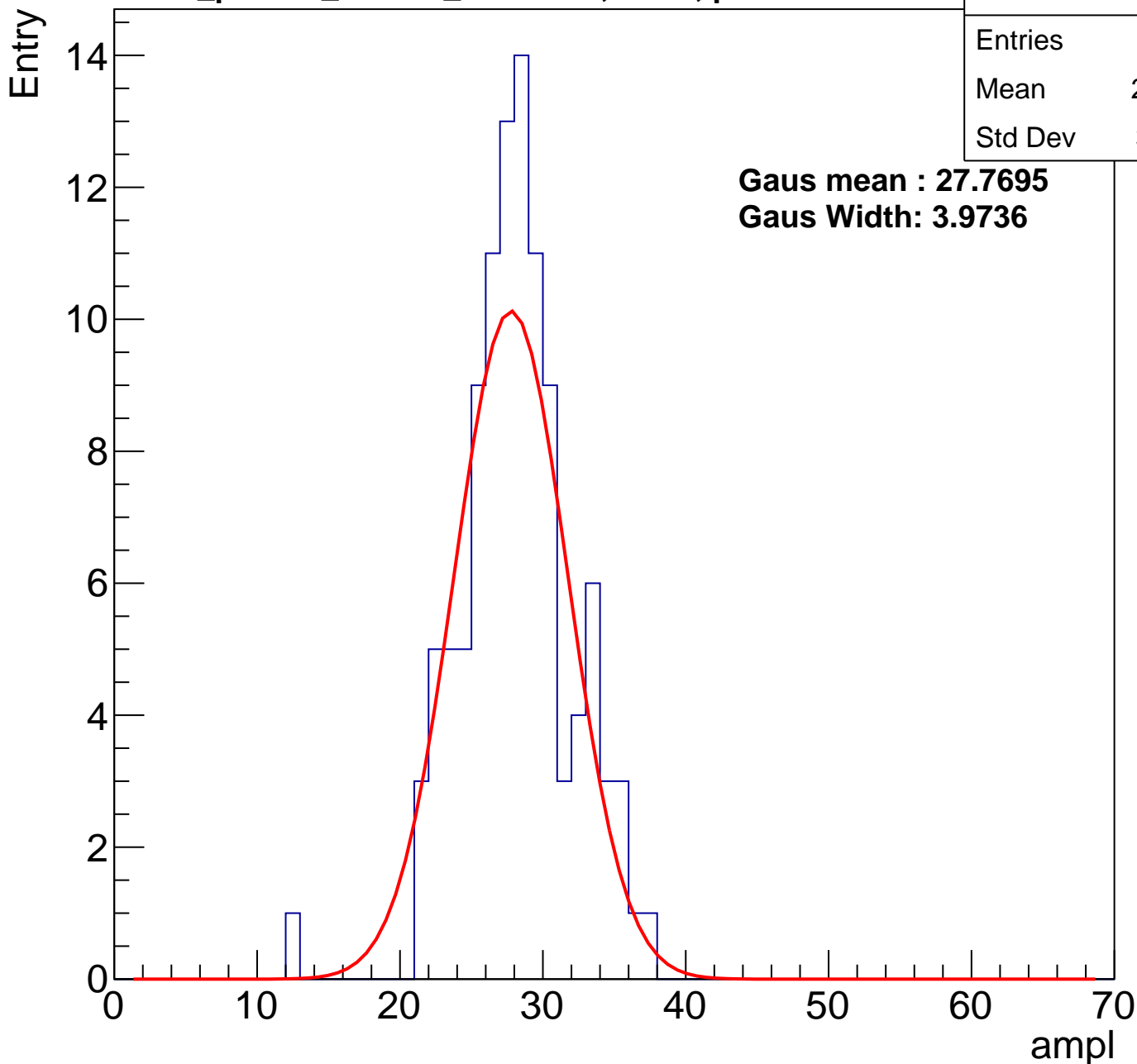
# B0L101S, U16-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 27.68 |
| Std Dev | 3.871 |

**Gaus mean : 27.7695**

**Gaus Width: 3.9736**



# B0L101S, U16-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 33.57 |
| Std Dev | 4.058 |

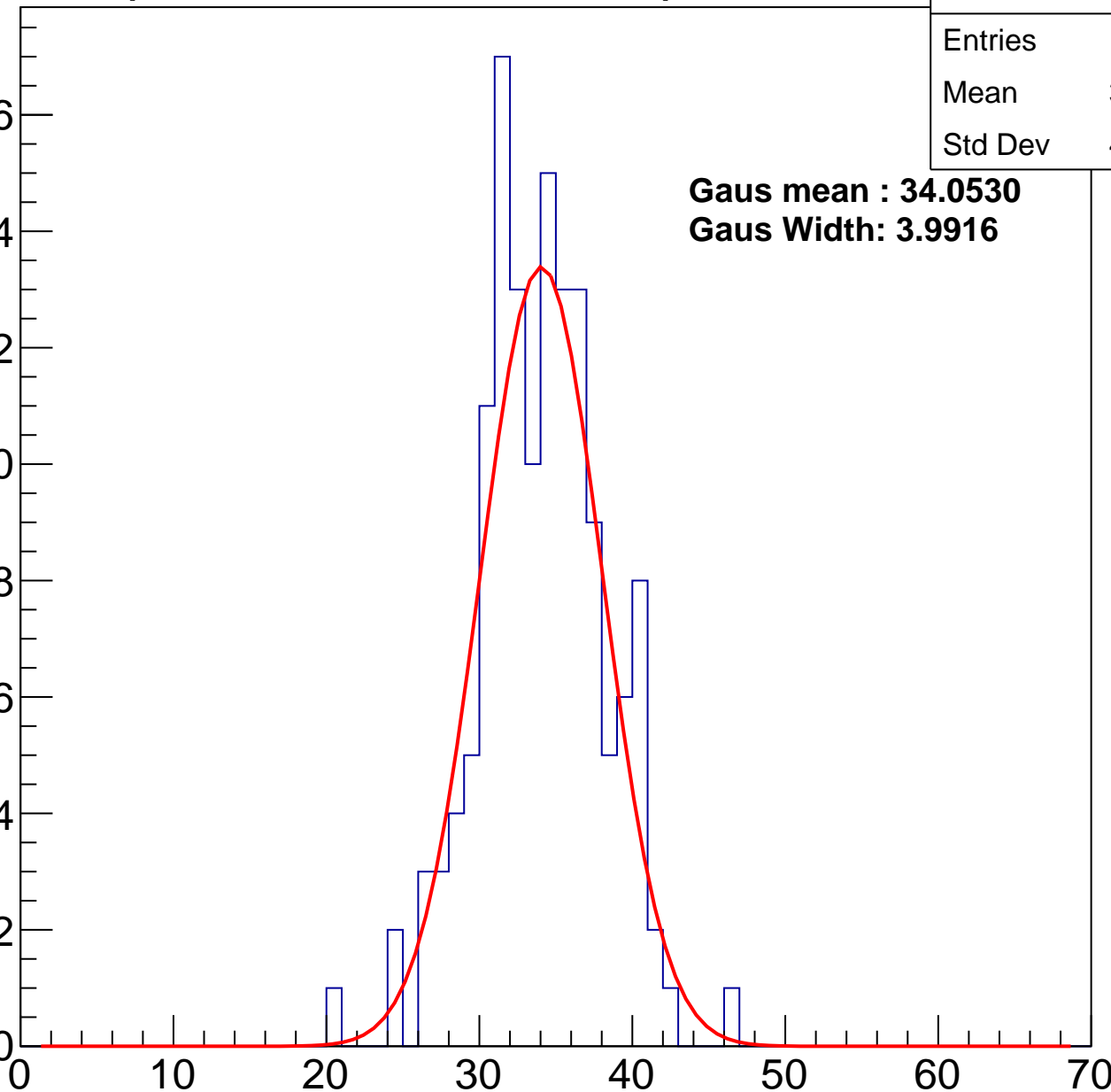
**Gaus mean : 34.0530**

**Gaus Width: 3.9916**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

133

Mean

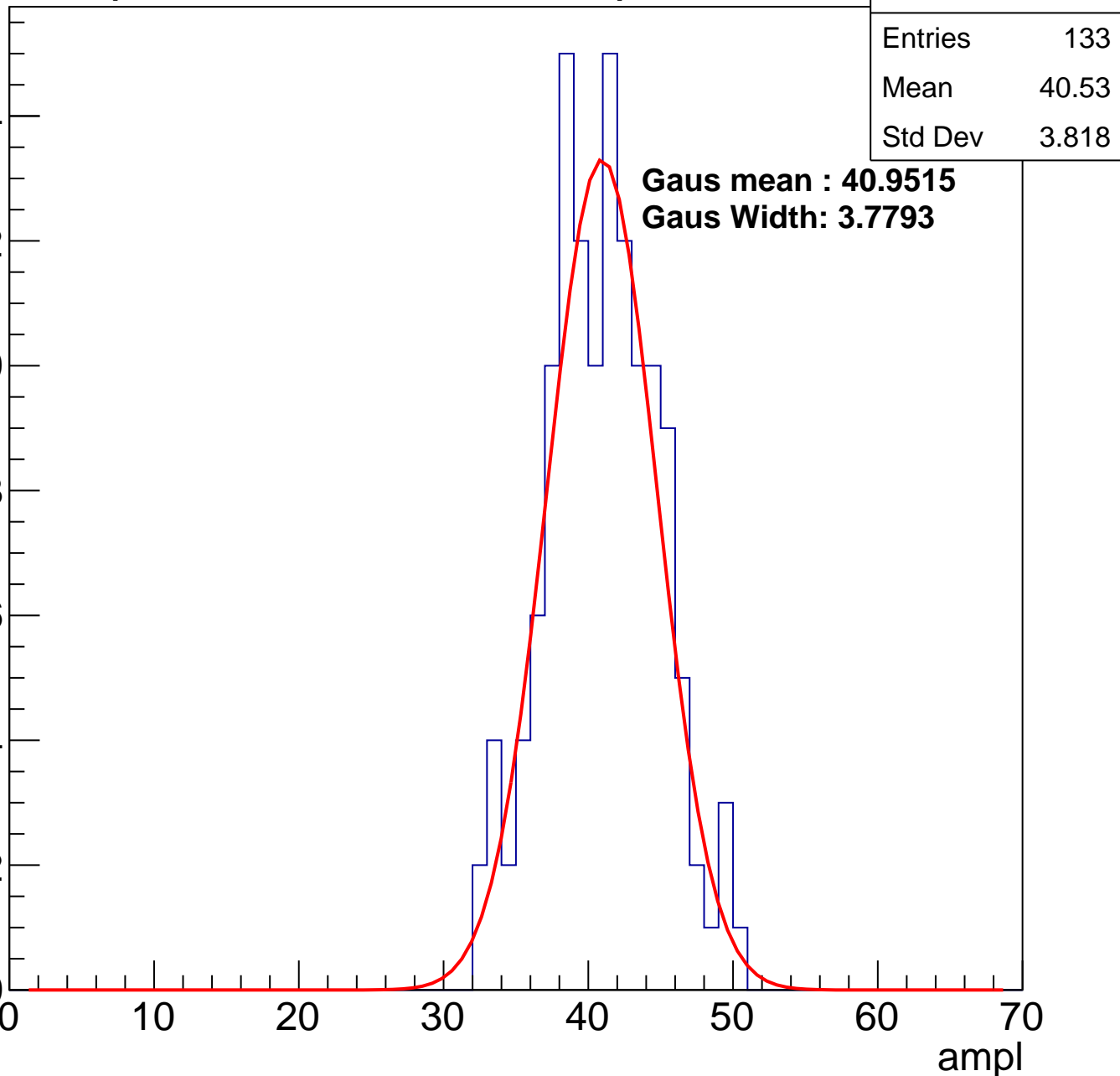
40.53

Std Dev

3.818

**Gaus mean : 40.9515**

**Gaus Width: 3.7793**



# B0L101S, U16-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

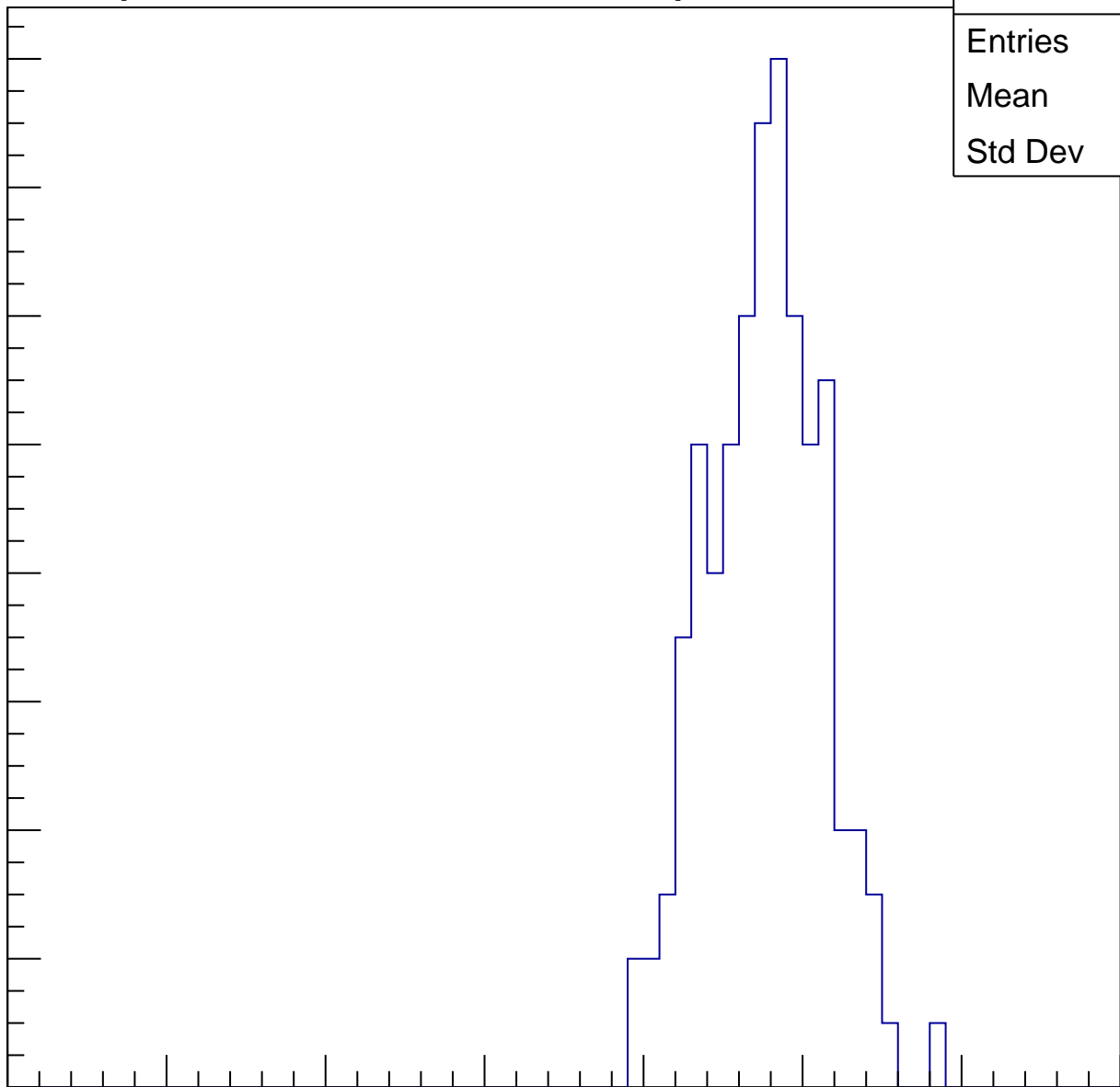
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 47.15 |
| Std Dev | 3.598 |

ampl

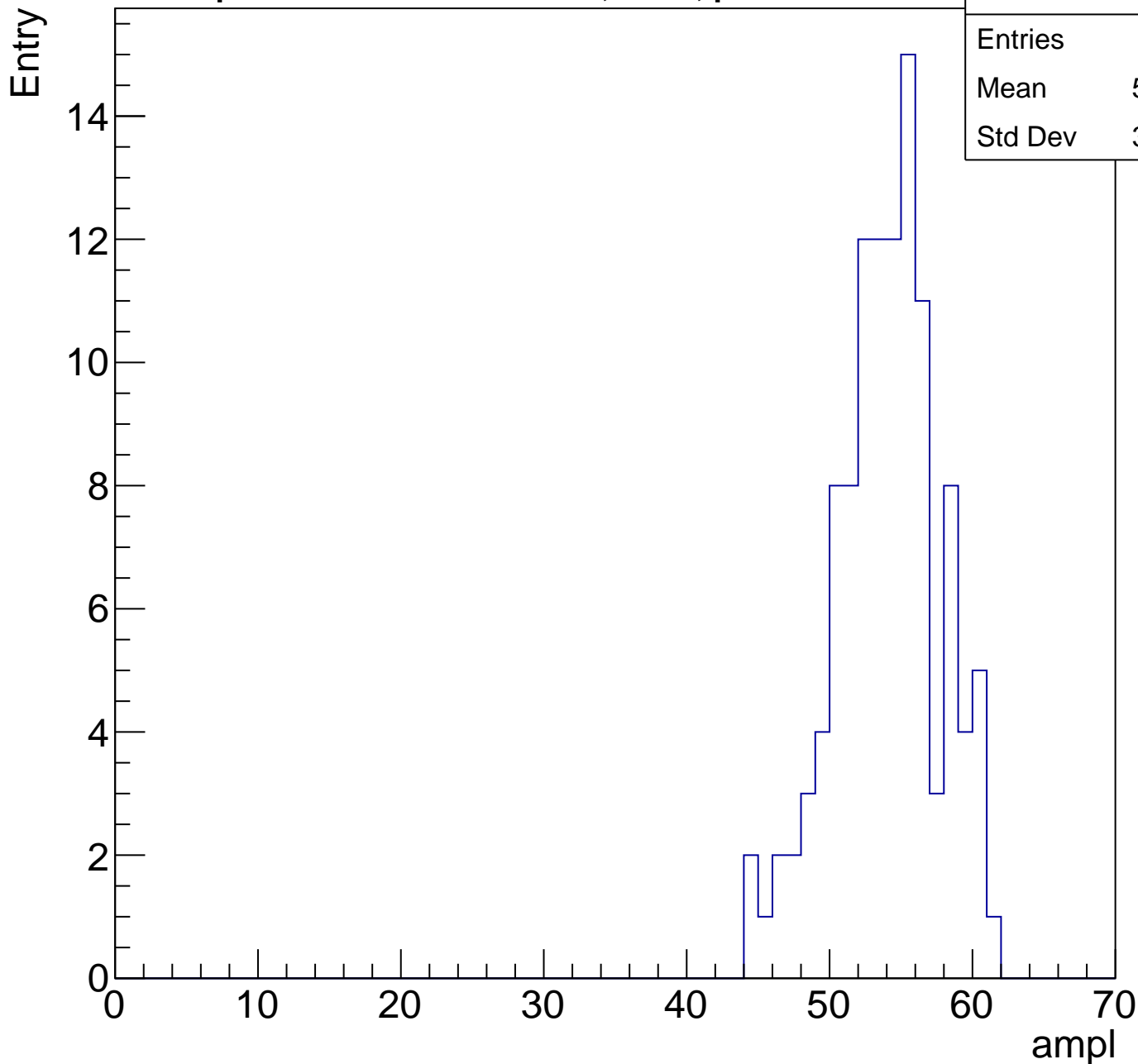
0 10 20 30 40 50 60 70



# B0L101S, U16-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 53.52 |
| Std Dev | 3.634 |



# B0L101S, U16-ch84, adc5

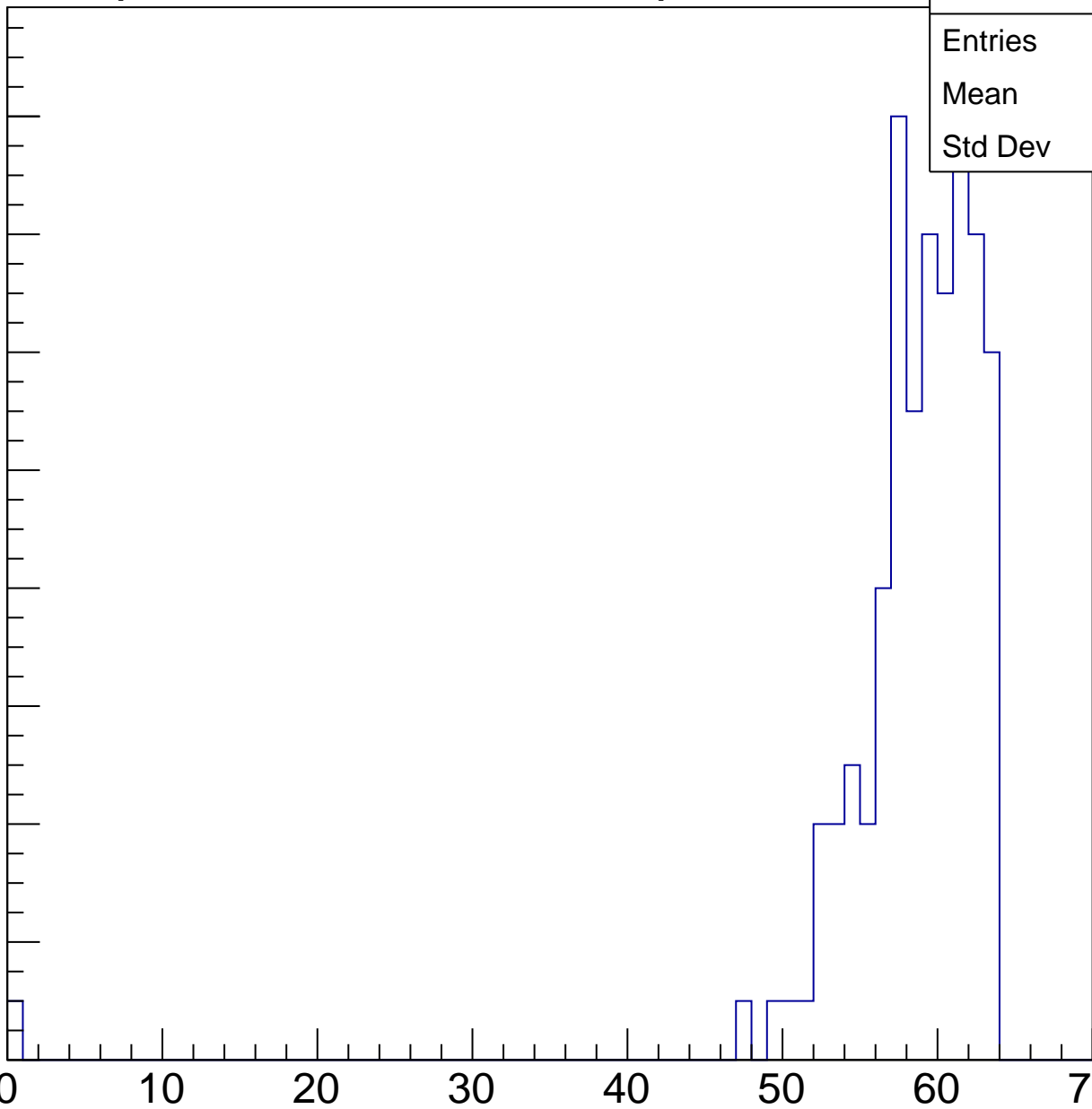
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 127   |
| Mean    | 58.05 |
| Std Dev | 6.167 |

ampl

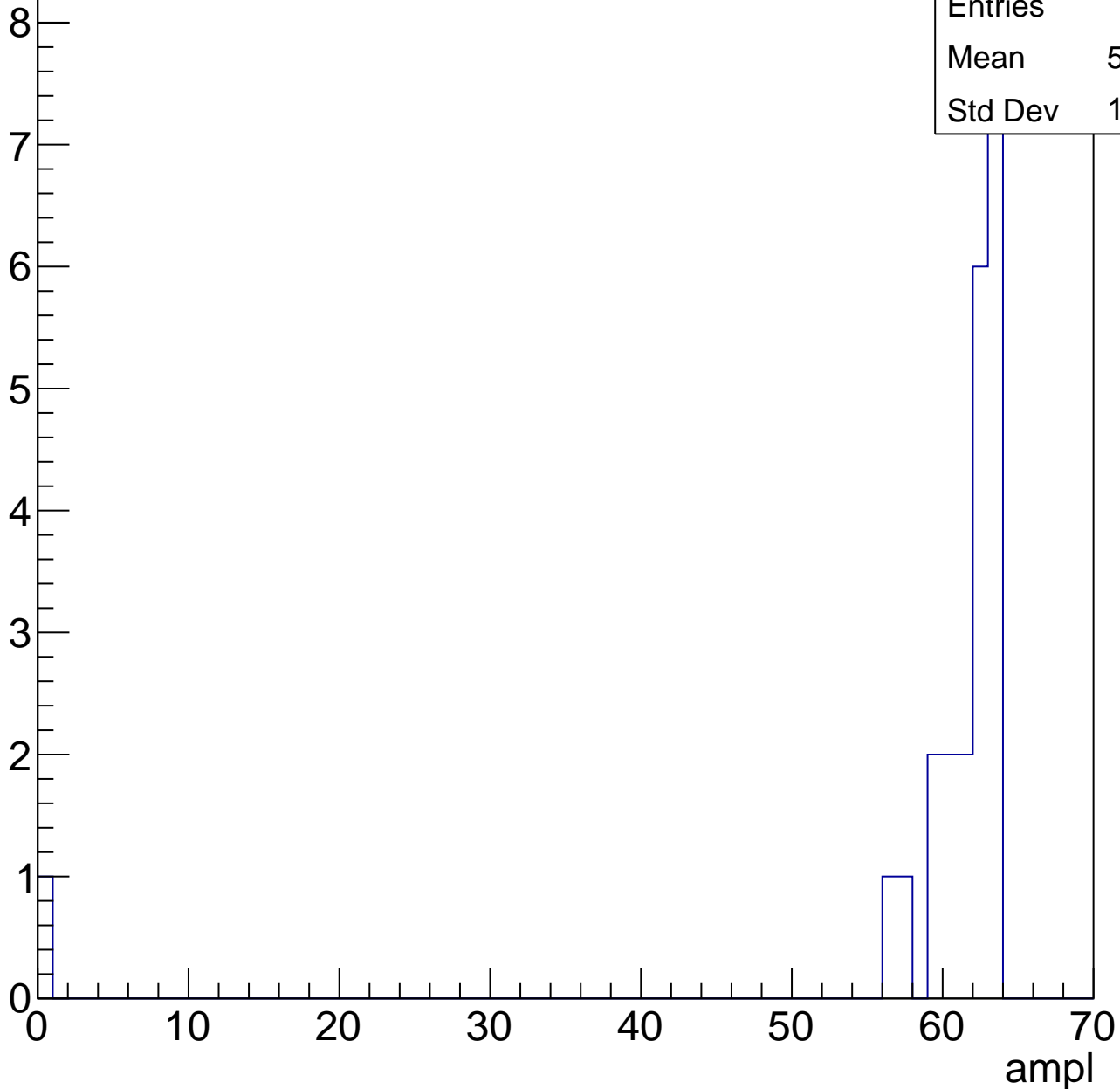


# B0L101S, U16-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 23    |
| Mean    | 58.65 |
| Std Dev | 12.65 |

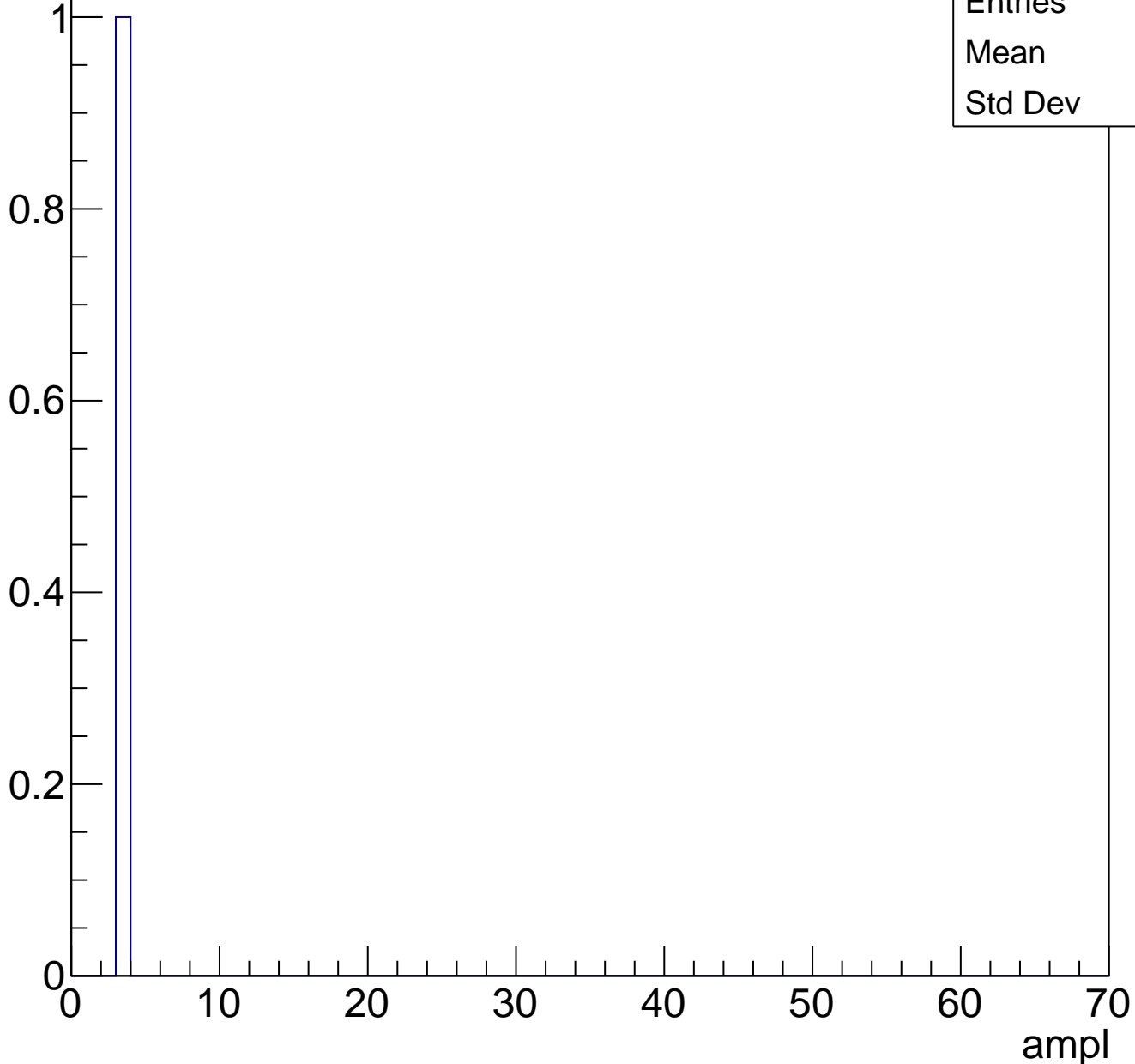




# B0L101S, U16-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch85, adc0

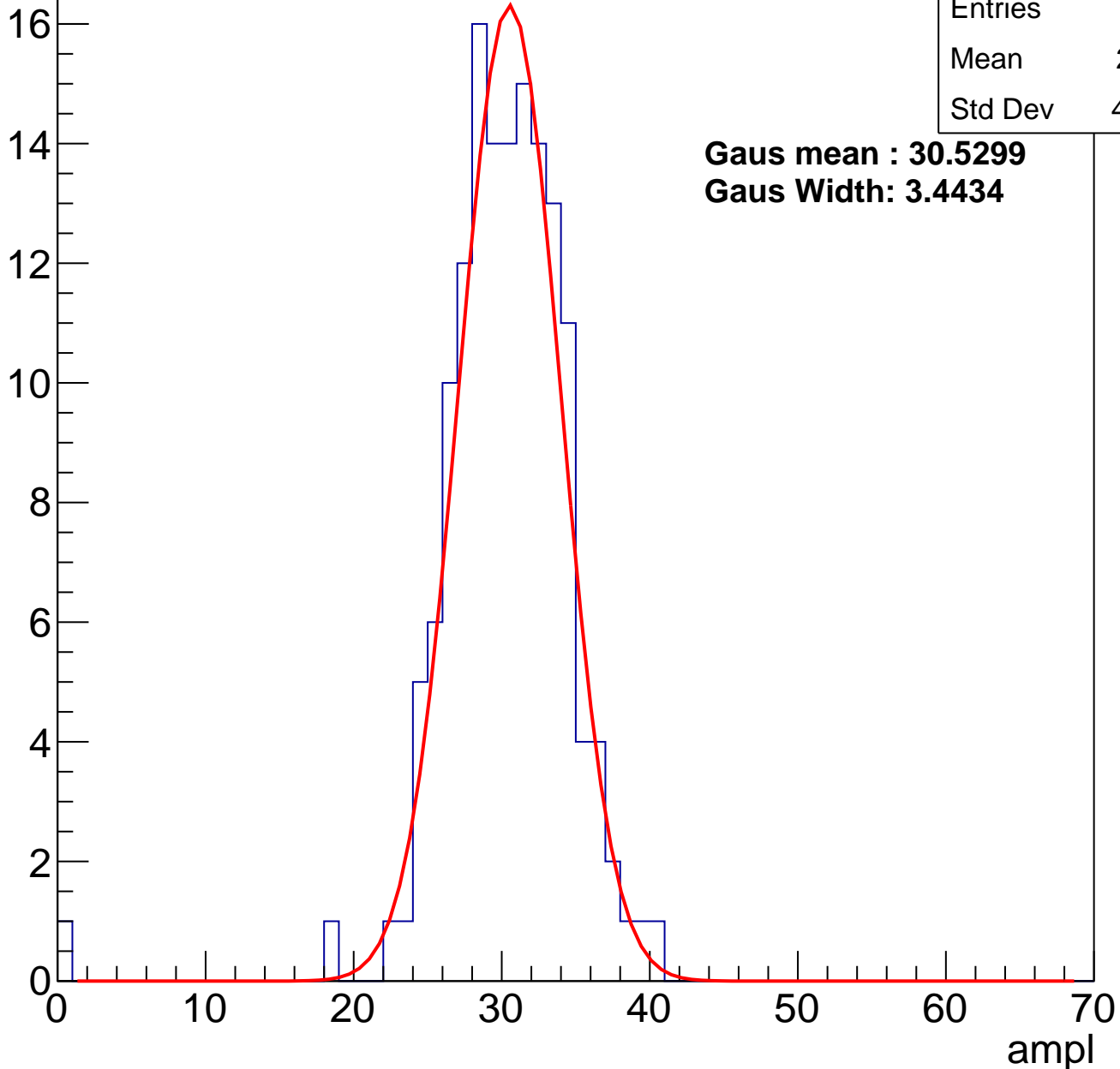
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 29.81 |
| Std Dev | 4.348 |

**Gaus mean : 30.5299**

**Gaus Width: 3.4434**

Entry

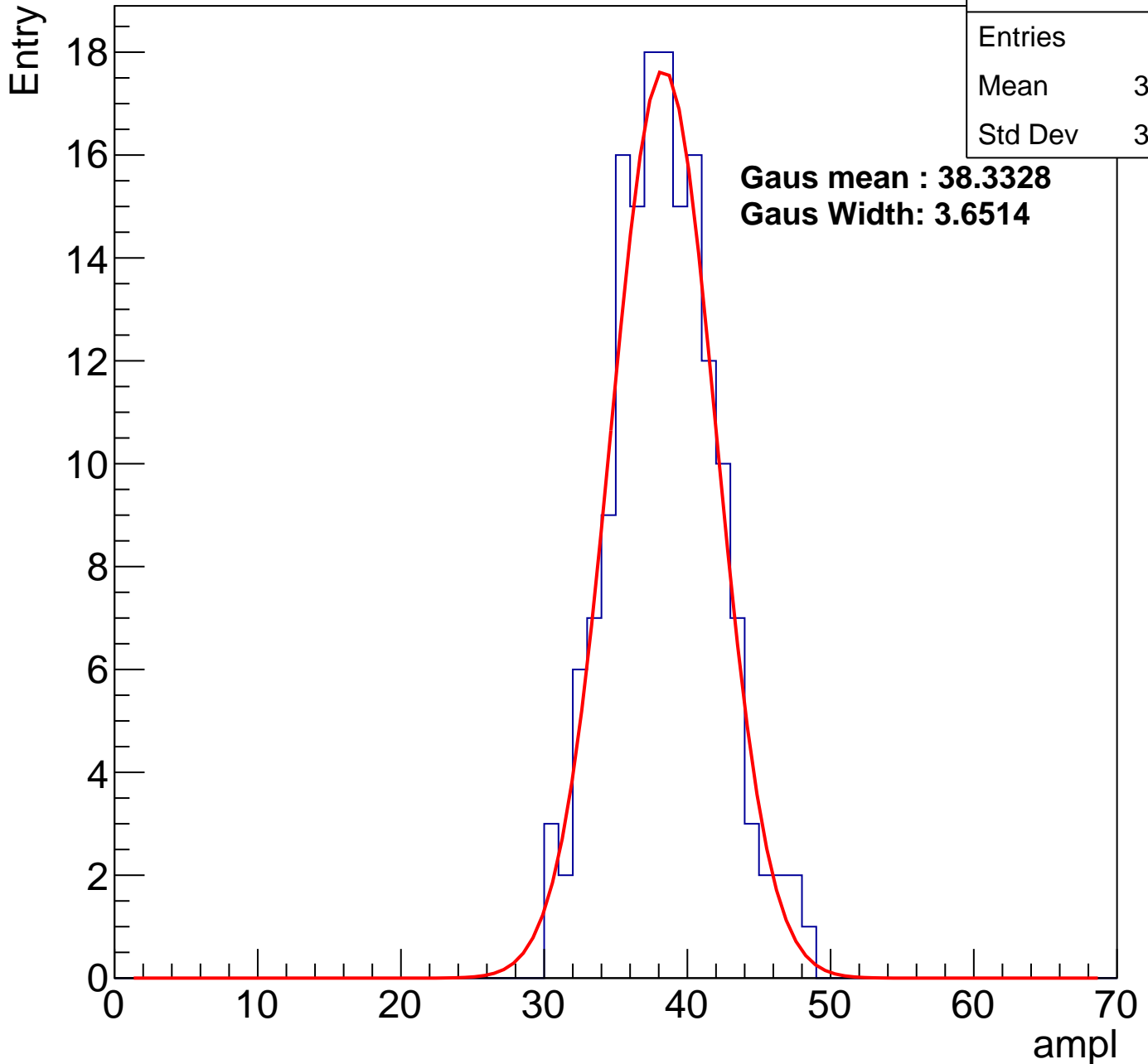


# B0L101S, U16-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 37.96 |
| Std Dev | 3.633 |

**Gaus mean : 38.3328**  
**Gaus Width: 3.6514**



# B0L101S, U16-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

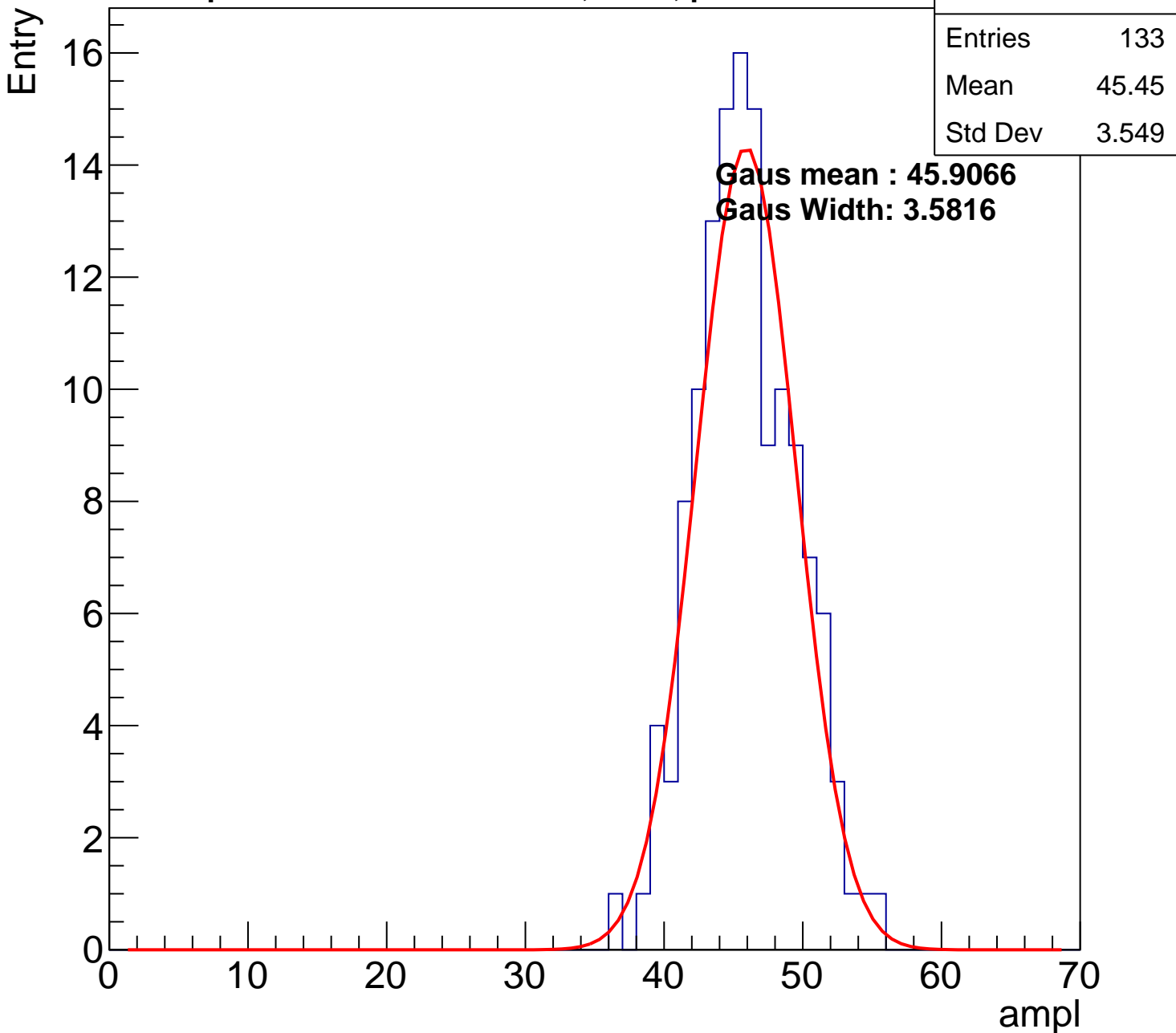
|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 45.45 |
| Std Dev | 3.549 |

**Gaus mean : 45.9066**

**Gaus Width: 3.5816**

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

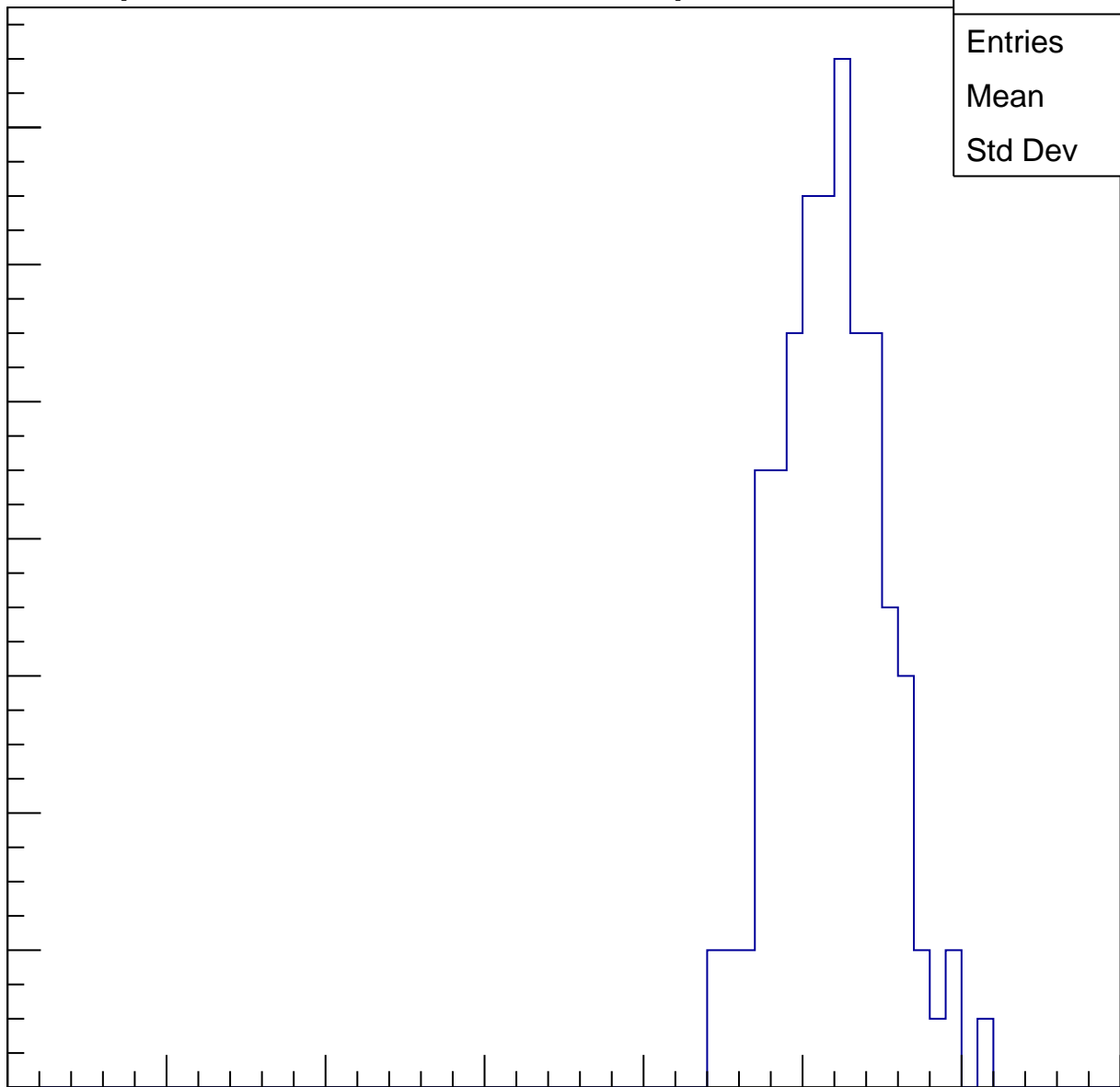
|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 51.33 |
| Std Dev | 3.279 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

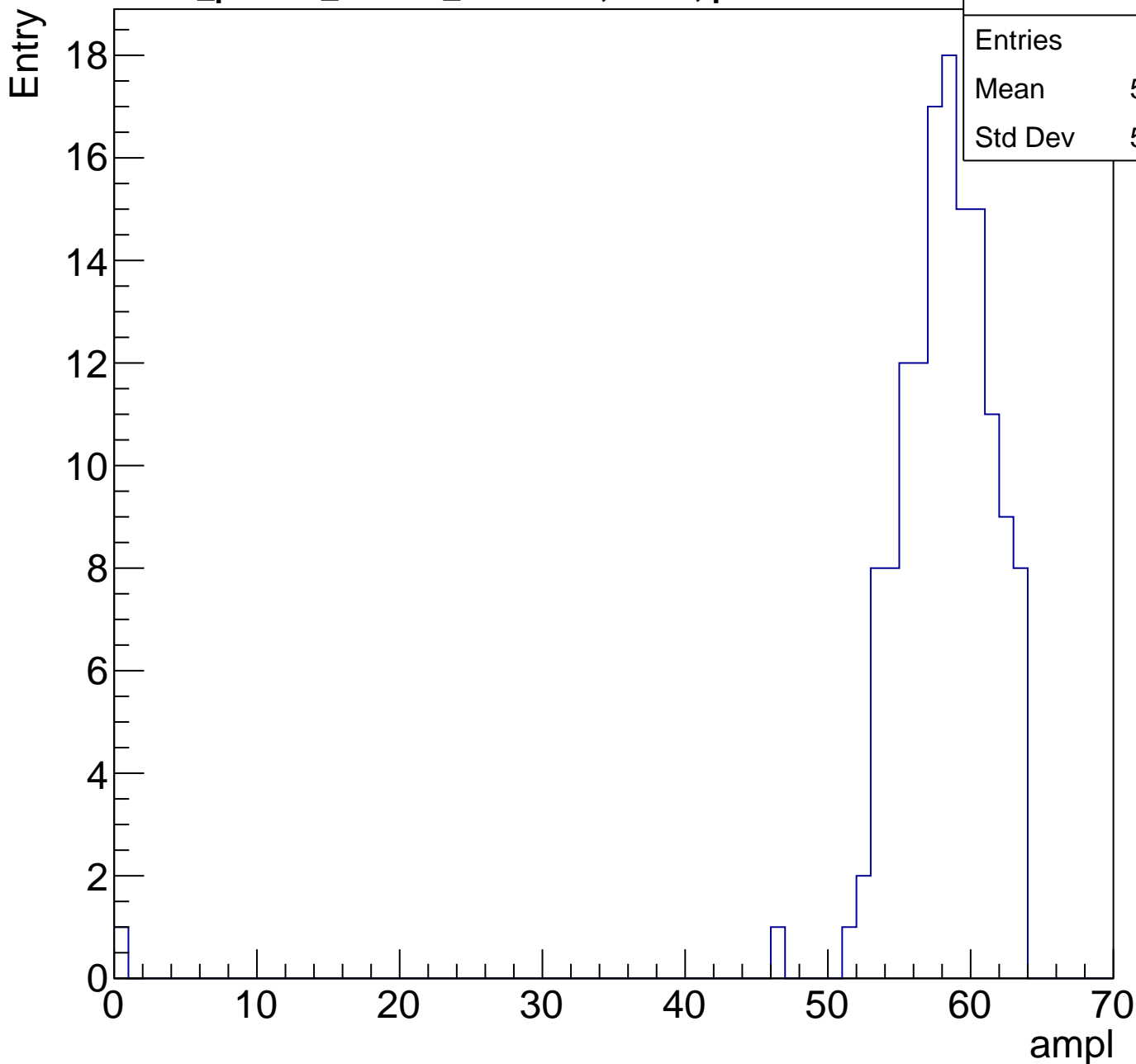
ampl



# B0L101S, U16-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 57.39 |
| Std Dev | 5.772 |

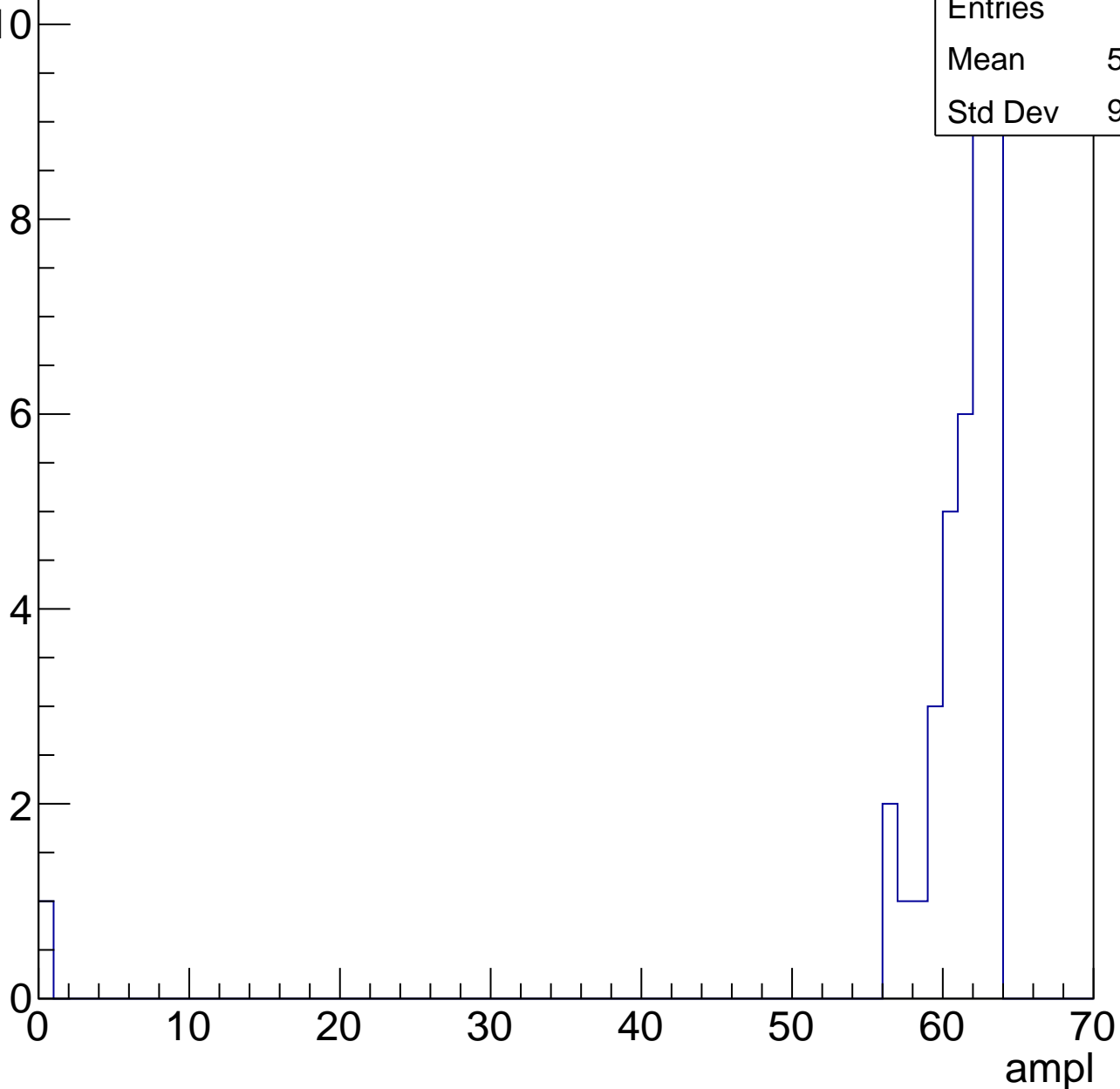


# B0L101S, U16-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 59.39 |
| Std Dev | 9.949 |



# B0L101S, U16-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

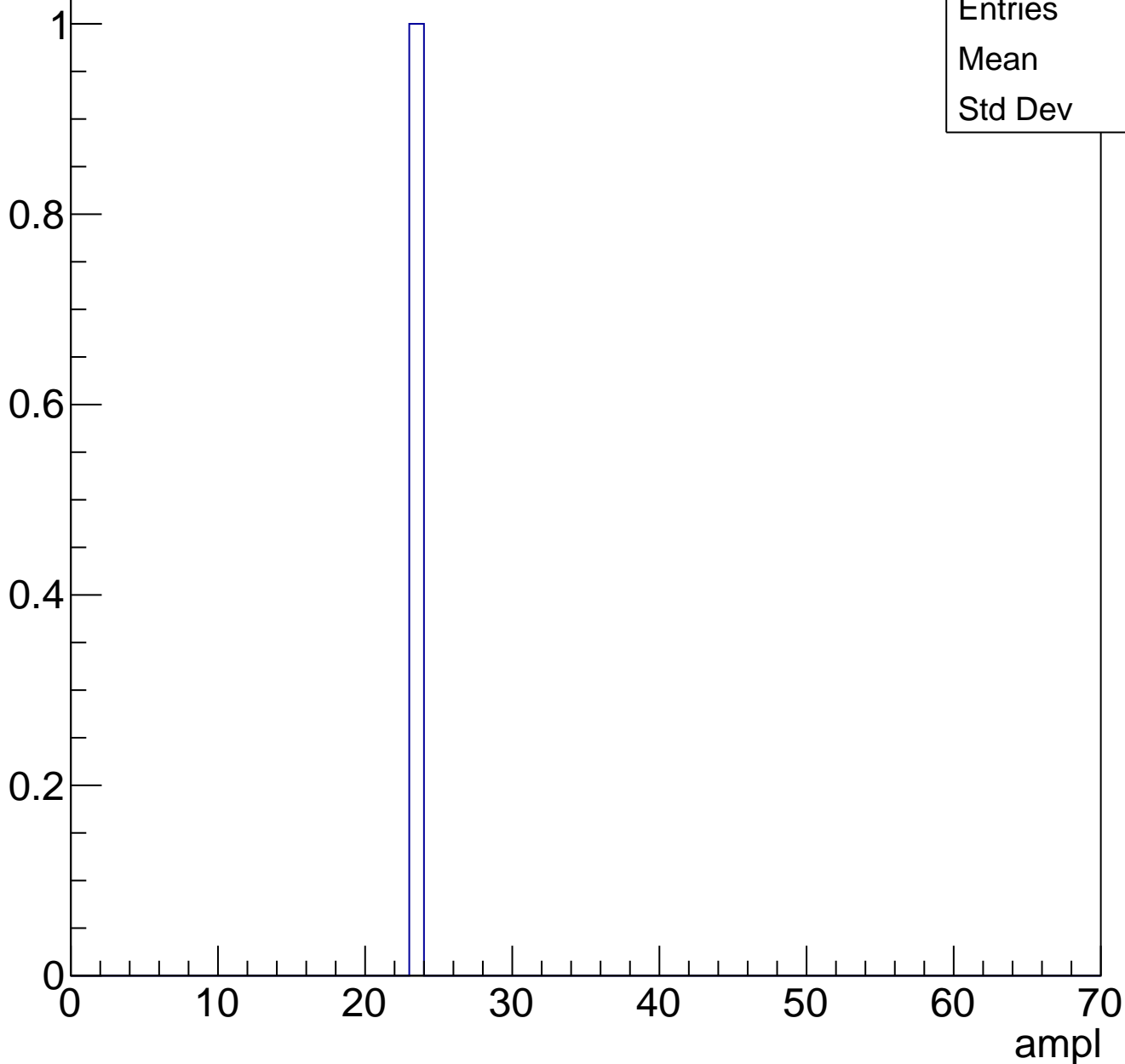




# B0L101S, U16-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch86, adc0

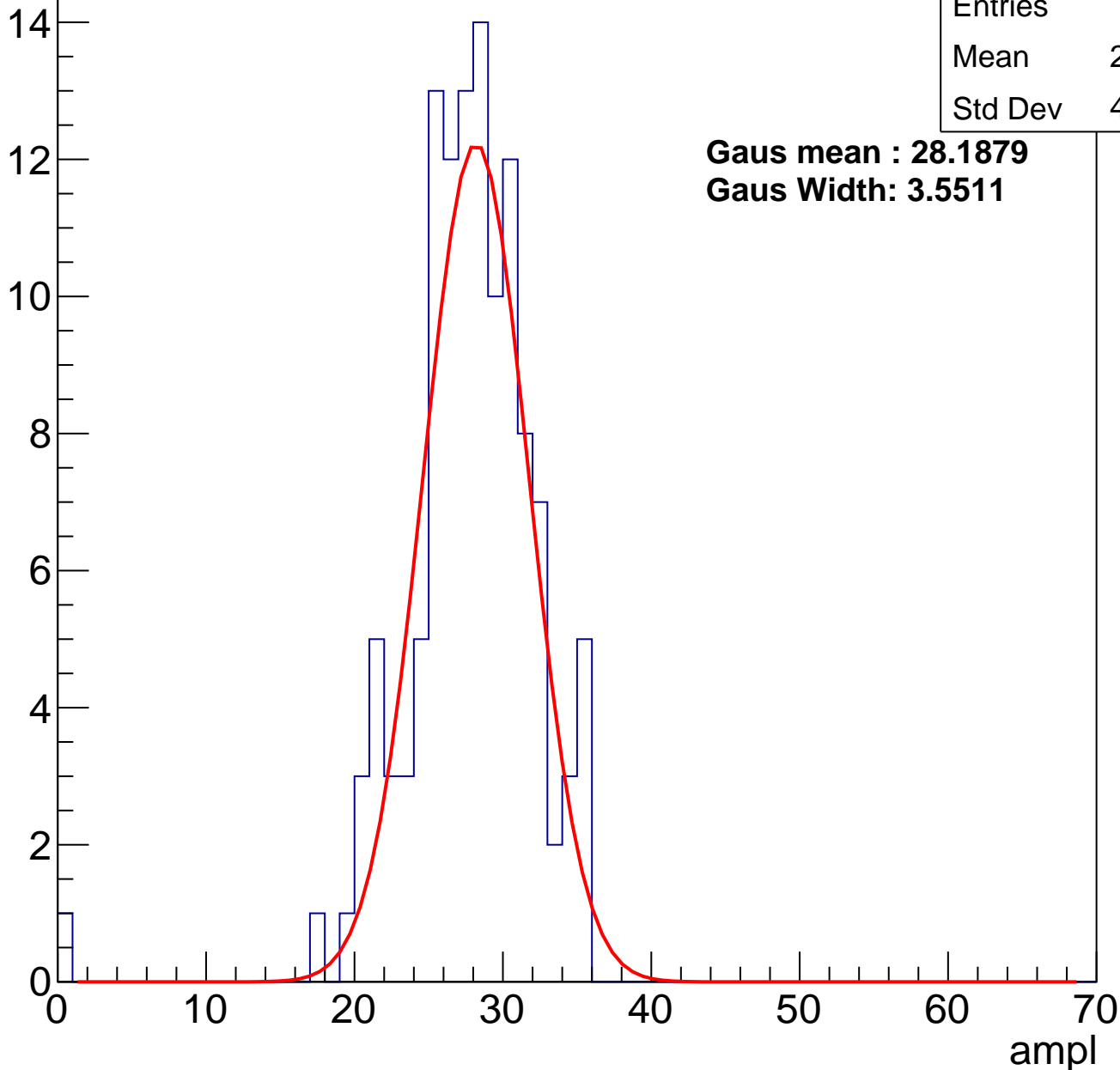
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 27.28 |
| Std Dev | 4.497 |

**Gaus mean : 28.1879**

**Gaus Width: 3.5511**

Entry



# B0L101S, U16-ch86, adc1

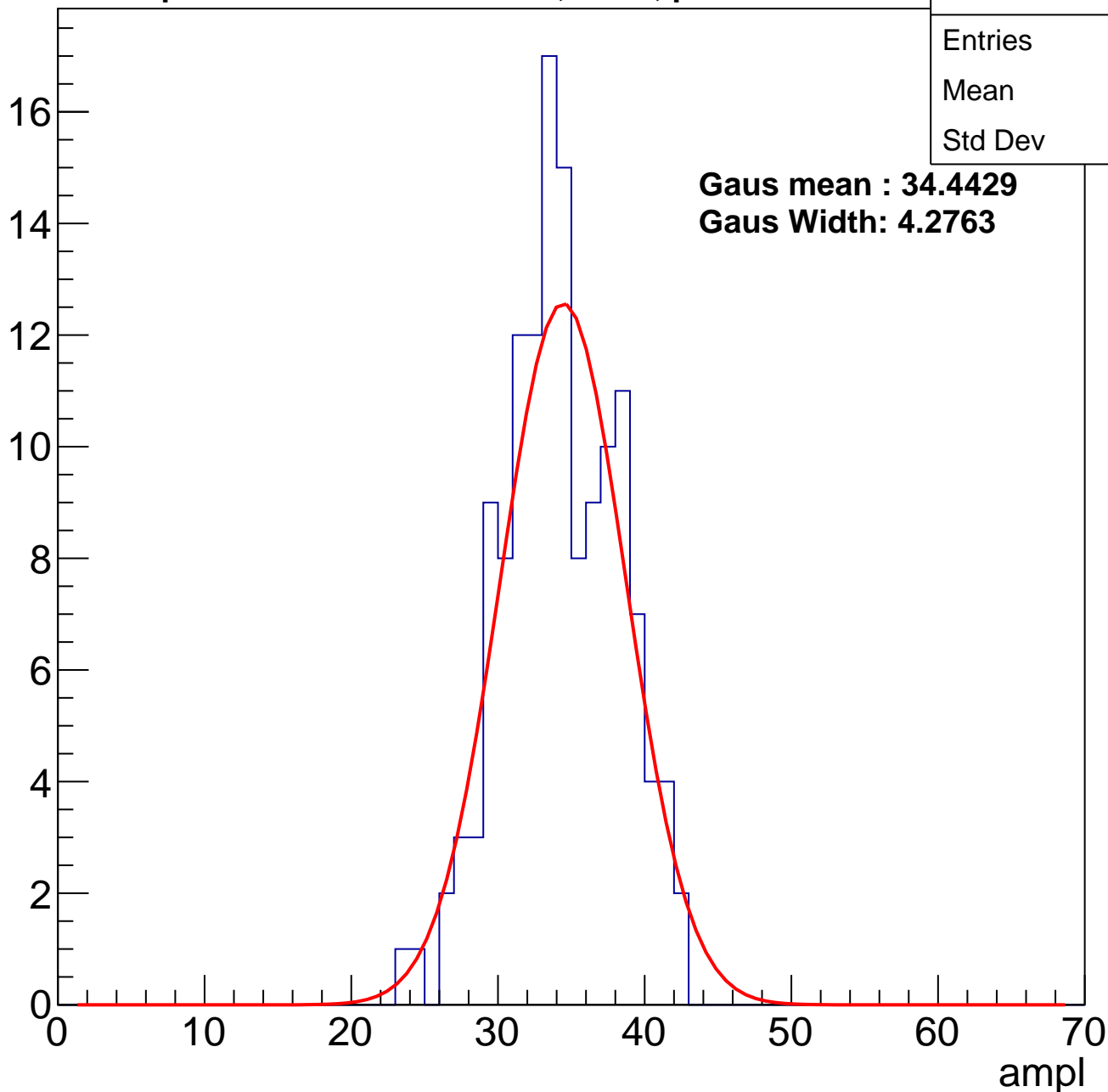
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |      |
|---------|------|
| Entries | 138  |
| Mean    | 33.8 |
| Std Dev | 3.88 |

**Gaus mean : 34.4429**

**Gaus Width: 4.2763**

Entry



# B0L101S, U16-ch86, adc2

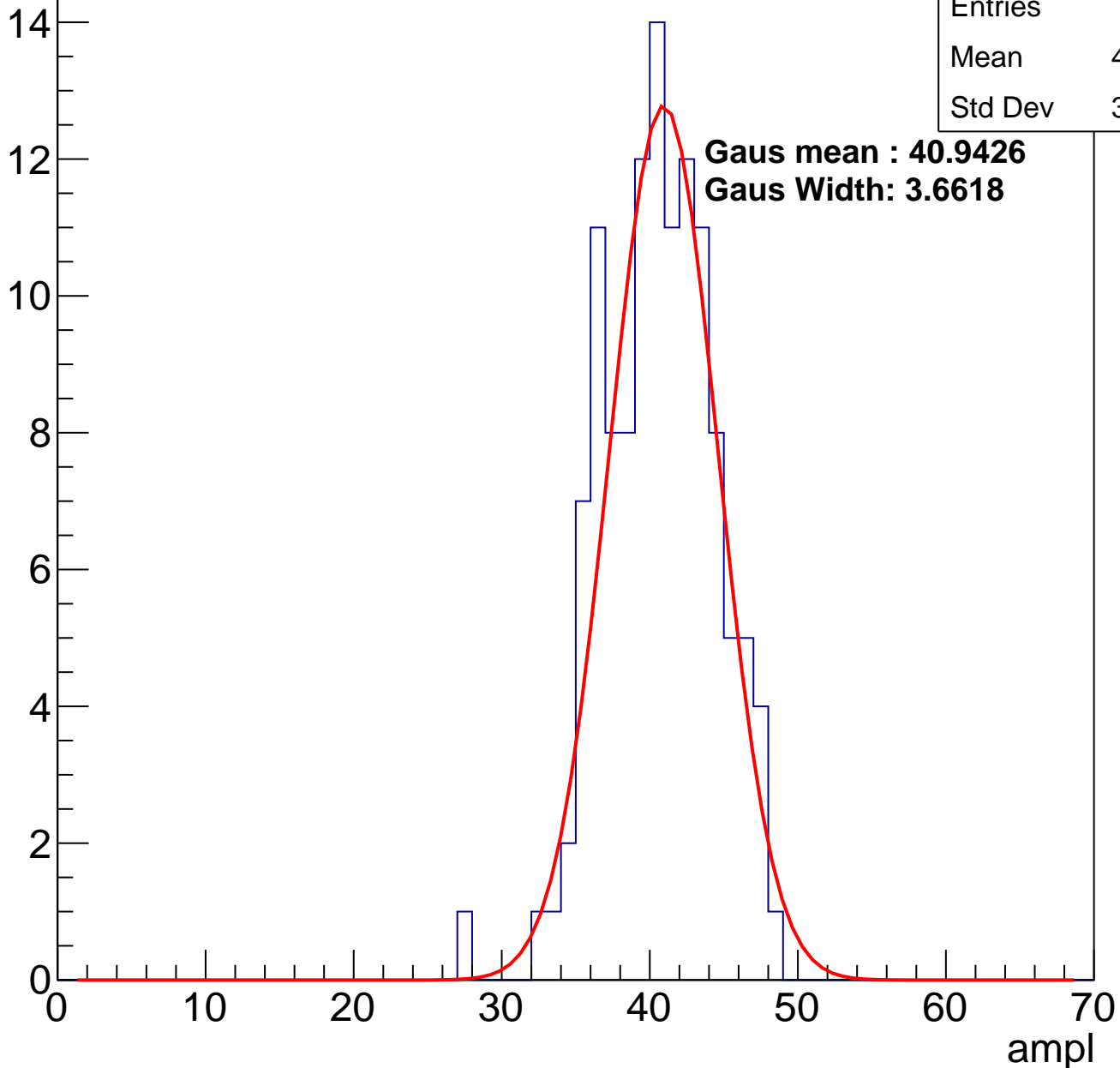
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 40.16 |
| Std Dev | 3.698 |

**Gaus mean : 40.9426**

**Gaus Width: 3.6618**

Entry

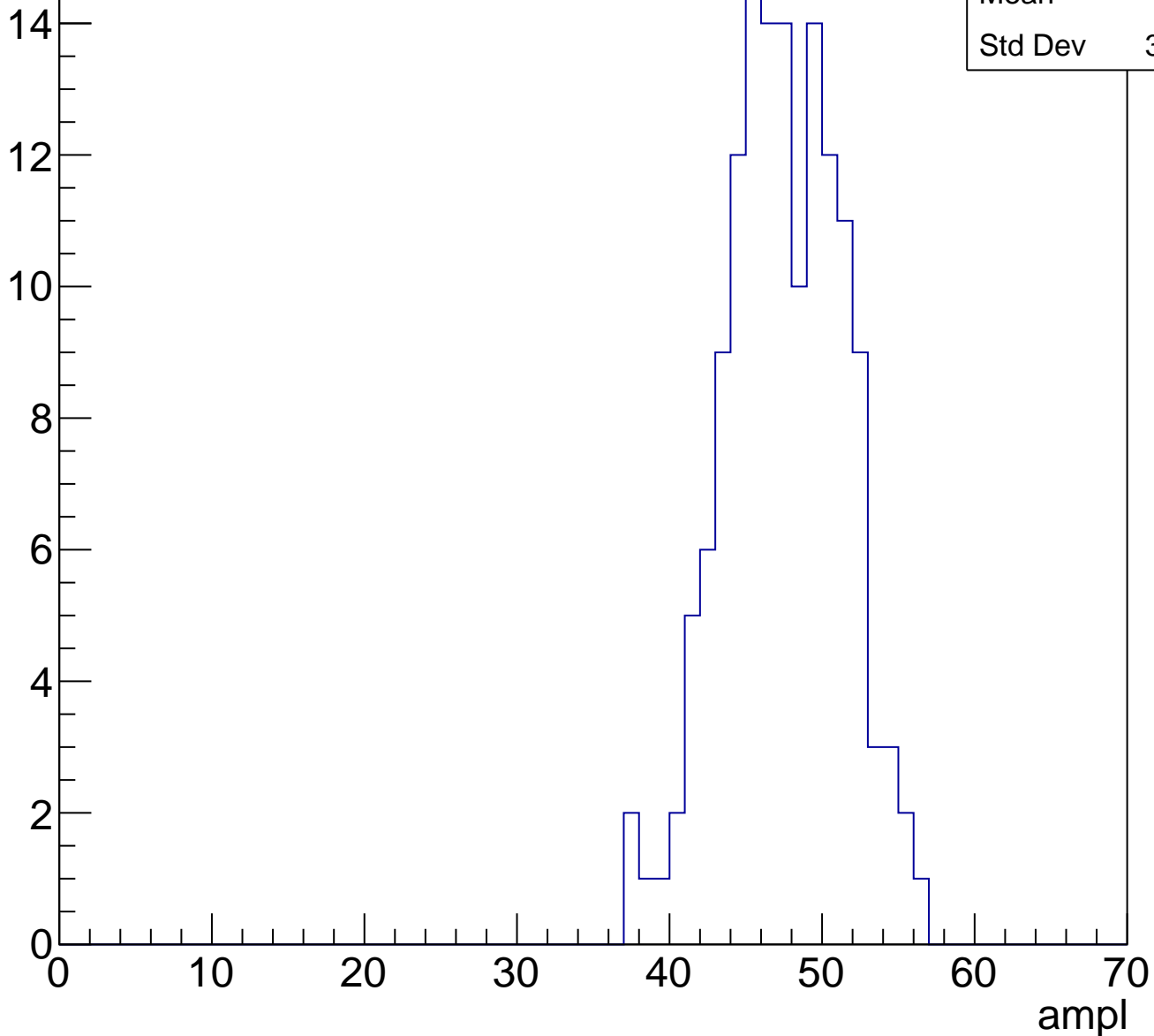


# B0L101S, U16-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 47    |
| Std Dev | 3.827 |

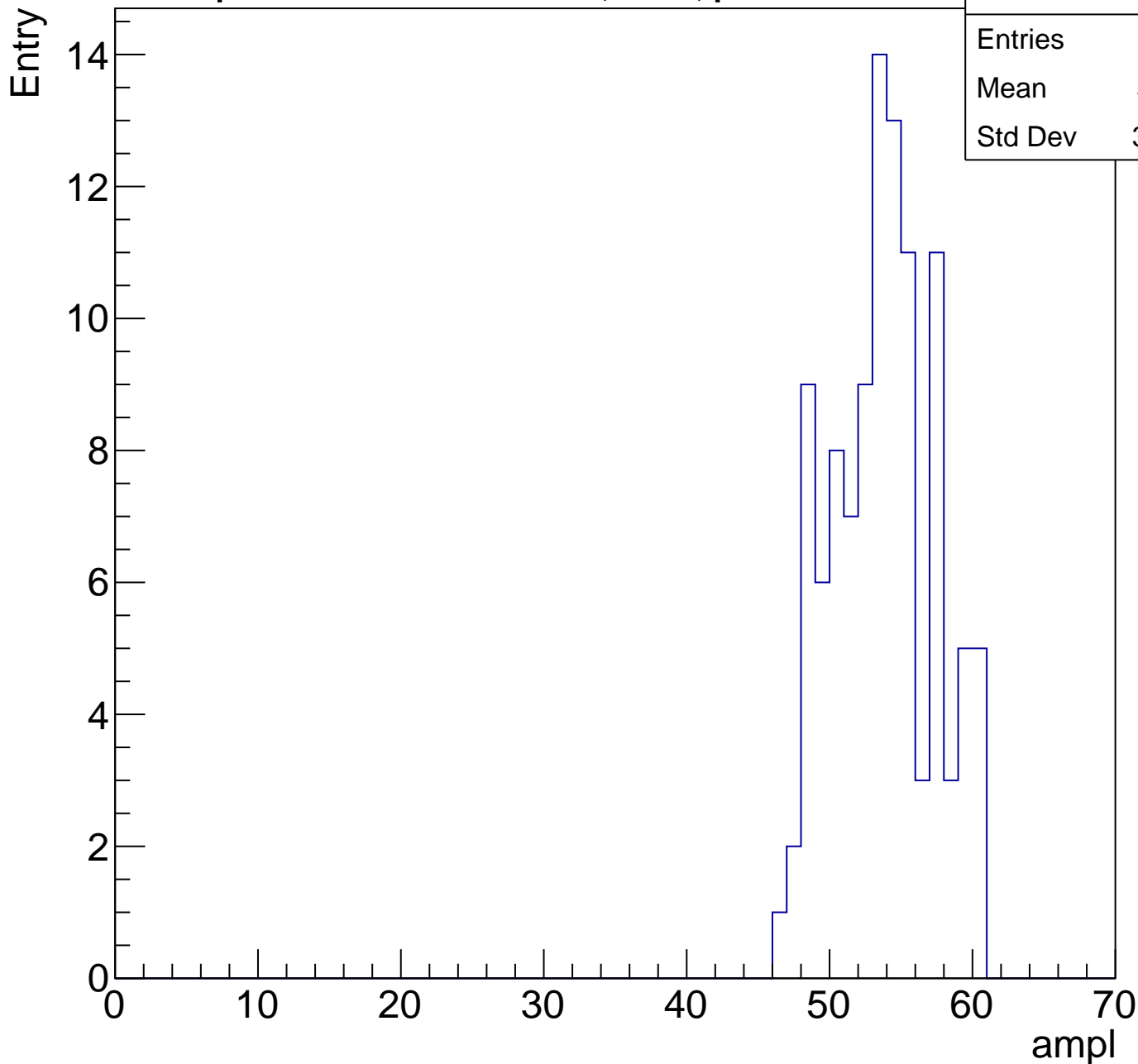
Entry



# B0L101S, U16-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 53.31 |
| Std Dev | 3.487 |

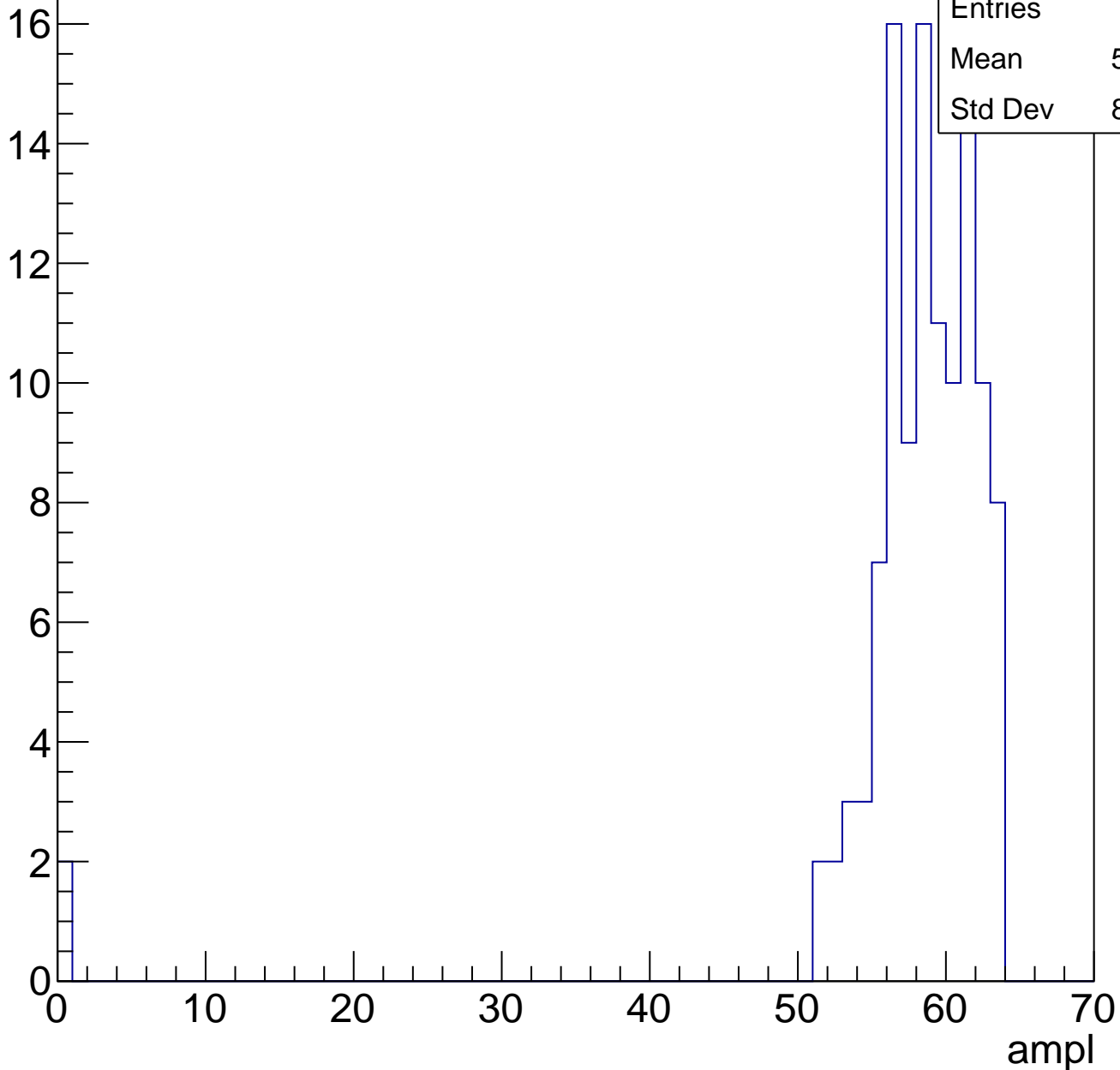


# B0L101S, U16-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 57.34 |
| Std Dev | 8.194 |

Entry



# B0L101S, U16-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 61.24 |
| Std Dev | 1.926 |

Entry

10

8

6

4

2

0

0

10

20

30

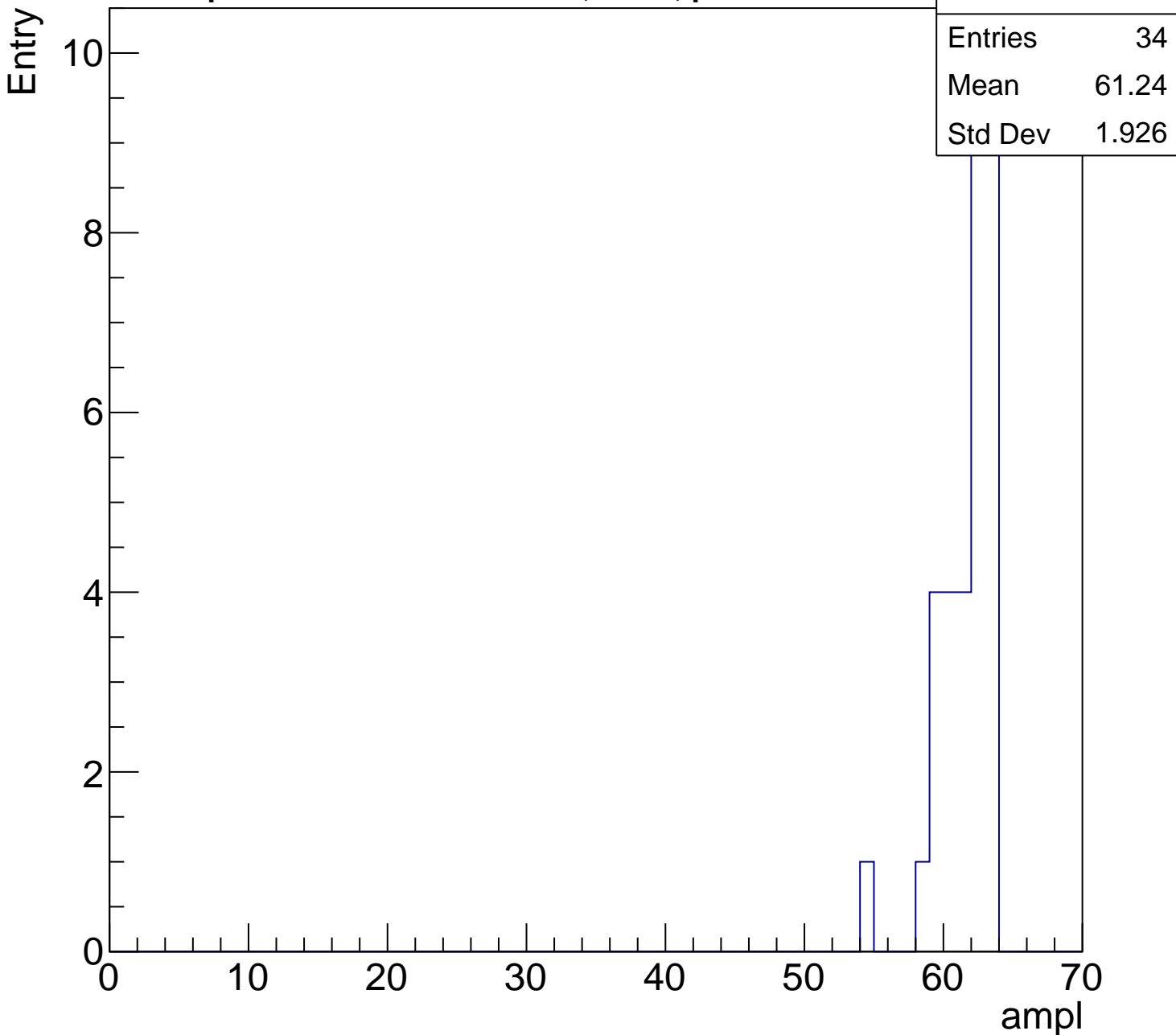
40

50

60

70

ampl

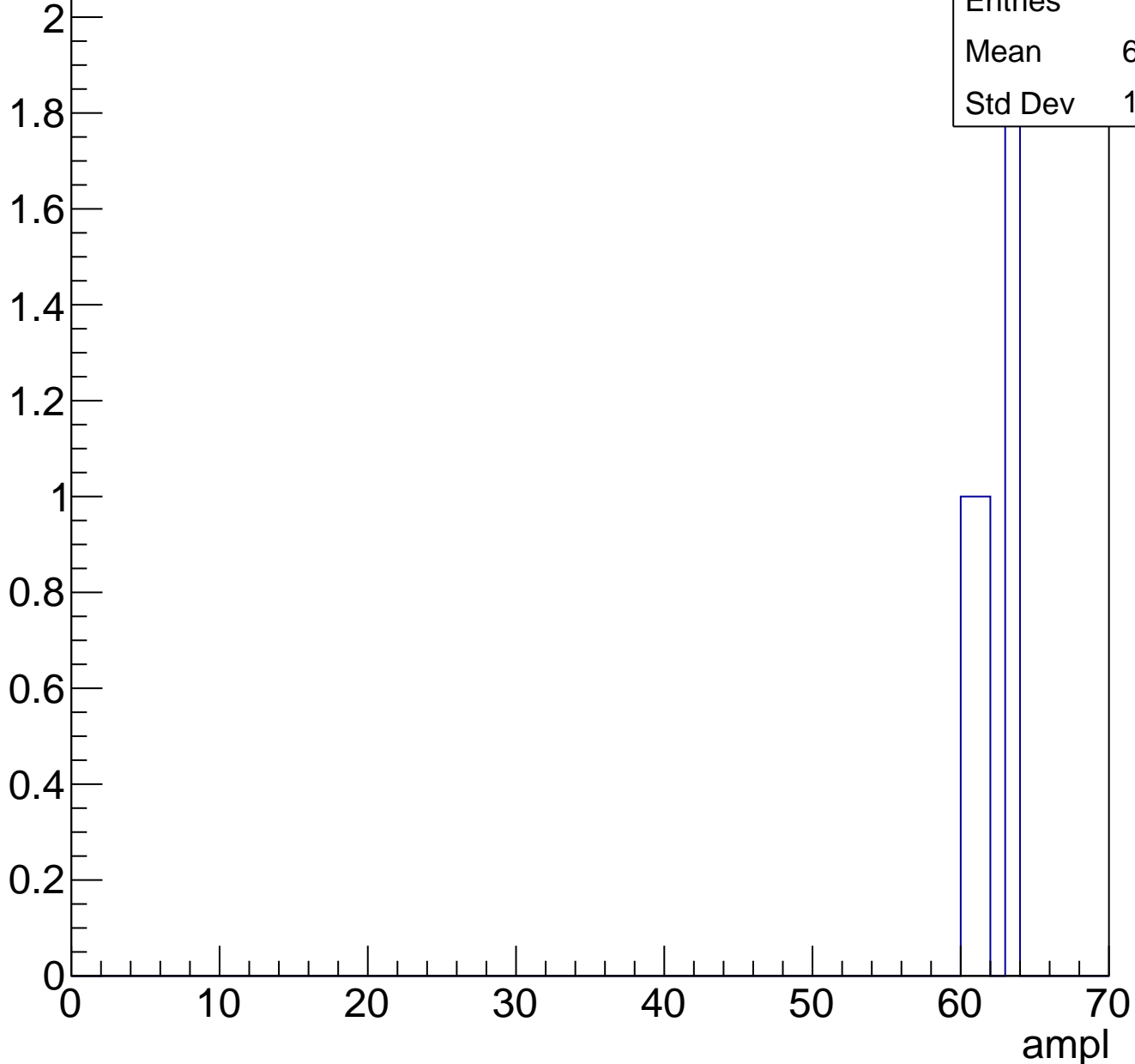




# B0L101S, U16-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



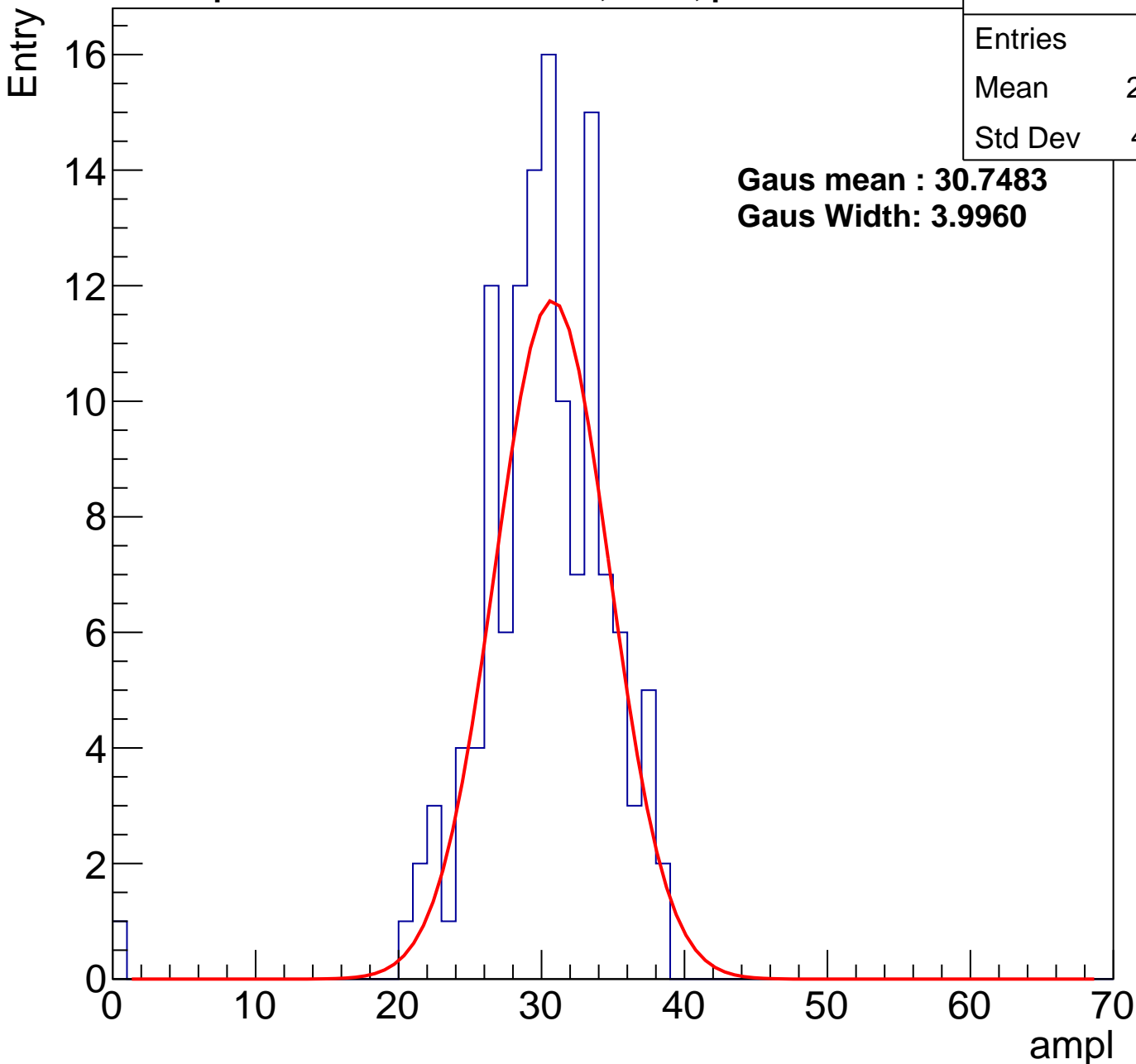
# B0L101S, U16-ch87, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 29.69 |
| Std Dev | 4.671 |

**Gaus mean : 30.7483**

**Gaus Width: 3.9960**



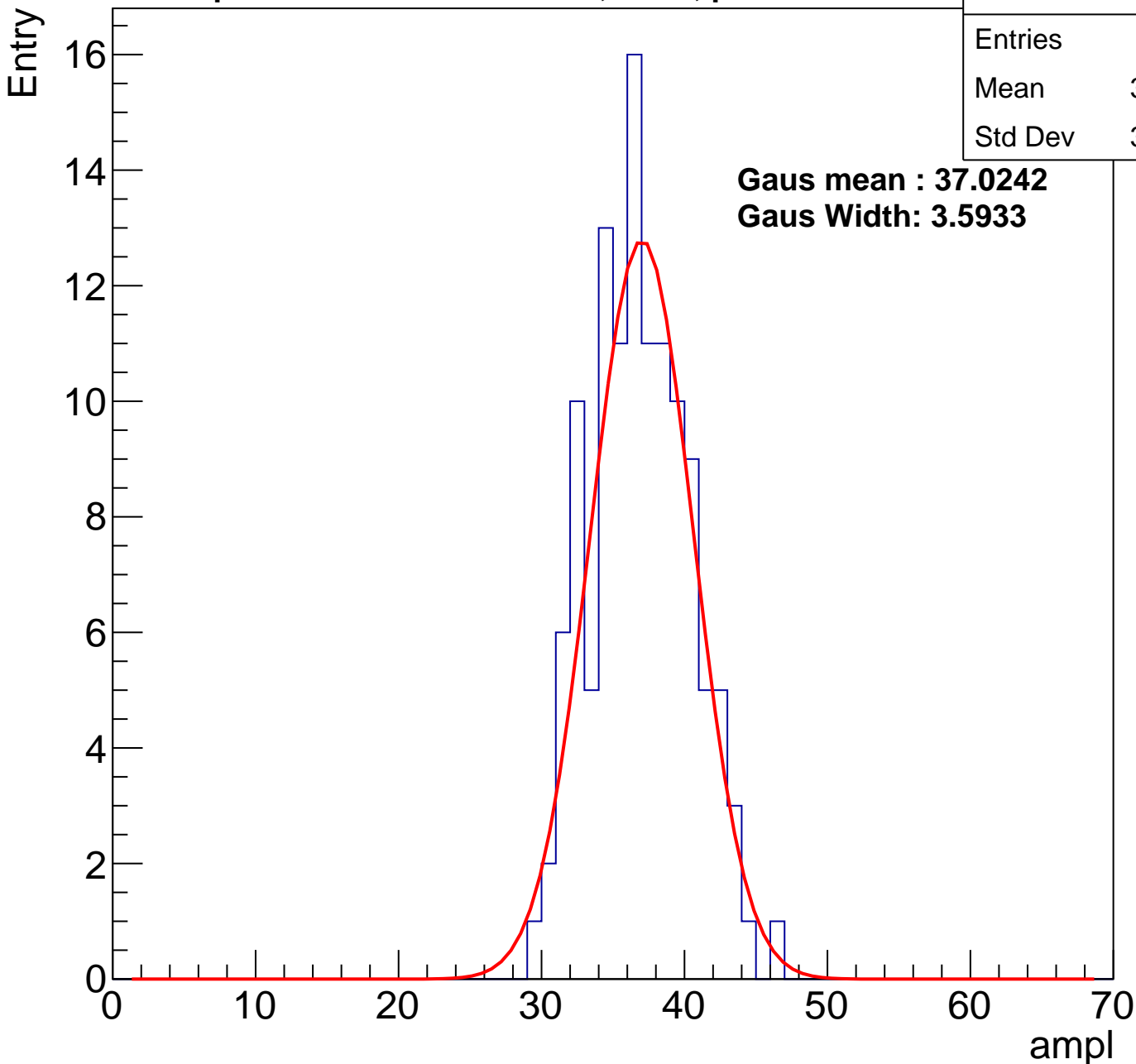
# B0L101S, U16-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 120   |
| Mean    | 36.43 |
| Std Dev | 3.442 |

**Gaus mean : 37.0242**

**Gaus Width: 3.5933**



# B0L101S, U16-ch87, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

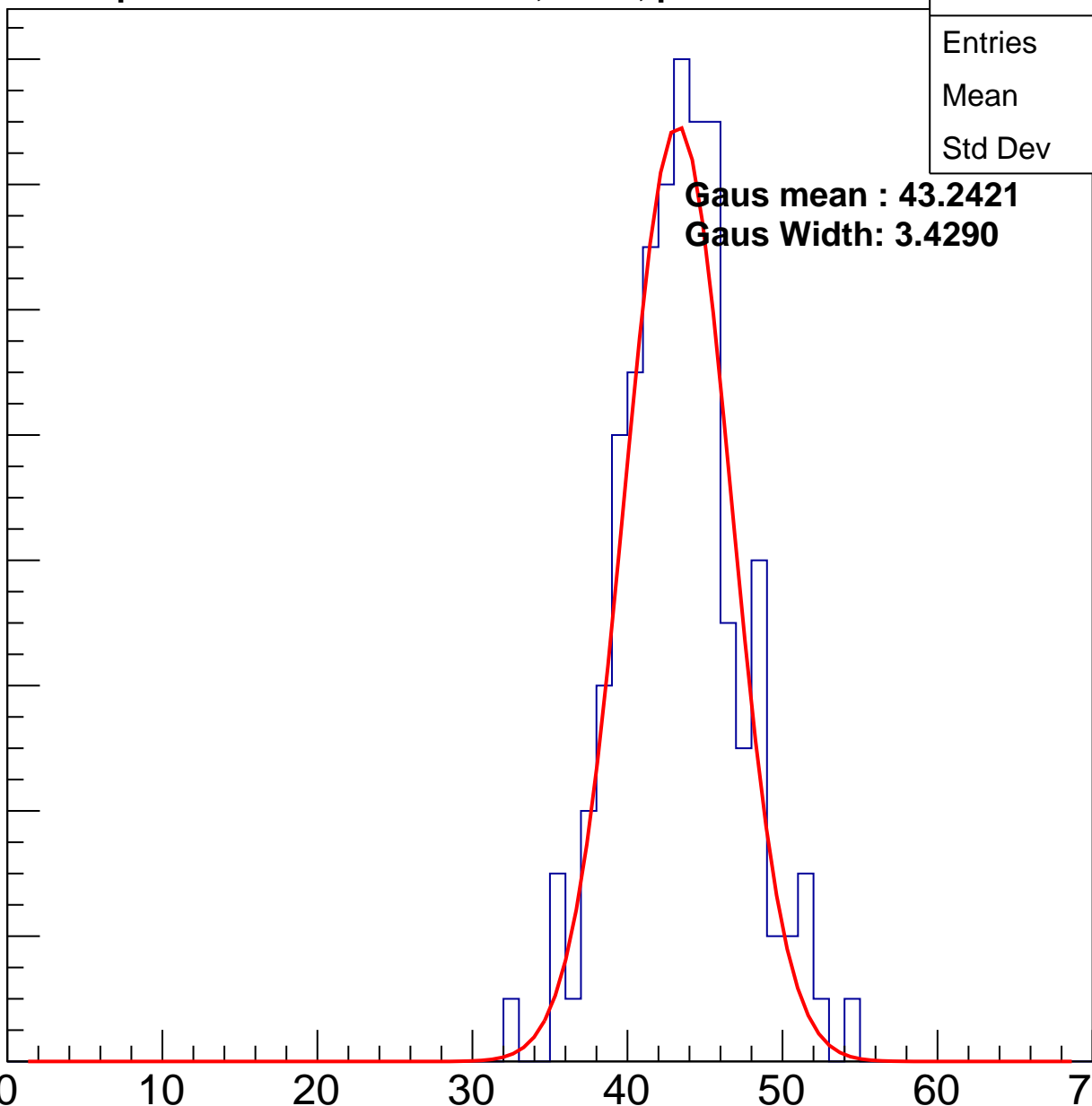
|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 42.91 |
| Std Dev | 3.77  |

**Gaus mean : 43.2421**

**Gaus Width: 3.4290**

0 10 20 30 40 50 60 70

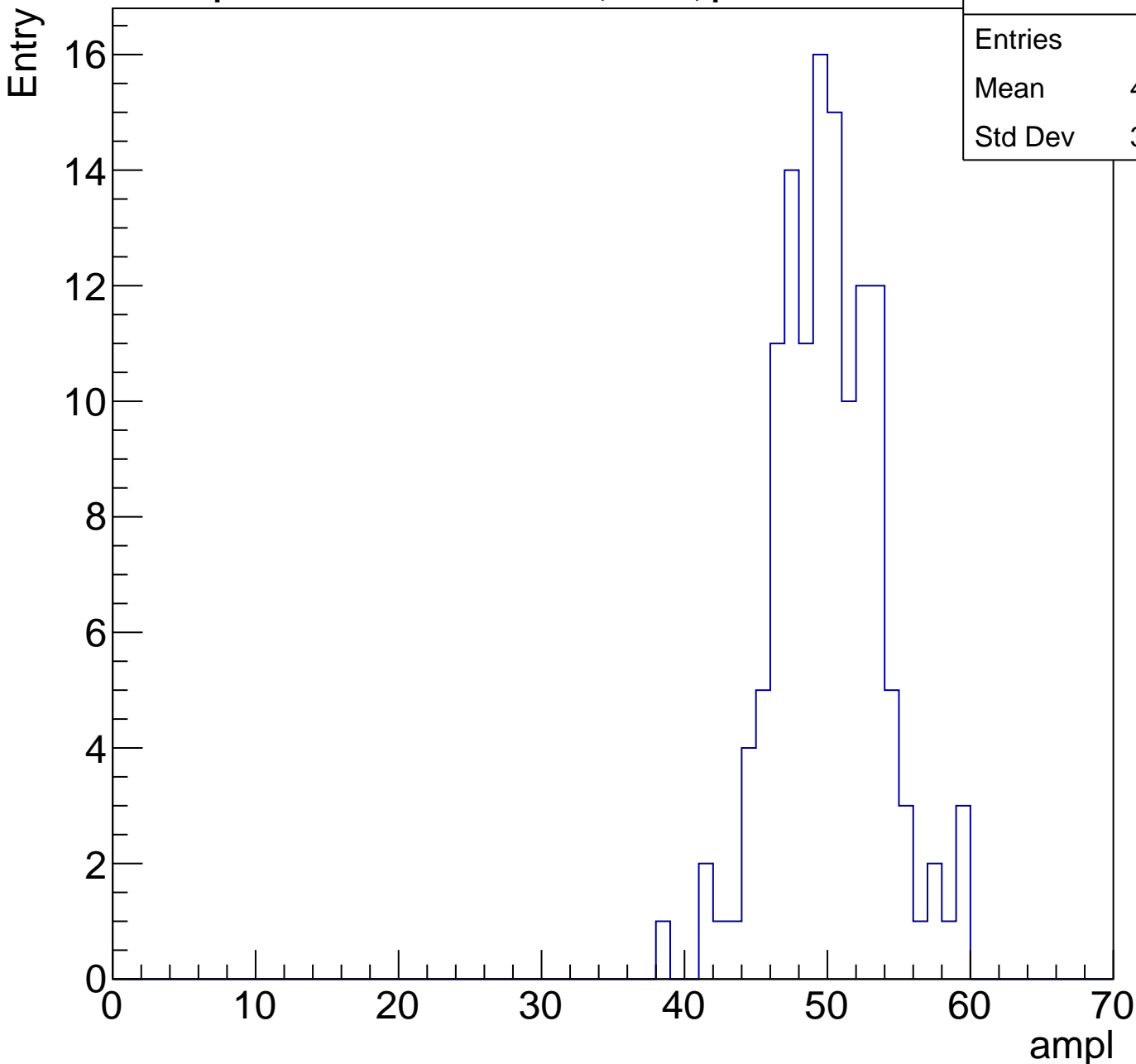
ampl



# B0L101S, U16-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 49.55 |
| Std Dev | 3.707 |

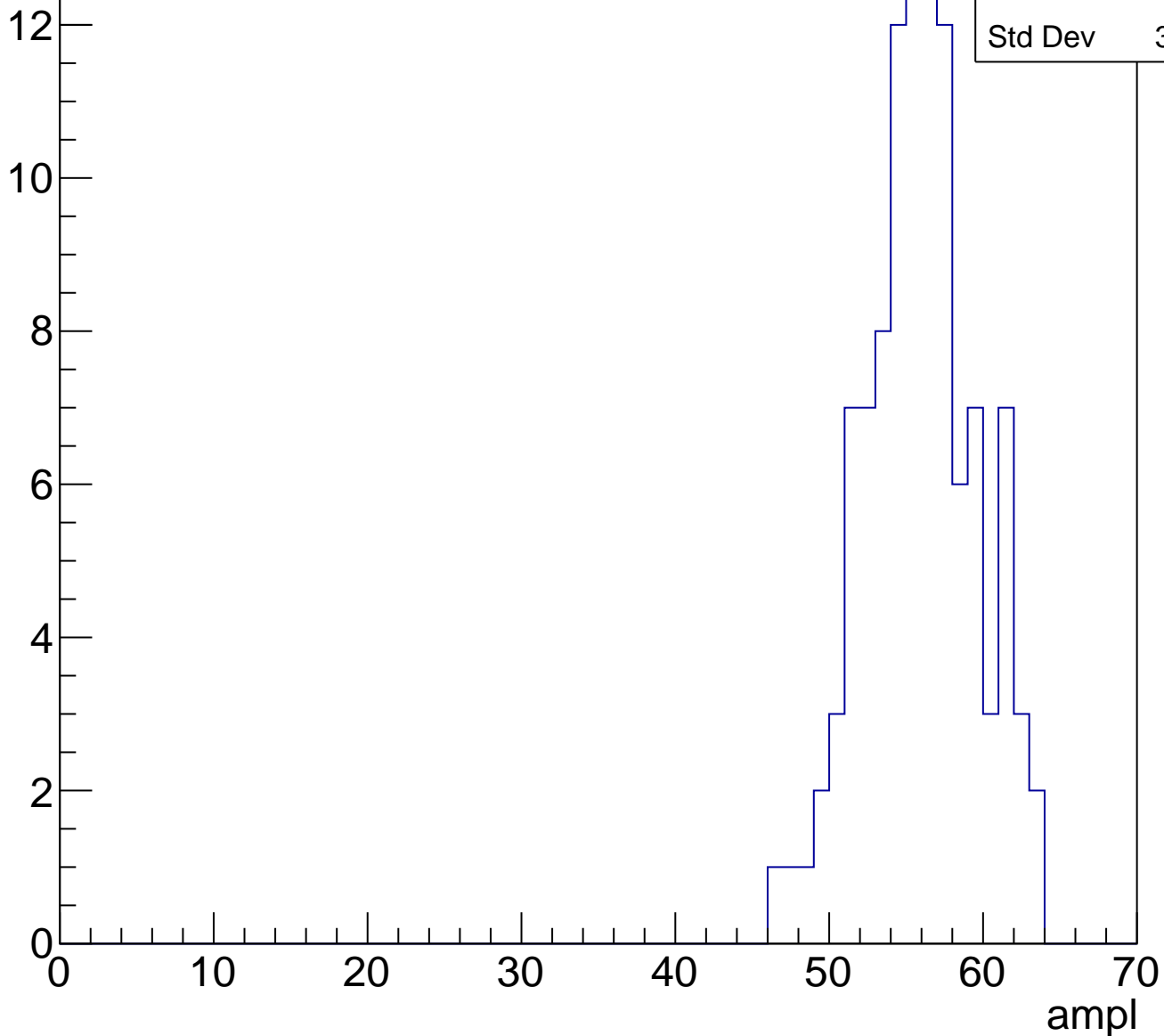


# B0L101S, U16-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 55.45 |
| Std Dev | 3.557 |

Entry



# B0L101S, U16-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 103   |
| Mean    | 58.65 |
| Std Dev | 8.599 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

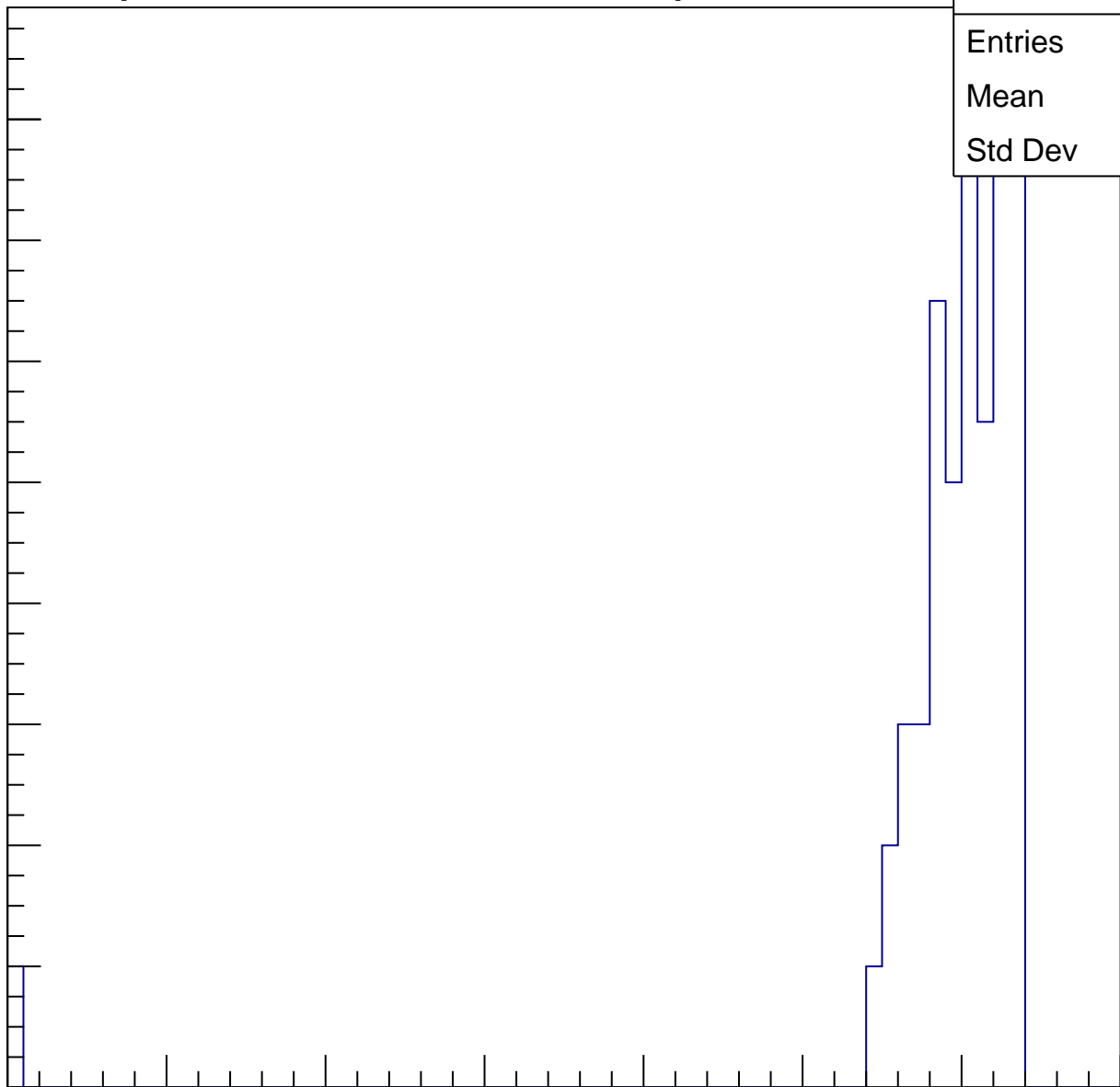
40

50

60

70

ampl



# B0L101S, U16-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 5      |
| Mean    | 62     |
| Std Dev | 0.8944 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch88, adc0

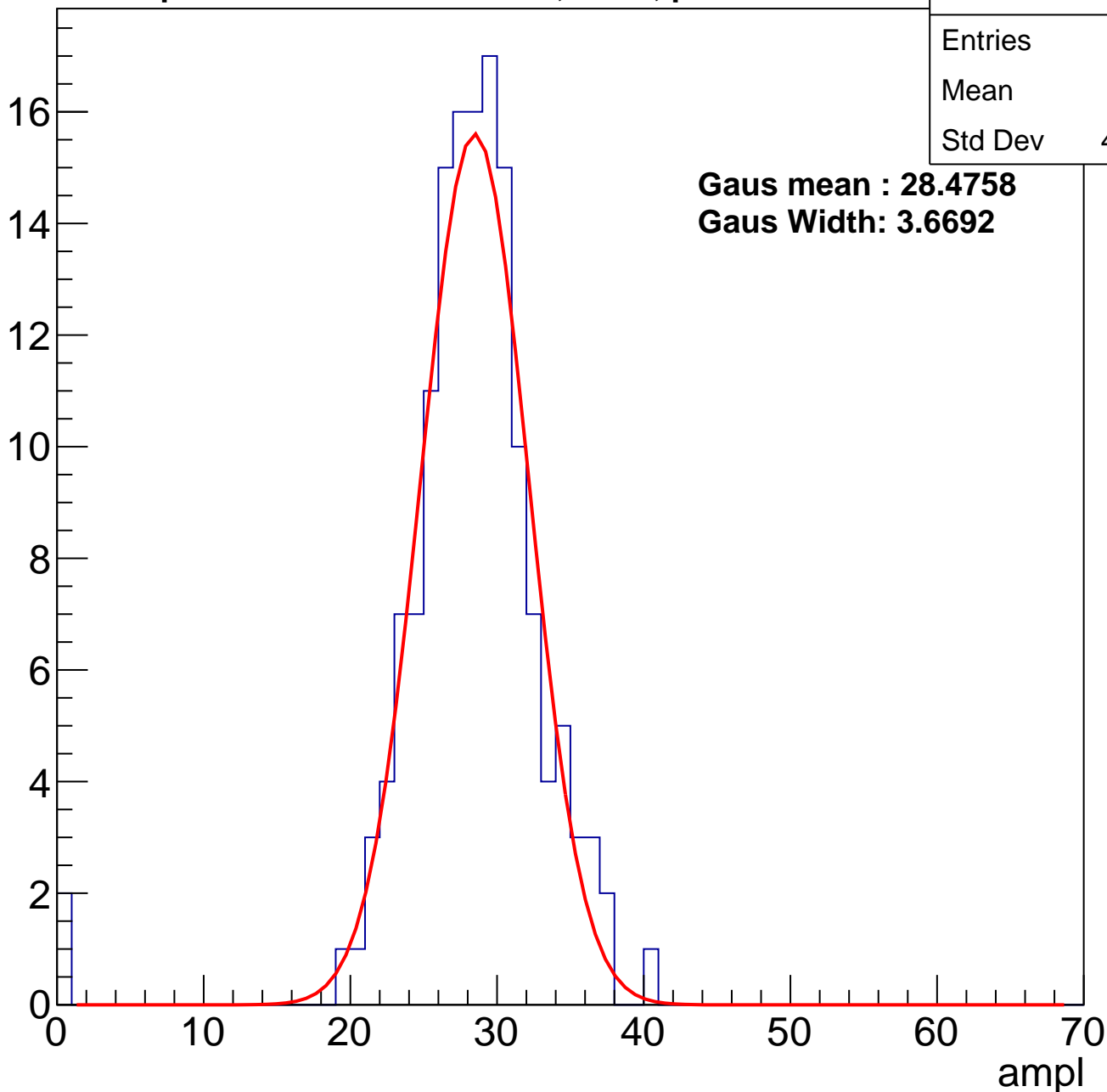
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 150   |
| Mean    | 27.8  |
| Std Dev | 4.942 |

**Gaus mean : 28.4758**

**Gaus Width: 3.6692**

Entry



# B0L101S, U16-ch88, adc1

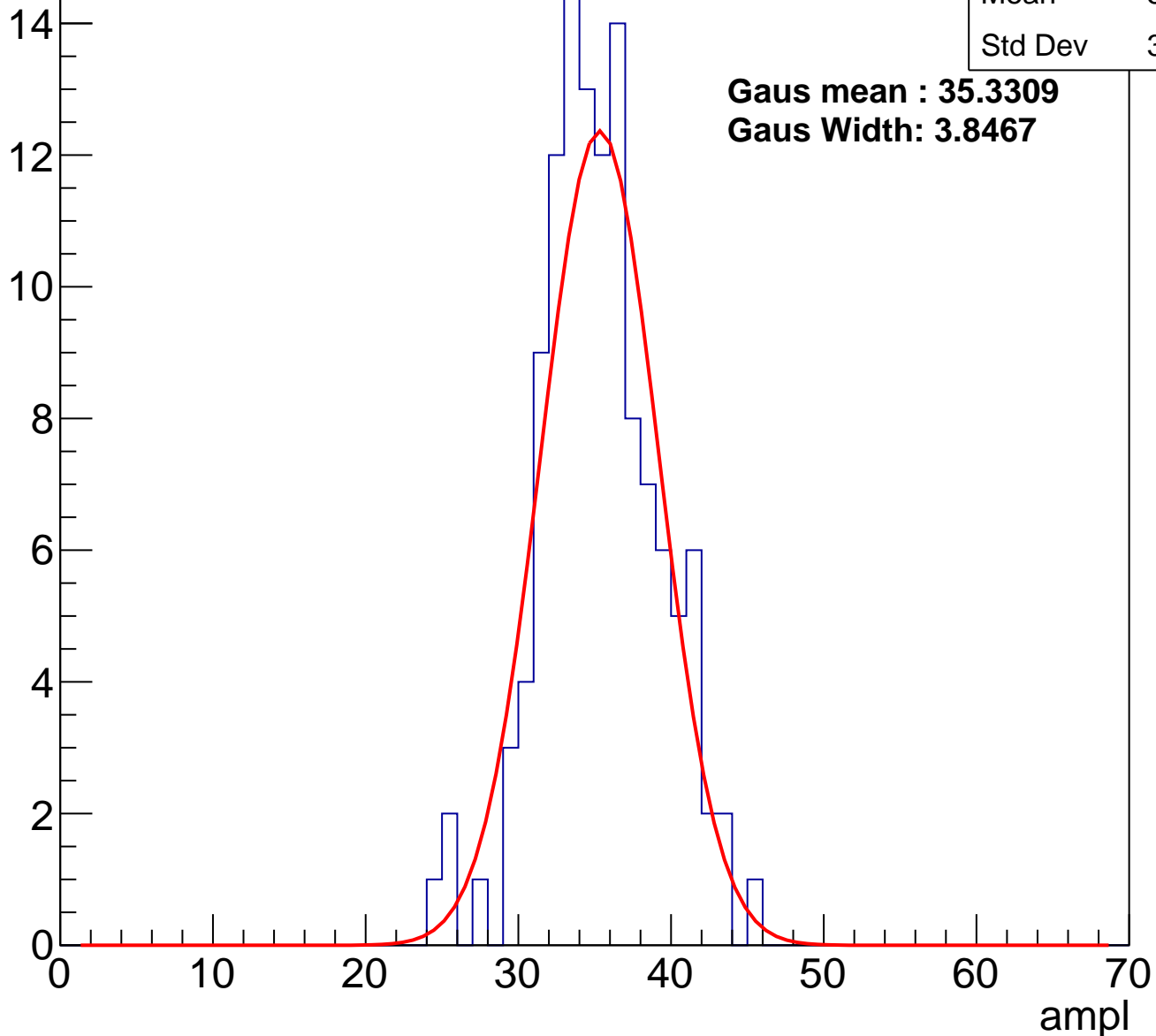
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 34.87 |
| Std Dev | 3.813 |

**Gaus mean : 35.3309**

**Gaus Width: 3.8467**

Entry



# B0L101S, U16-ch88, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

145

Mean

41.43

Std Dev

3.89

**Gaus mean : 41.7549**

**Gaus Width: 3.6379**

0

2

4

6

8

10

12

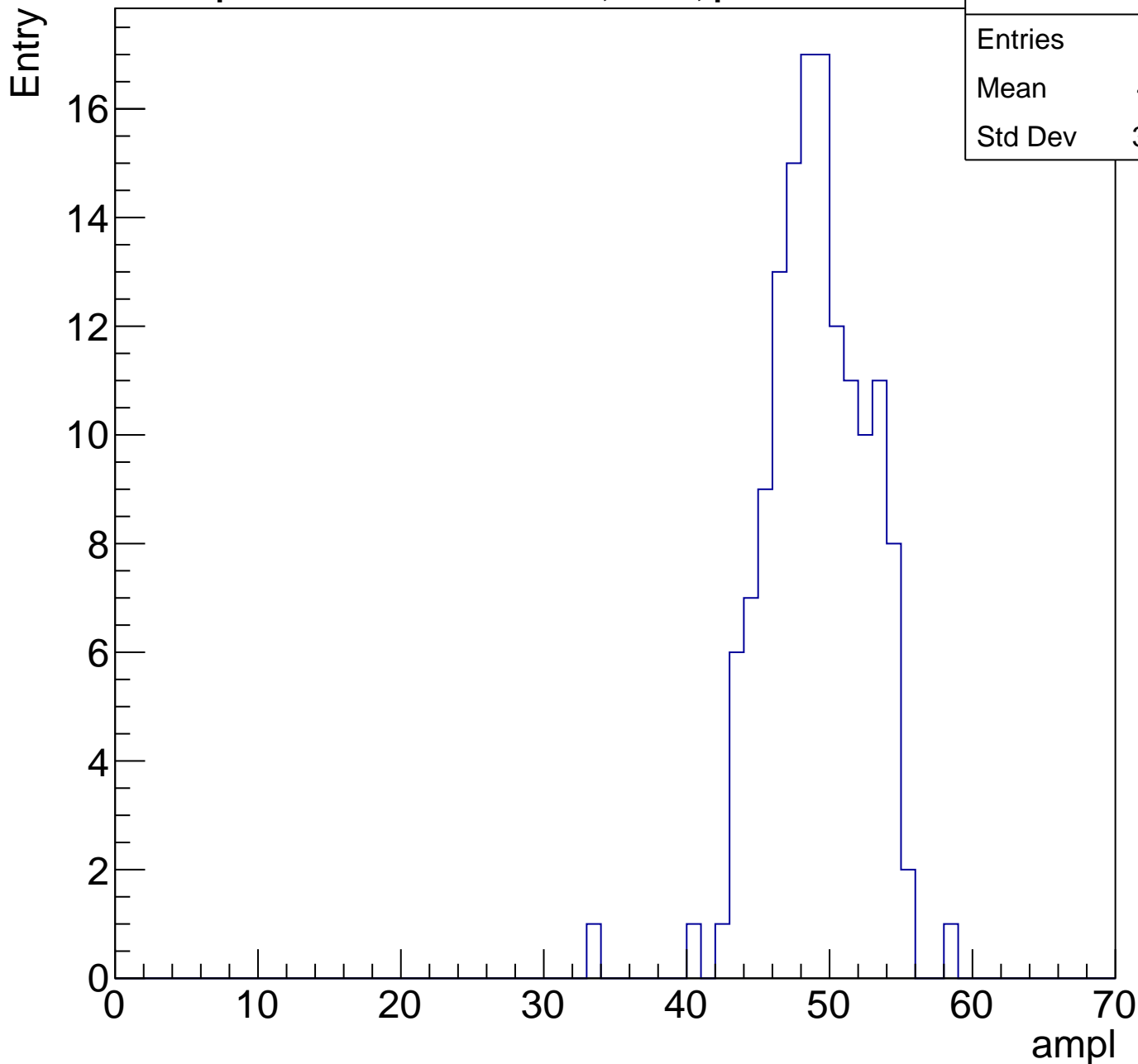
14

16

# B0L101S, U16-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 48.61 |
| Std Dev | 3.528 |



# B0L101S, U16-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

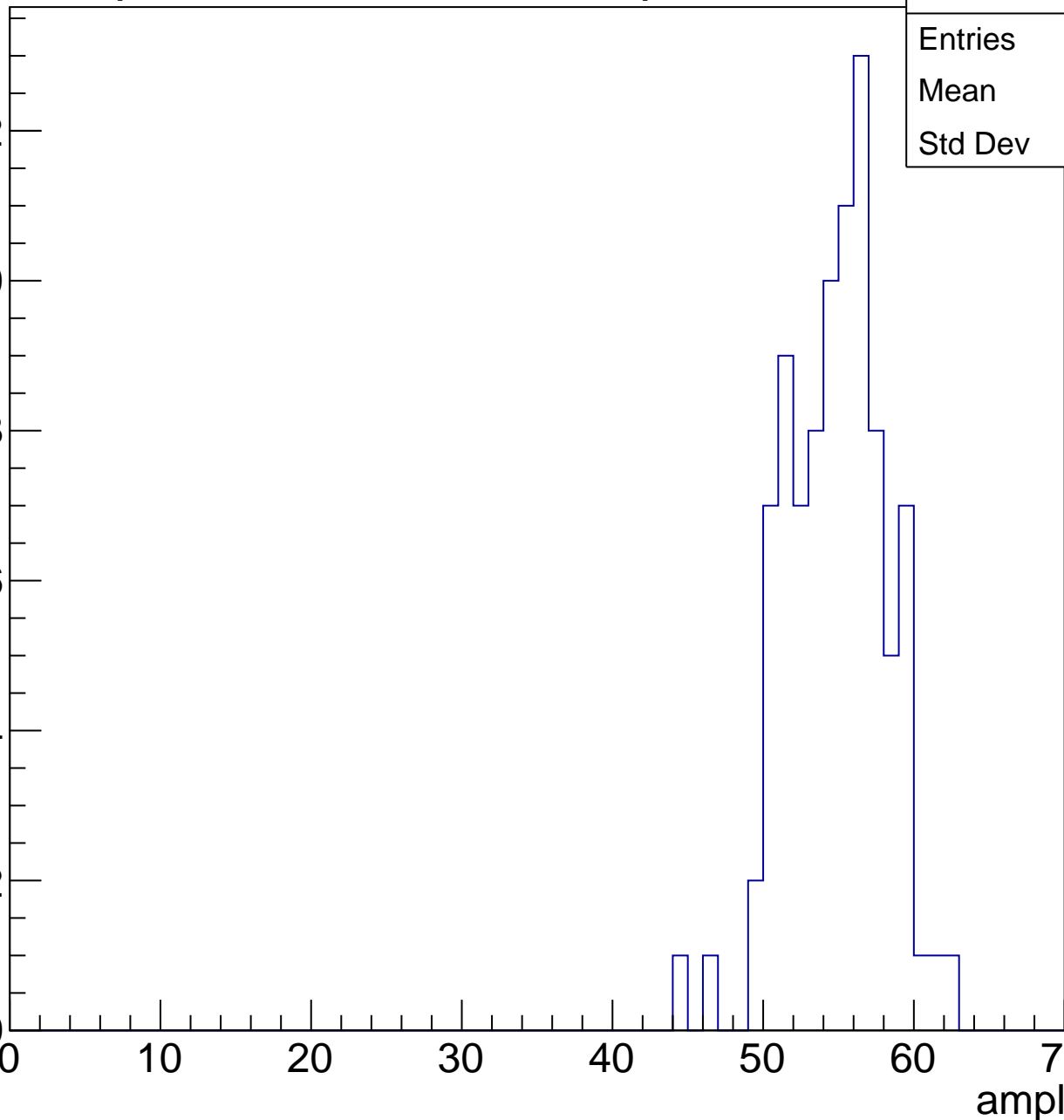
50

60

70

ampl

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 54.35 |
| Std Dev | 3.249 |



# B0L101S, U16-ch88, adc5

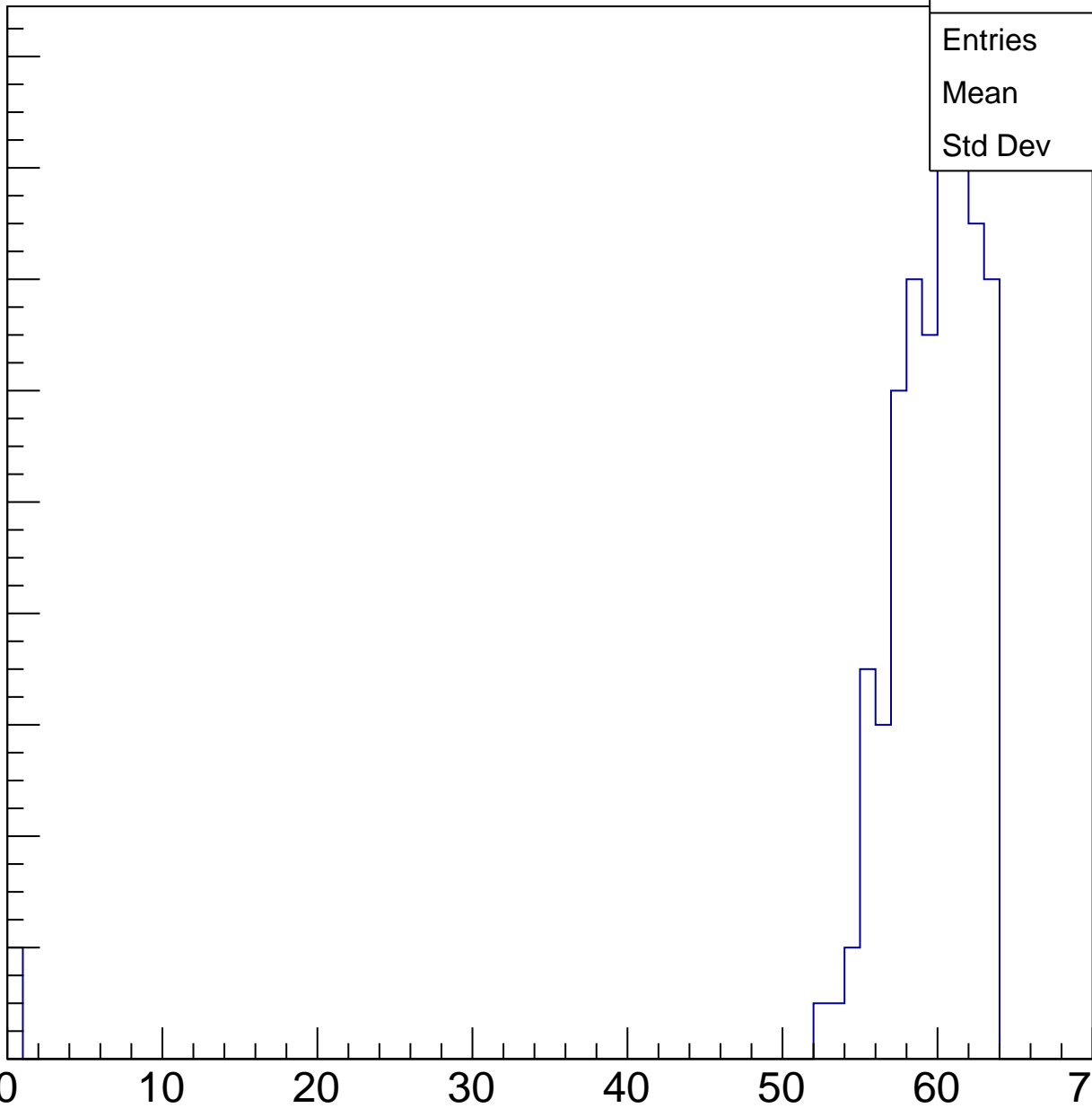
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 122   |
| Mean    | 58.41 |
| Std Dev | 7.959 |

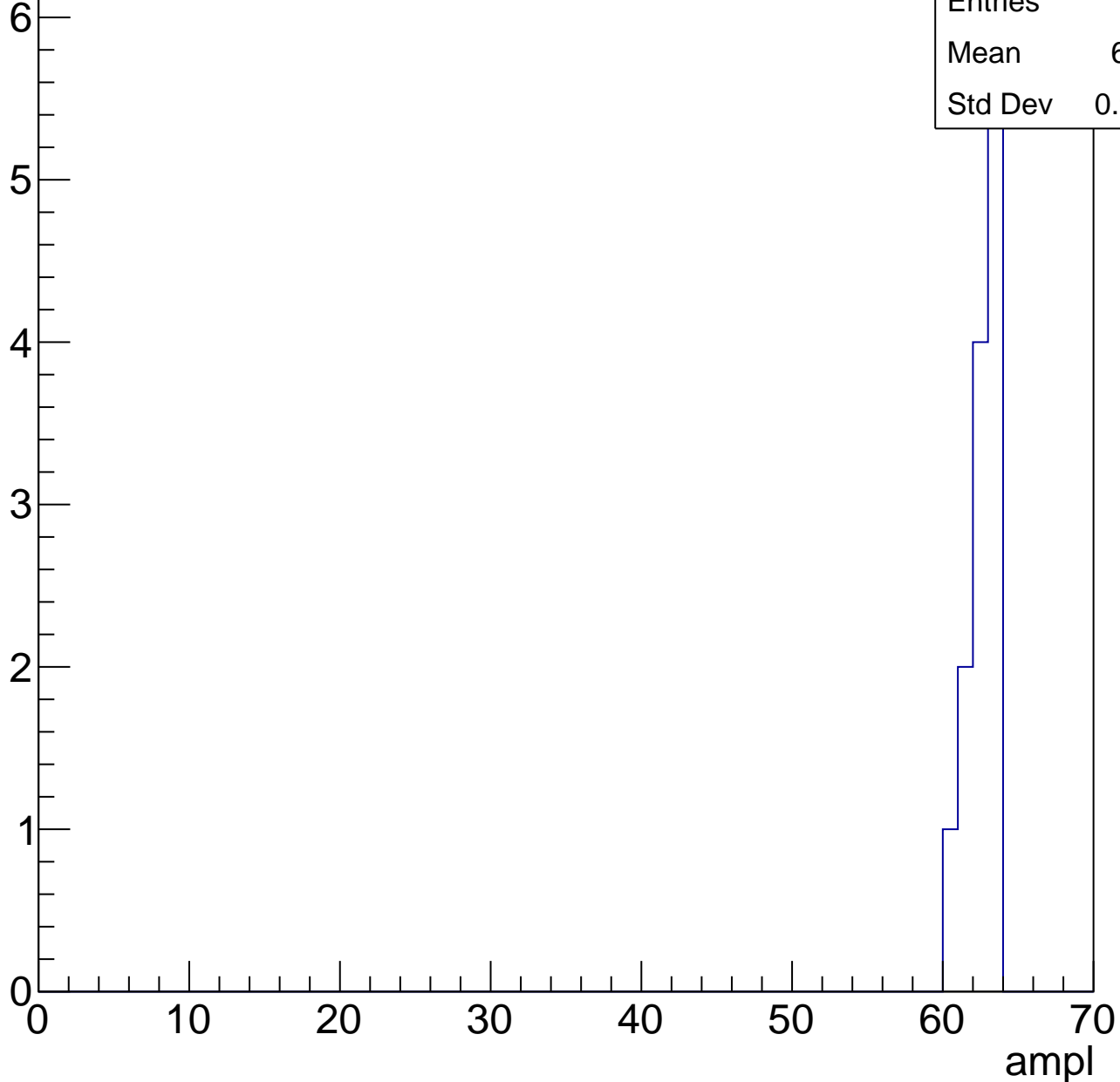
ampl



# B0L101S, U16-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch89, adc0

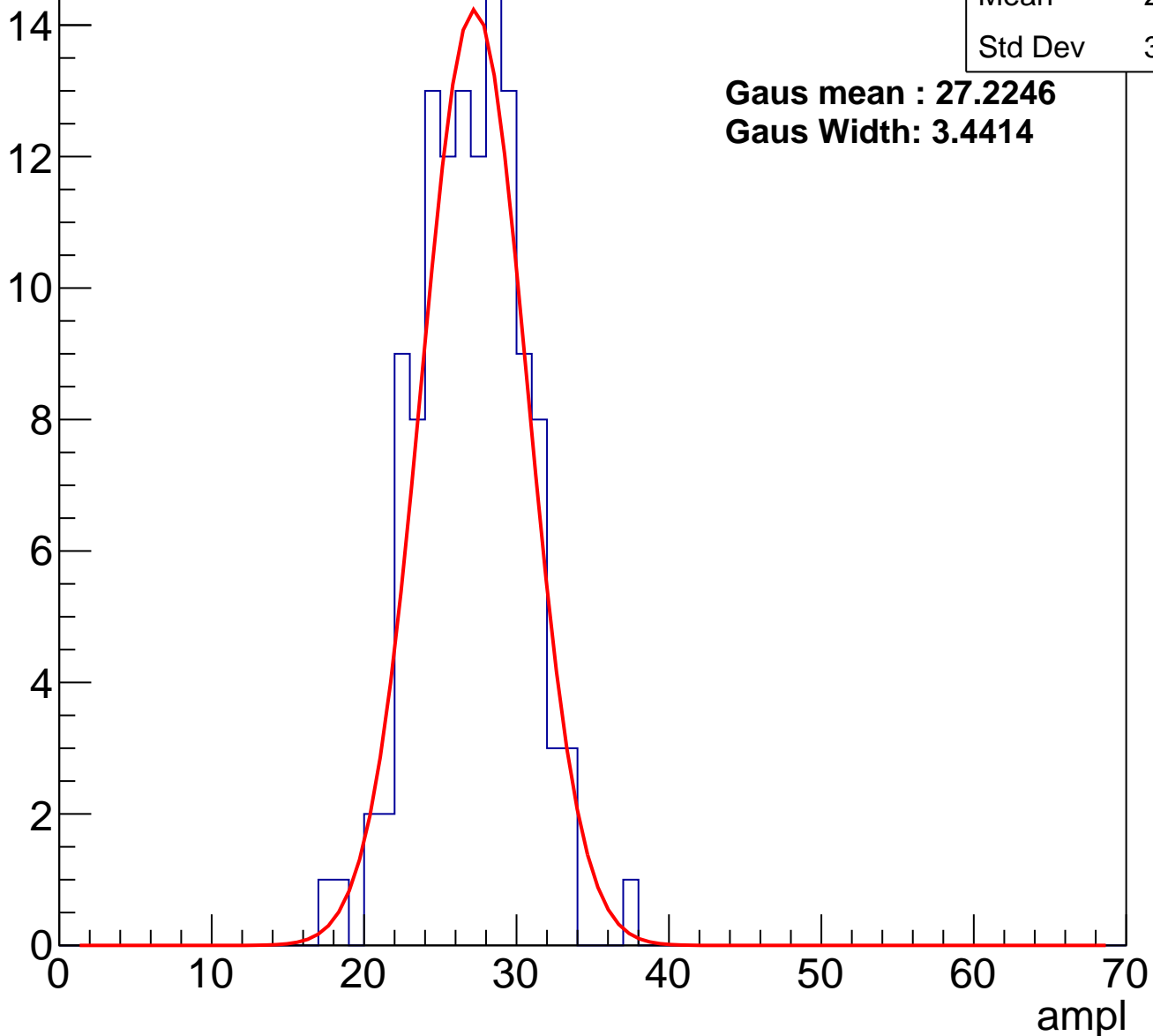
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 26.56 |
| Std Dev | 3.367 |

**Gaus mean : 27.2246**

**Gaus Width: 3.4414**

Entry



# B0L101S, U16-ch89, adc1

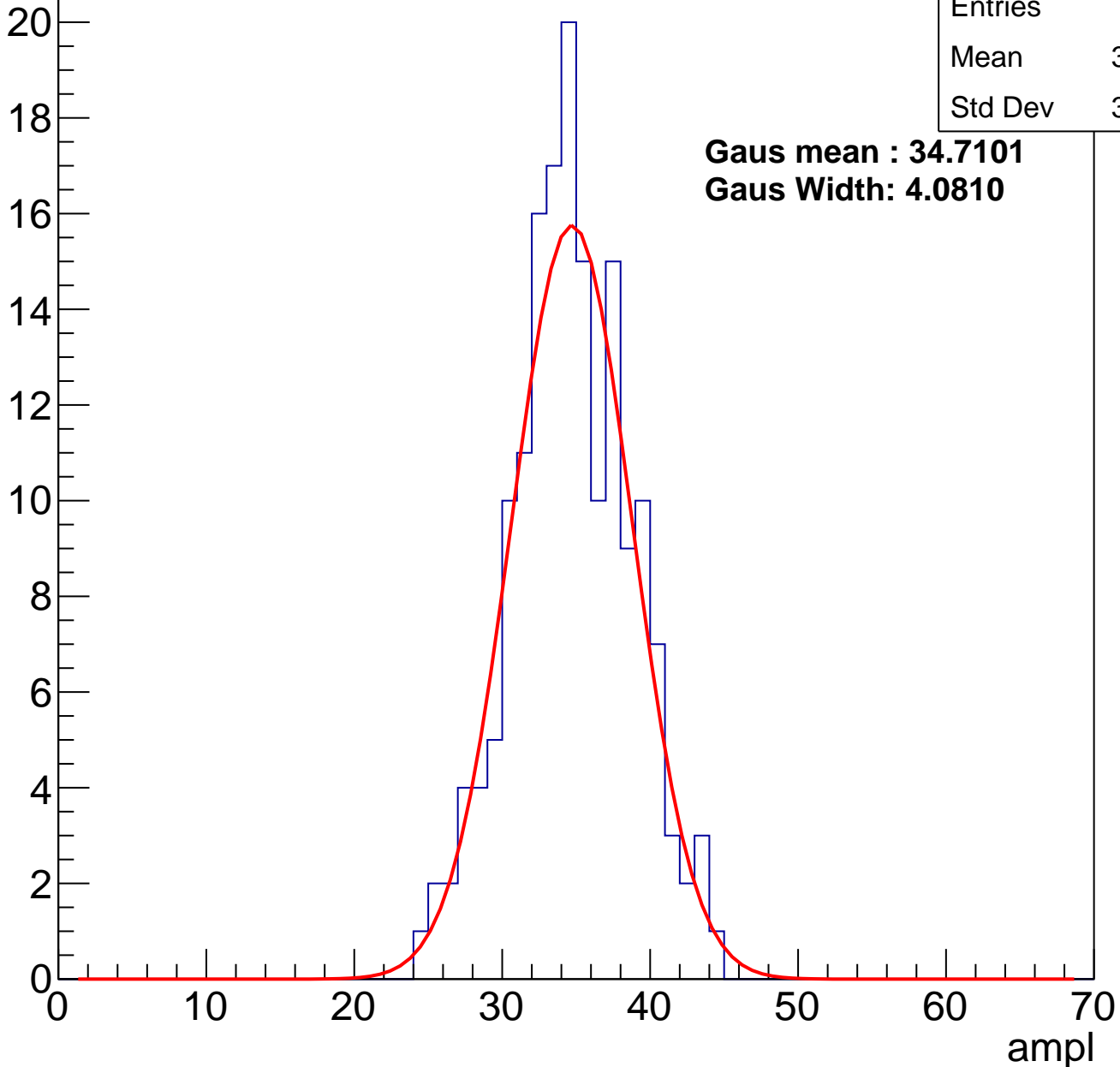
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 167   |
| Mean    | 34.23 |
| Std Dev | 3.976 |

**Gaus mean : 34.7101**

**Gaus Width: 4.0810**

Entry



# B0L101S, U16-ch89, adc2

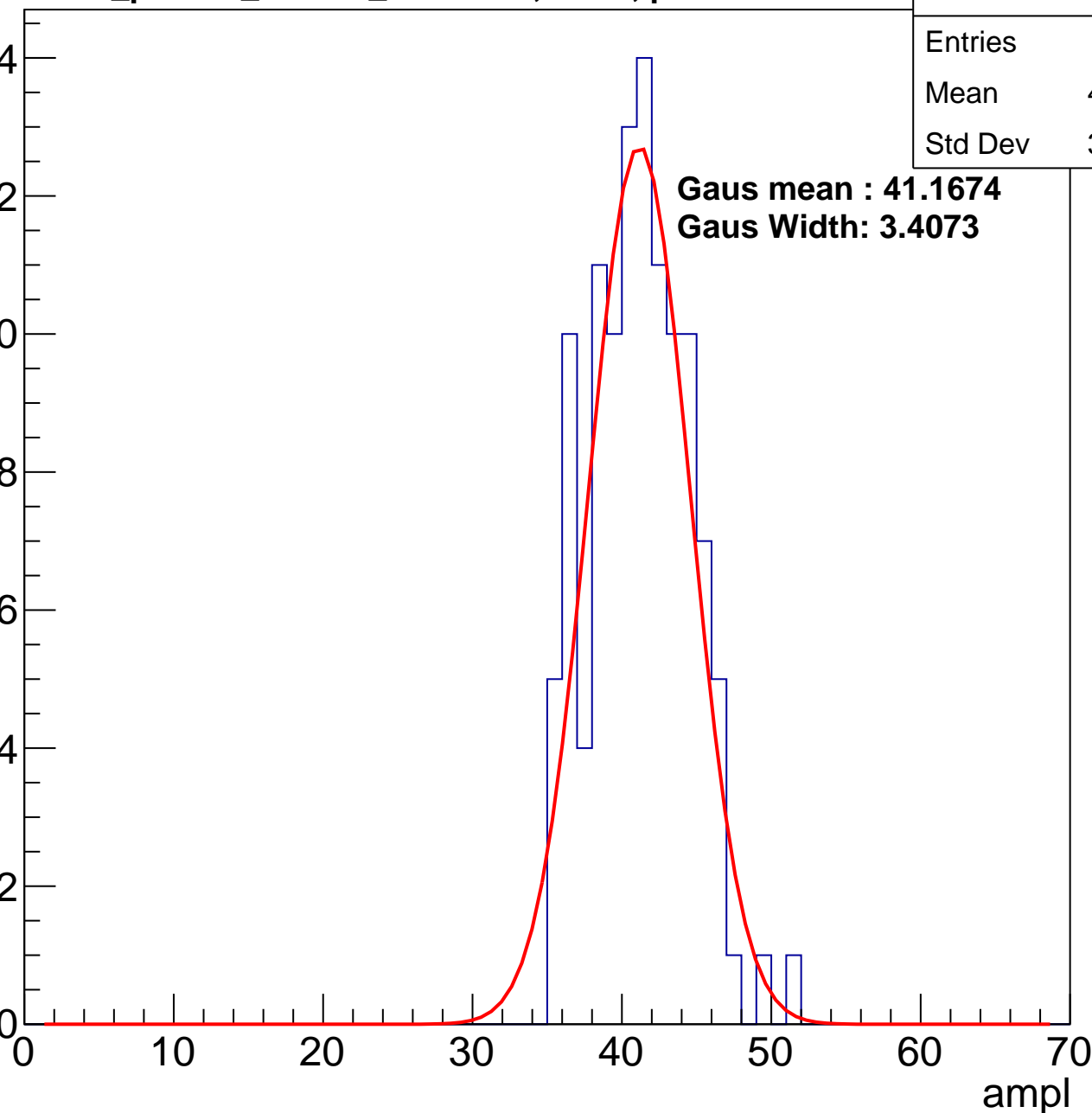
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 40.79 |
| Std Dev | 3.284 |

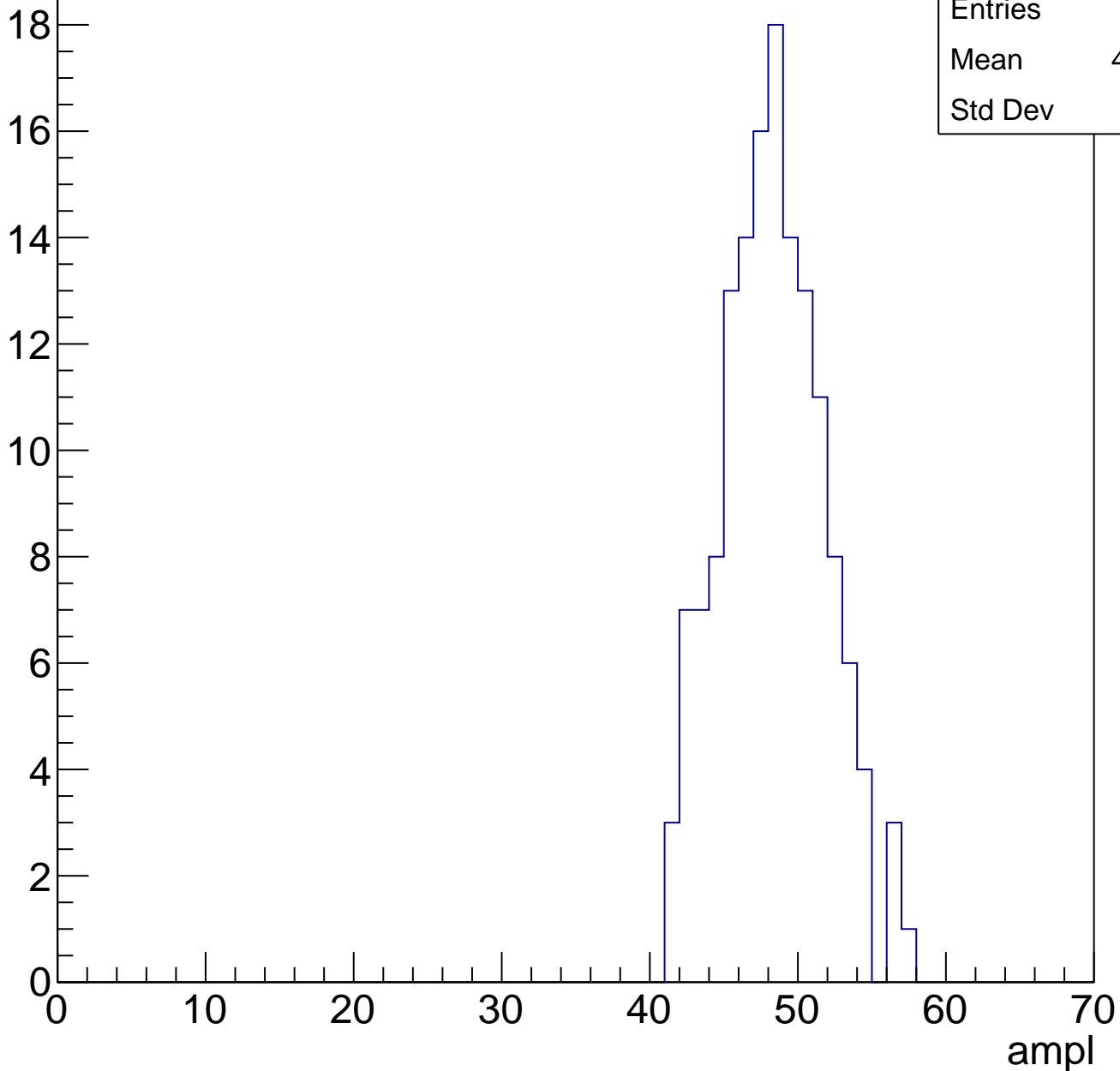
**Gaus mean : 41.1674**  
**Gaus Width: 3.4073**



# B0L101S, U16-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 47.86 |
| Std Dev | 3.46  |

# B0L101S, U16-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

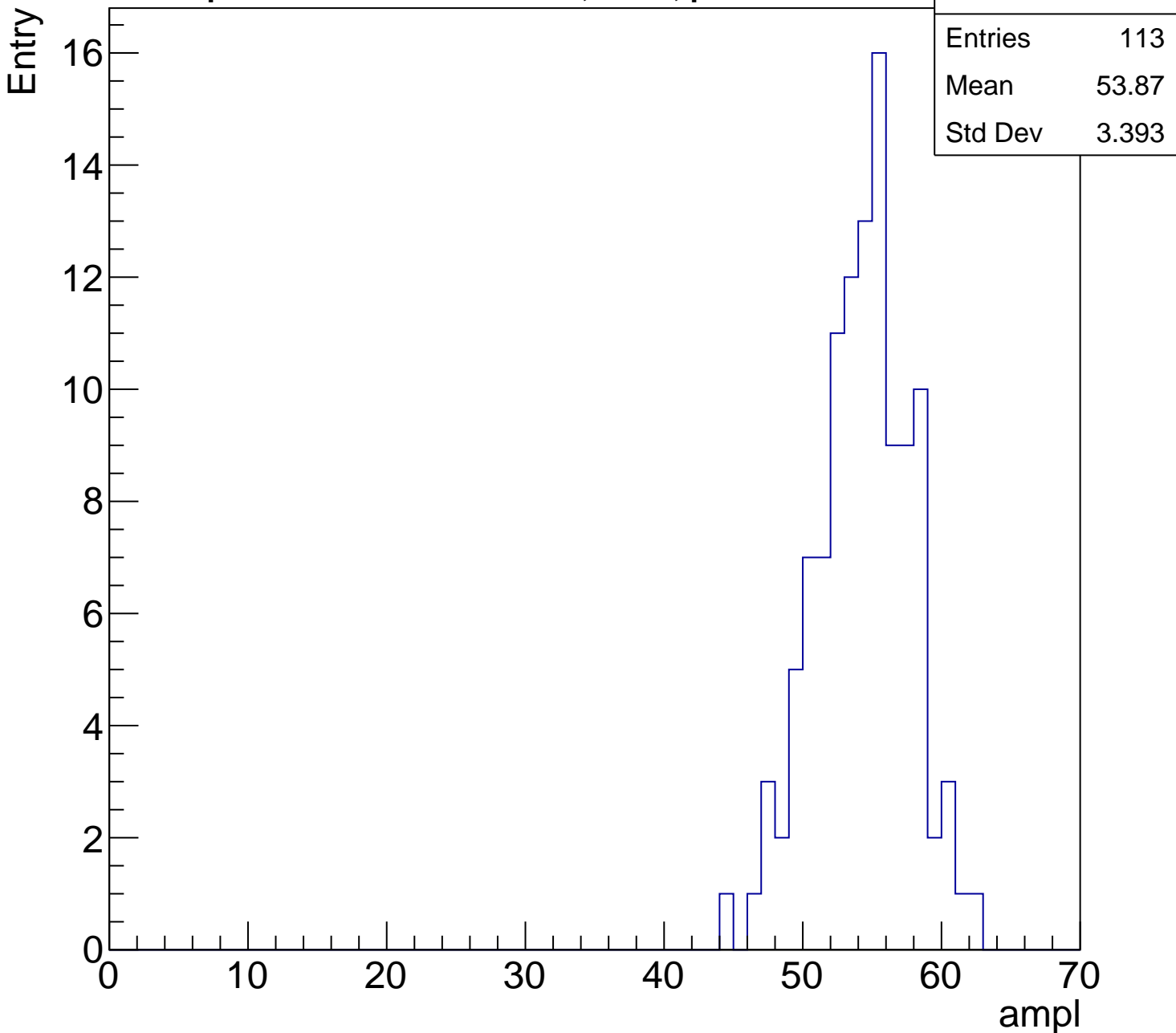
|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 53.87 |
| Std Dev | 3.393 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

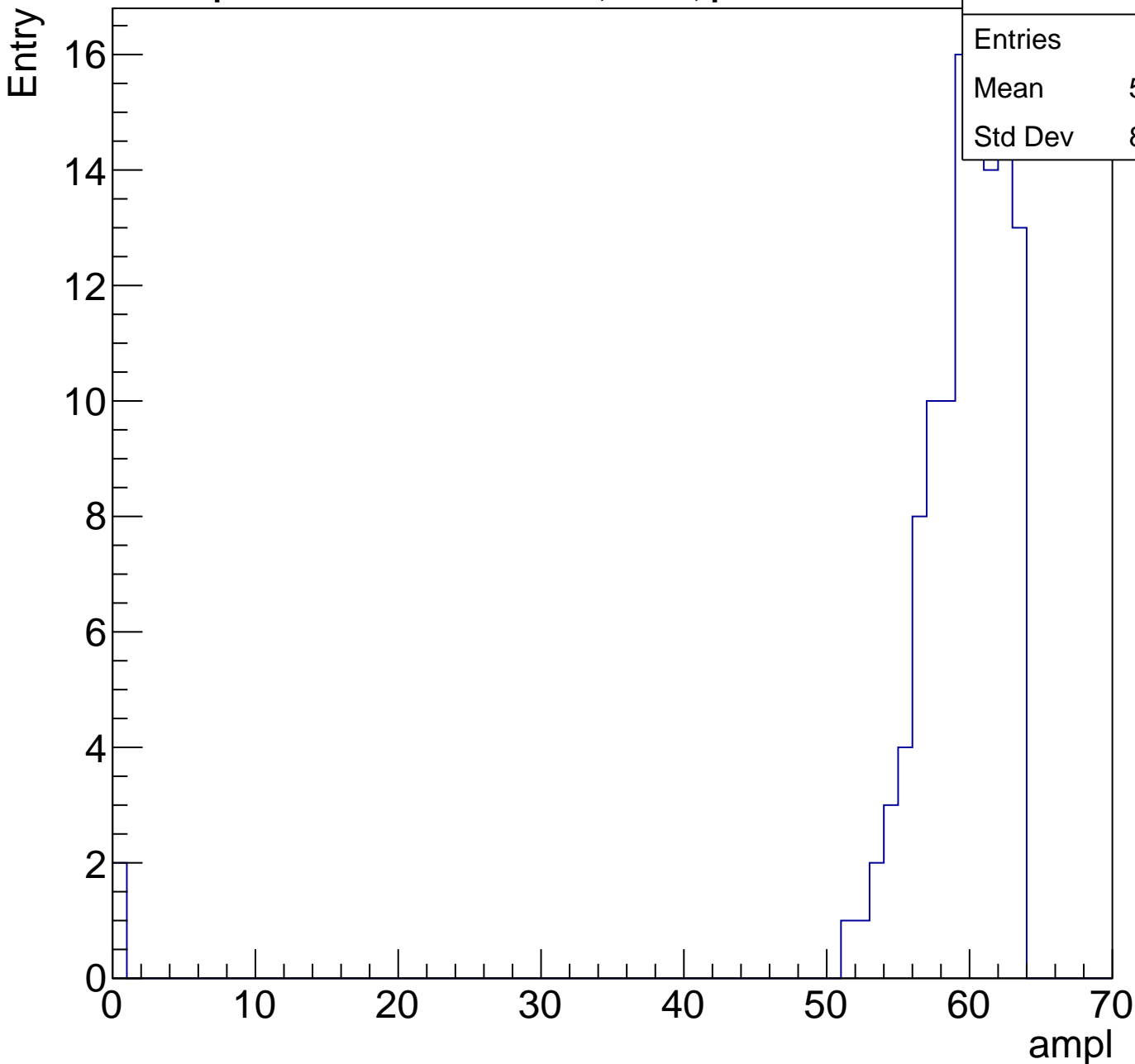
ampl



# B0L101S, U16-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 58.26 |
| Std Dev | 8.183 |

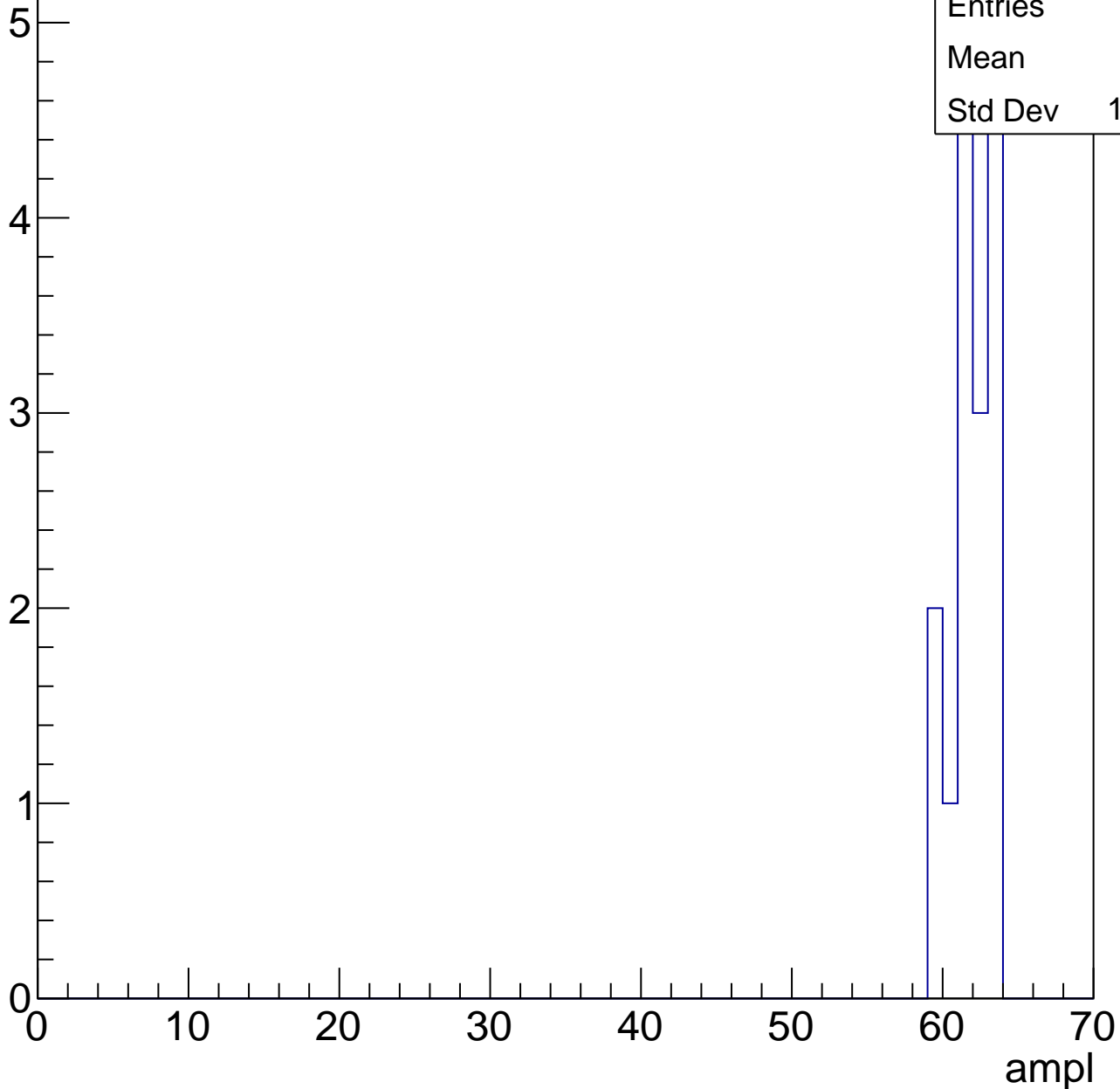


# B0L101S, U16-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 16    |
| Mean    | 61.5  |
| Std Dev | 1.323 |





# B0L101S, U16-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 28.02 |
| Std Dev | 6.471 |

**Gaus mean : 29.2040**

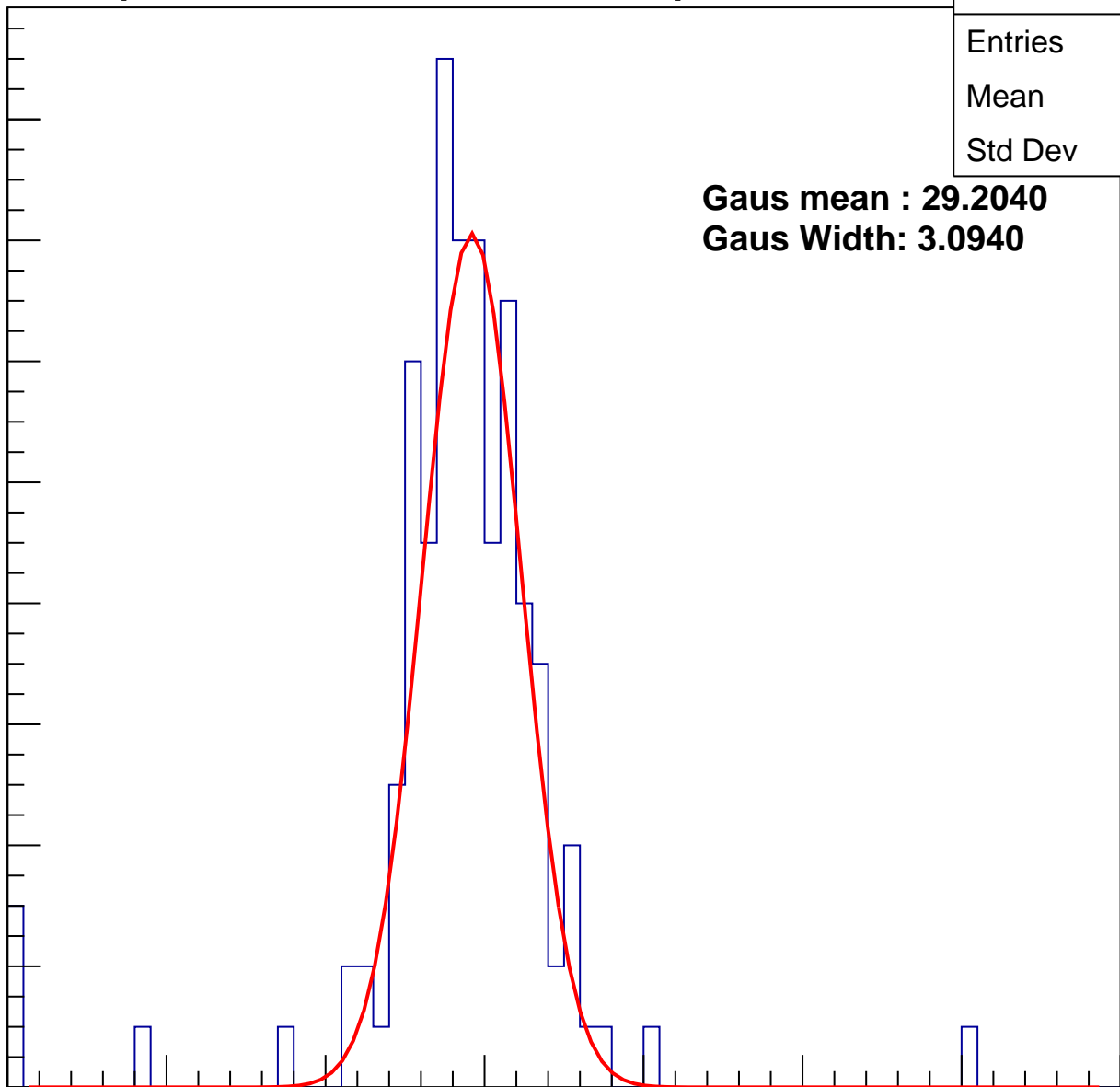
**Gaus Width: 3.0940**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch90, adc1

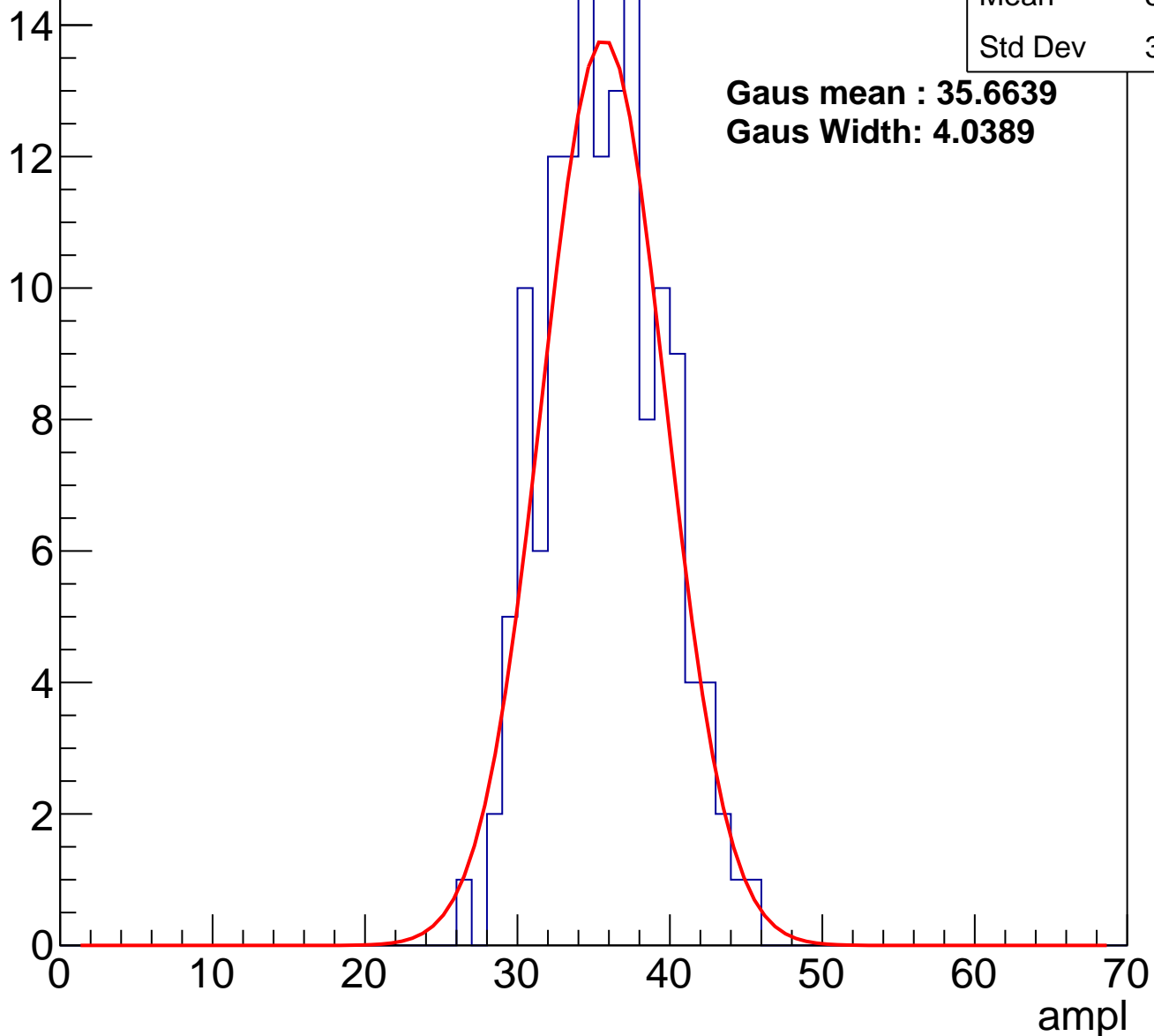
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 35.26 |
| Std Dev | 3.795 |

**Gaus mean : 35.6639**

**Gaus Width: 4.0389**

Entry



# B0L101S, U16-ch90, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 87    |
| Mean    | 40.48 |
| Std Dev | 3.616 |

**Gaus mean : 41.0342**

**Gaus Width: 3.9464**

Entry

10

8

6

4

2

0

0

10

20

30

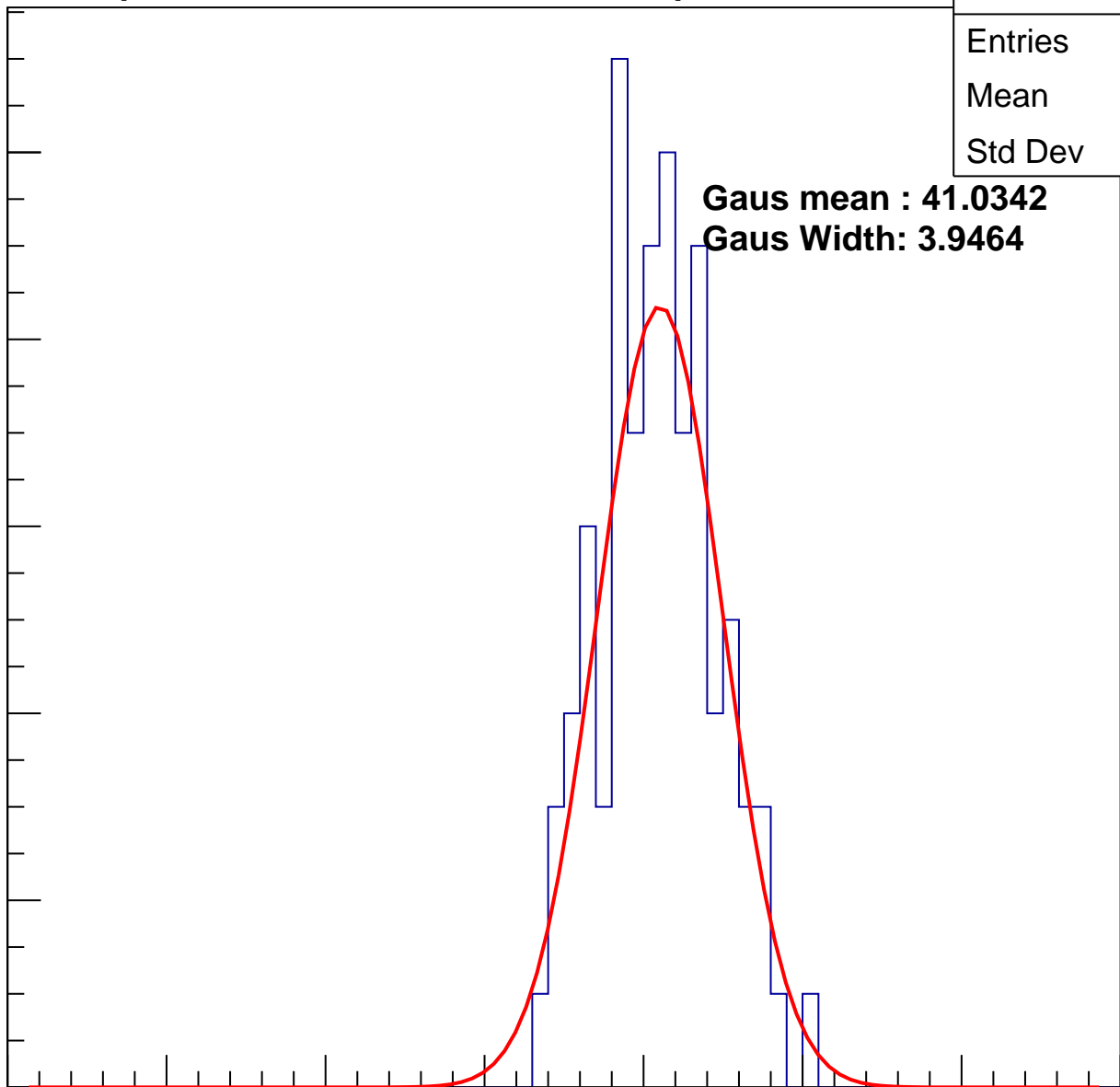
40

50

60

70

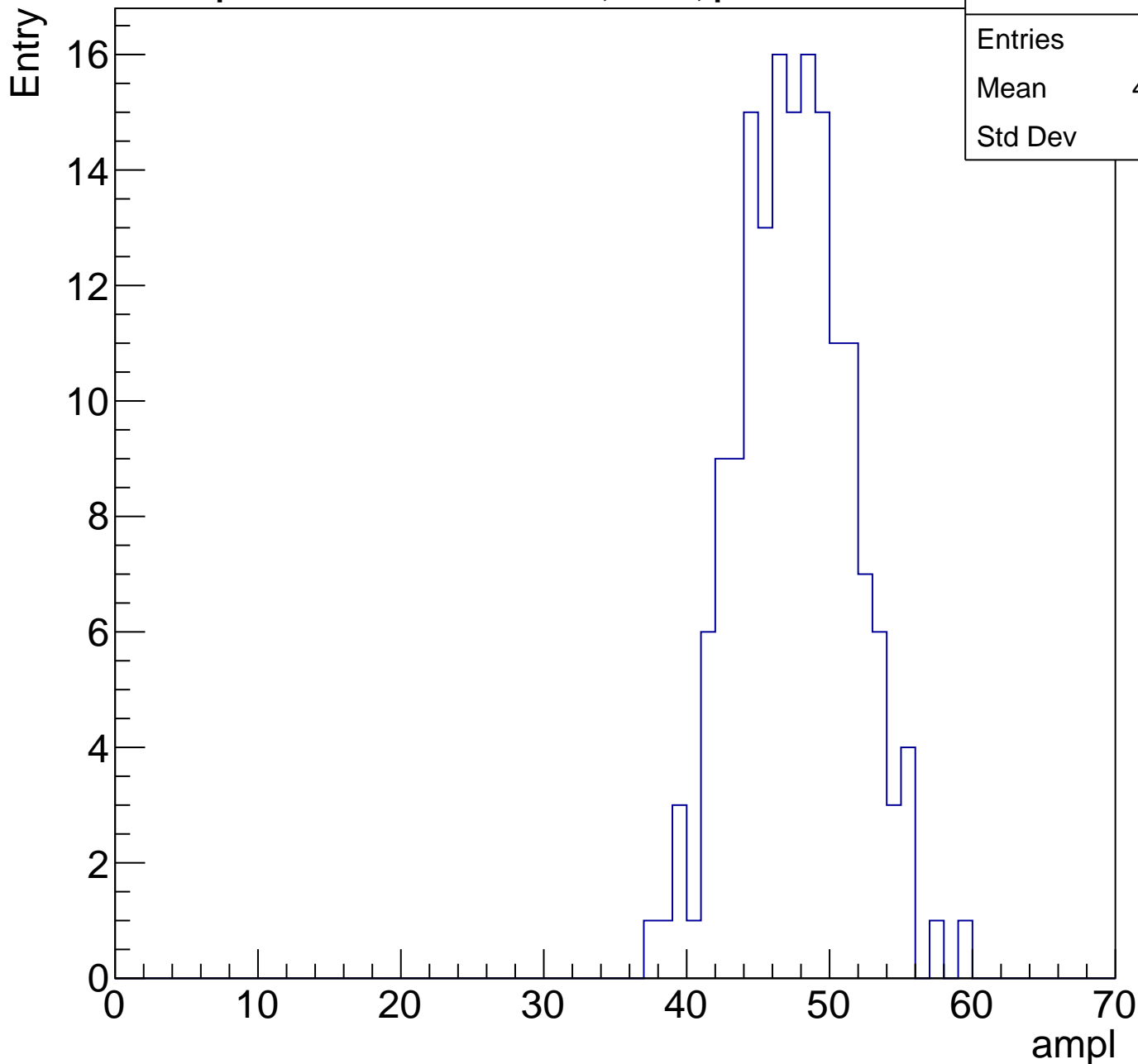
ampl



# B0L101S, U16-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

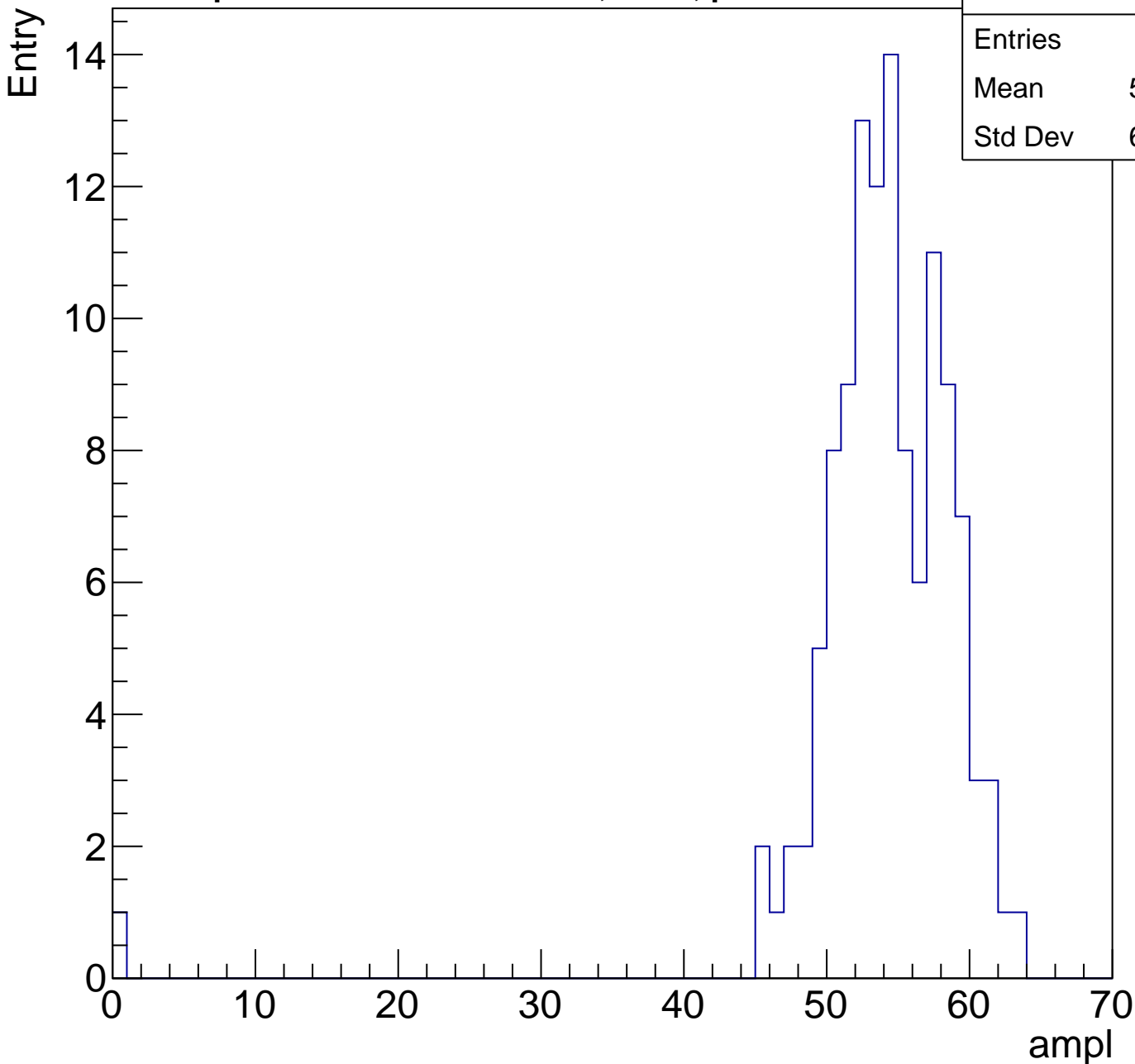
|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 47.09 |
| Std Dev | 3.98  |



# B0L101S, U16-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 53.59 |
| Std Dev | 6.203 |



# B0L101S, U16-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

83

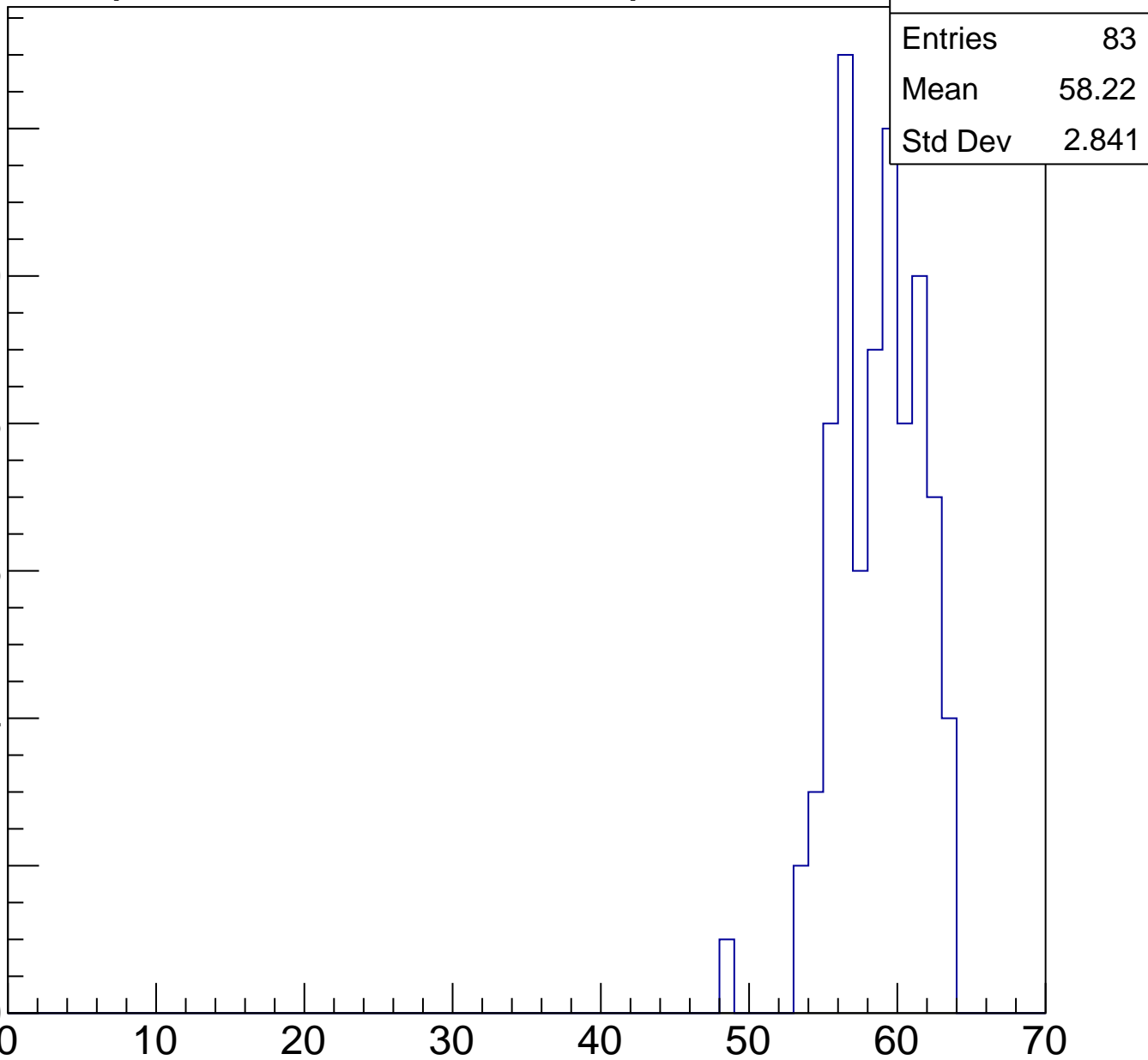
Mean

58.22

Std Dev

2.841

ampl



# B0L101S, U16-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 59.87 |
| Std Dev | 9.1   |

0

2

4

6

8

10

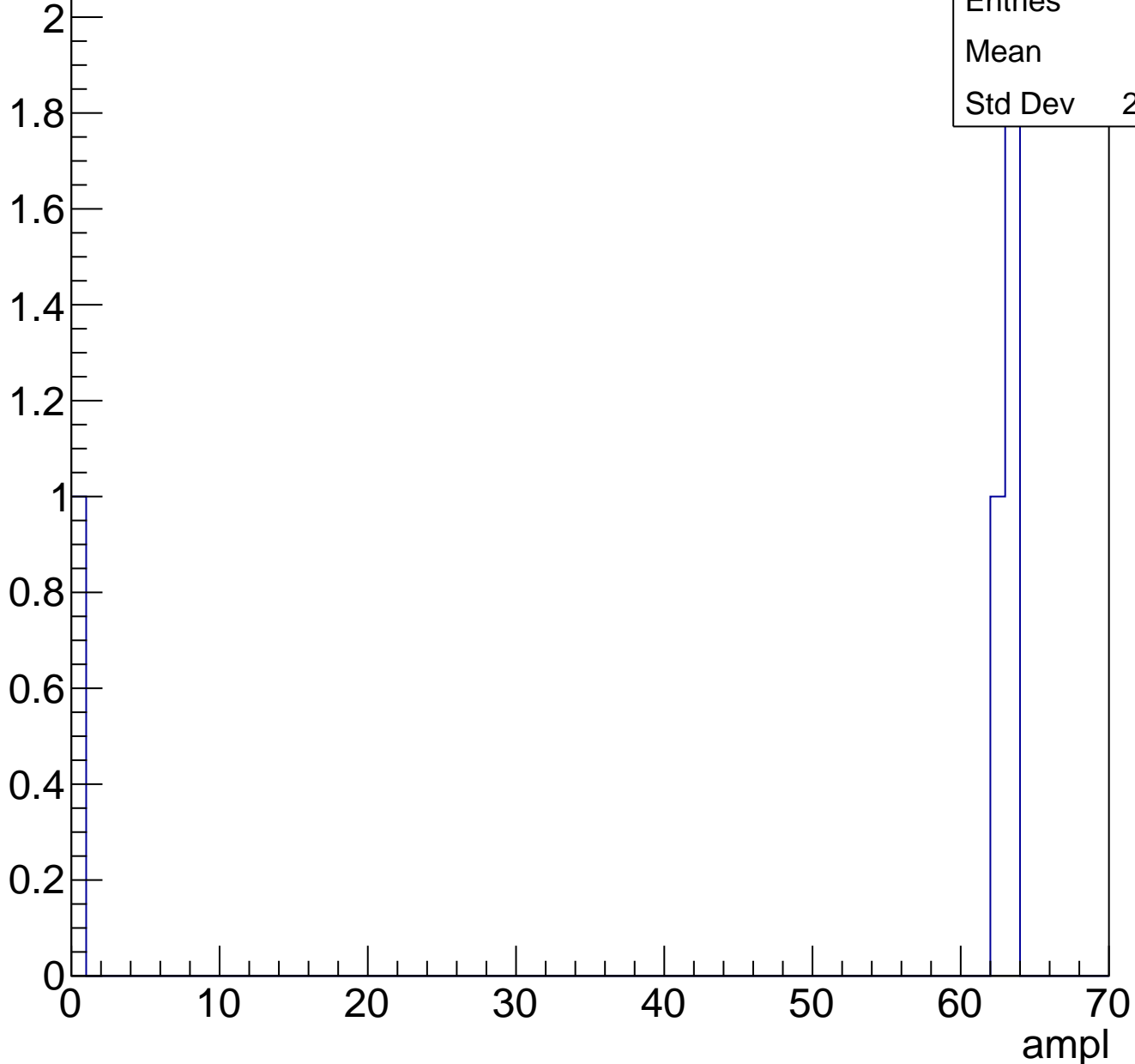
12



# B0L101S, U16-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch91, adc0

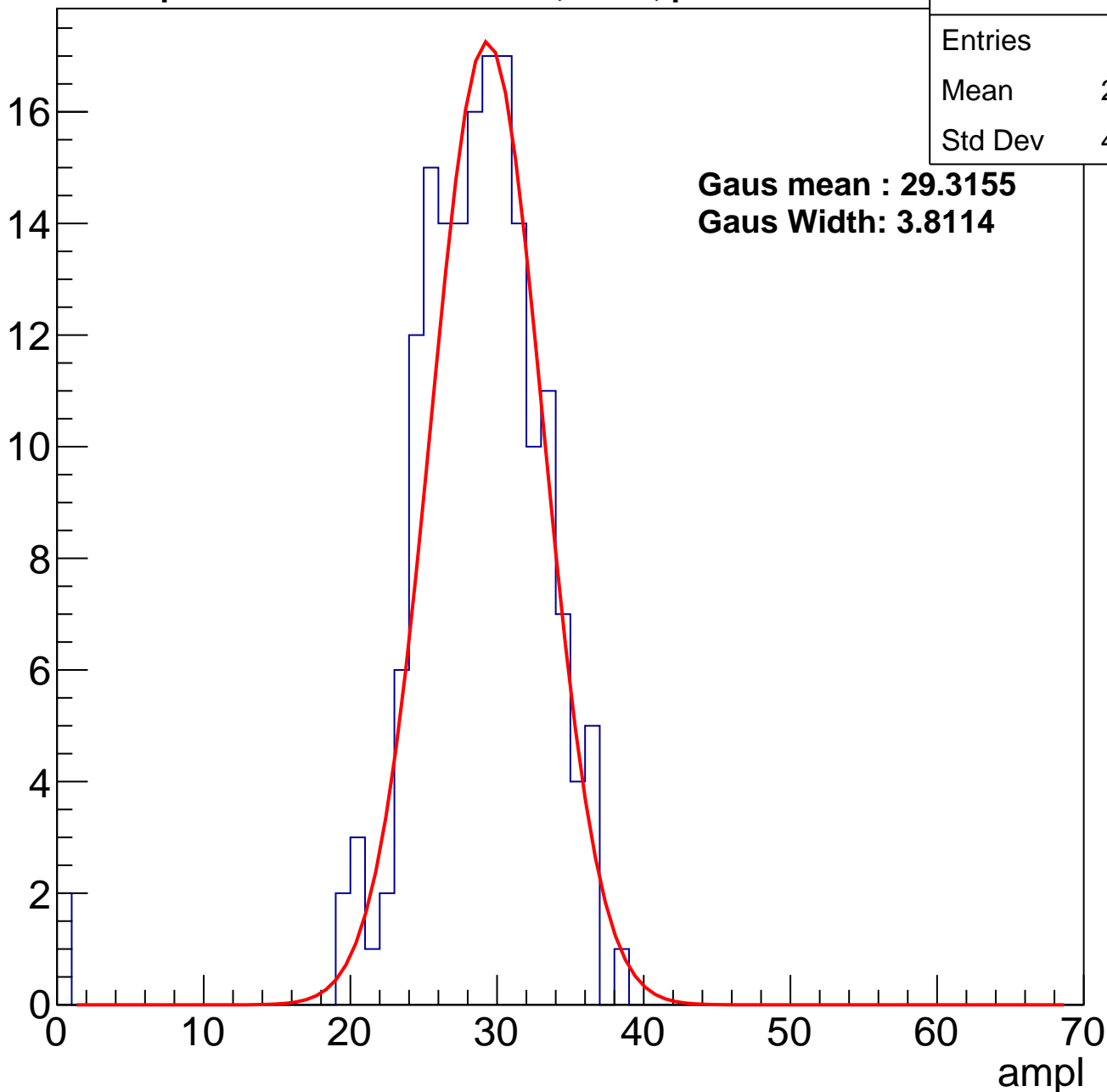
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 173   |
| Mean    | 28.15 |
| Std Dev | 4.868 |

**Gaus mean : 29.3155**

**Gaus Width: 3.8114**

Entry



# B0L101S, U16-ch91, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 112   |
| Mean    | 36.29 |
| Std Dev | 3.823 |

**Gaus mean : 36.6598**

**Gaus Width: 3.8087**

Entry

12

10

8

6

4

2

0

0

10

20

30

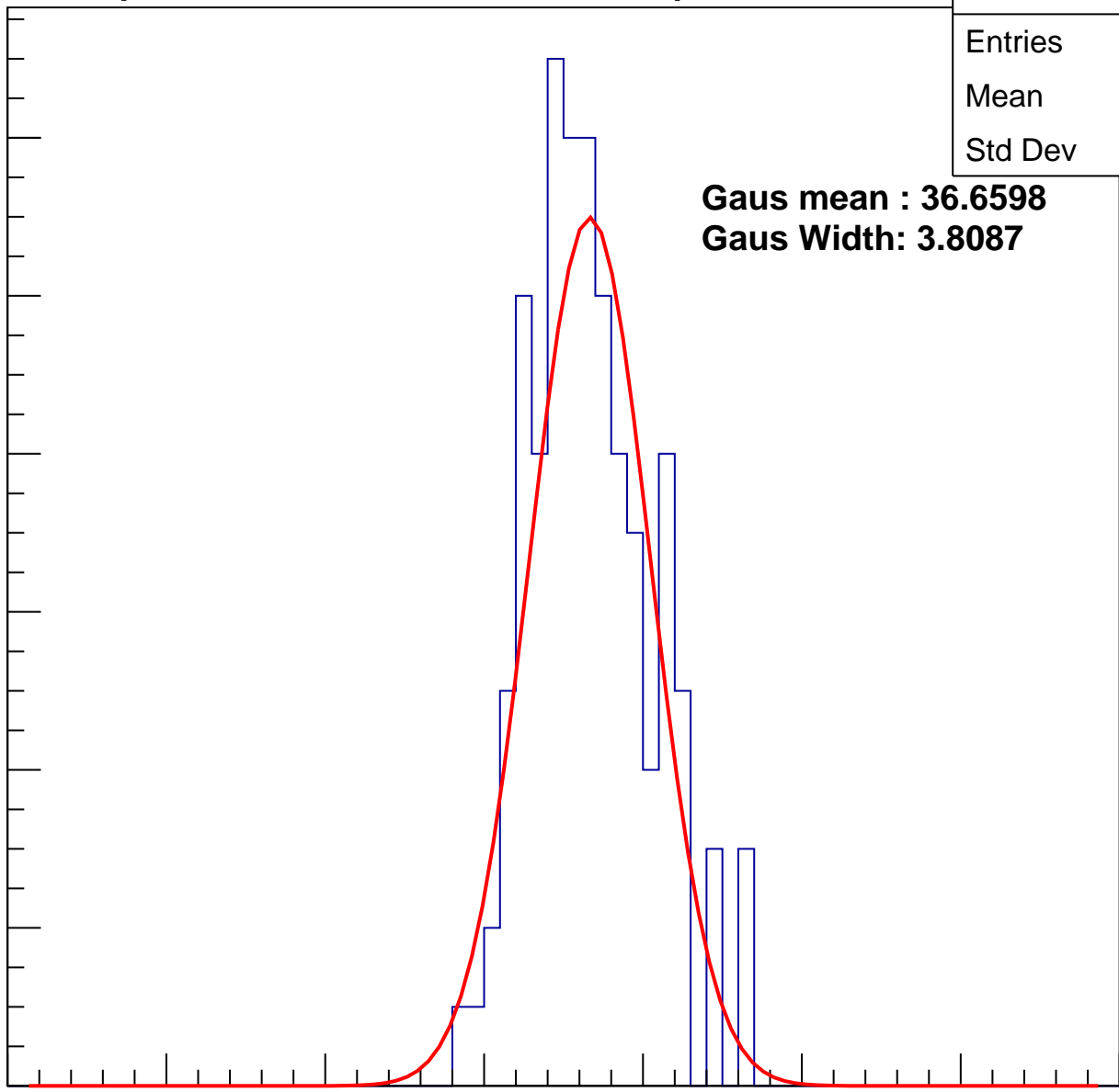
40

50

60

70

ampl



# B0L101S, U16-ch91, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

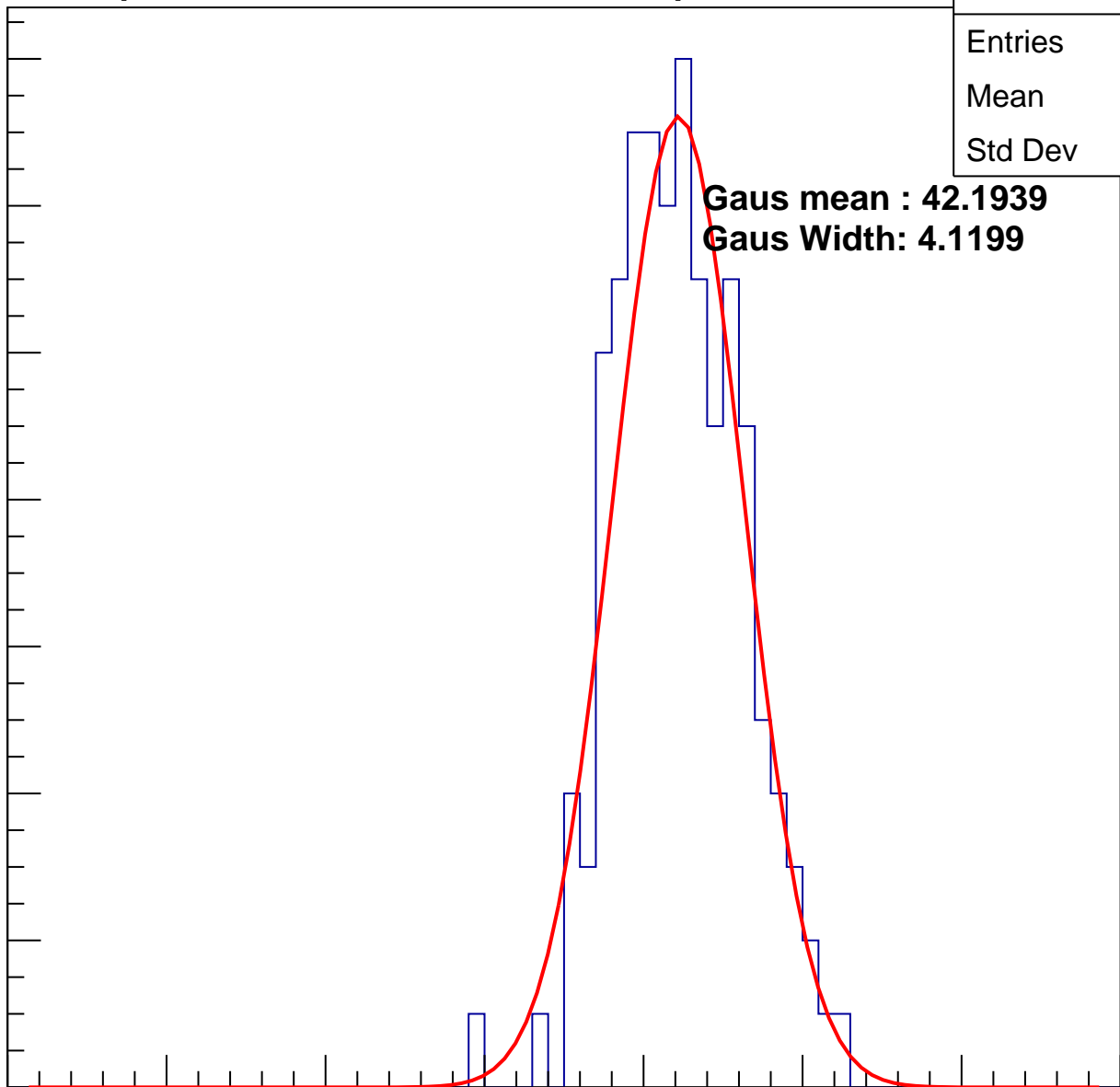
|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 41.74 |
| Std Dev | 3.962 |

**Gaus mean : 42.1939**

**Gaus Width: 4.1199**

0 10 20 30 40 50 60 70

ampl

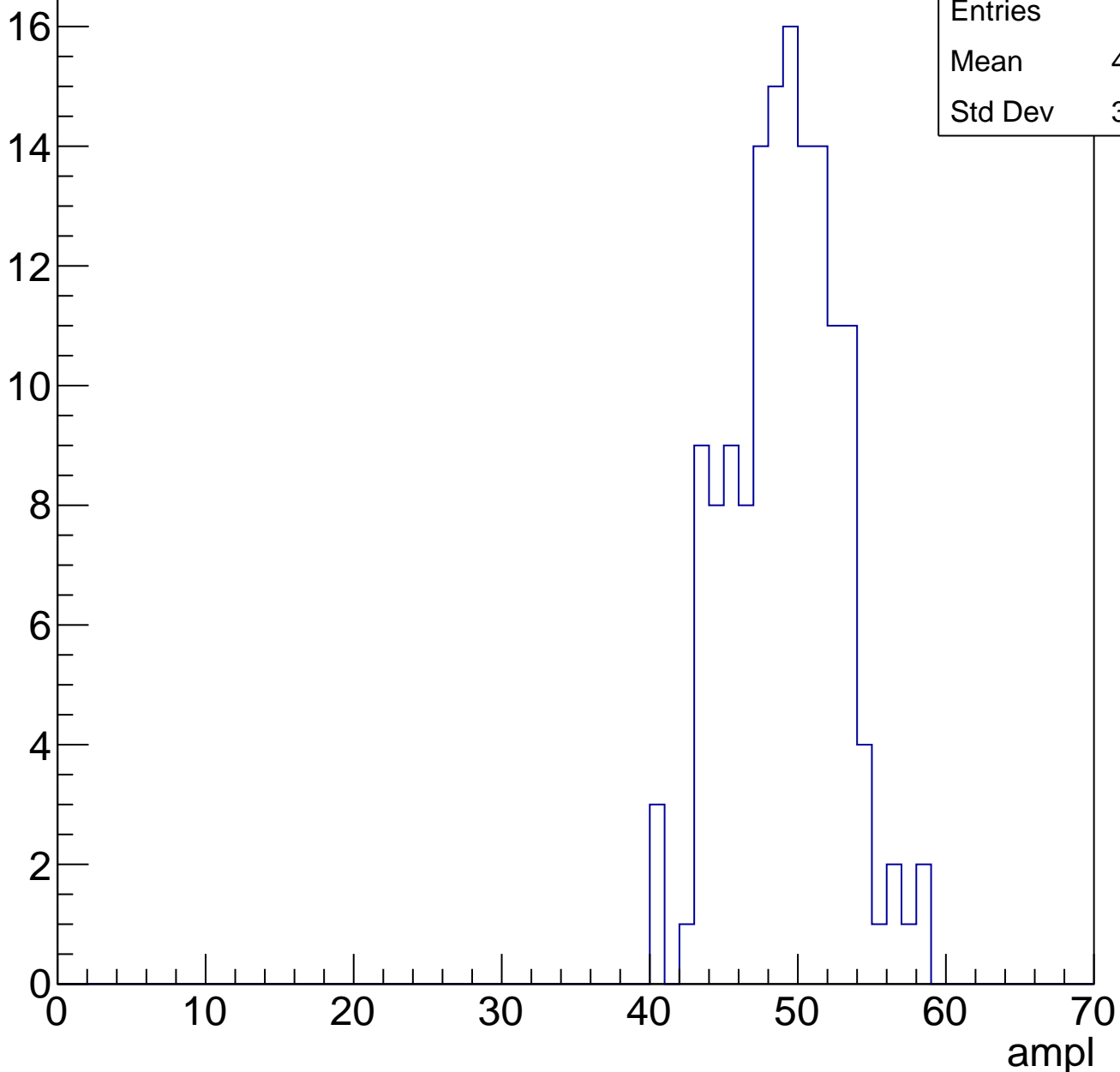


# B0L101S, U16-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 48.68 |
| Std Dev | 3.639 |

Entry



# B0L101S, U16-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

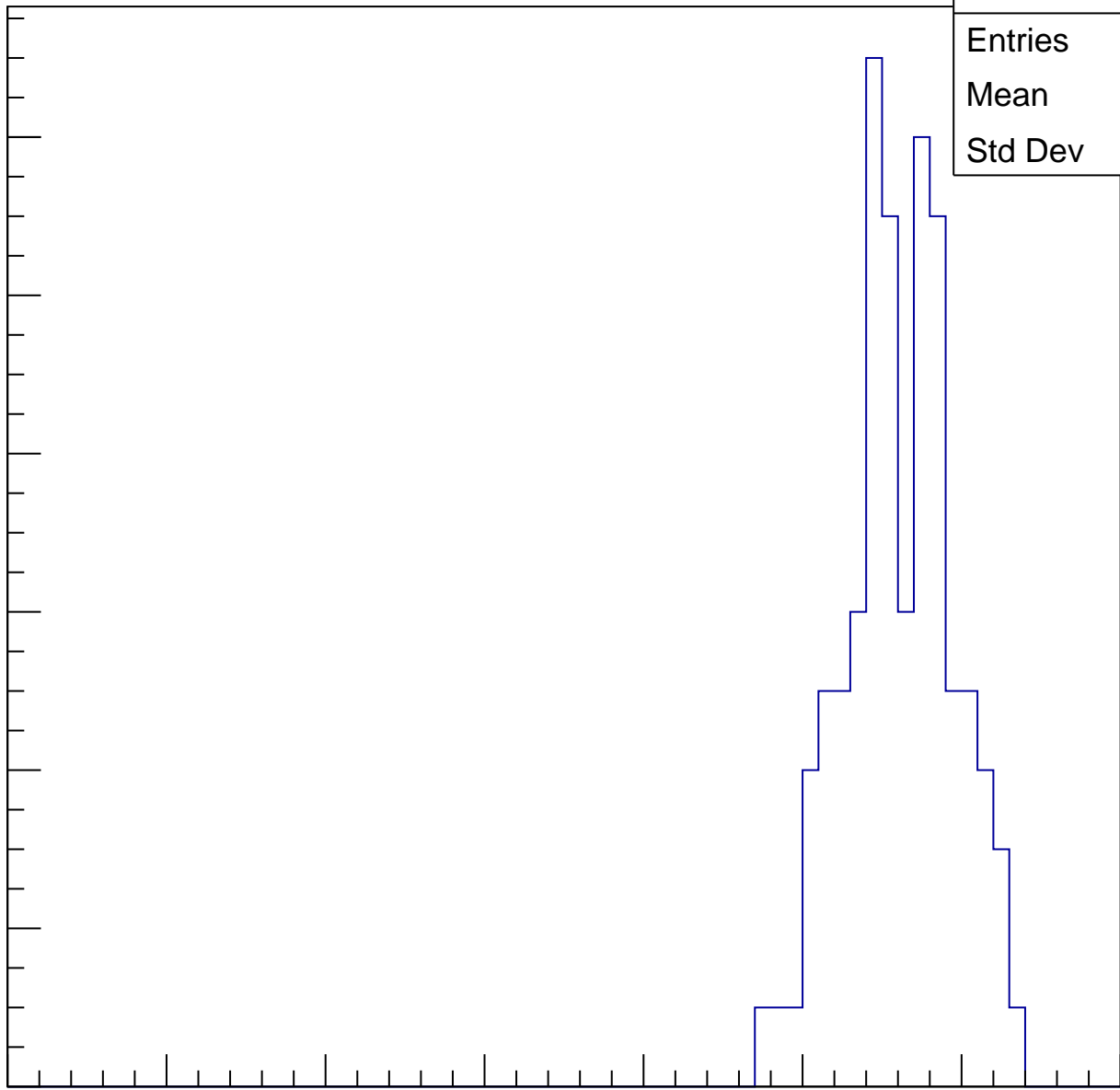
2

0

|         |       |
|---------|-------|
| Entries | 94    |
| Mean    | 55.64 |
| Std Dev | 3.399 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch91, adc5

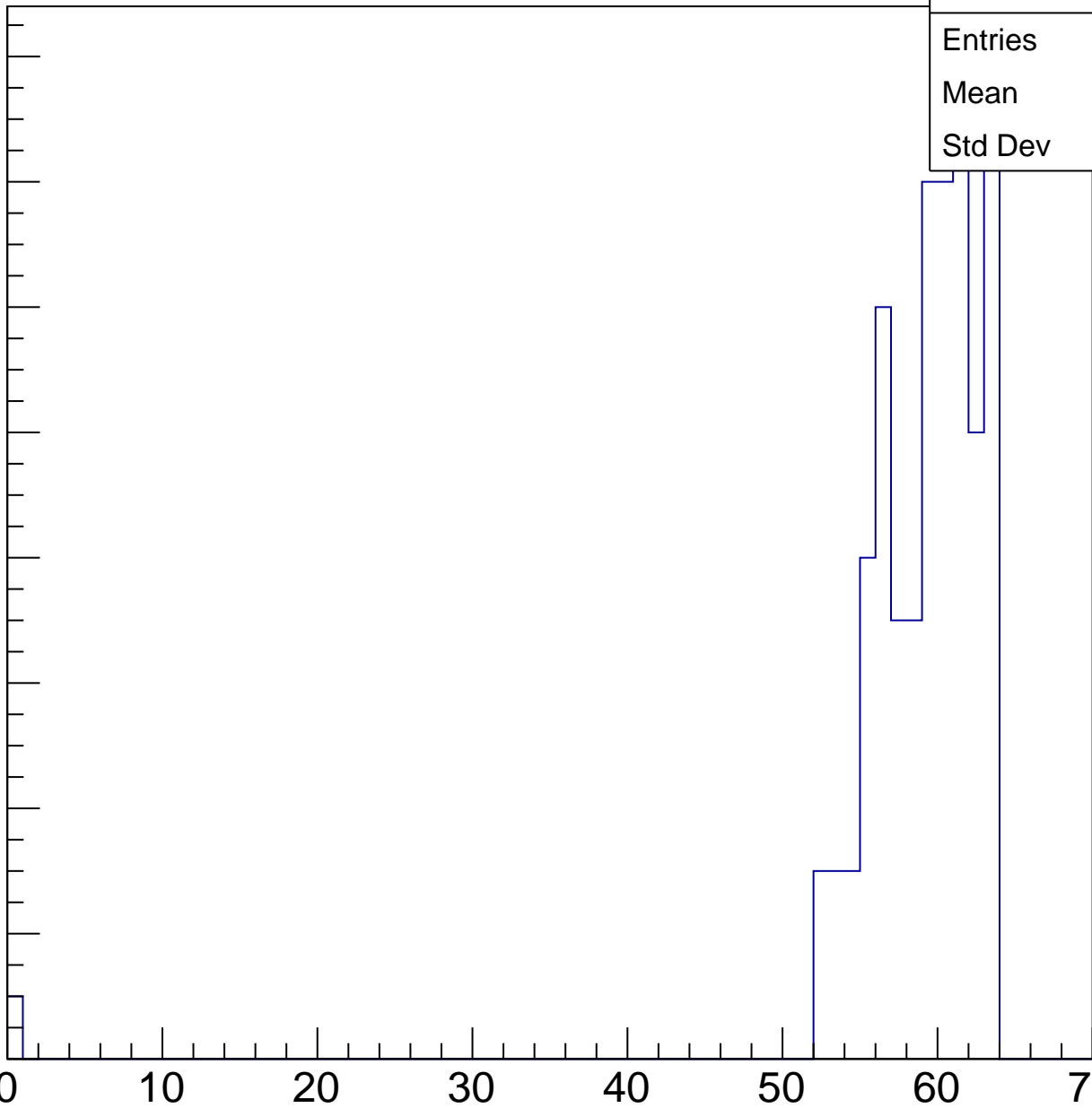
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 58.43 |
| Std Dev | 6.286 |

ampl

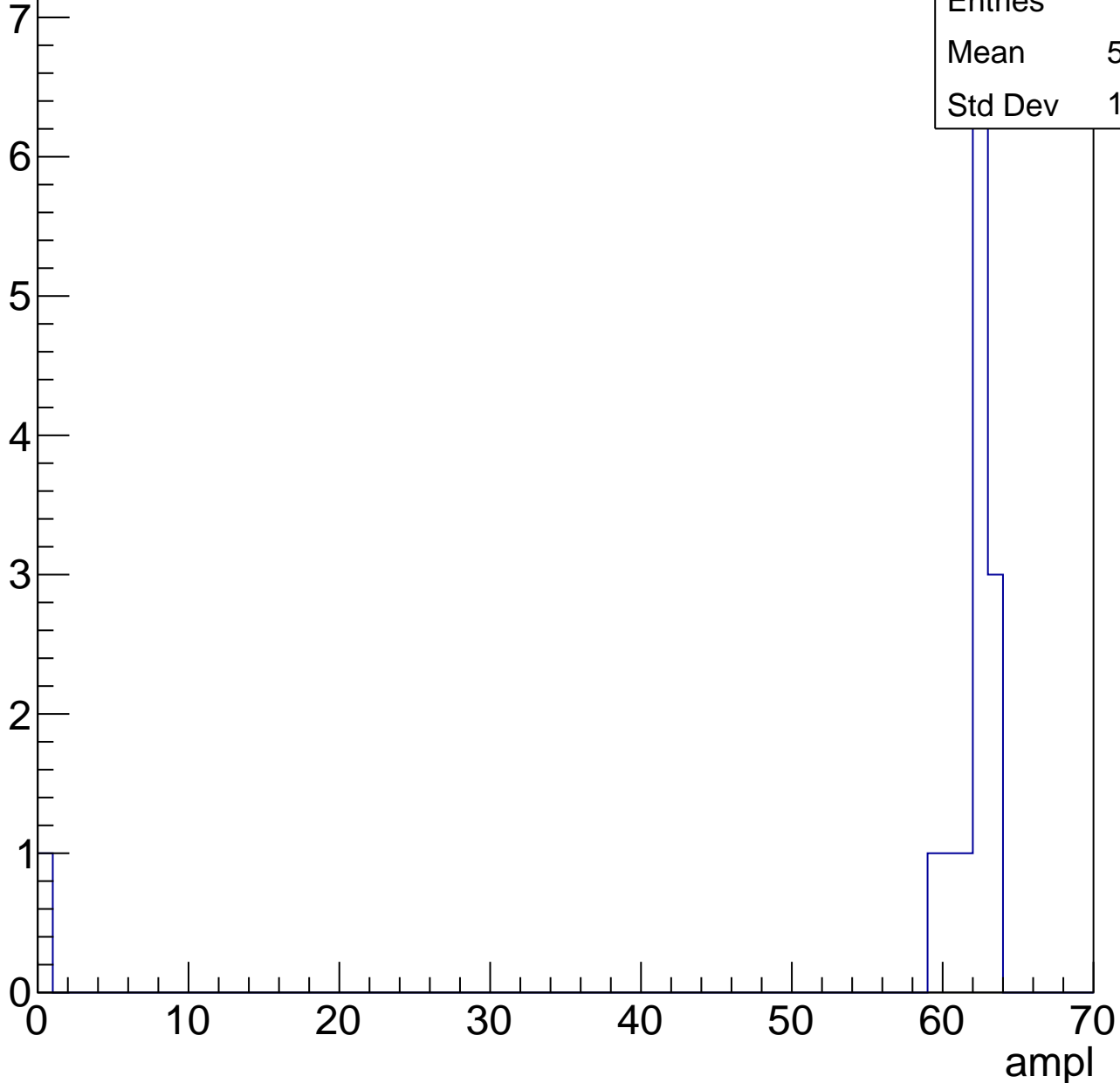


# B0L101S, U16-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 57.36 |
| Std Dev | 15.94 |





# B0L101S, U16-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch92, adc0

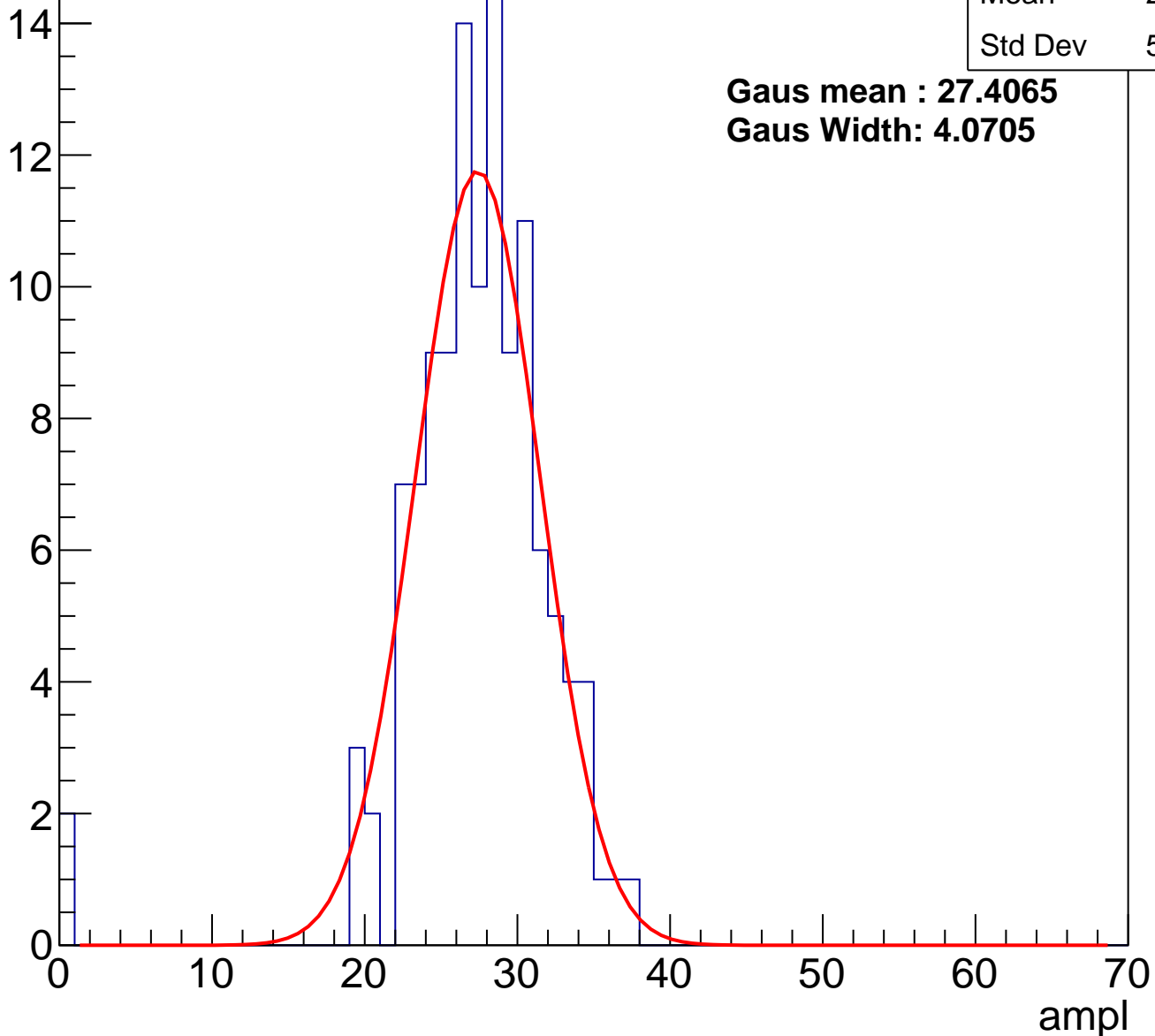
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 120   |
| Mean    | 26.83 |
| Std Dev | 5.098 |

**Gaus mean : 27.4065**

**Gaus Width: 4.0705**

Entry



# B0L101S, U16-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 33.27 |
| Std Dev | 3.691 |

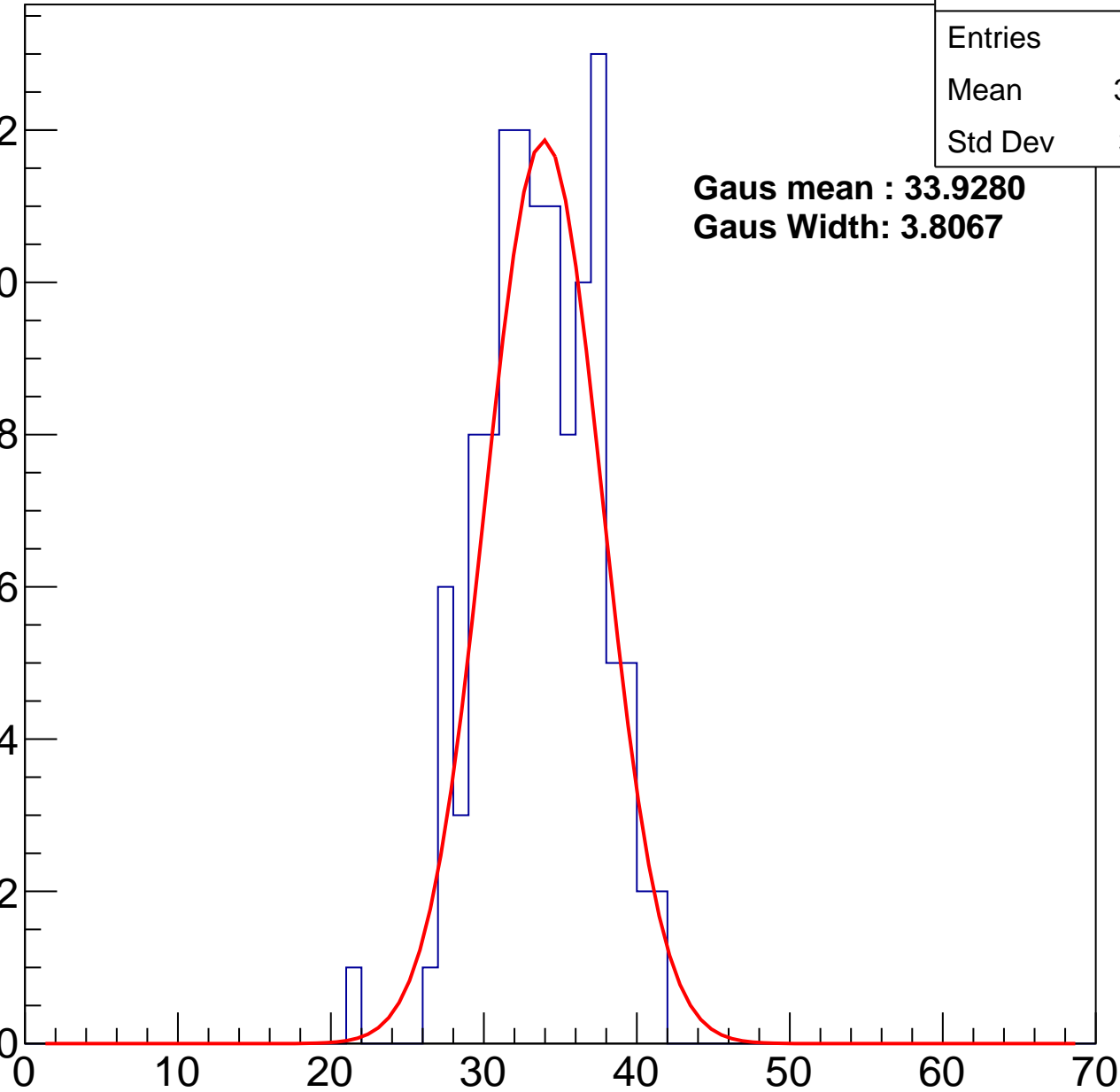
**Gaus mean : 33.9280**

**Gaus Width: 3.8067**

Entry

12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch92, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

157

Mean

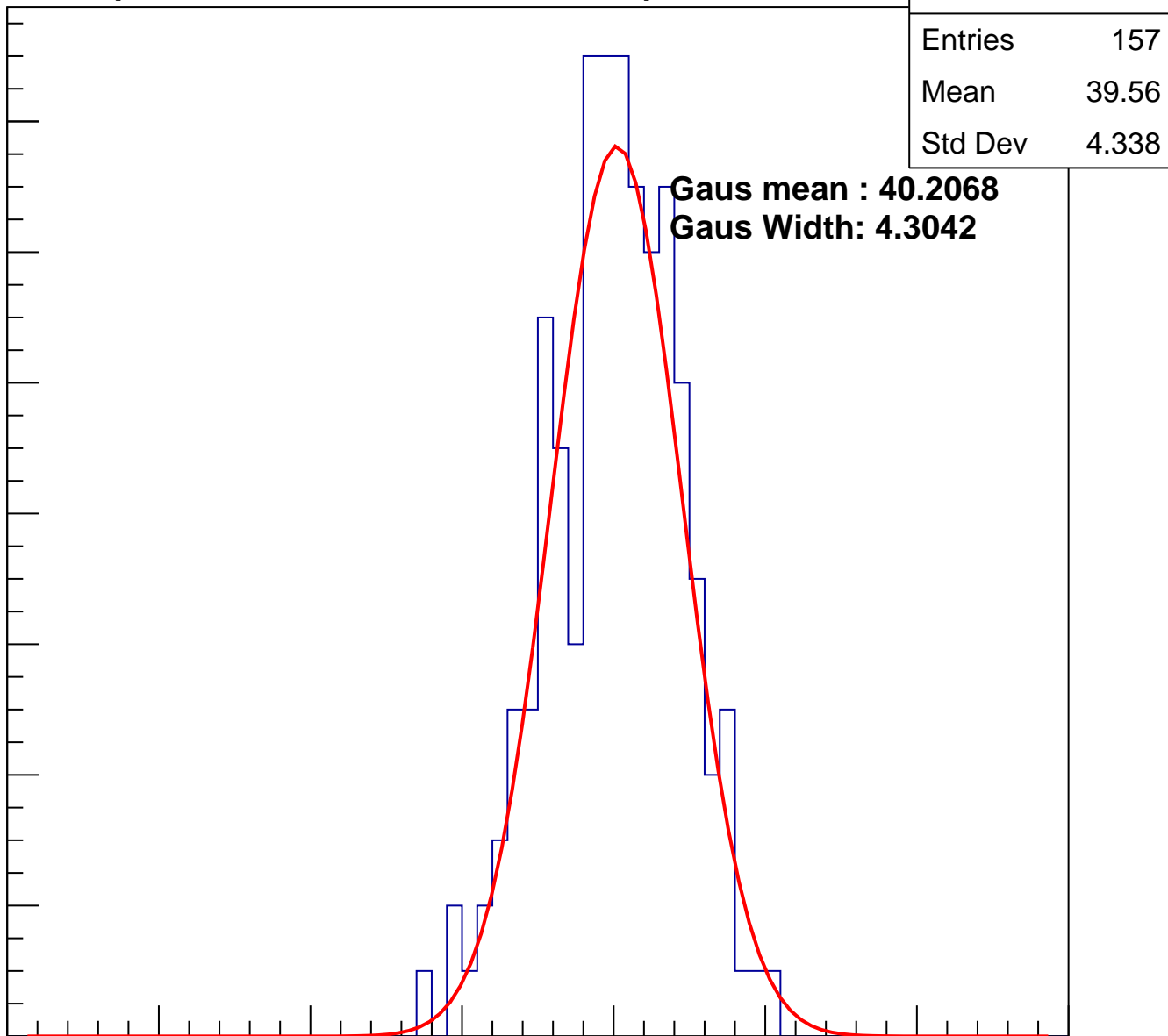
39.56

Std Dev

4.338

**Gaus mean : 40.2068**

**Gaus Width: 4.3042**



# B0L101S, U16-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

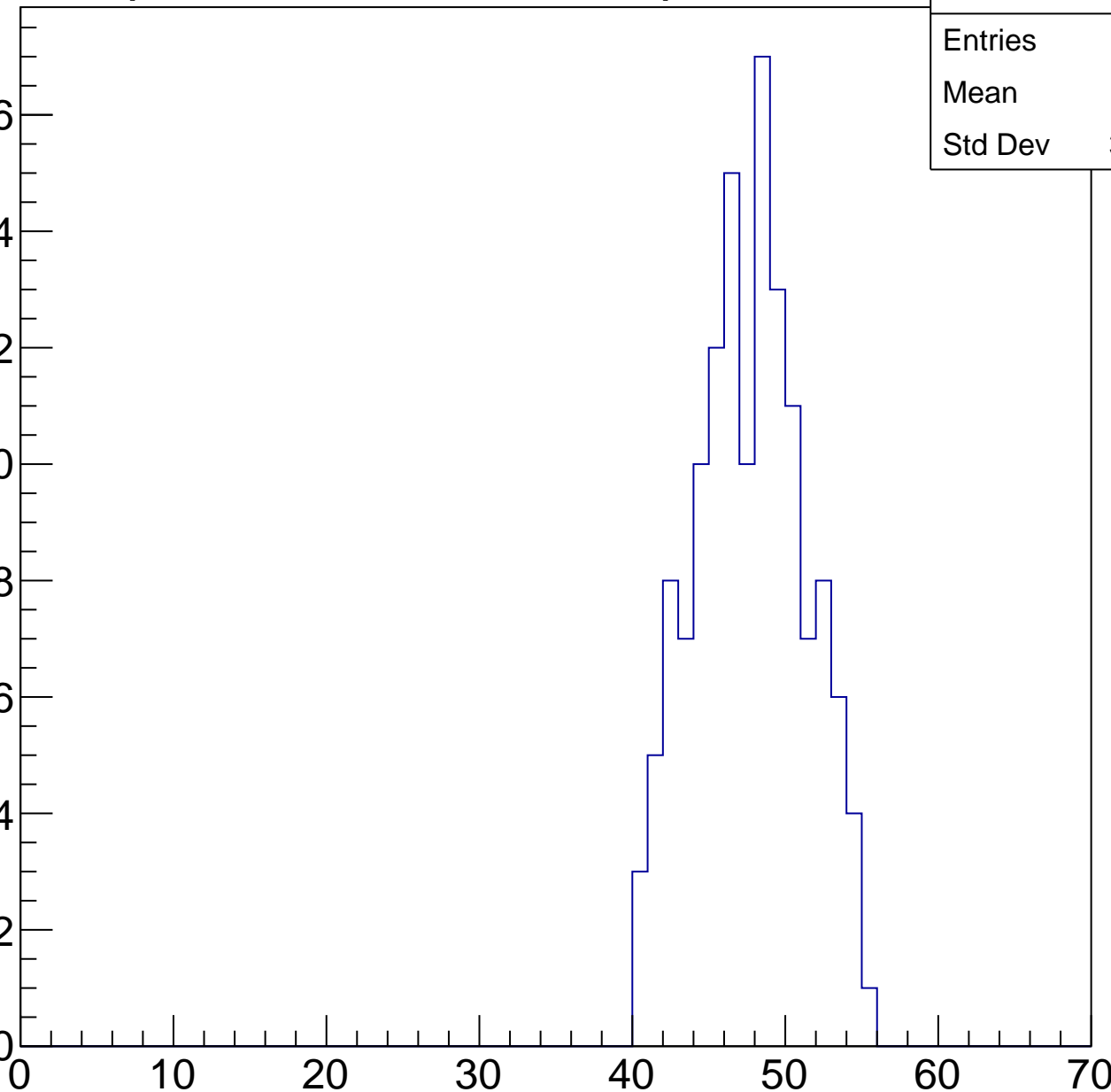
Mean

47.2

Std Dev

3.566

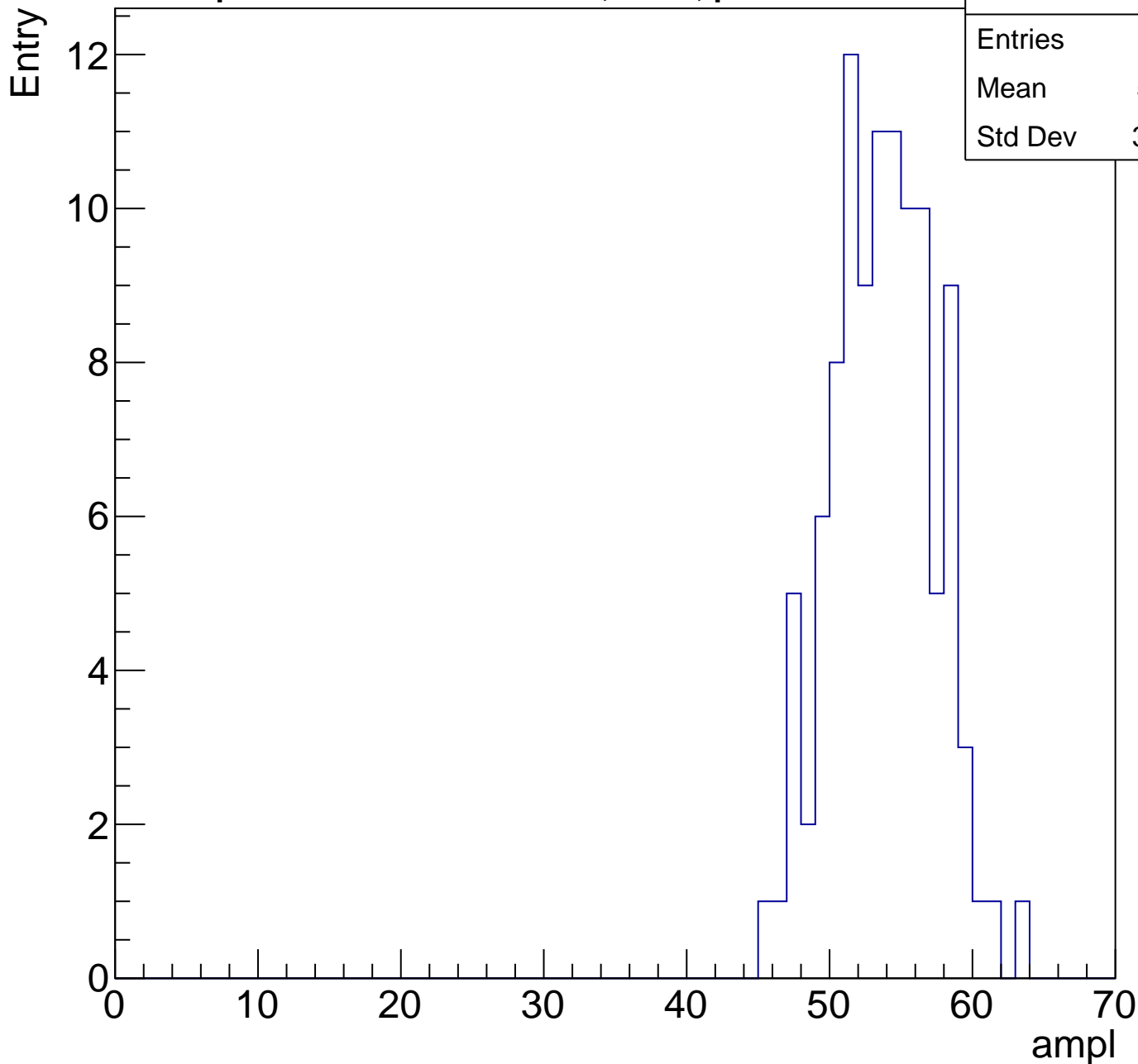
ampl



# B0L101S, U16-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 106   |
| Mean    | 53.31 |
| Std Dev | 3.535 |



# B0L101S, U16-ch92, adc5

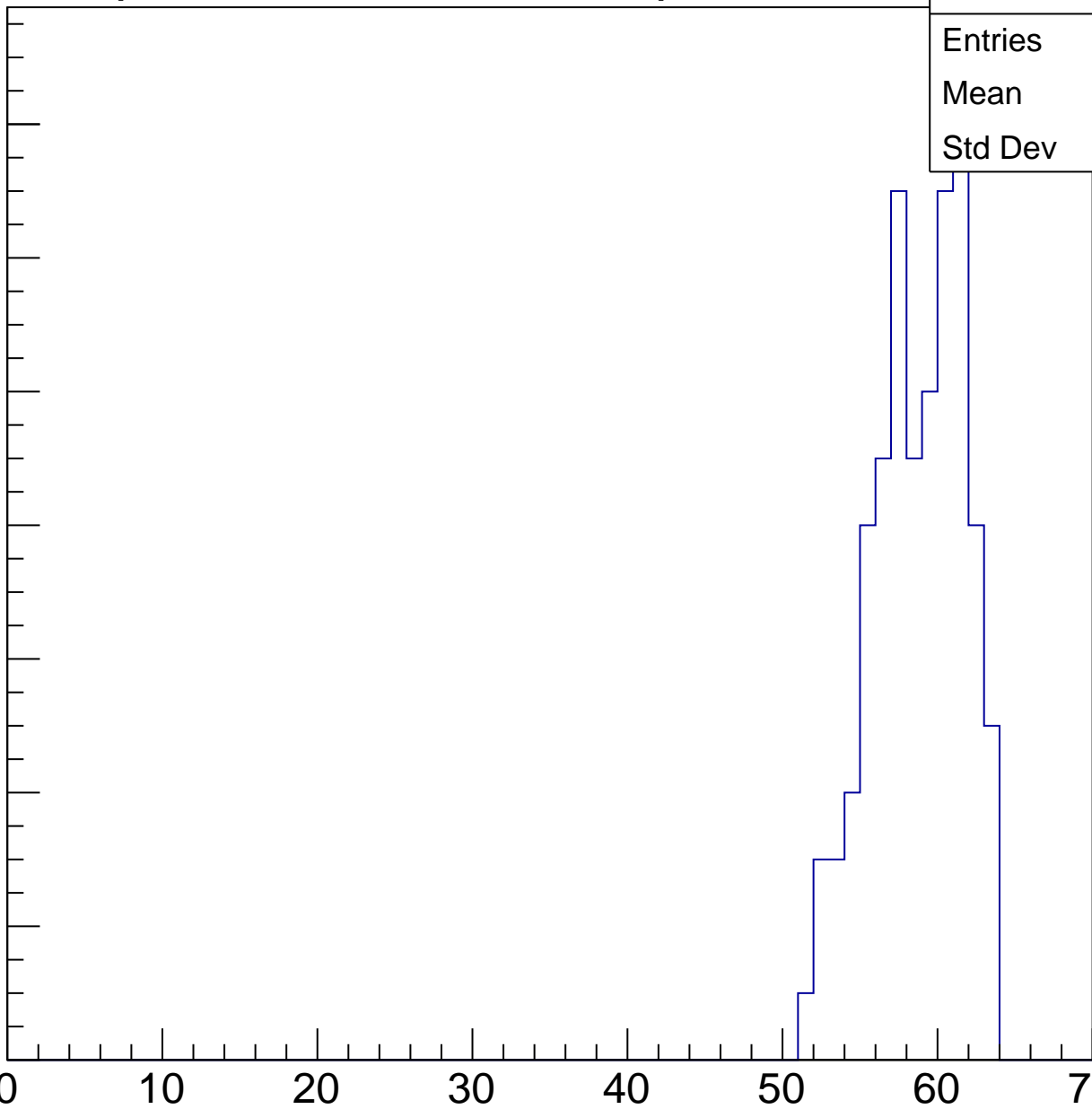
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 58.27 |
| Std Dev | 2.918 |

ampl



# B0L101S, U16-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 58.22 |
| Std Dev | 12.22 |

0

2

4

6

8

10

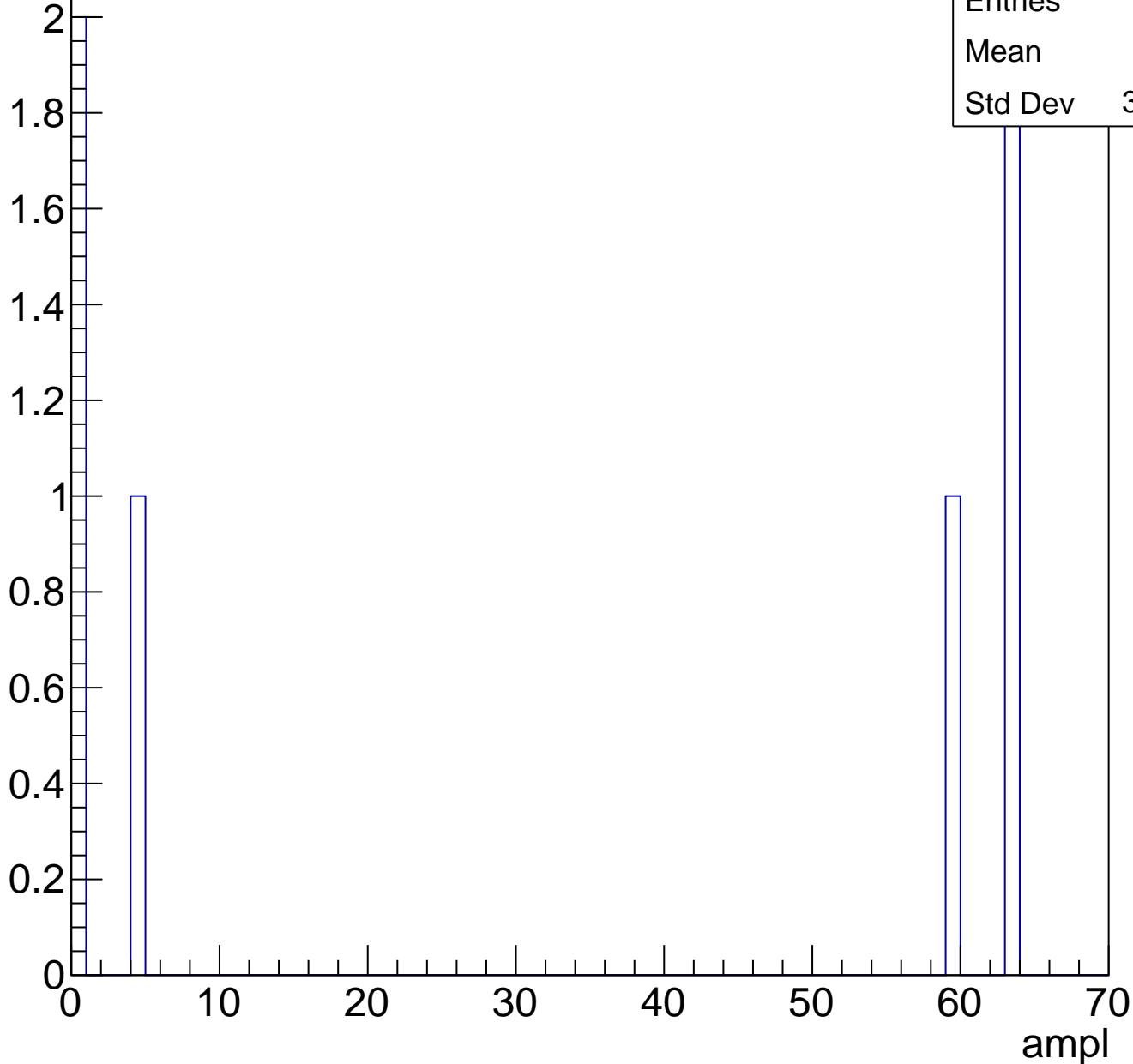
12



# B0L101S, U16-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch93, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 27.8  |
| Std Dev | 3.681 |

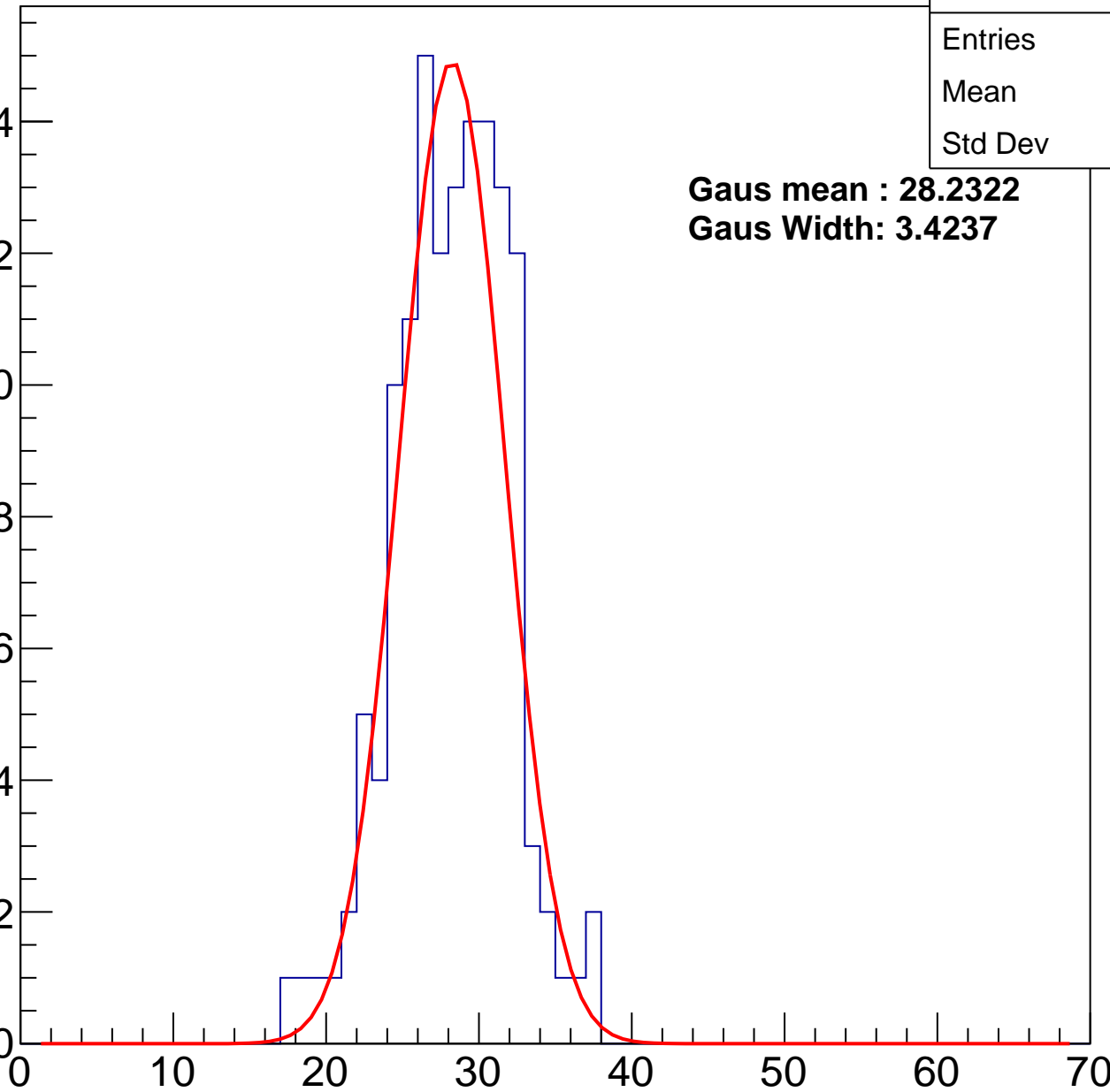
**Gaus mean : 28.2322**

**Gaus Width: 3.4237**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



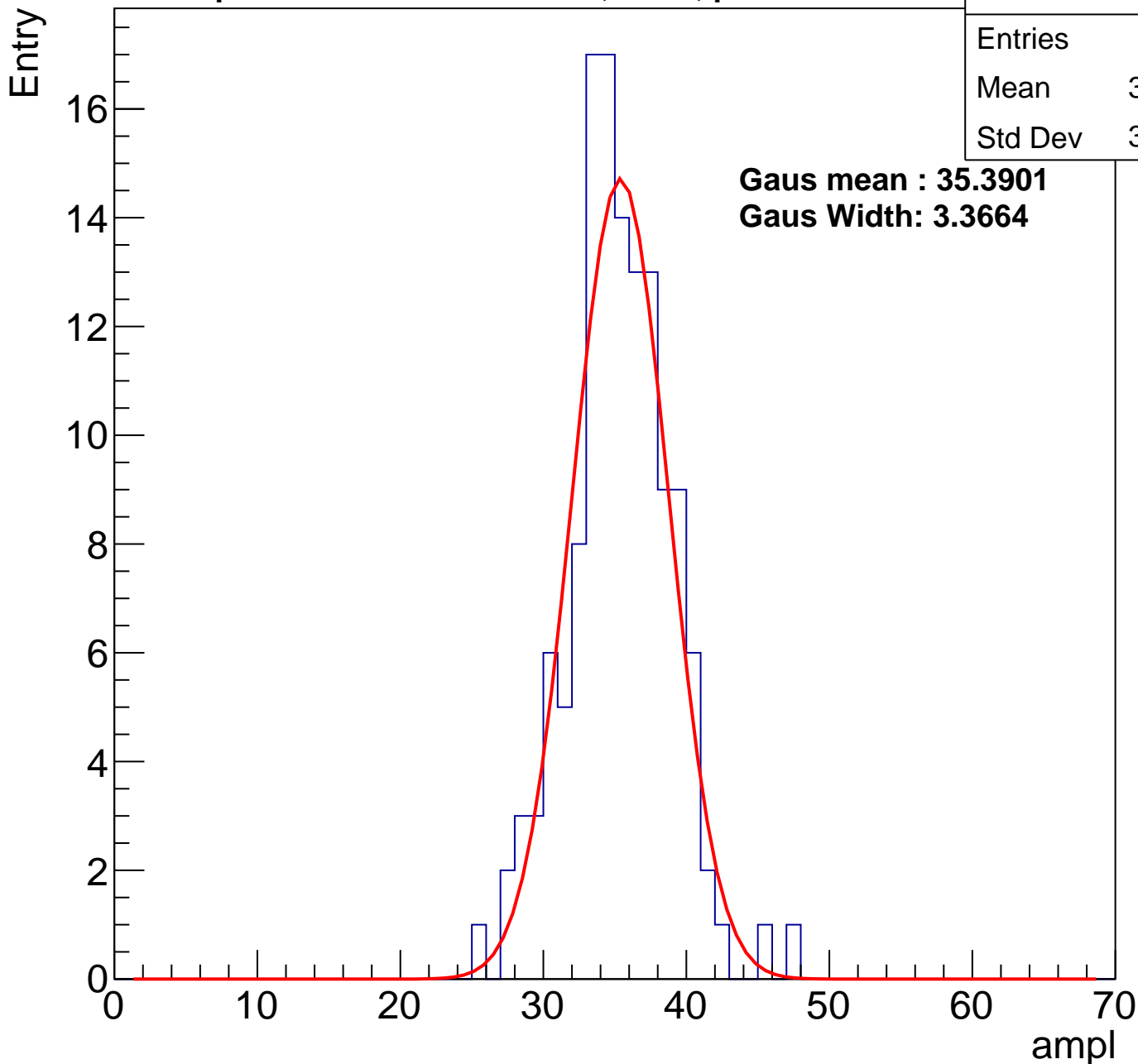
# B0L101S, U16-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 34.87 |
| Std Dev | 3.573 |

**Gaus mean : 35.3901**

**Gaus Width: 3.3664**



# B0L101S, U16-ch93, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

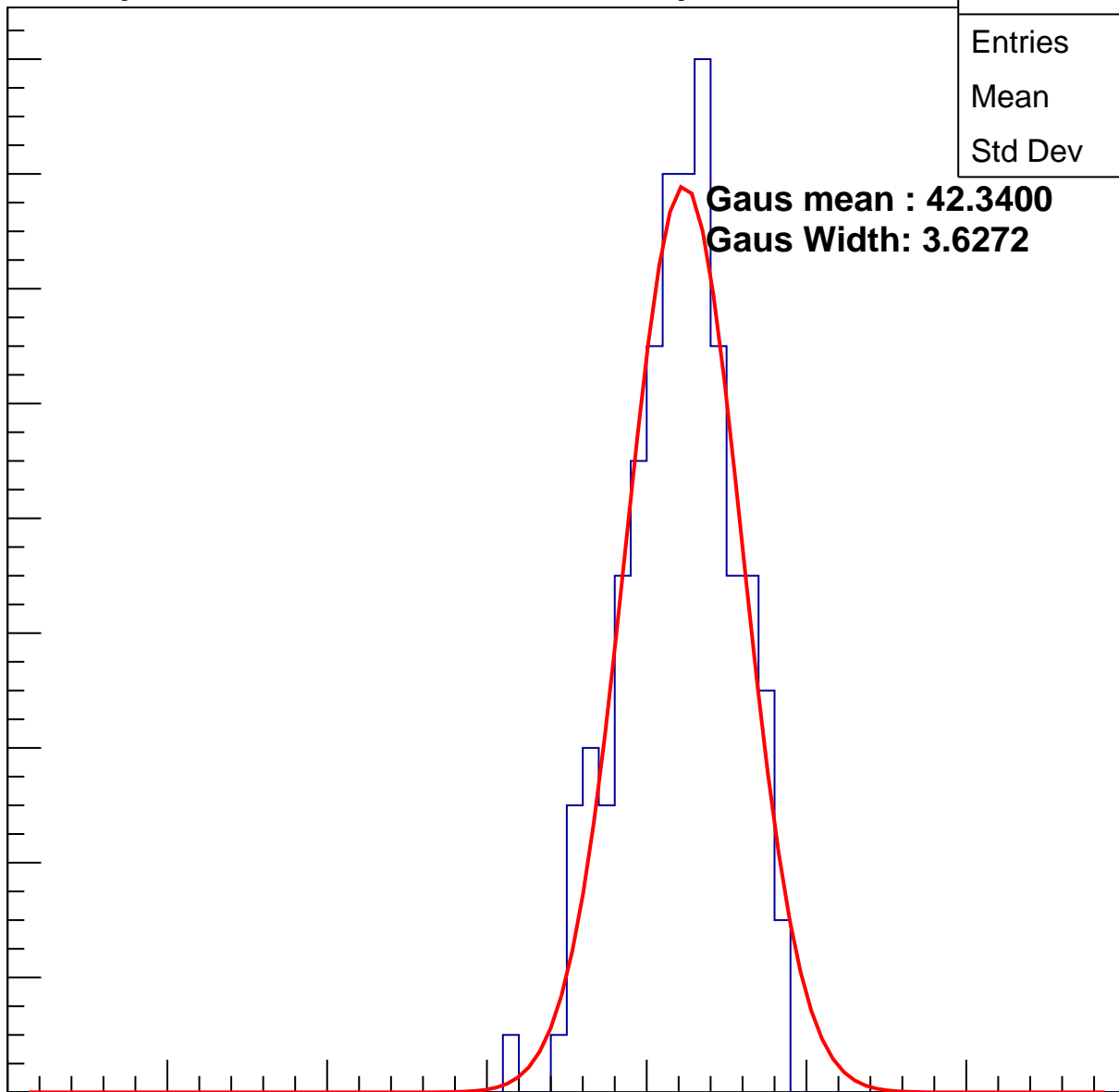
|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 41.54 |
| Std Dev | 3.387 |

**Gaus mean : 42.3400**

**Gaus Width: 3.6272**

0 10 20 30 40 50 60 70

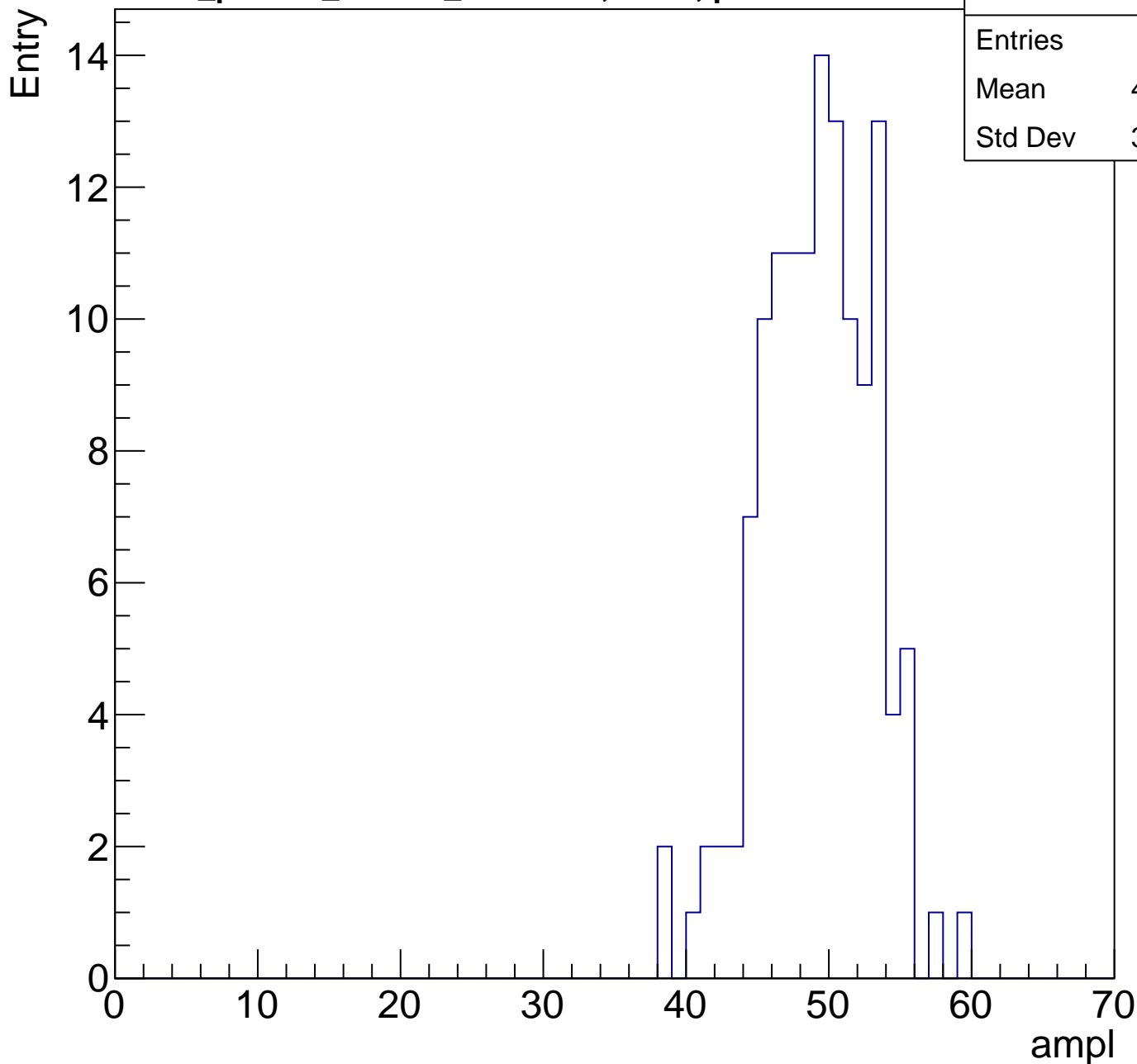
ampl



# B0L101S, U16-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

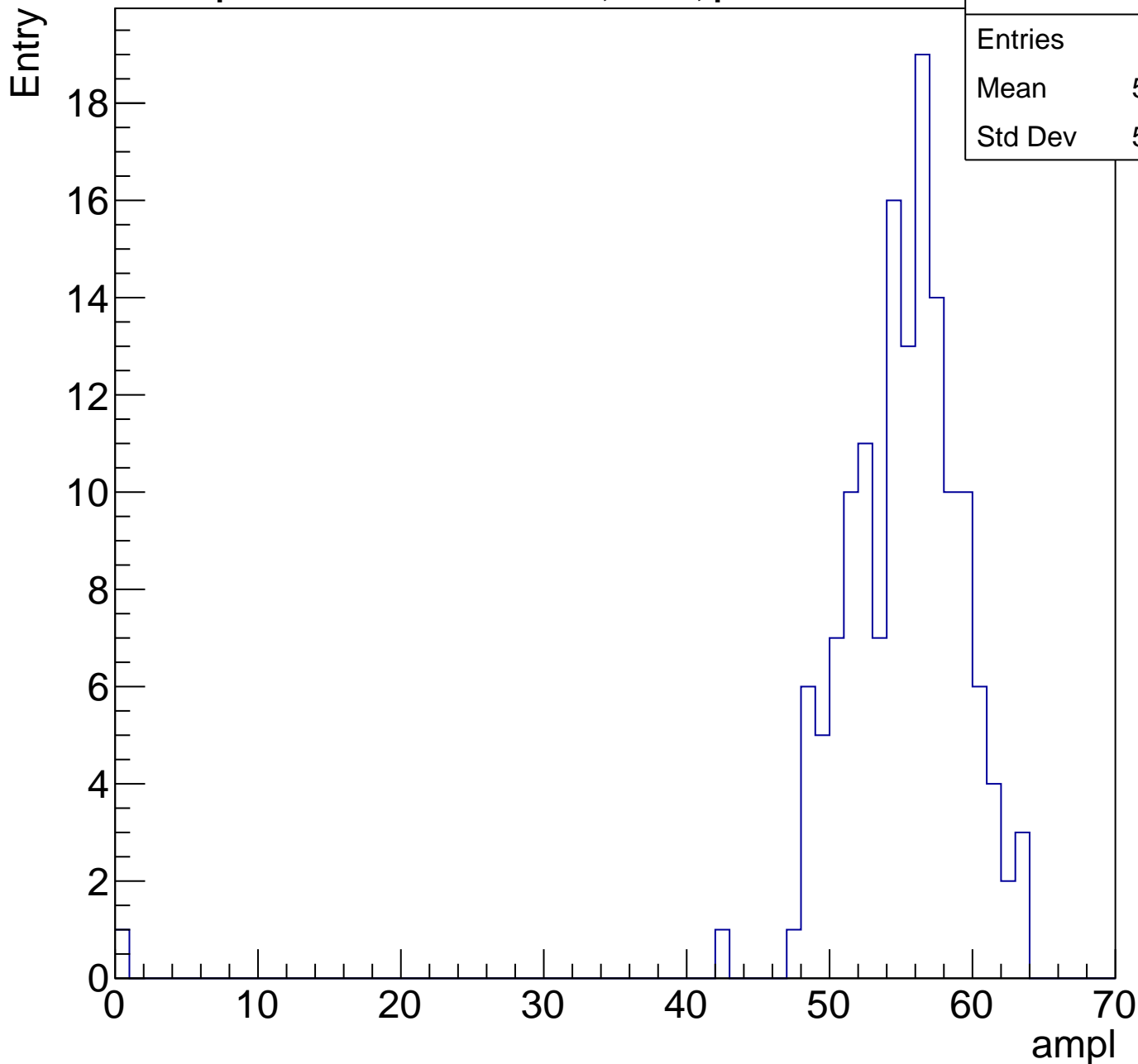
|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 48.74 |
| Std Dev | 3.825 |



# B0L101S, U16-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 54.47 |
| Std Dev | 5.898 |



# B0L101S, U16-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

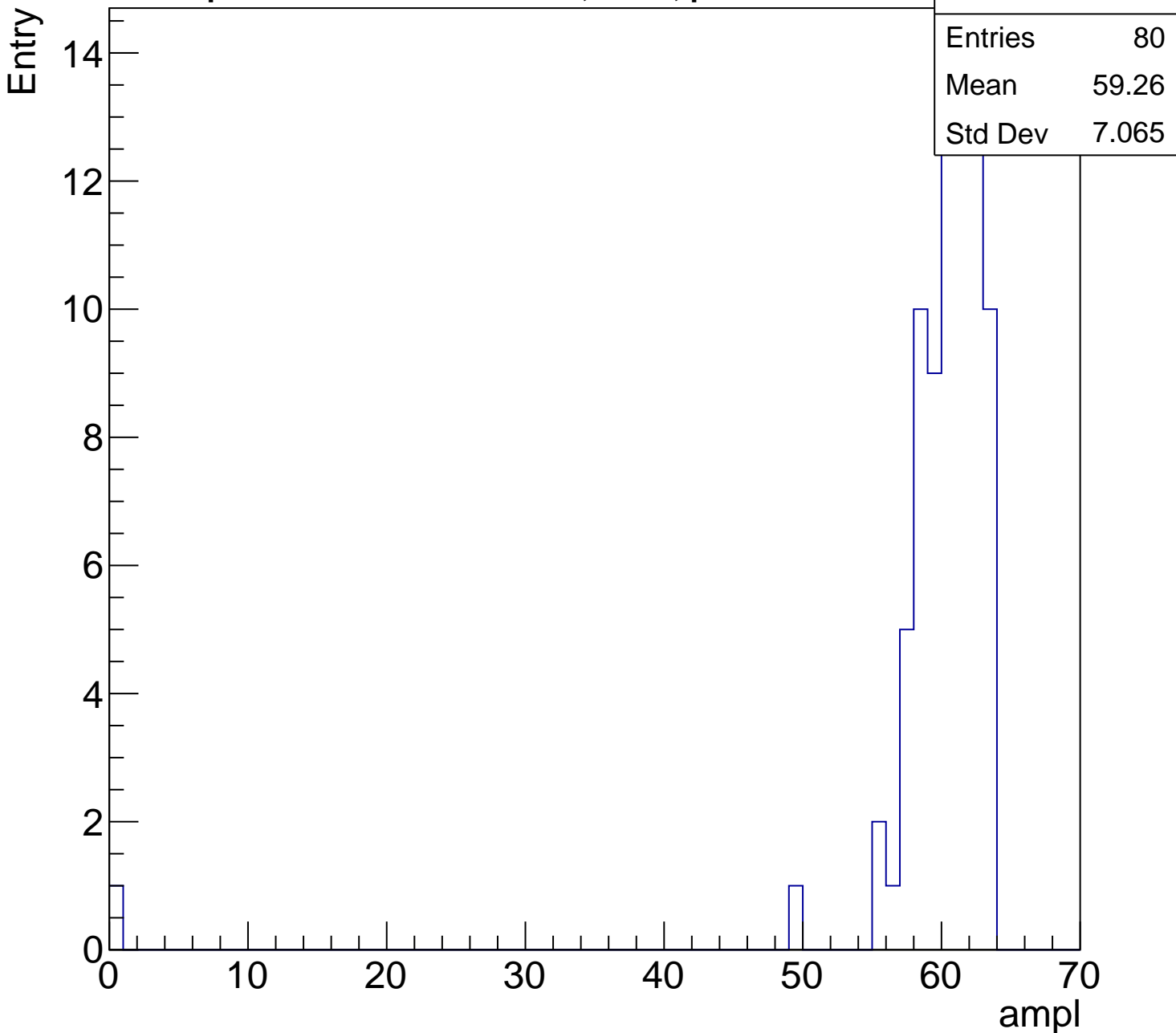
Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 59.26 |
| Std Dev | 7.065 |

ampl

0 10 20 30 40 50 60 70

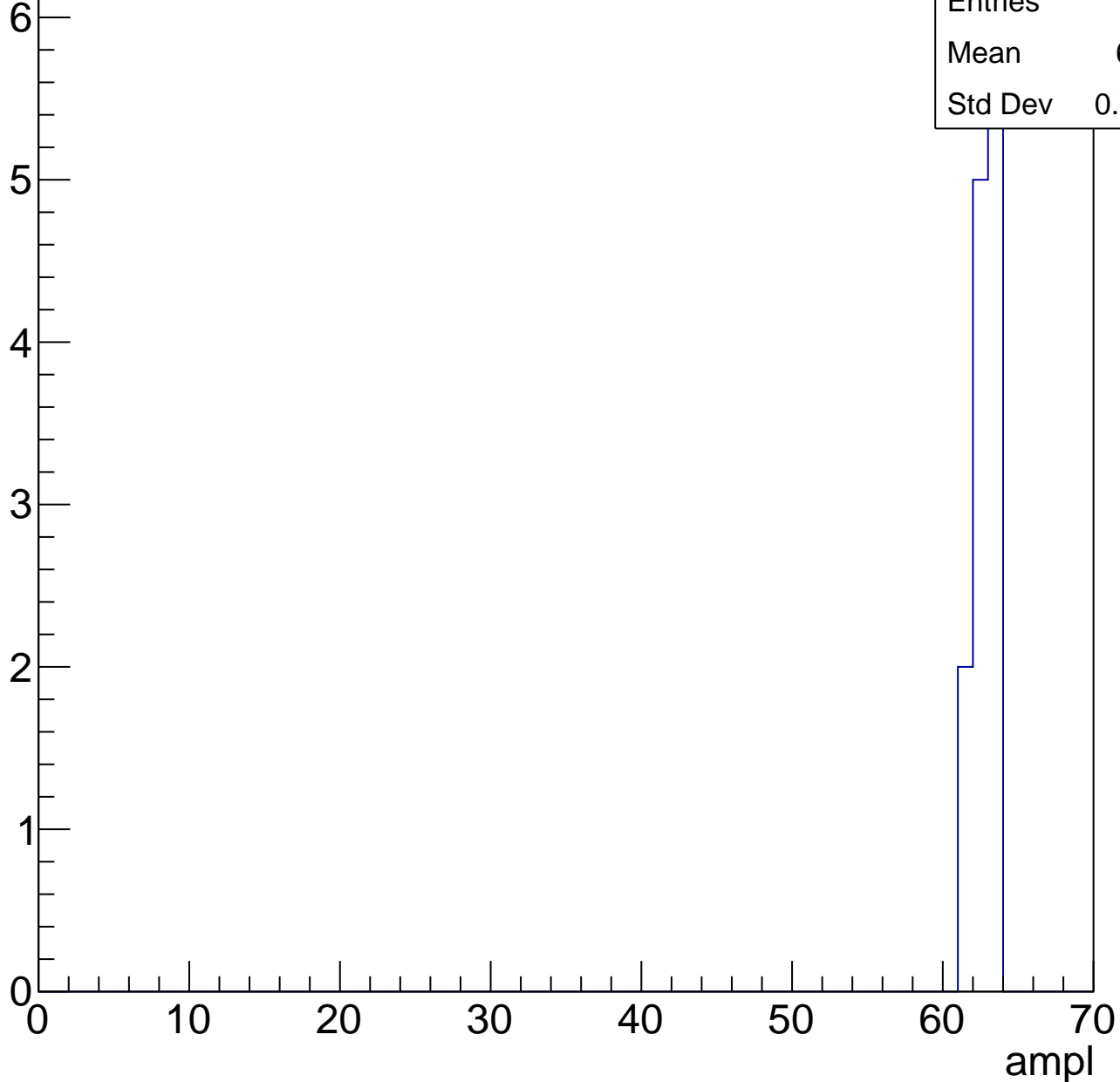


# B0L101S, U16-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 13     |
| Mean    | 62.31  |
| Std Dev | 0.7216 |





# B0L101S, U16-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch94, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 30.53 |
| Std Dev | 3.587 |

**Gaus mean : 30.9405**

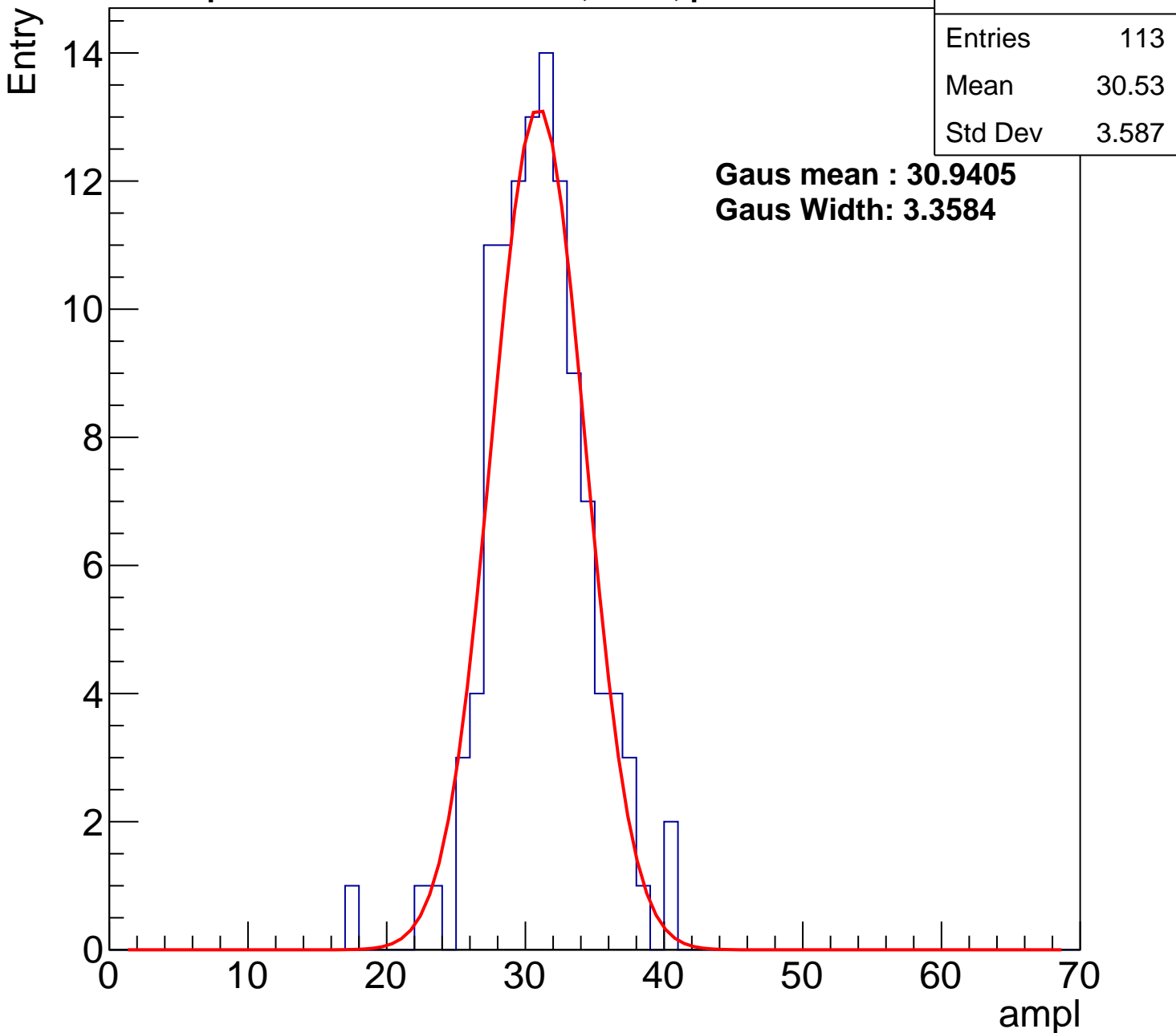
**Gaus Width: 3.3584**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

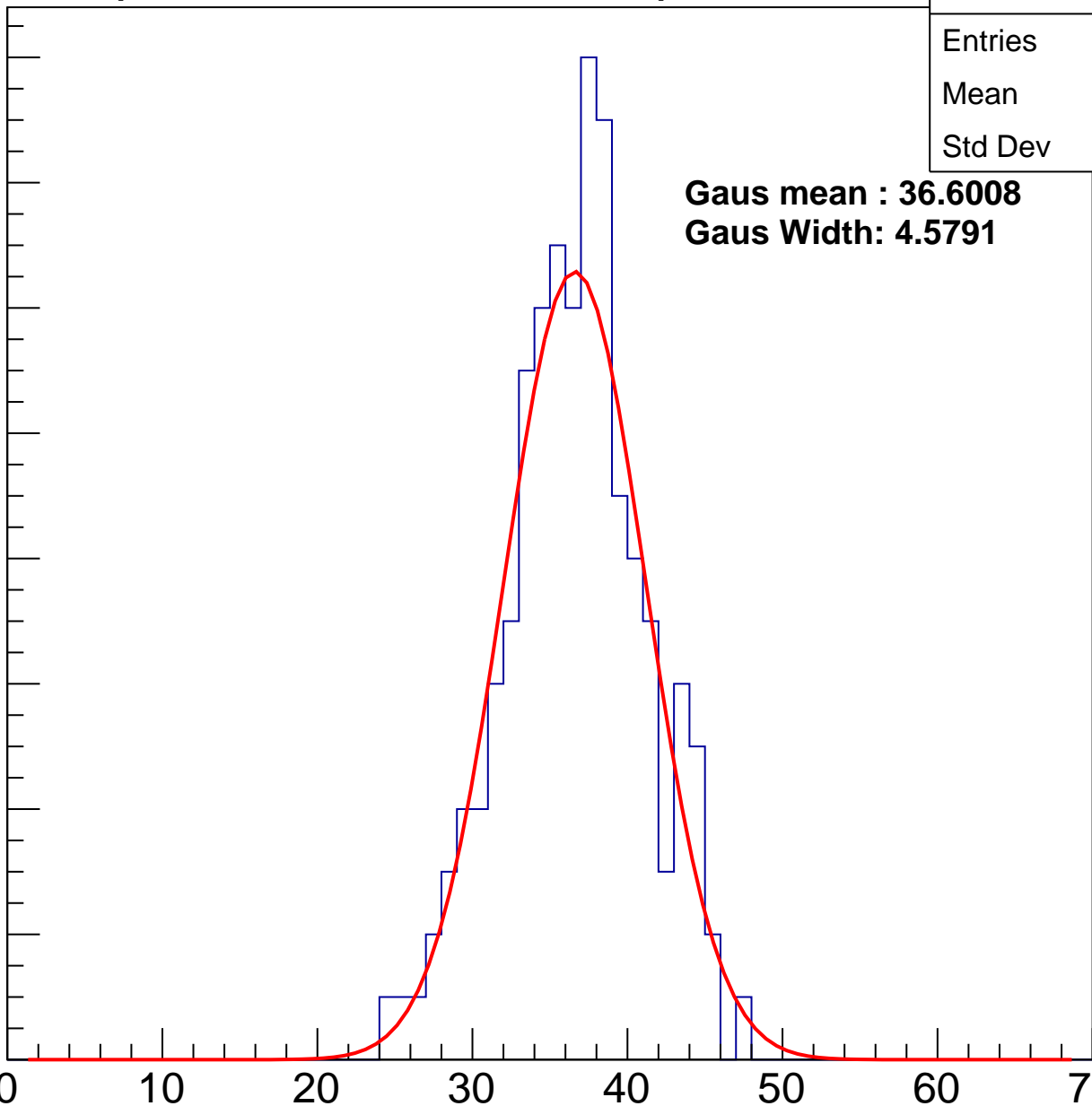
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 149   |
| Mean    | 36.09 |
| Std Dev | 4.446 |

**Gaus mean : 36.6008**

**Gaus Width: 4.5791**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U16-ch94, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

105

Mean

42.52

Std Dev

3.727

Gaus mean : 42.7051

Gaus Width: 4.0055

0

2

4

6

8

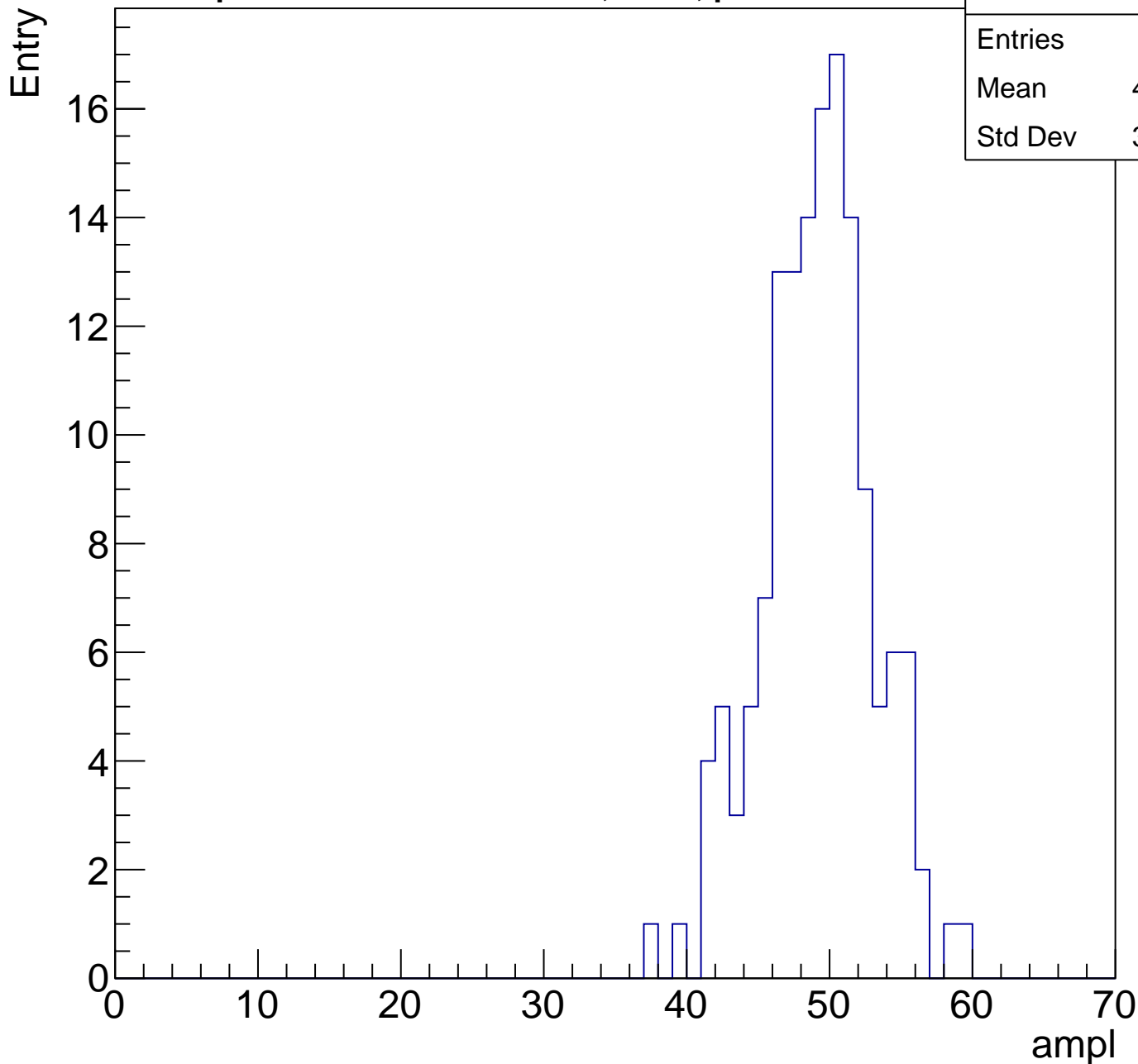
10

12

# B0L101S, U16-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 48.66 |
| Std Dev | 3.865 |



# B0L101S, U16-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

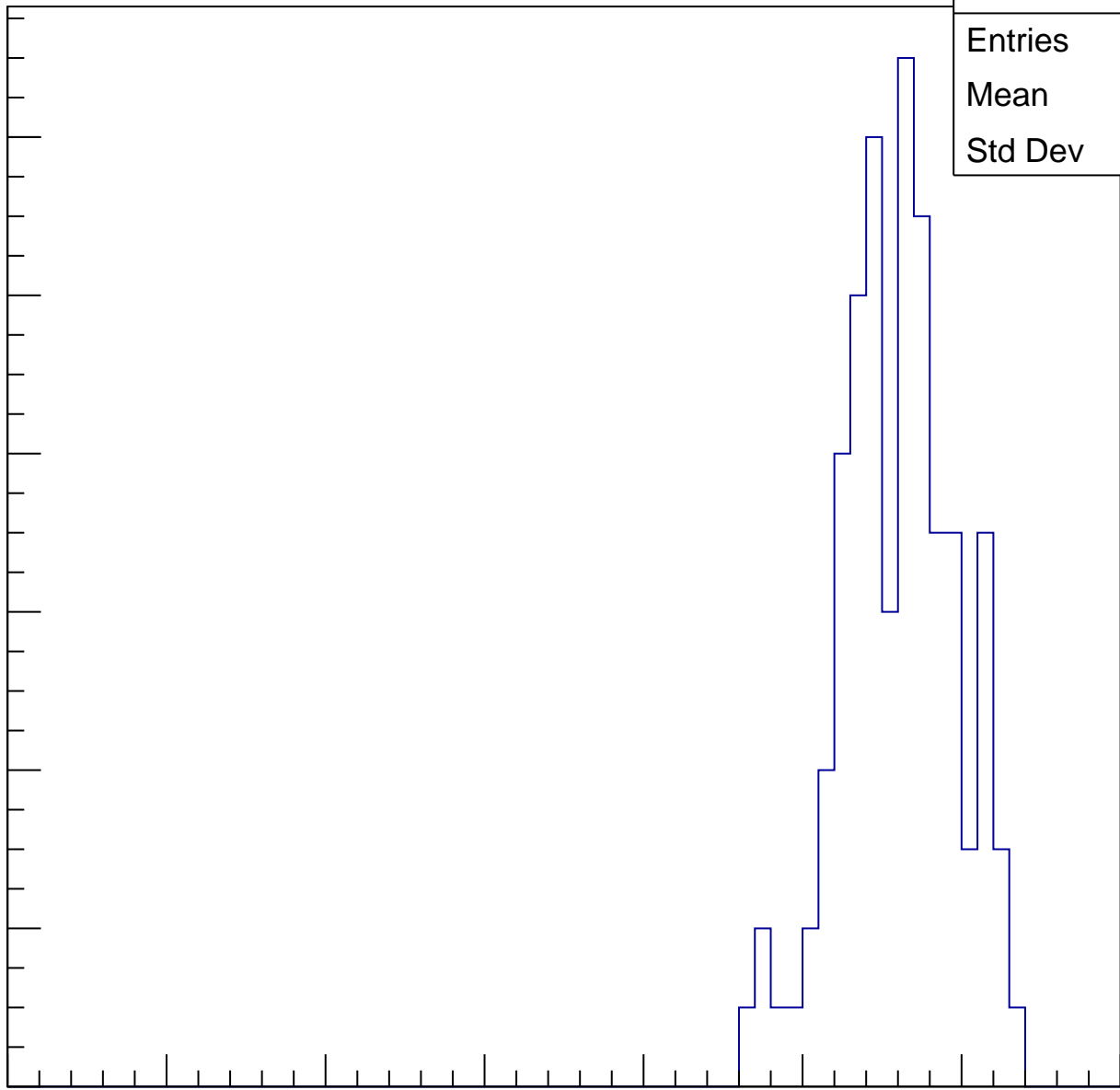
2

0

|         |       |
|---------|-------|
| Entries | 99    |
| Mean    | 55.51 |
| Std Dev | 3.591 |

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

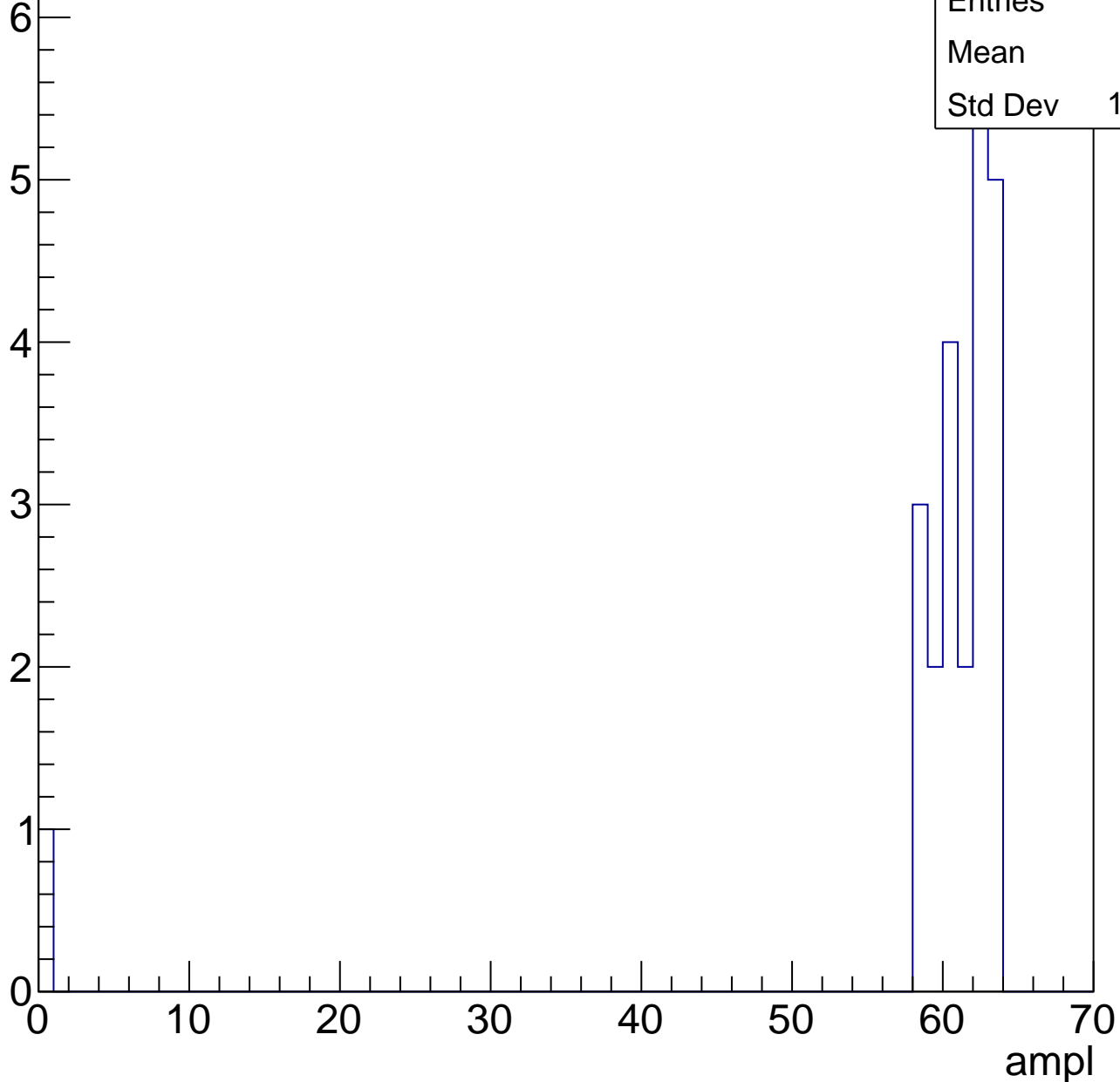
ampl

|         |       |
|---------|-------|
| Entries | 100   |
| Mean    | 58.09 |
| Std Dev | 6.741 |

# B0L101S, U16-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

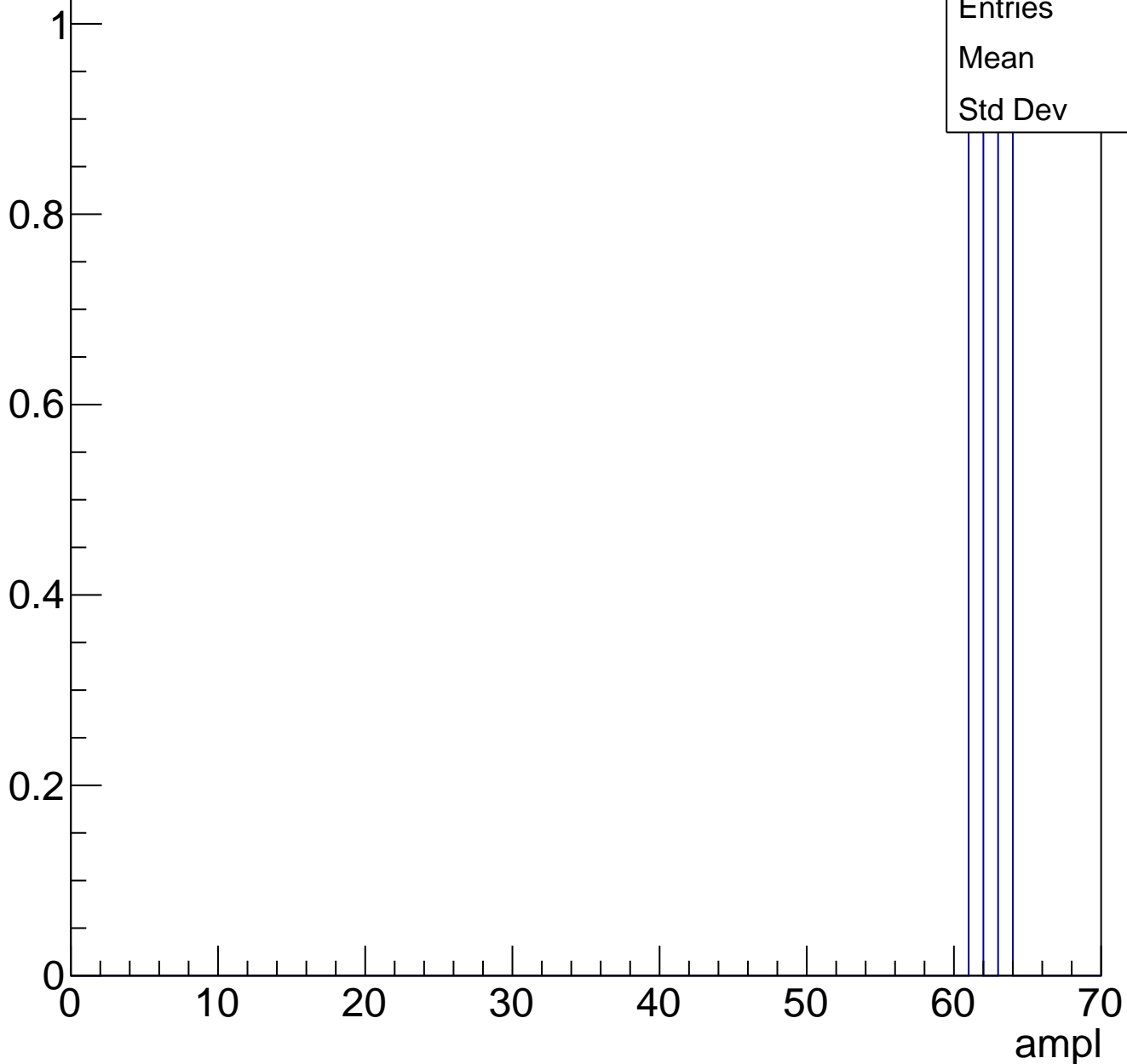




# B0L101S, U16-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch95, adc0

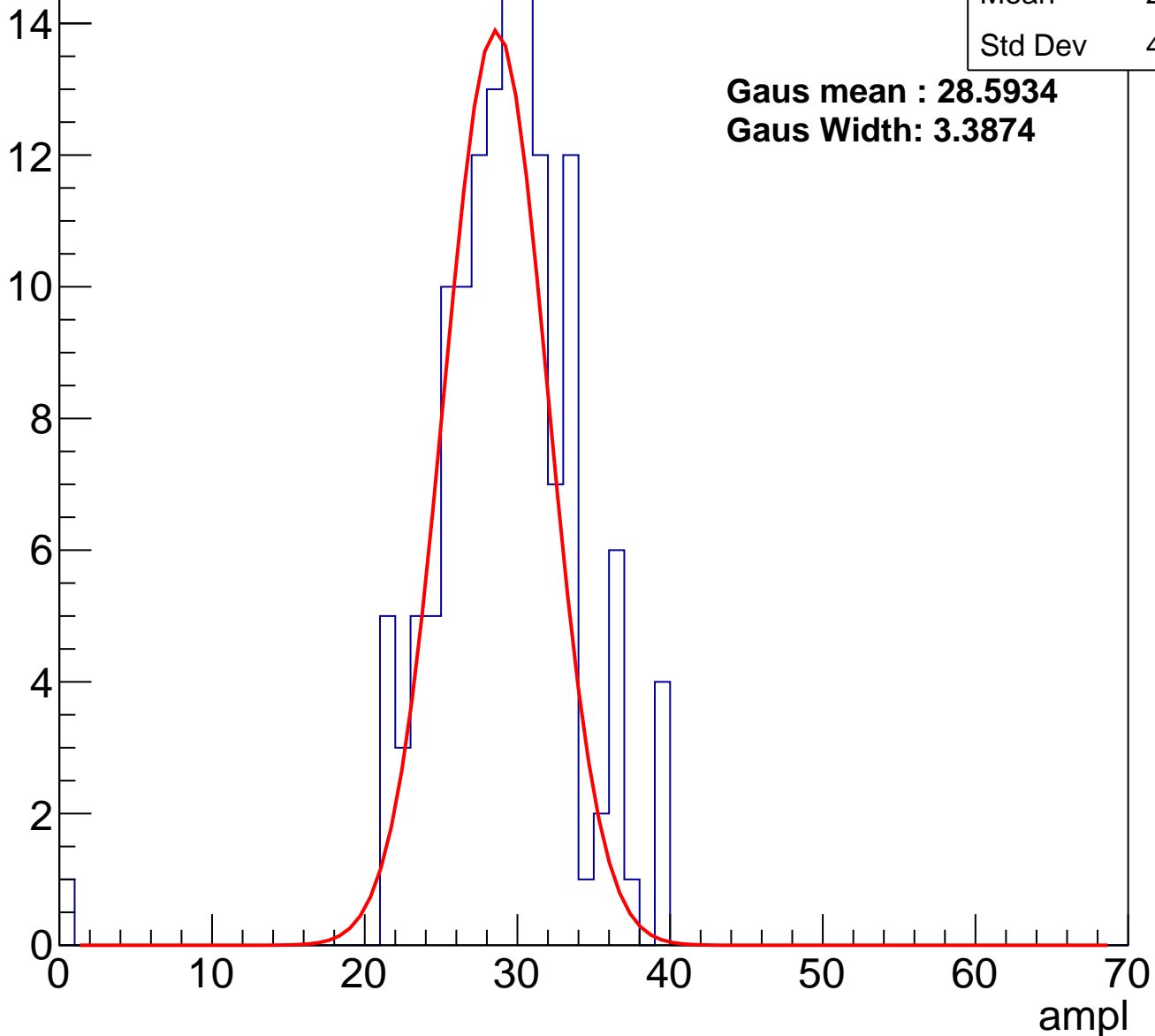
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 28.73 |
| Std Dev | 4.713 |

**Gaus mean : 28.5934**

**Gaus Width: 3.3874**

Entry



# B0L101S, U16-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 134   |
| Mean    | 35.9  |
| Std Dev | 3.587 |

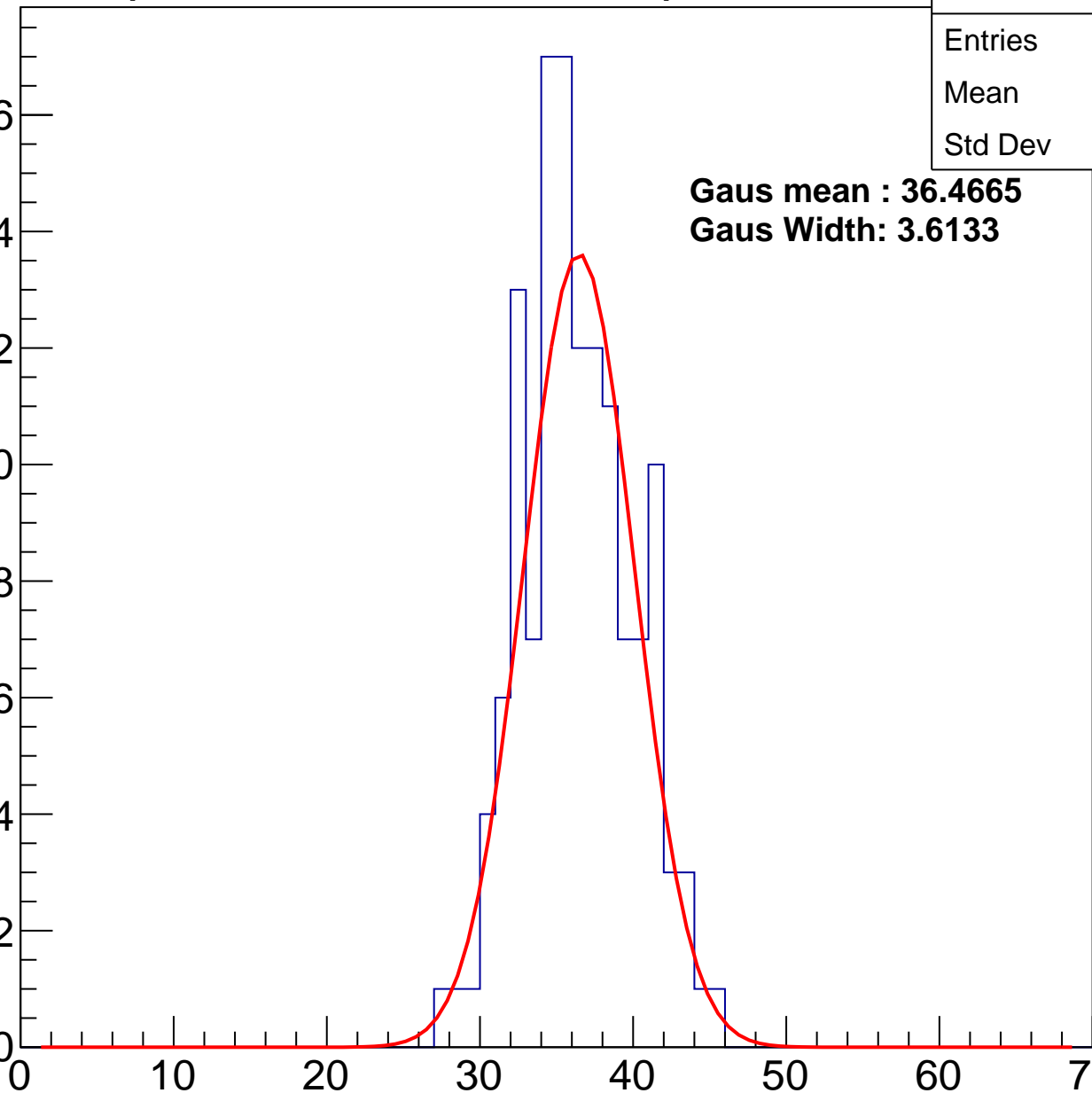
**Gaus mean : 36.4665**

**Gaus Width: 3.6133**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch95, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

144

Mean

42.73

Std Dev

3.951

**Gaus mean : 43.3529**

**Gaus Width: 3.9367**

0

2

4

6

8

10

12

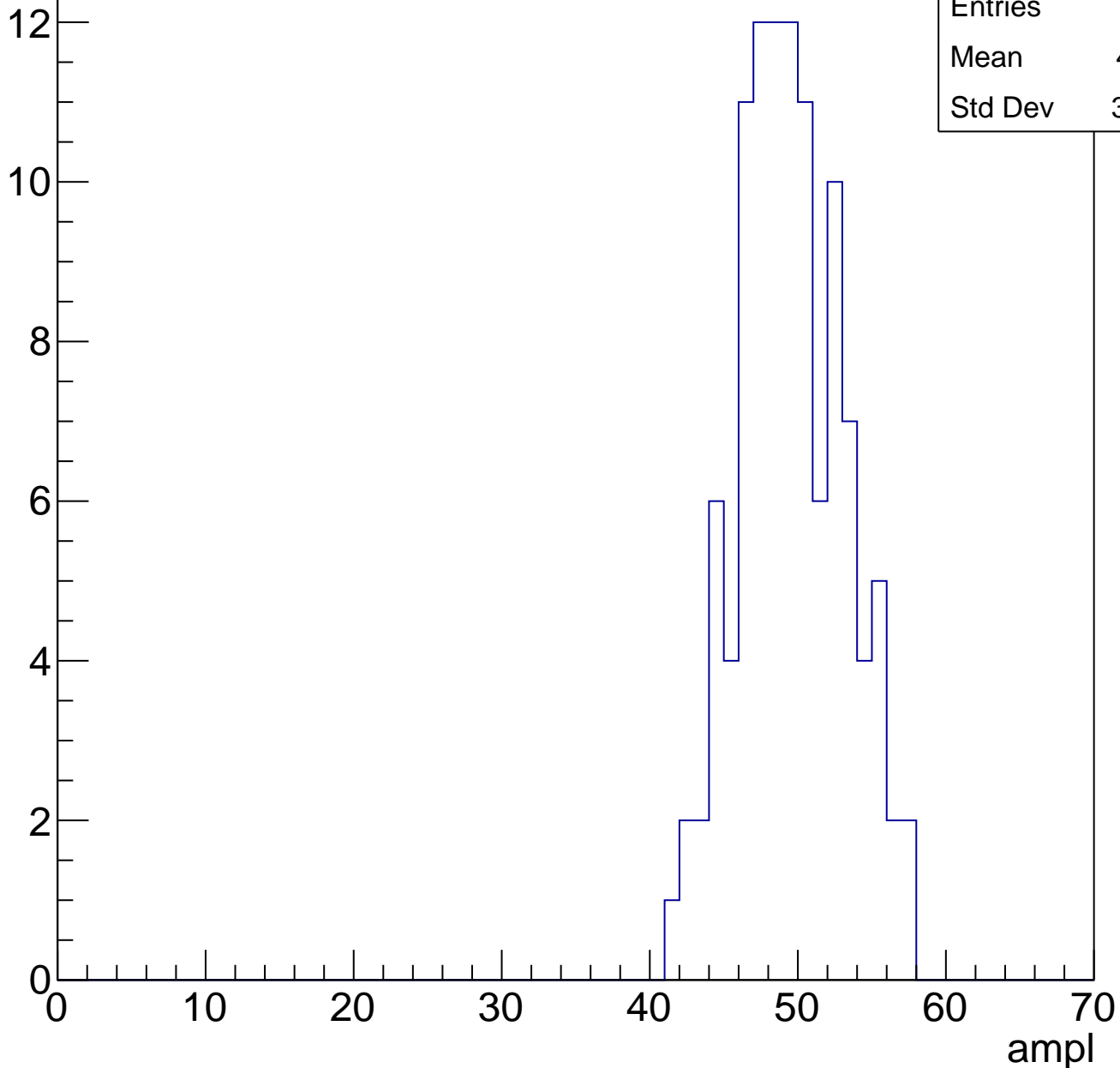
14

# B0L101S, U16-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 109   |
| Mean    | 49.11 |
| Std Dev | 3.518 |

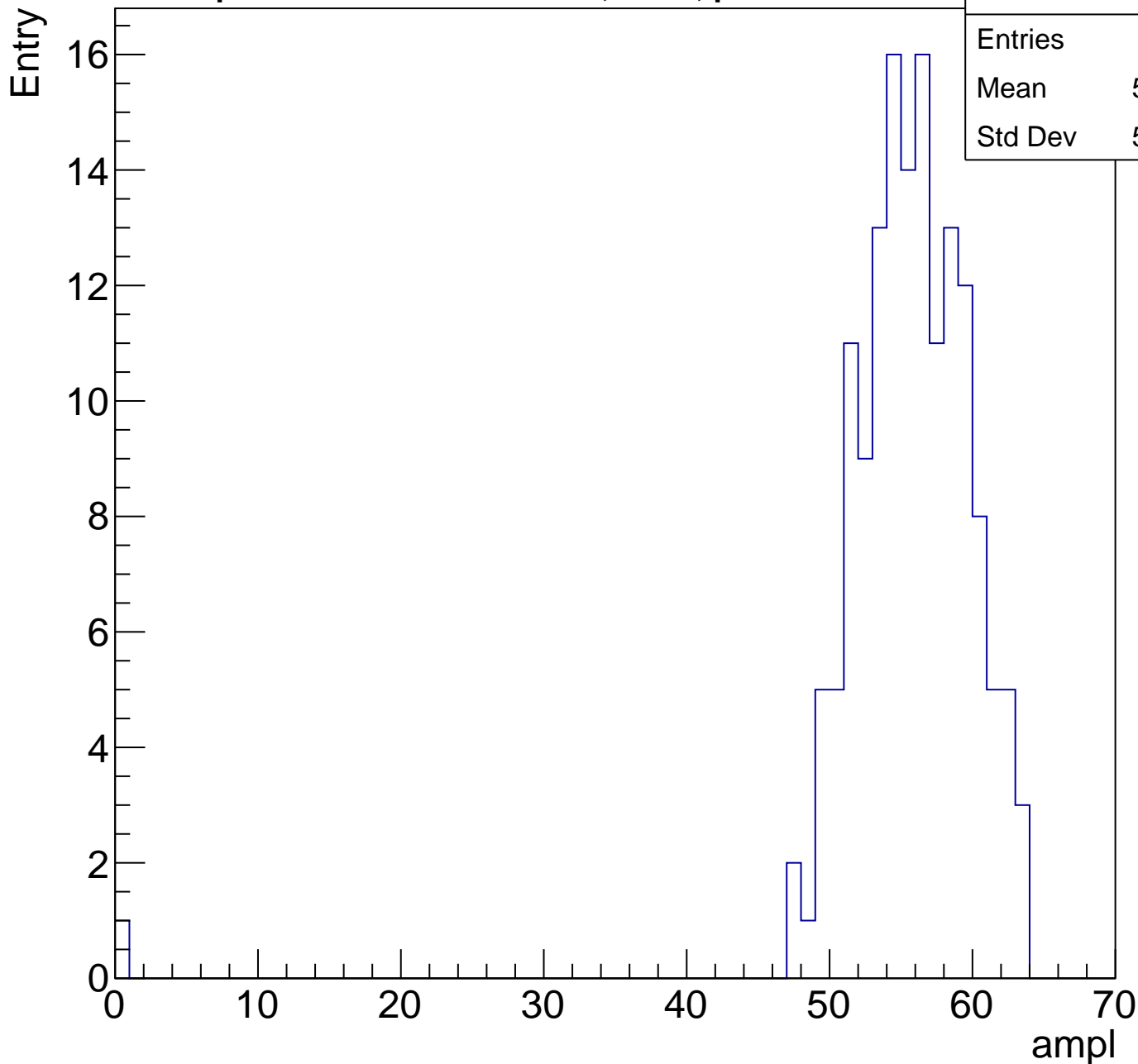
Entry



# B0L101S, U16-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 150   |
| Mean    | 55.05 |
| Std Dev | 5.784 |



# B0L101S, U16-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

70

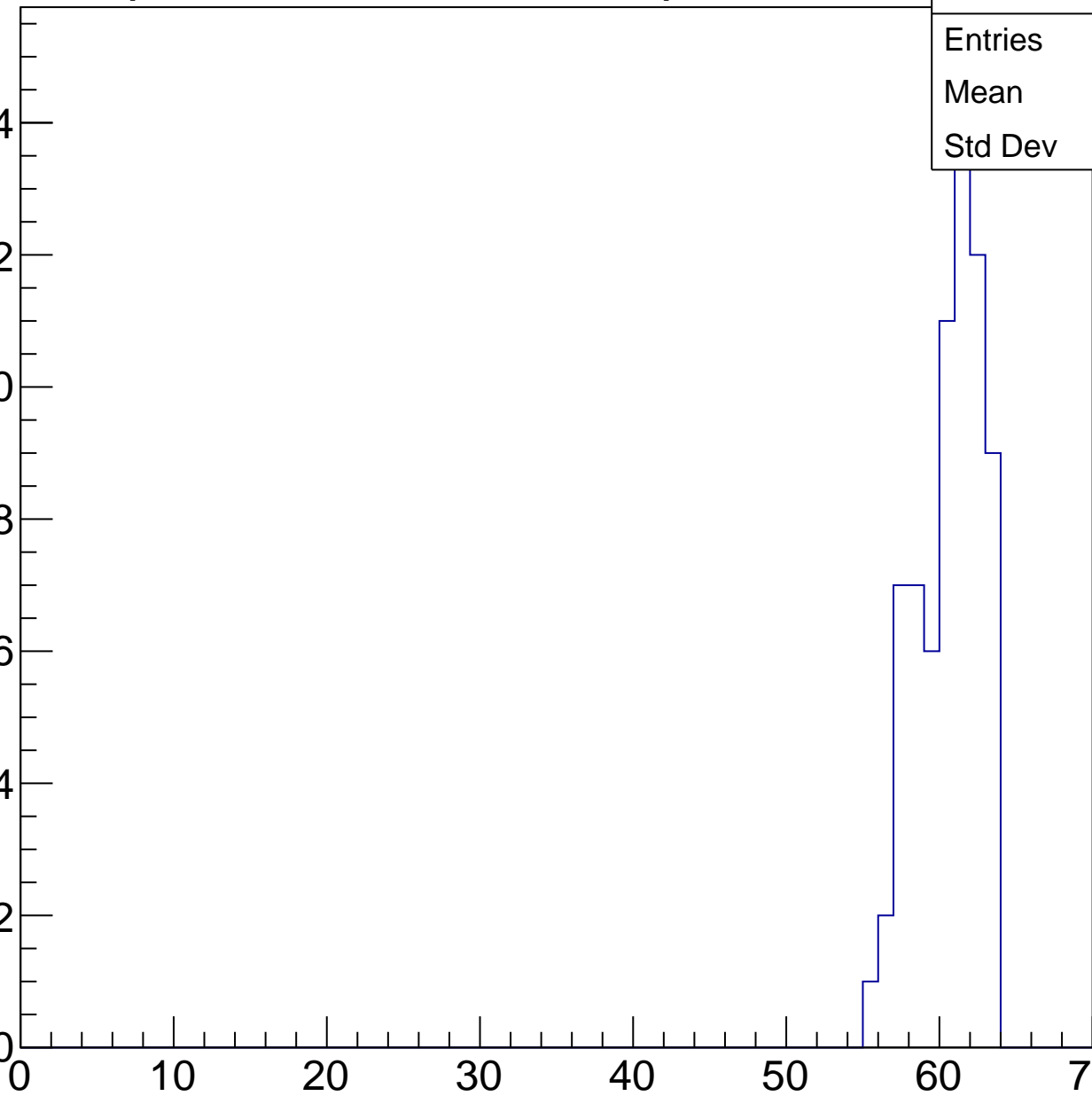
Mean

60.17

Std Dev

2.056

ampl

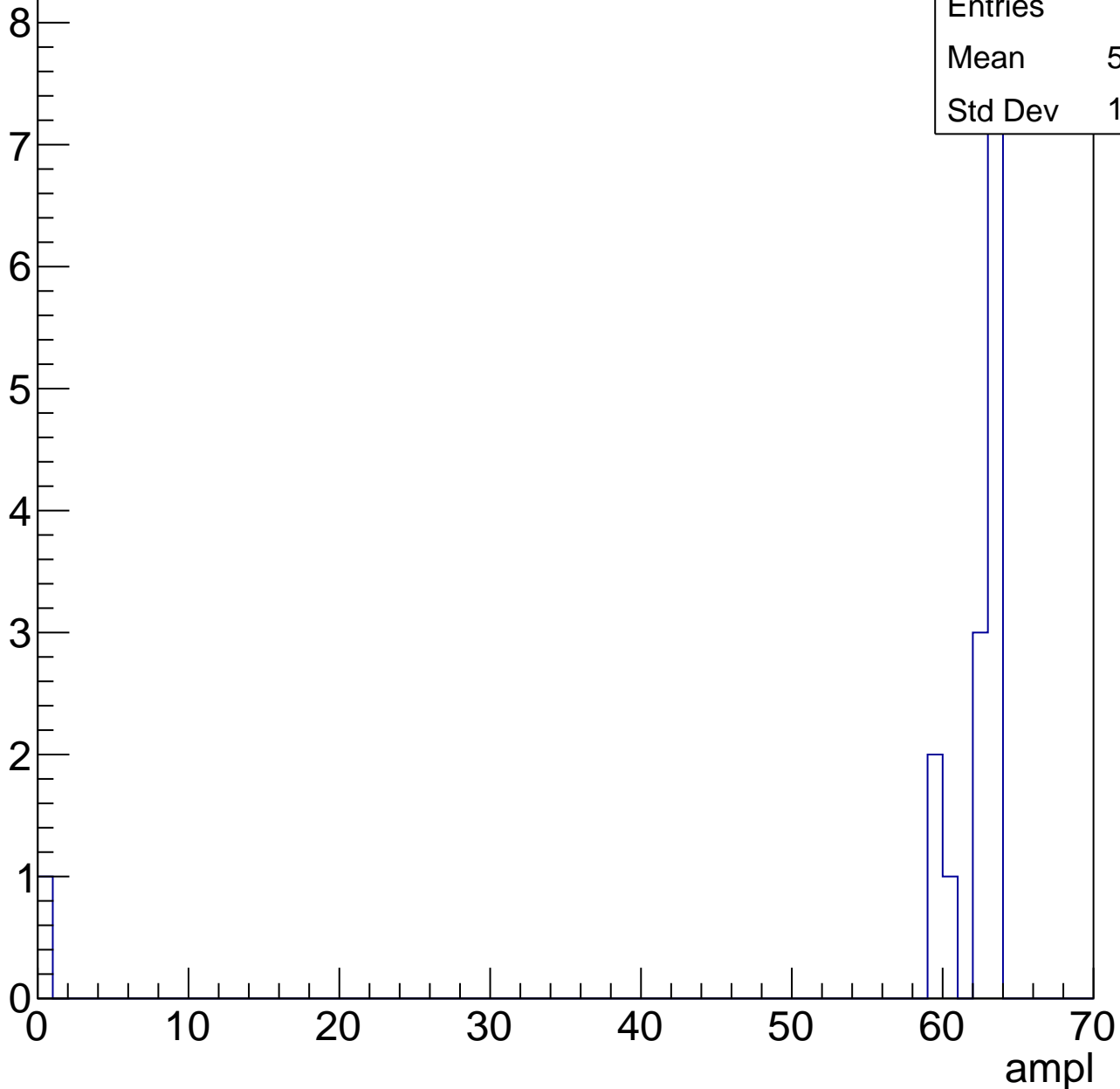


# B0L101S, U16-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 15    |
| Mean    | 57.87 |
| Std Dev | 15.53 |

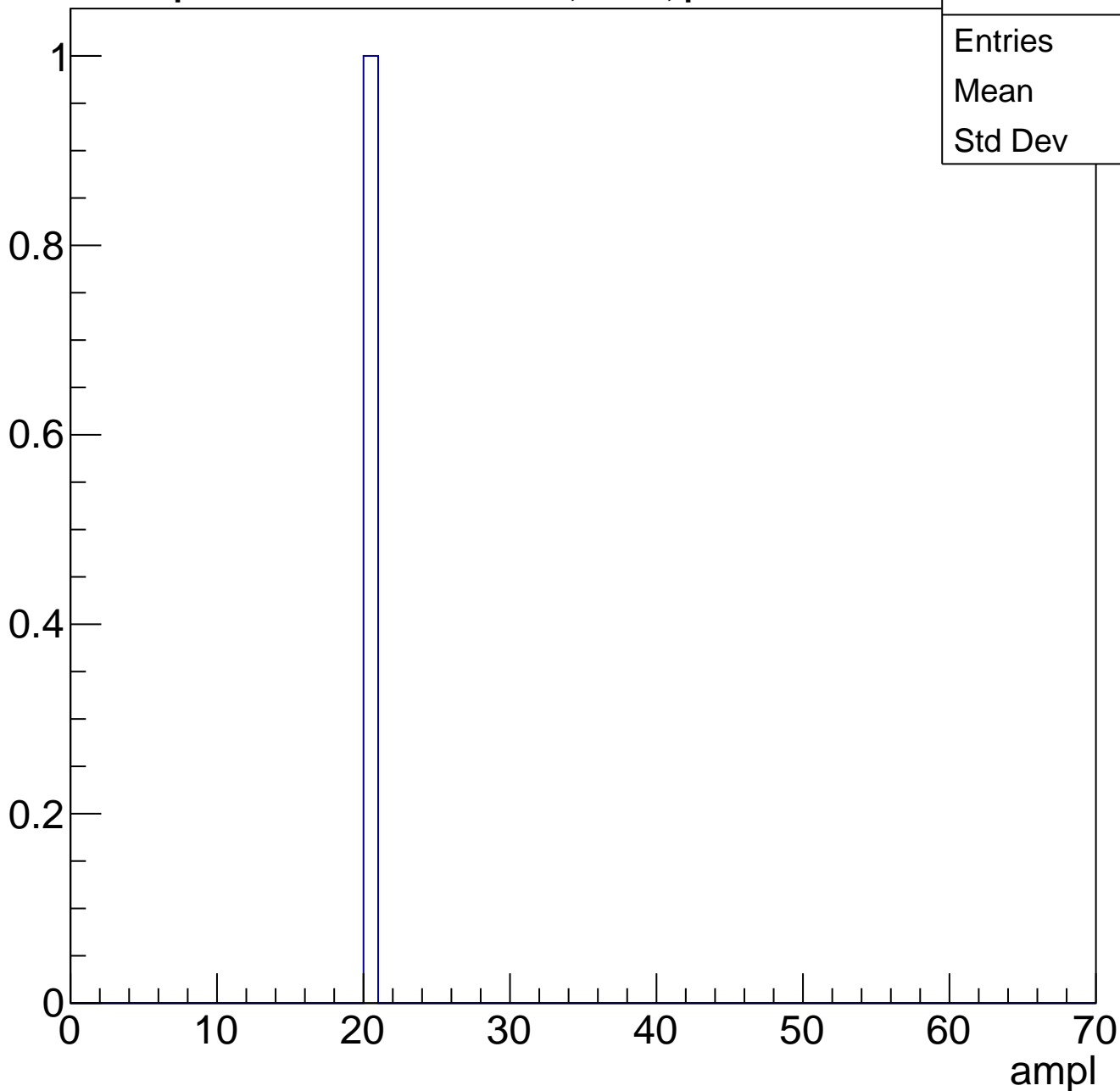




# B0L101S, U16-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 20 |
| Std Dev | 0  |

# B0L101S, U16-ch96, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 27.82 |
| Std Dev | 3.716 |

**Gaus mean : 28.6415**

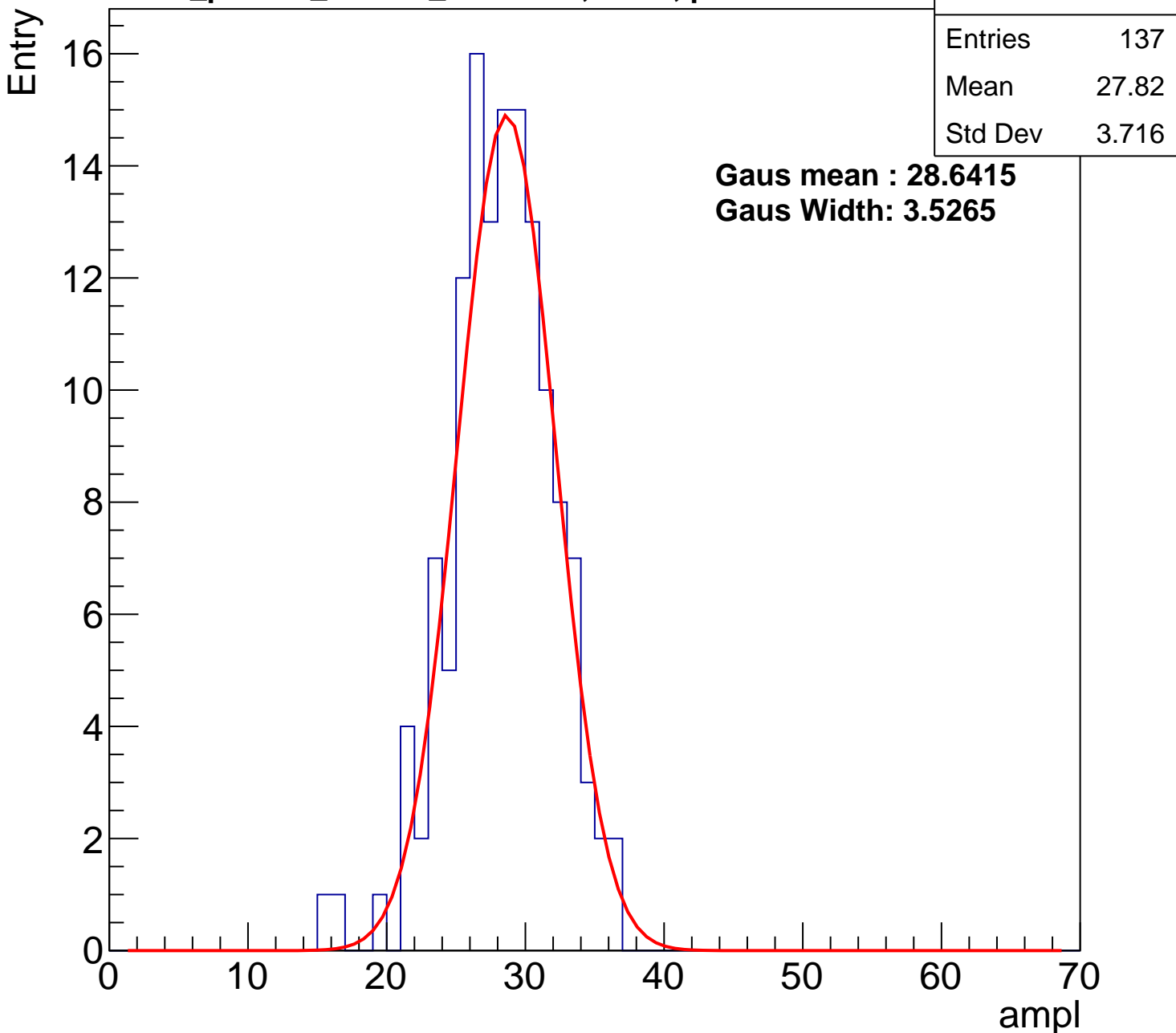
**Gaus Width: 3.5265**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 153   |
| Mean    | 35.29 |
| Std Dev | 3.783 |

**Gaus mean : 35.9955**

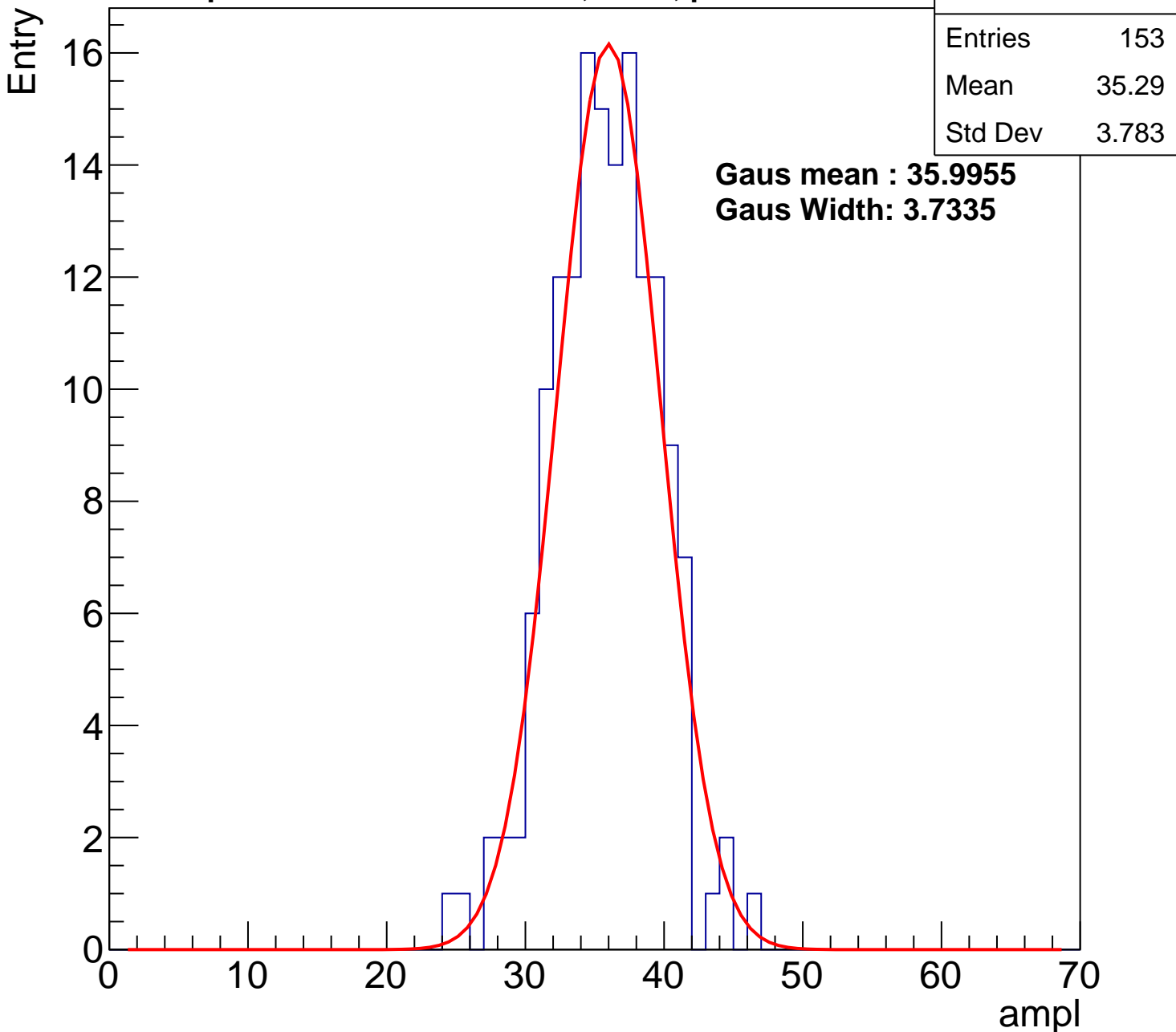
**Gaus Width: 3.7335**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch96, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

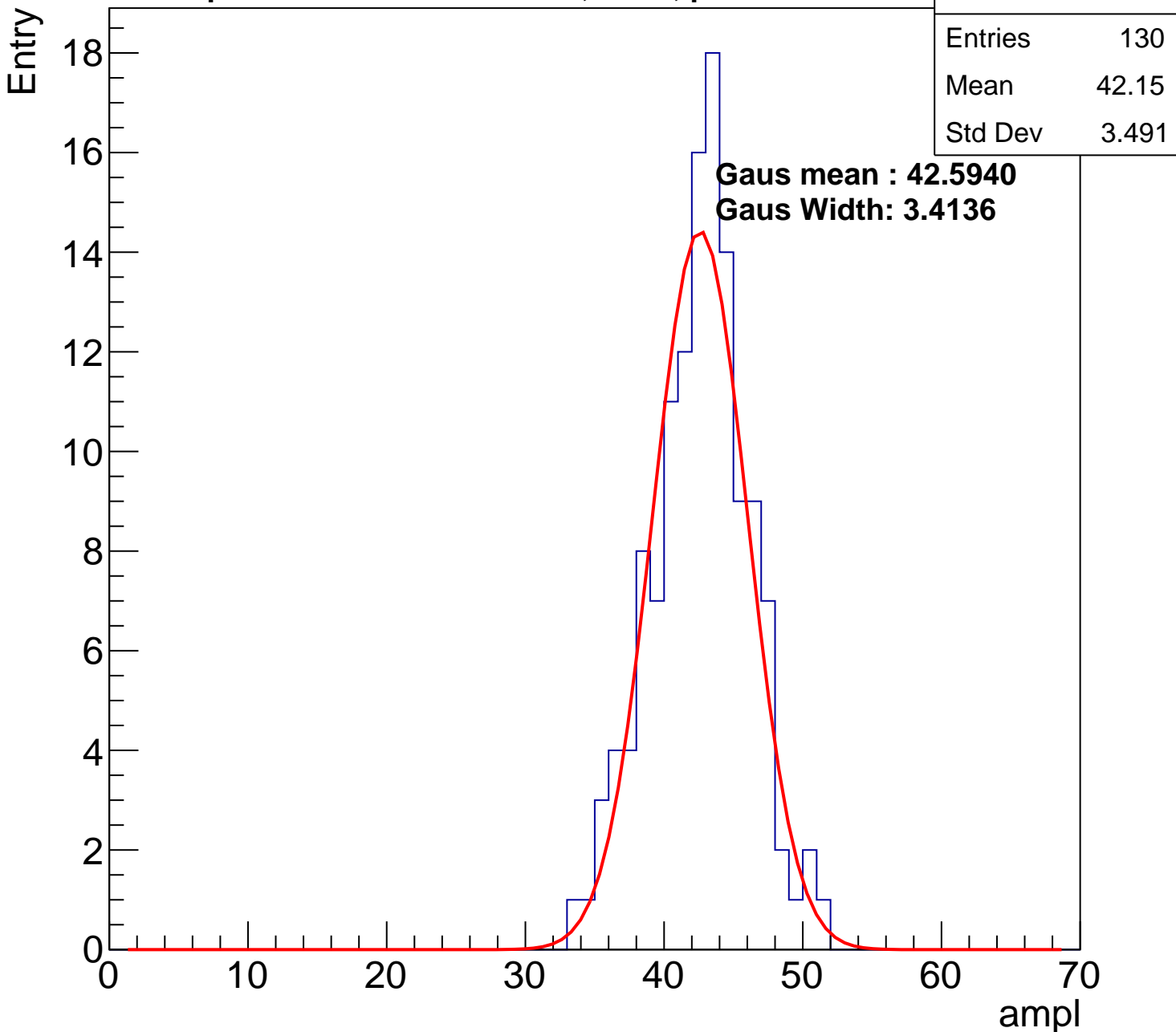
|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 42.15 |
| Std Dev | 3.491 |

**Gaus mean : 42.5940**

**Gaus Width: 3.4136**

0 10 20 30 40 50 60 70

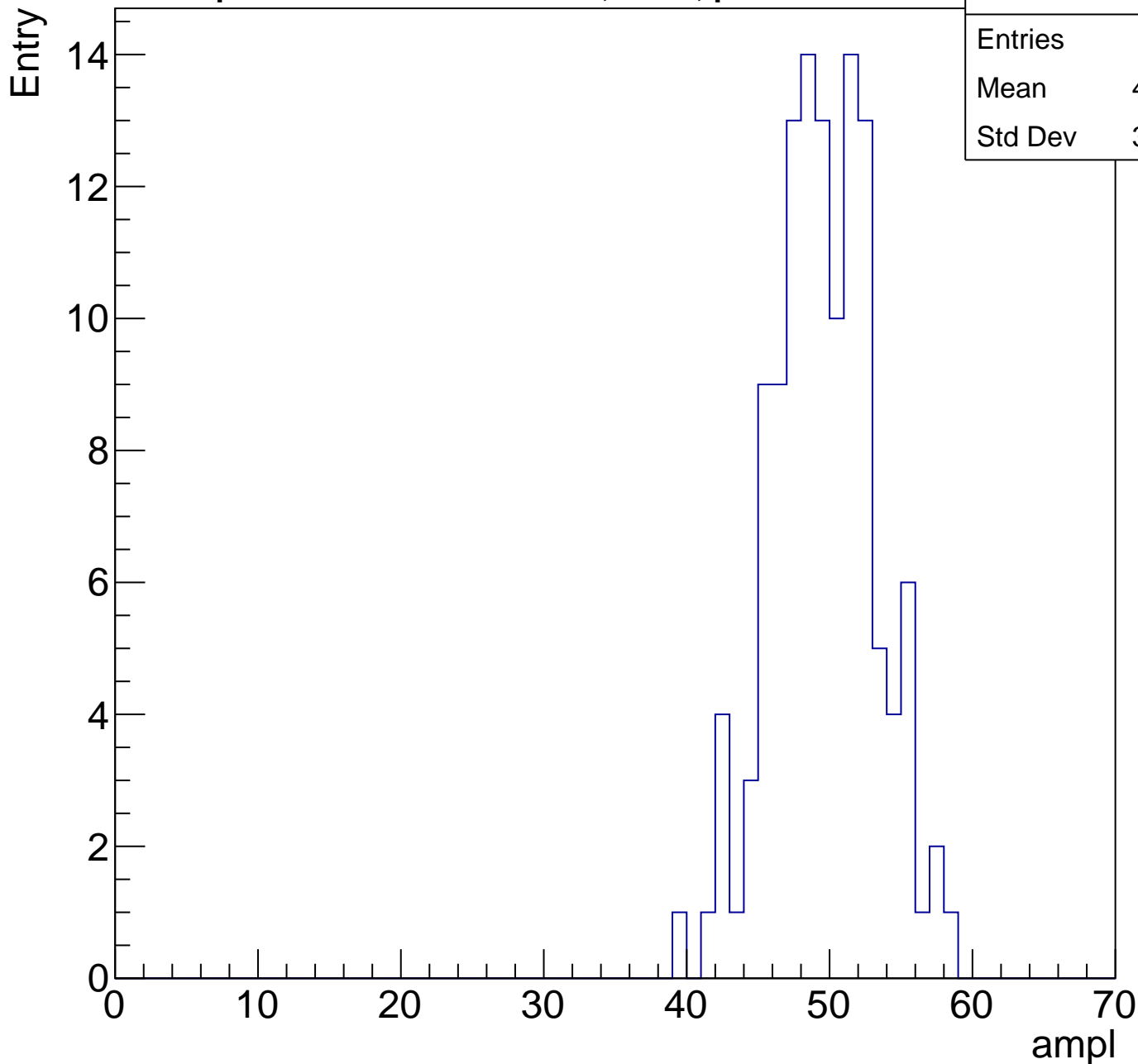
ampl



# B0L101S, U16-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

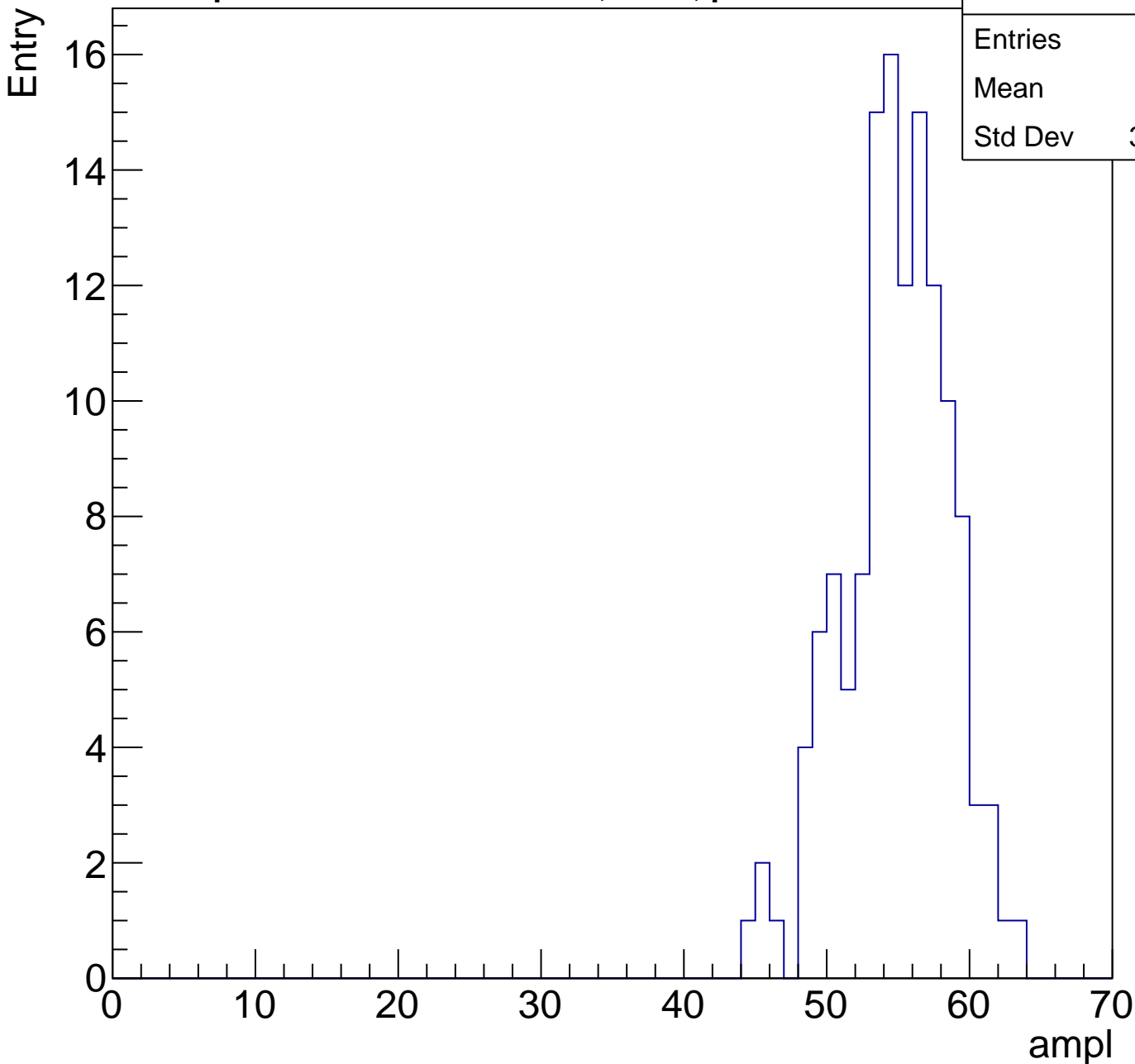
|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 49.12 |
| Std Dev | 3.589 |



# B0L101S, U16-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 54.4  |
| Std Dev | 3.672 |



# B0L101S, U16-ch96, adc5

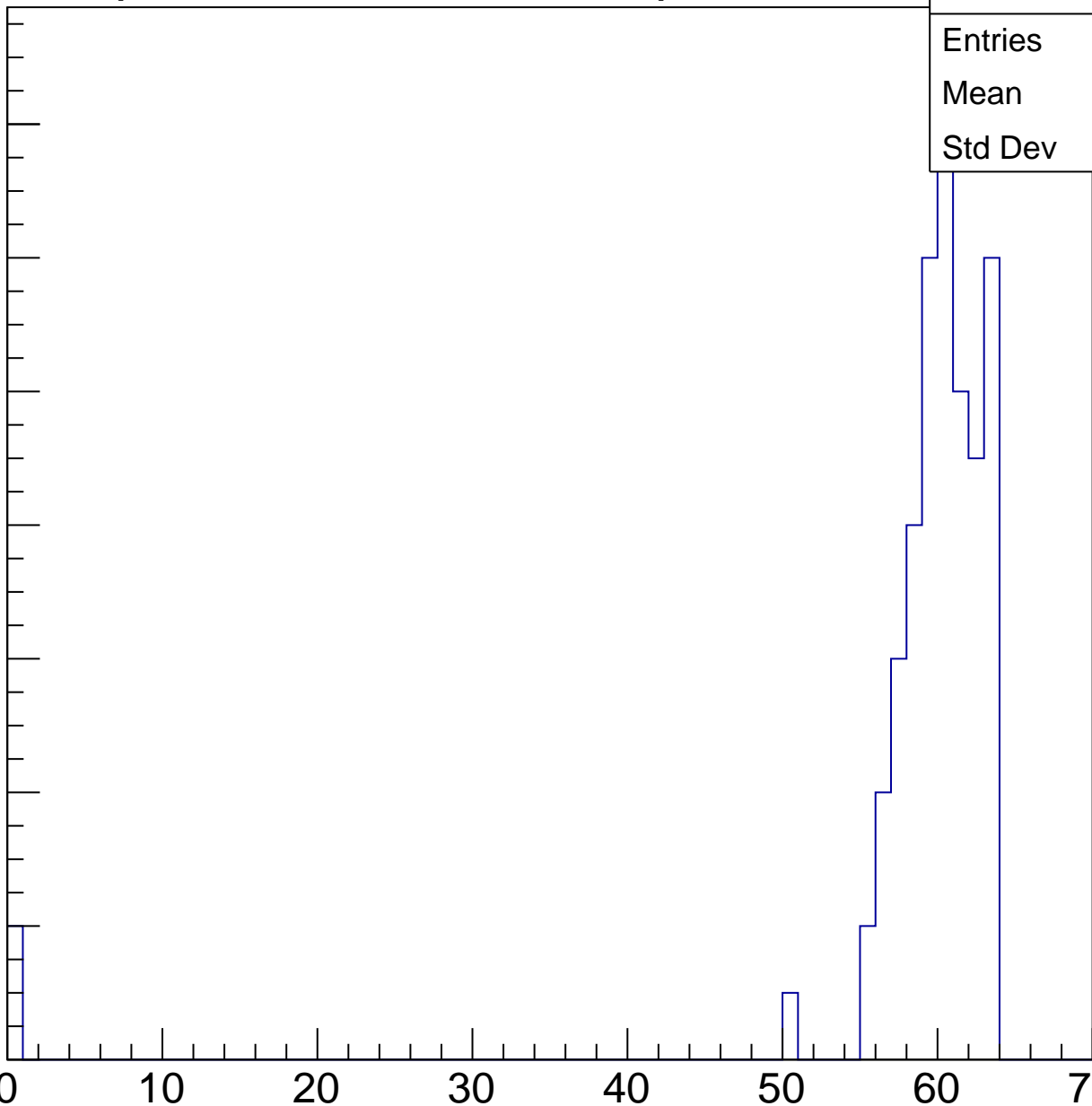
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 58.3  |
| Std Dev | 9.578 |

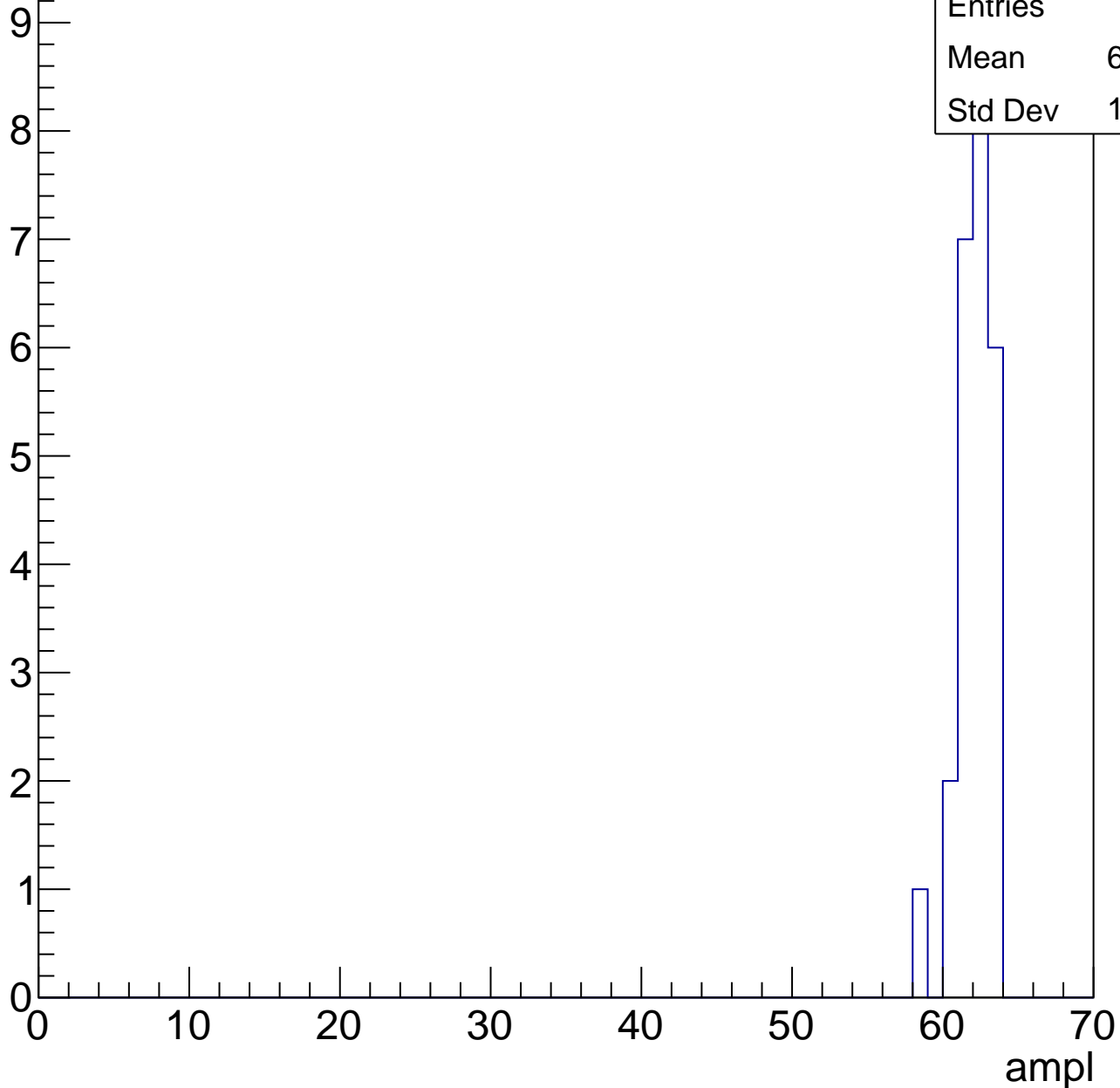
ampl



# B0L101S, U16-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 28.75 |
| Std Dev | 5.034 |

**Gaus mean : 29.3657**

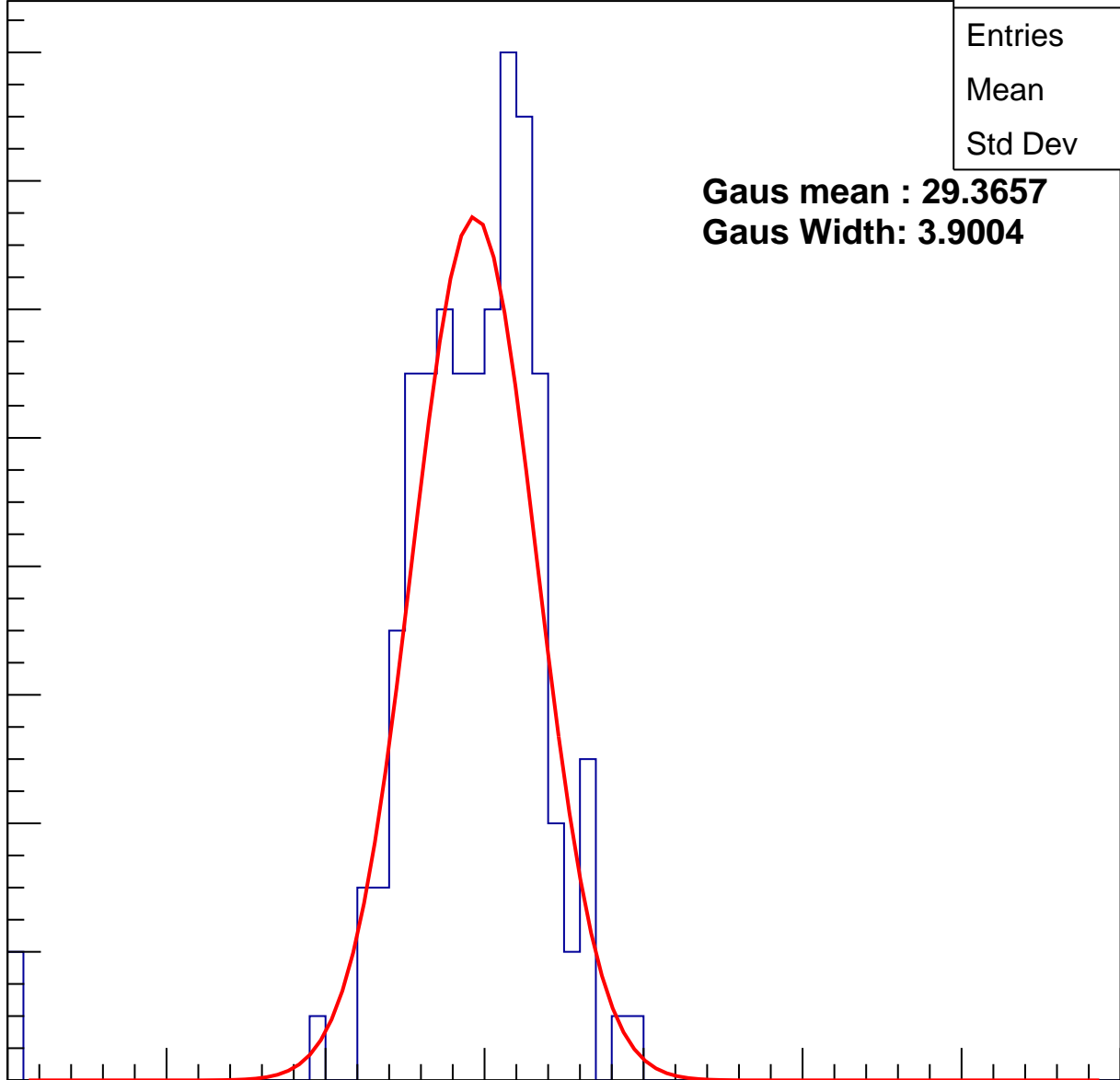
**Gaus Width: 3.9004**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch97, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 35.22 |
| Std Dev | 4.676 |

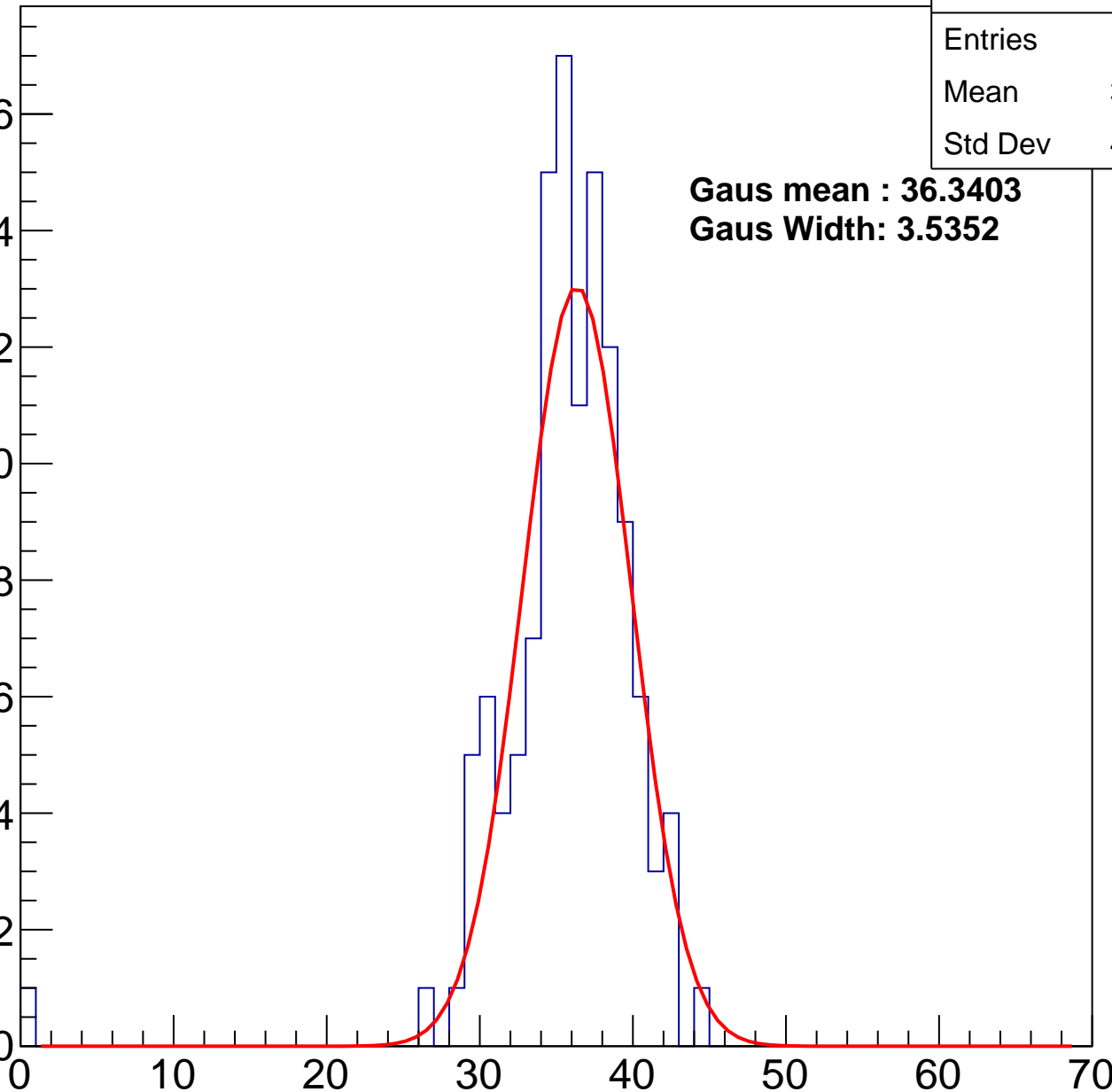
**Gaus mean : 36.3403**

**Gaus Width: 3.5352**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch97, adc2

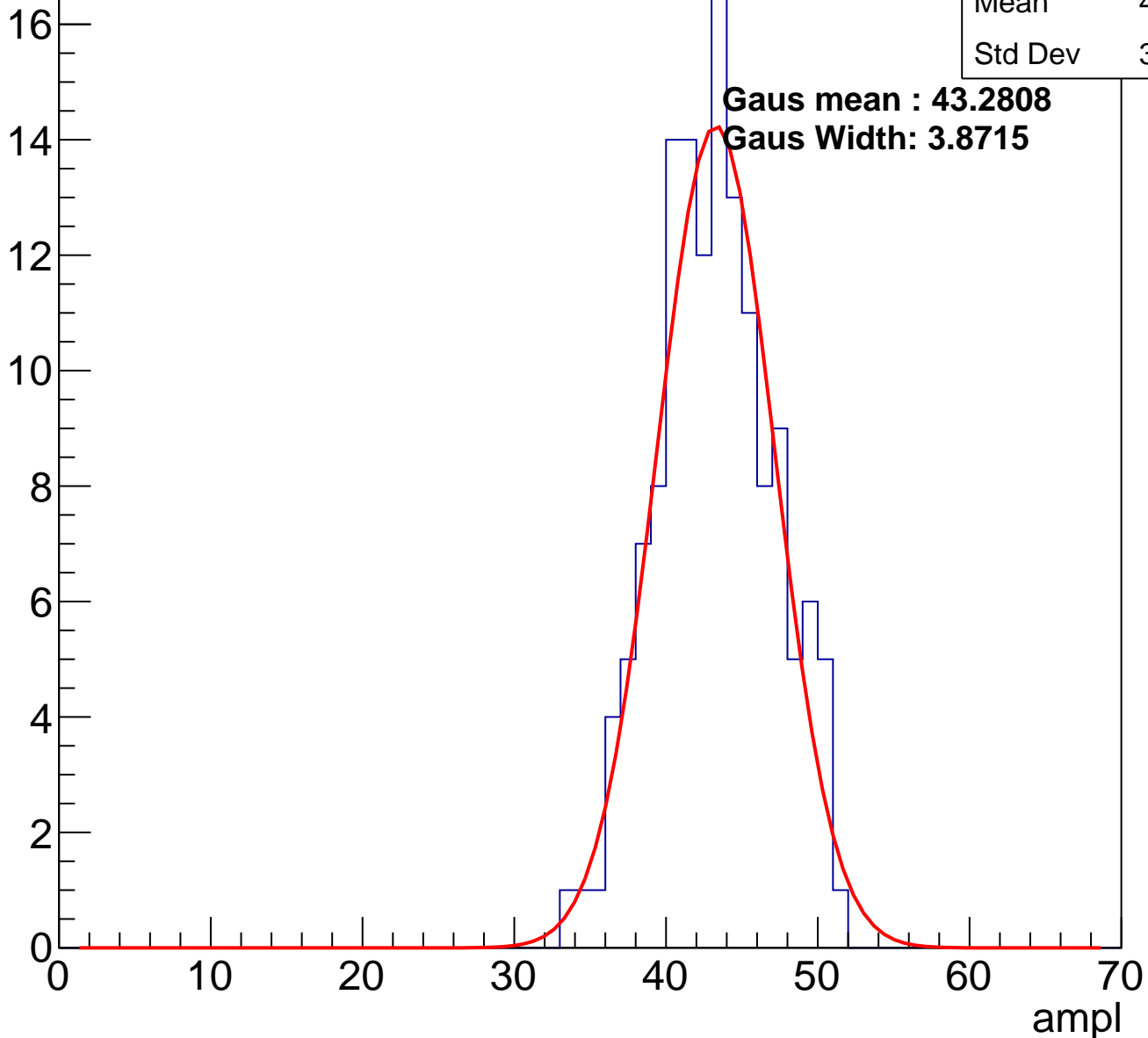
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 42.75 |
| Std Dev | 3.783 |

**Gaus mean : 43.2808**

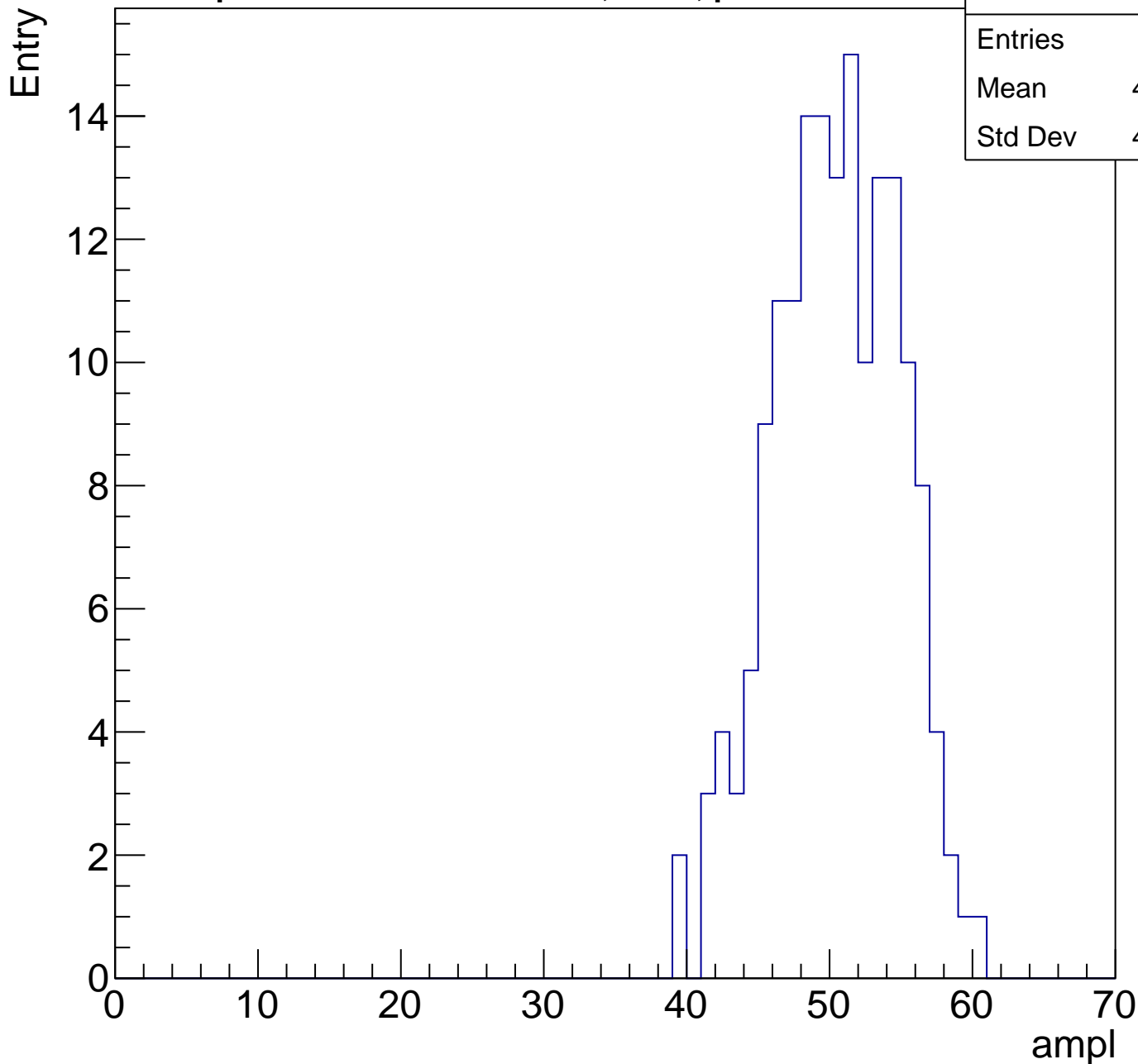
**Gaus Width: 3.8715**



# B0L101S, U16-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 166   |
| Mean    | 49.95 |
| Std Dev | 4.294 |



# B0L101S, U16-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

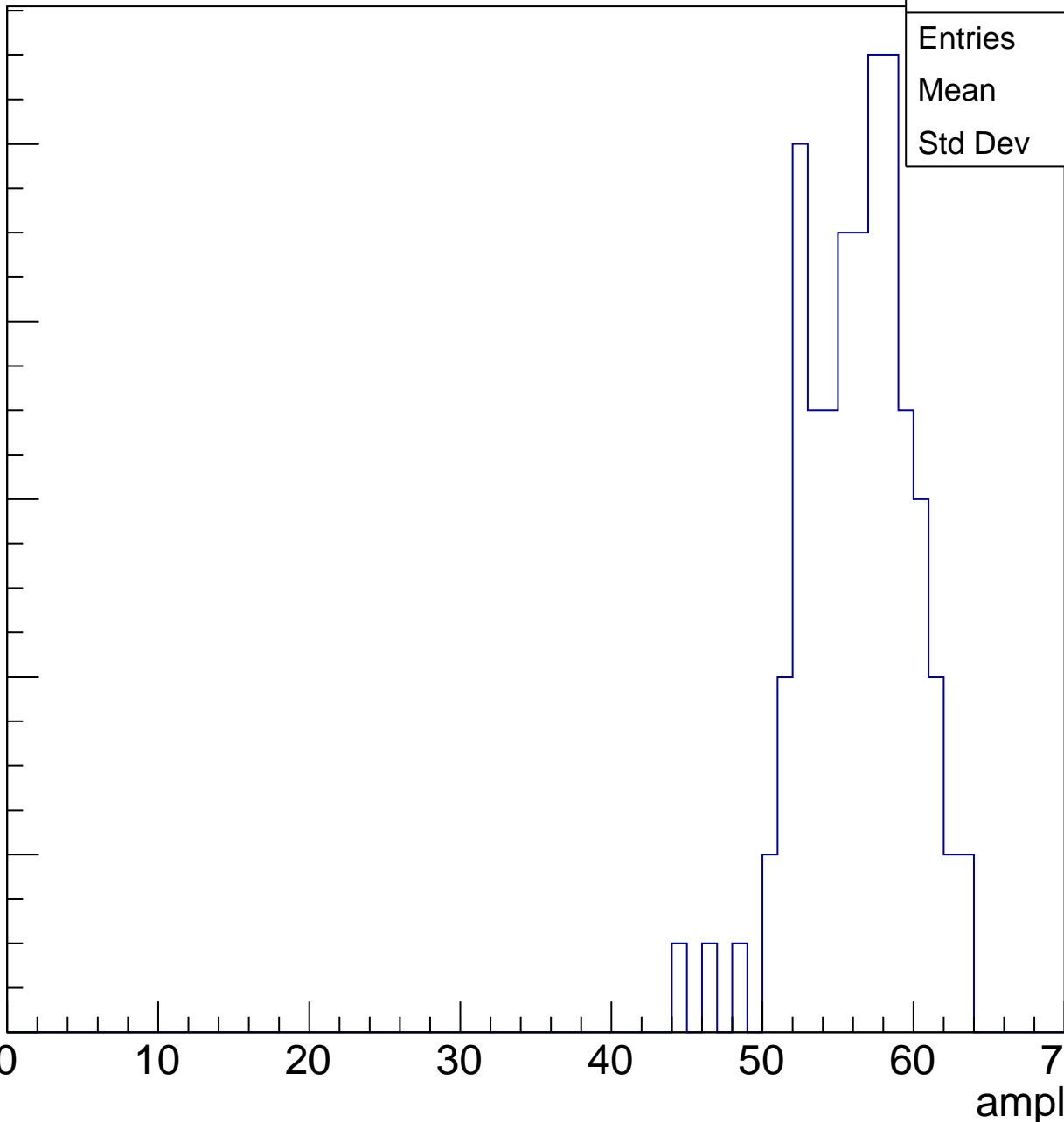
30

40

50

ampl

|         |       |
|---------|-------|
| Entries | 94    |
| Mean    | 55.77 |
| Std Dev | 3.601 |



# B0L101S, U16-ch97, adc5

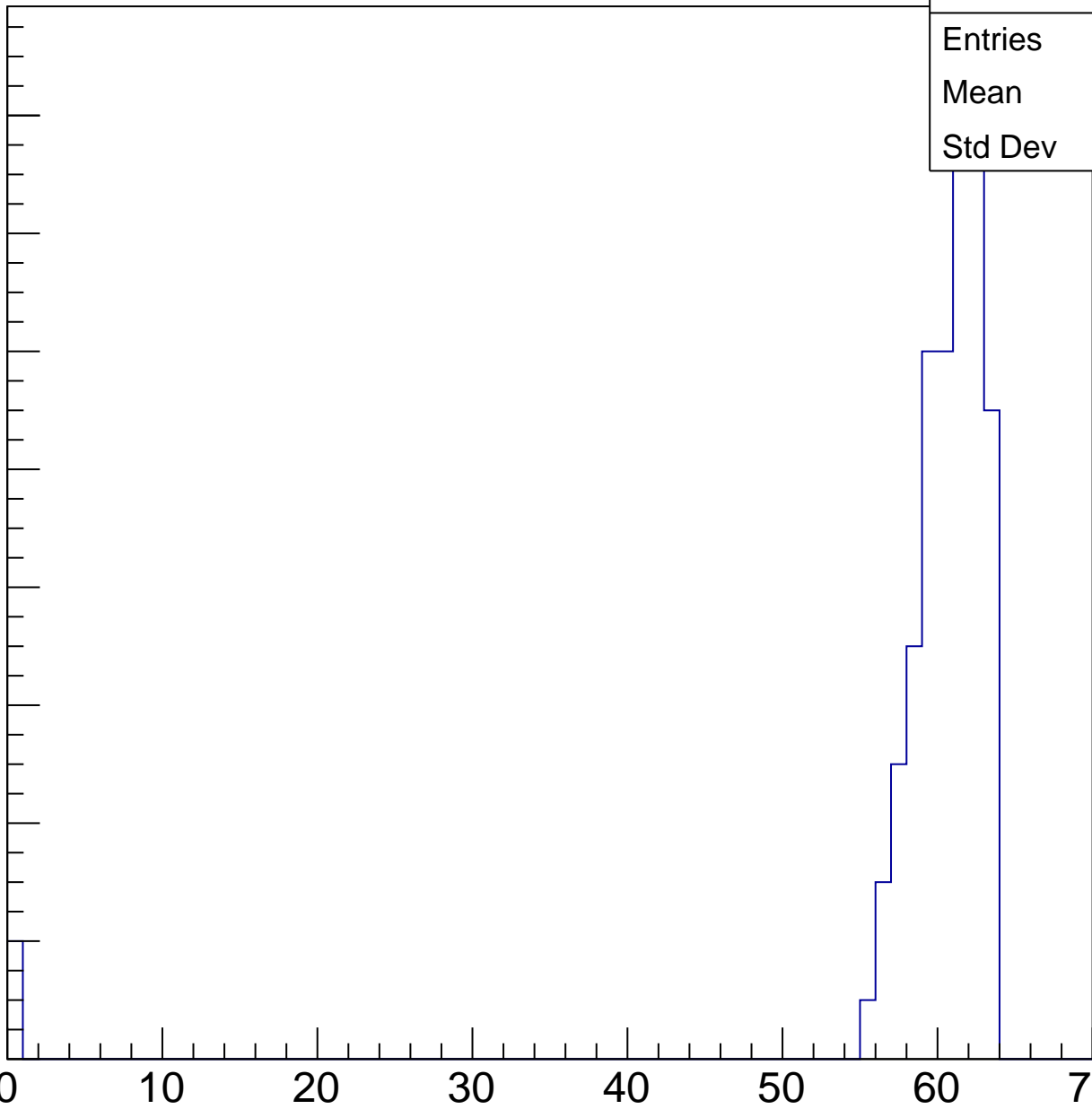
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 58.9  |
| Std Dev | 9.299 |

ampl

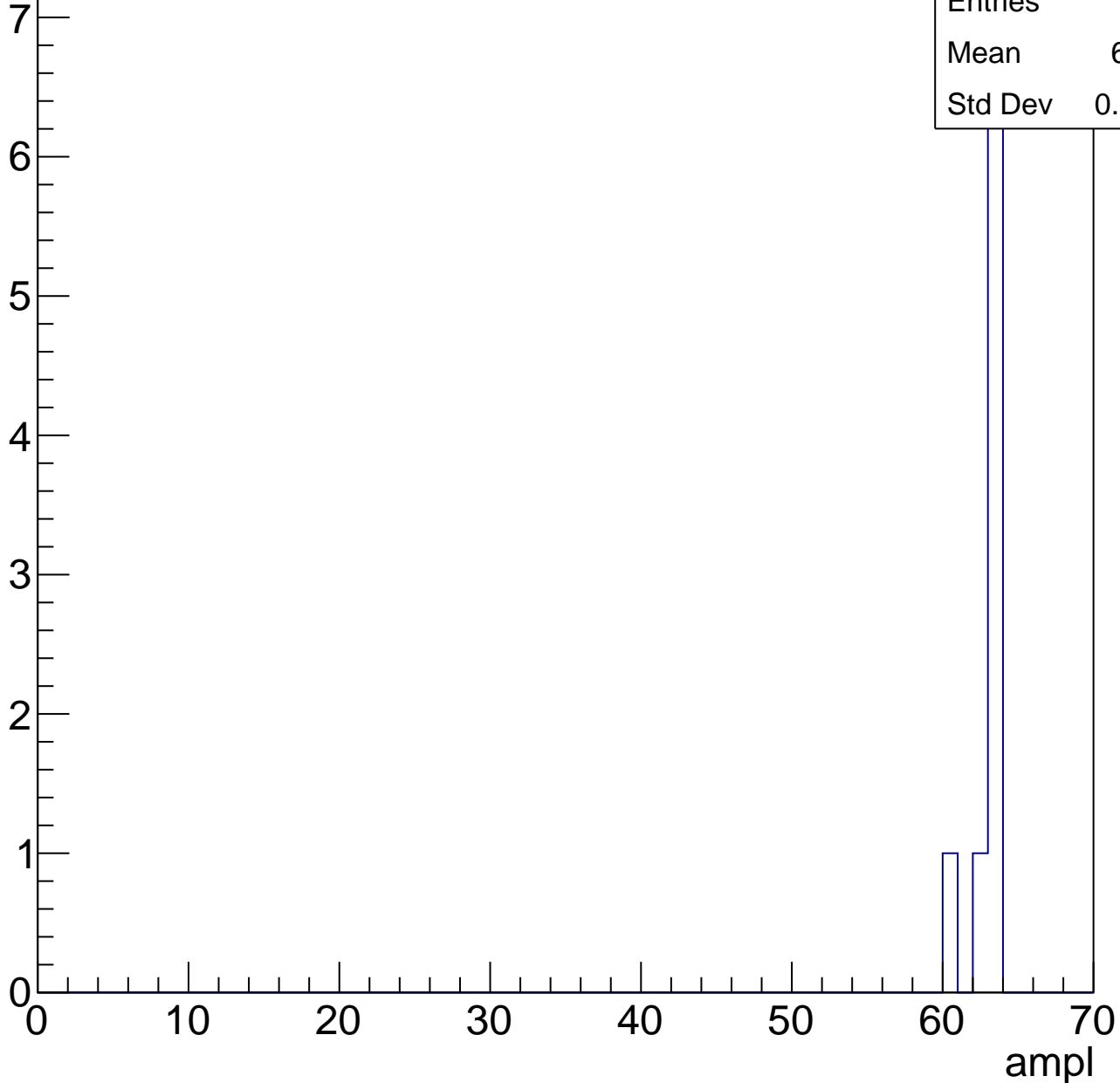


# B0L101S, U16-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |        |
|---------|--------|
| Entries | 9      |
| Mean    | 62.56  |
| Std Dev | 0.9558 |





# B0L101S, U16-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch98, adc0

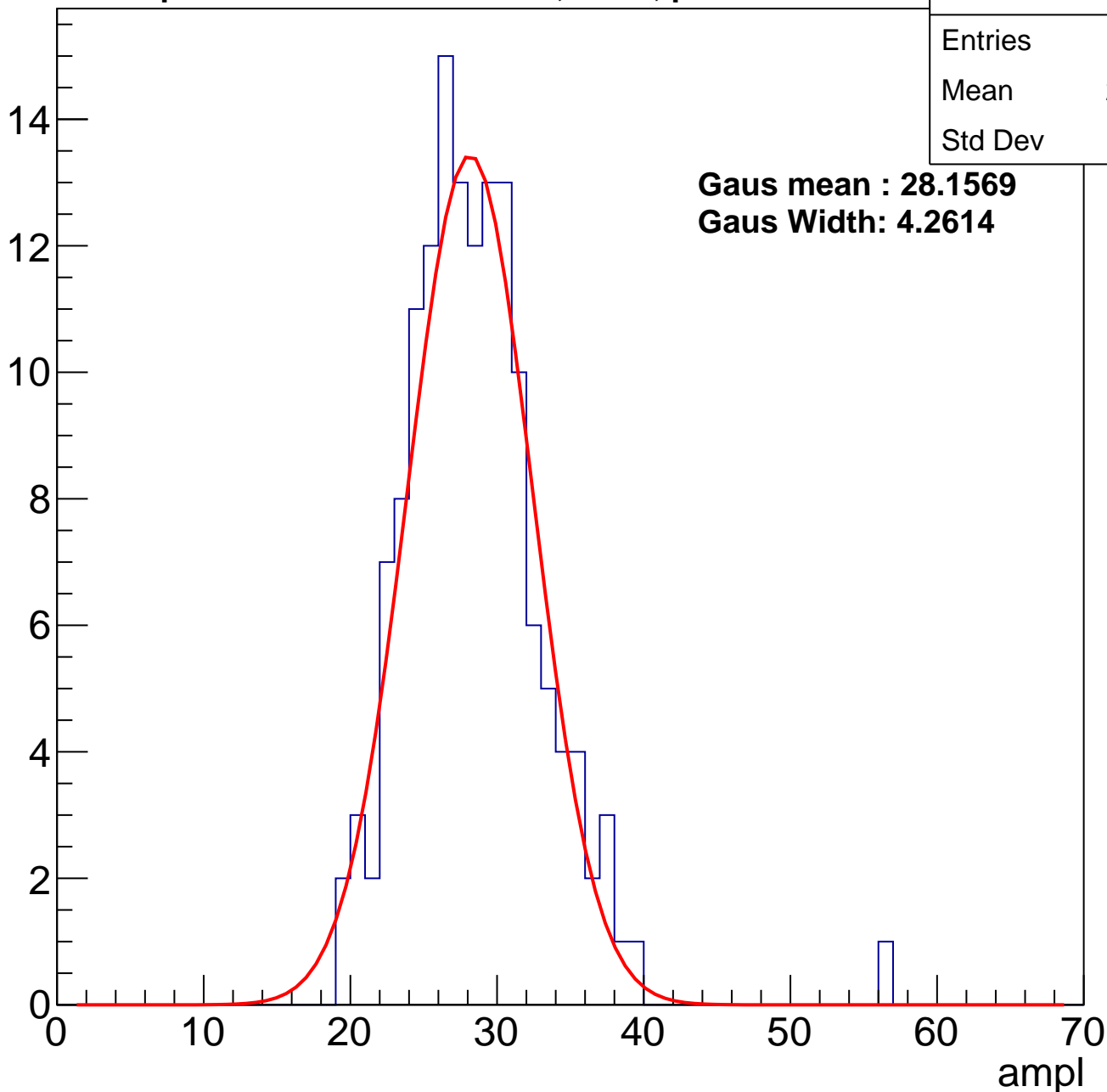
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 28.01 |
| Std Dev | 4.76  |

**Gaus mean : 28.1569**

**Gaus Width: 4.2614**

Entry



# B0L101S, U16-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 34.97 |
| Std Dev | 4.099 |

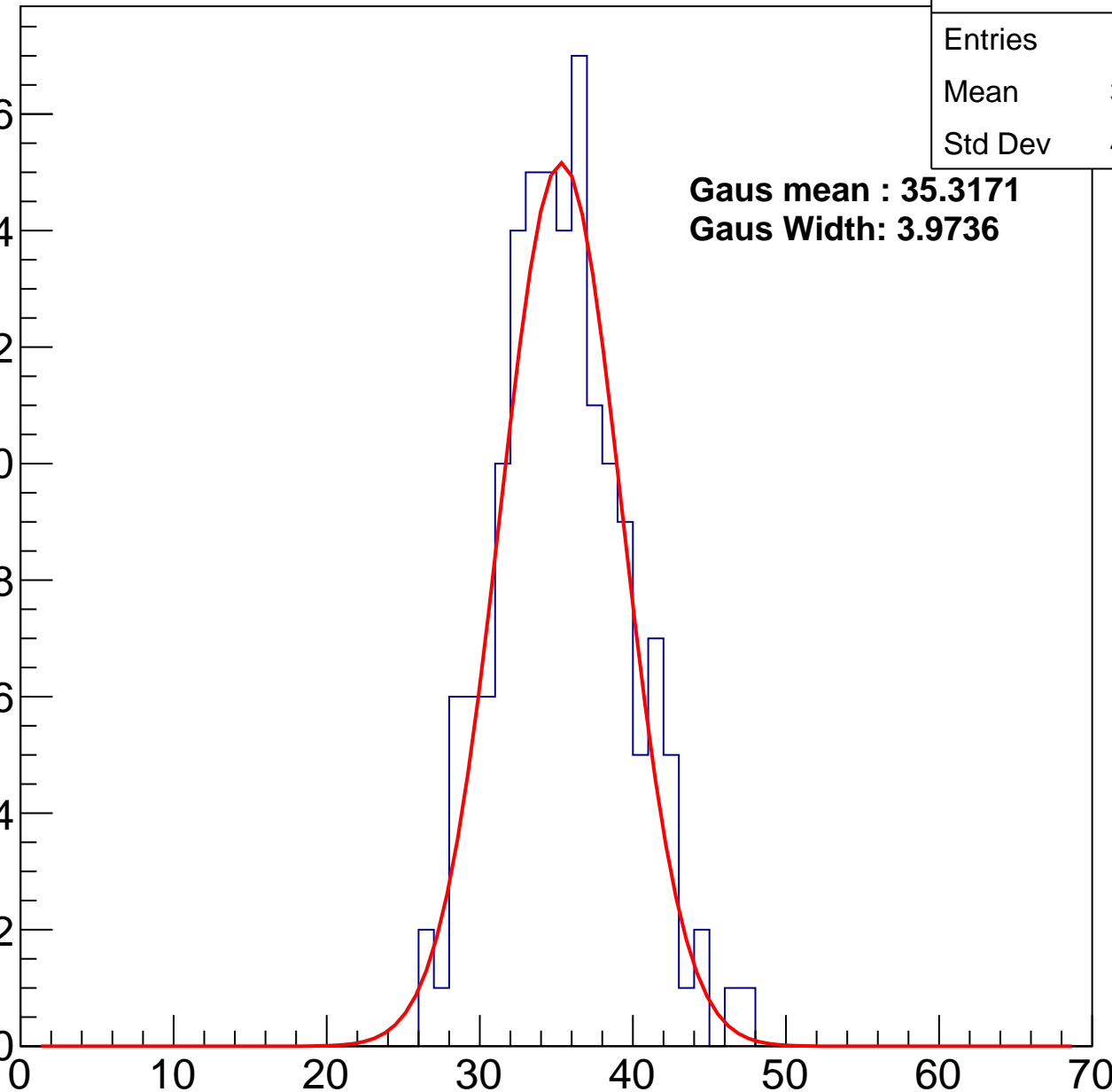
**Gaus mean : 35.3171**

**Gaus Width: 3.9736**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch98, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

|         |       |
|---------|-------|
| Entries | 91    |
| Mean    | 41.34 |
| Std Dev | 3.163 |

**Gaus mean : 41.9013**

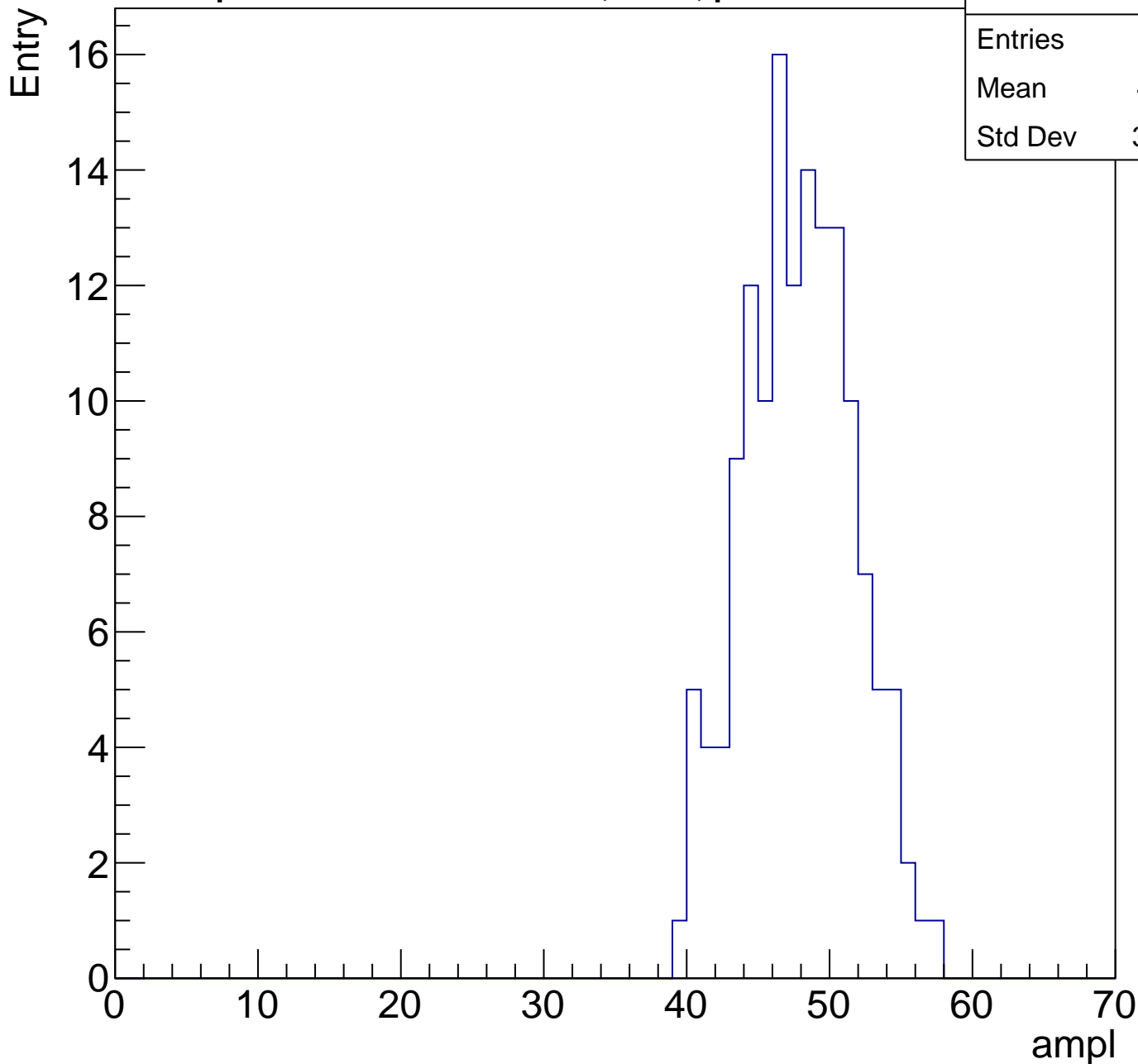
**Gaus Width: 3.1503**



# B0L101S, U16-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

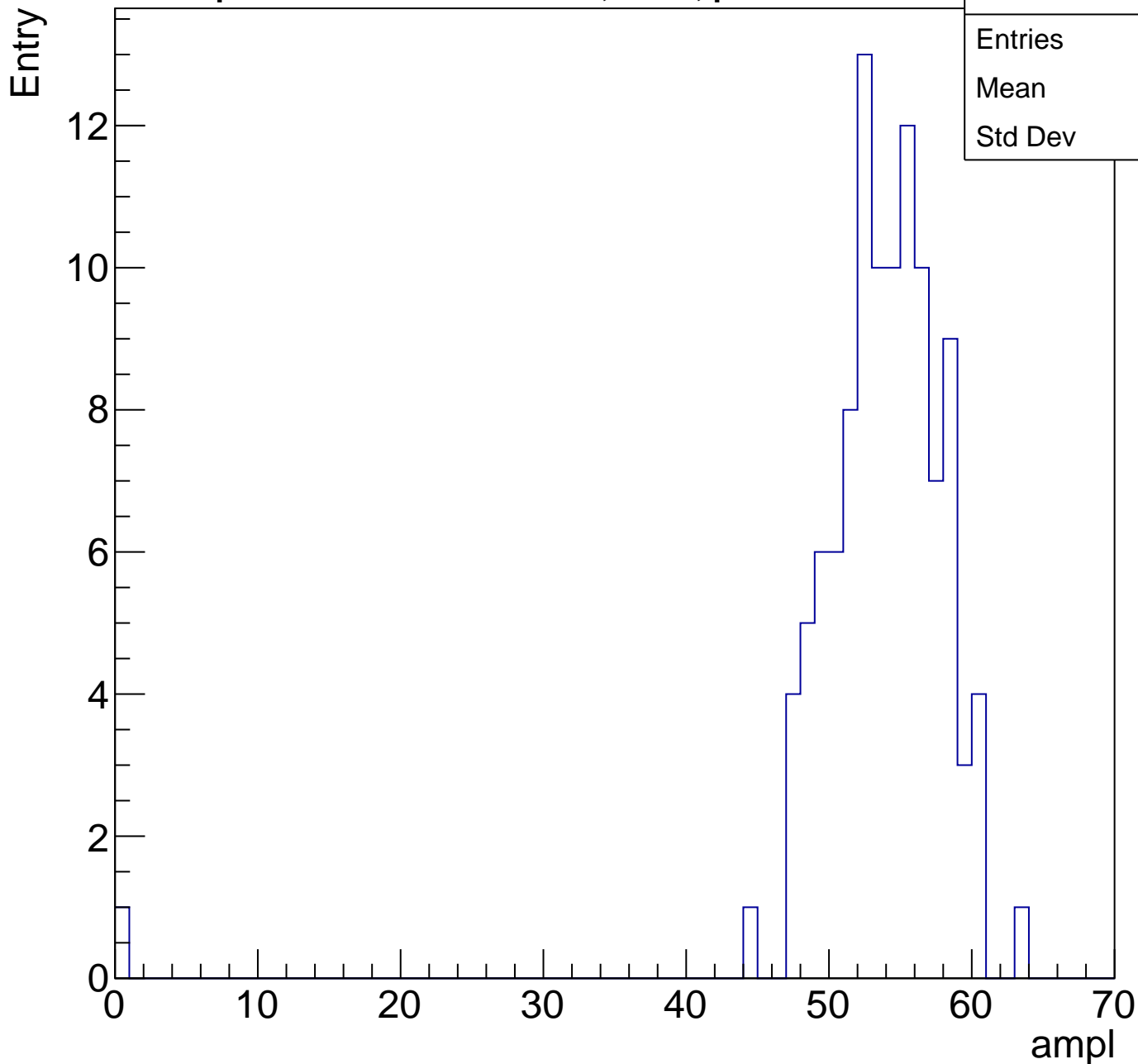
|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 47.41 |
| Std Dev | 3.809 |



# B0L101S, U16-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

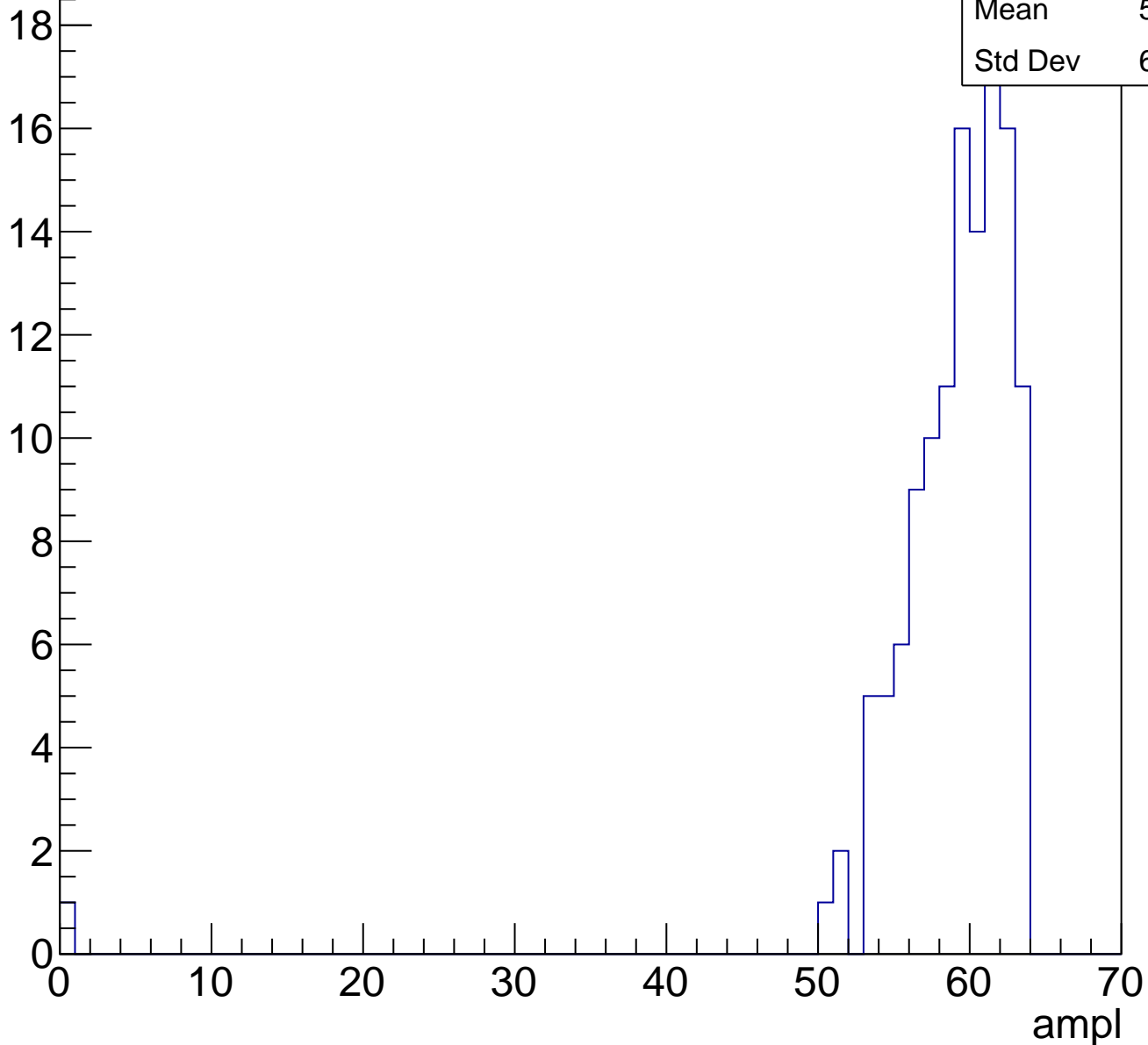
|         |      |
|---------|------|
| Entries | 110  |
| Mean    | 53.1 |
| Std Dev | 6.21 |



# B0L101S, U16-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

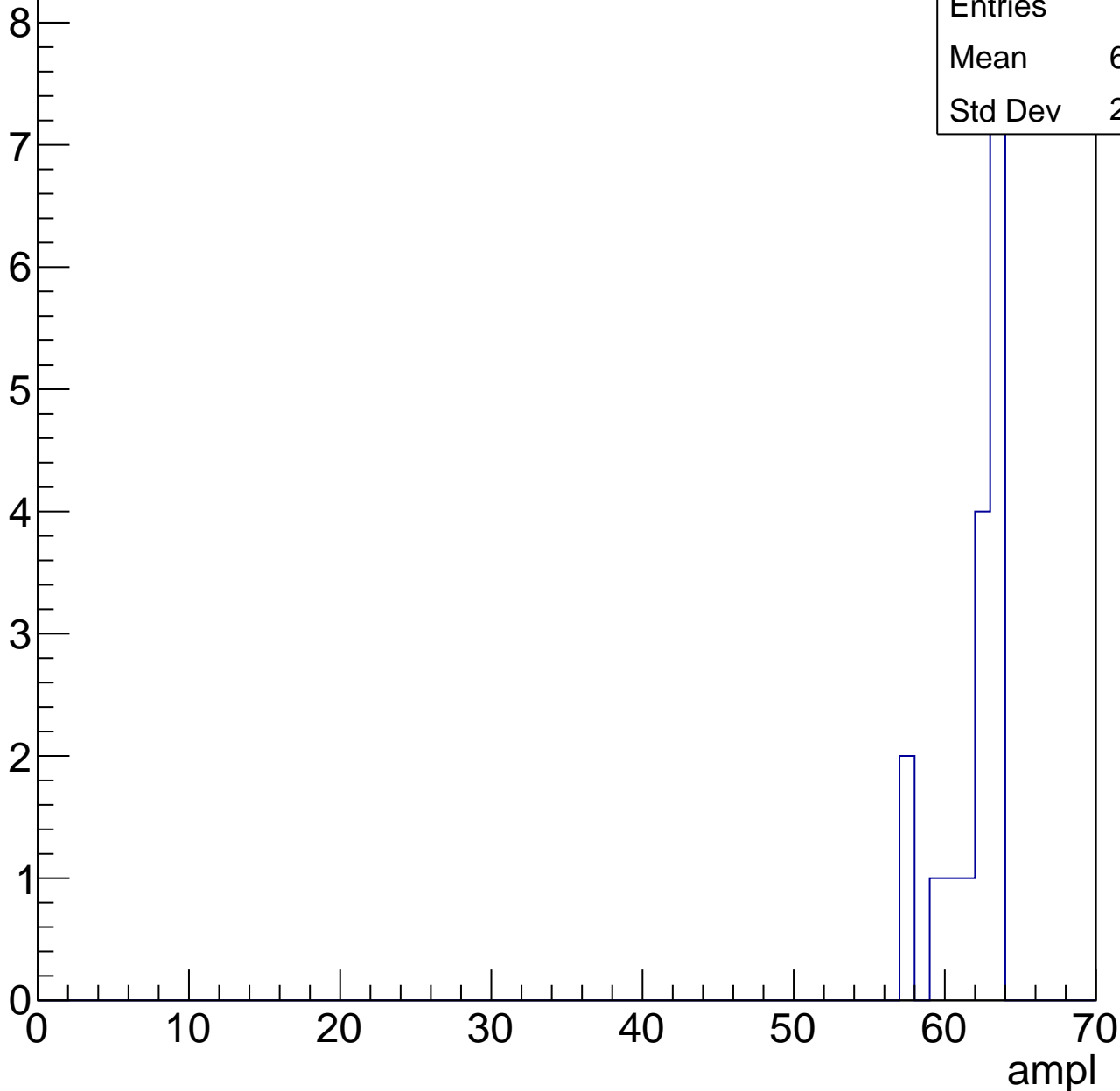


# B0L101S, U16-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 17    |
| Mean    | 61.53 |
| Std Dev | 2.003 |





# B0L101S, U16-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch99, adc0

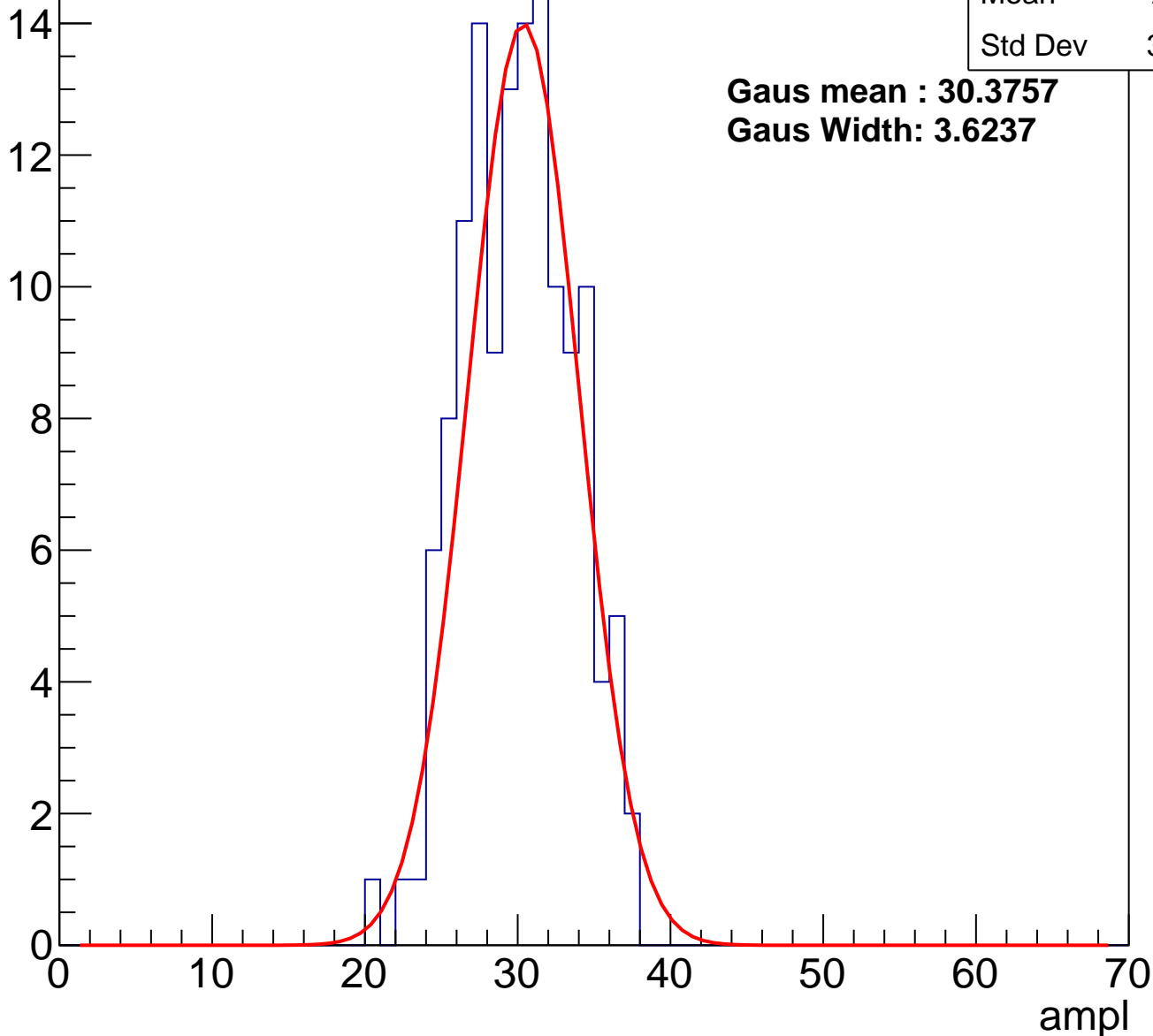
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 29.61 |
| Std Dev | 3.505 |

**Gaus mean : 30.3757**

**Gaus Width: 3.6237**

Entry

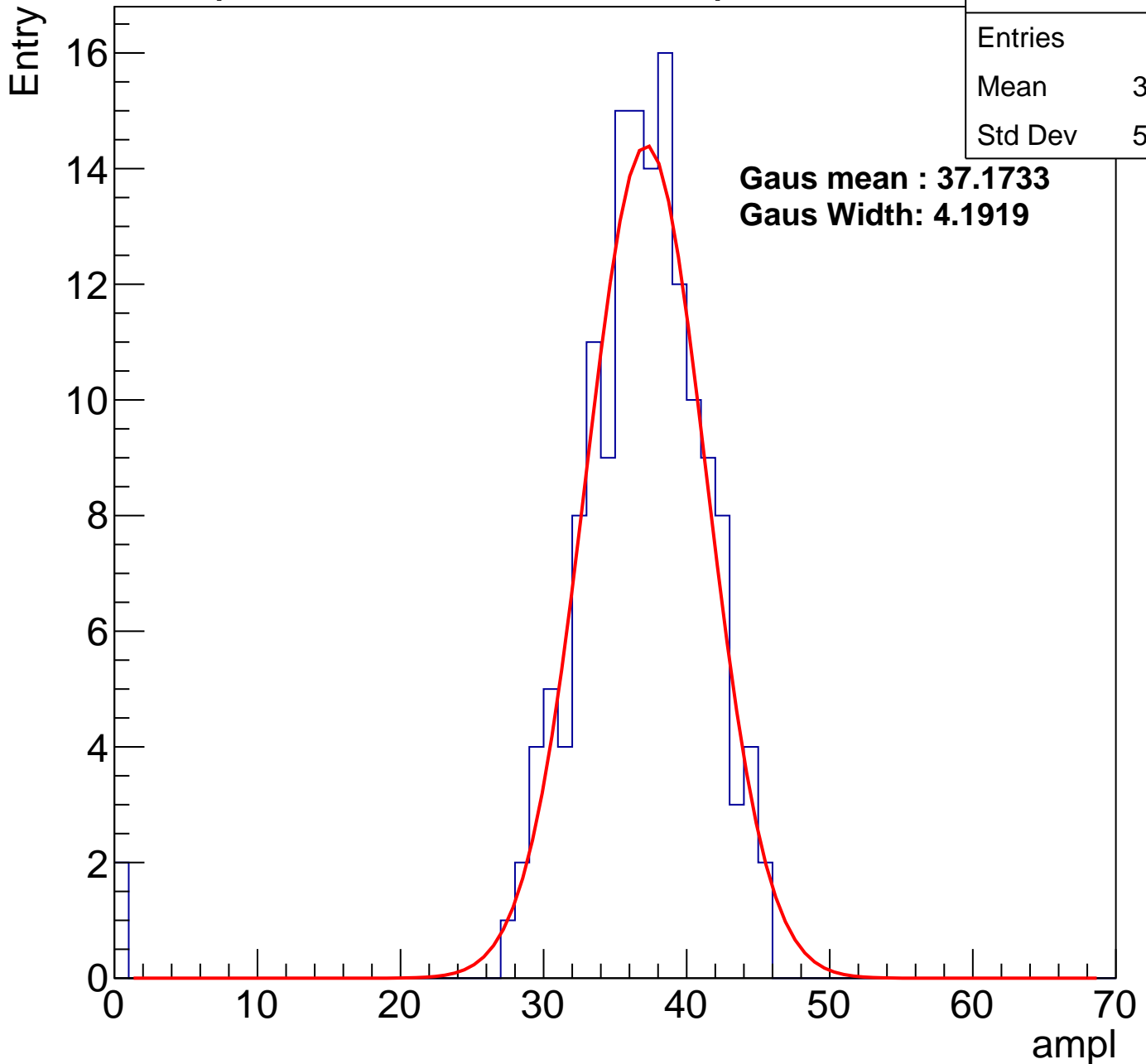


# B0L101S, U16-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 154   |
| Mean    | 36.08 |
| Std Dev | 5.685 |

**Gaus mean : 37.1733**  
**Gaus Width: 4.1919**



# B0L101S, U16-ch99, adc2

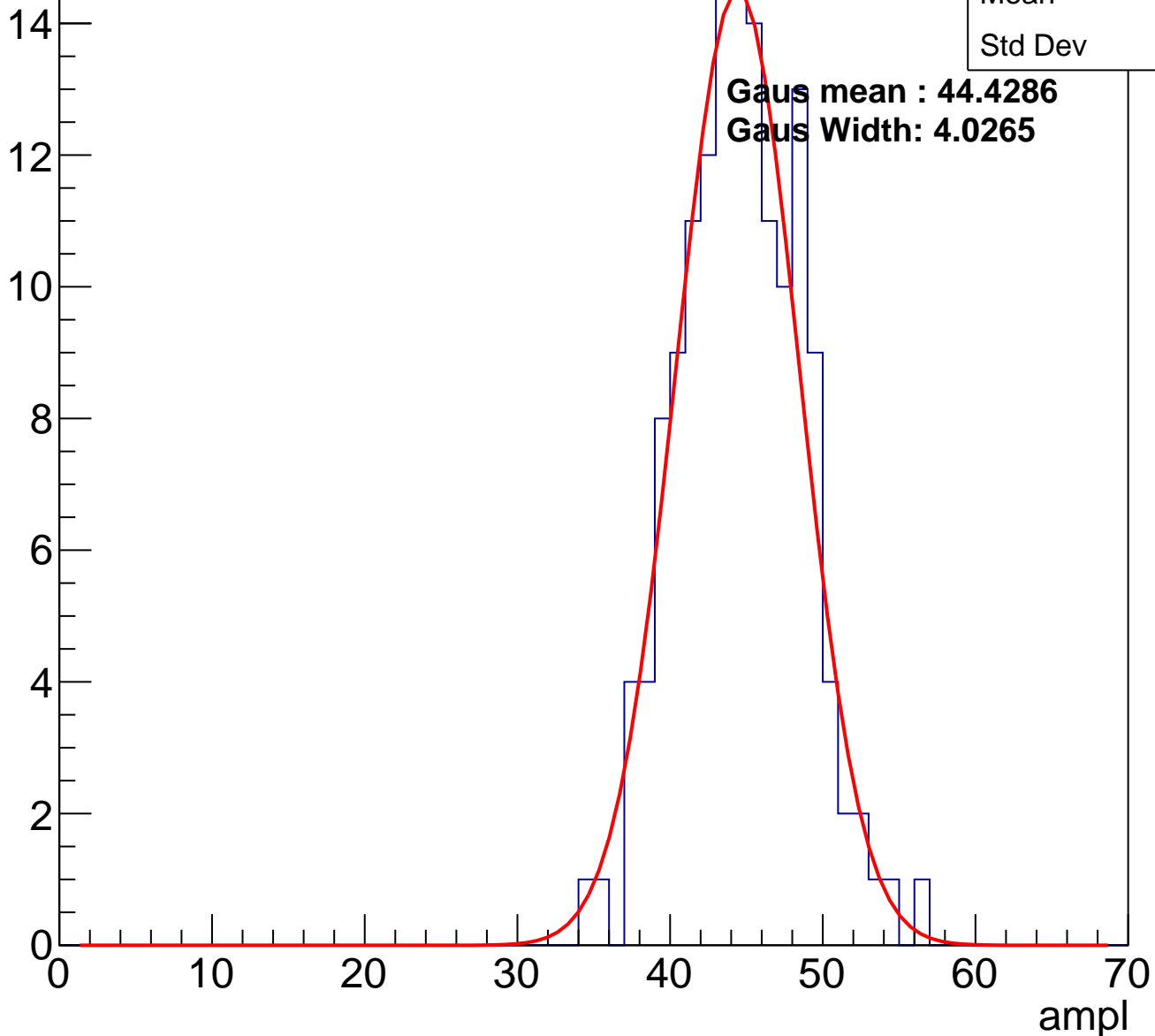
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |      |
|---------|------|
| Entries | 148  |
| Mean    | 44.2 |
| Std Dev | 3.94 |

**Gaus mean : 44.4286**

**Gaus Width: 4.0265**

Entry



# B0L101S, U16-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

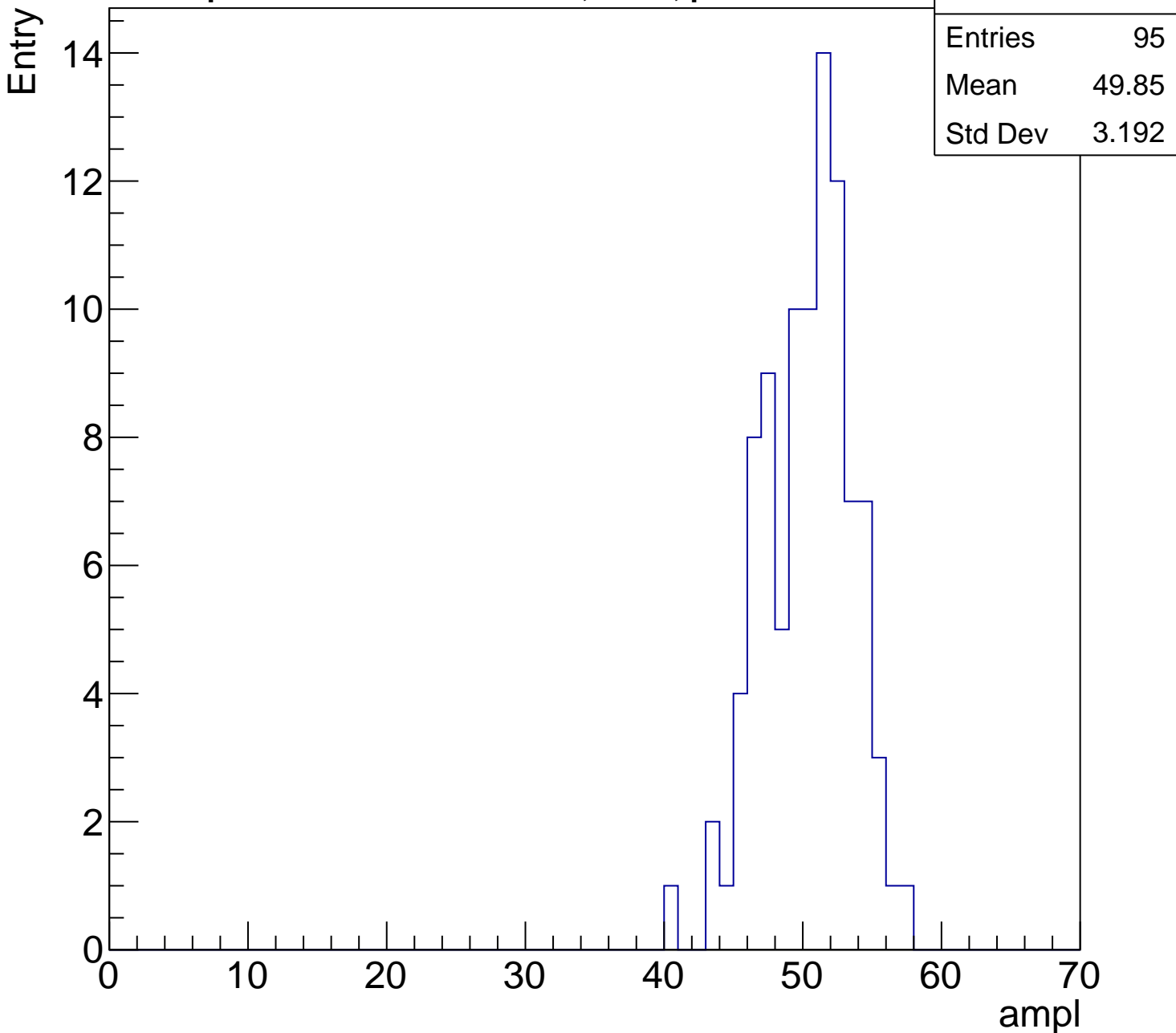
|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 49.85 |
| Std Dev | 3.192 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

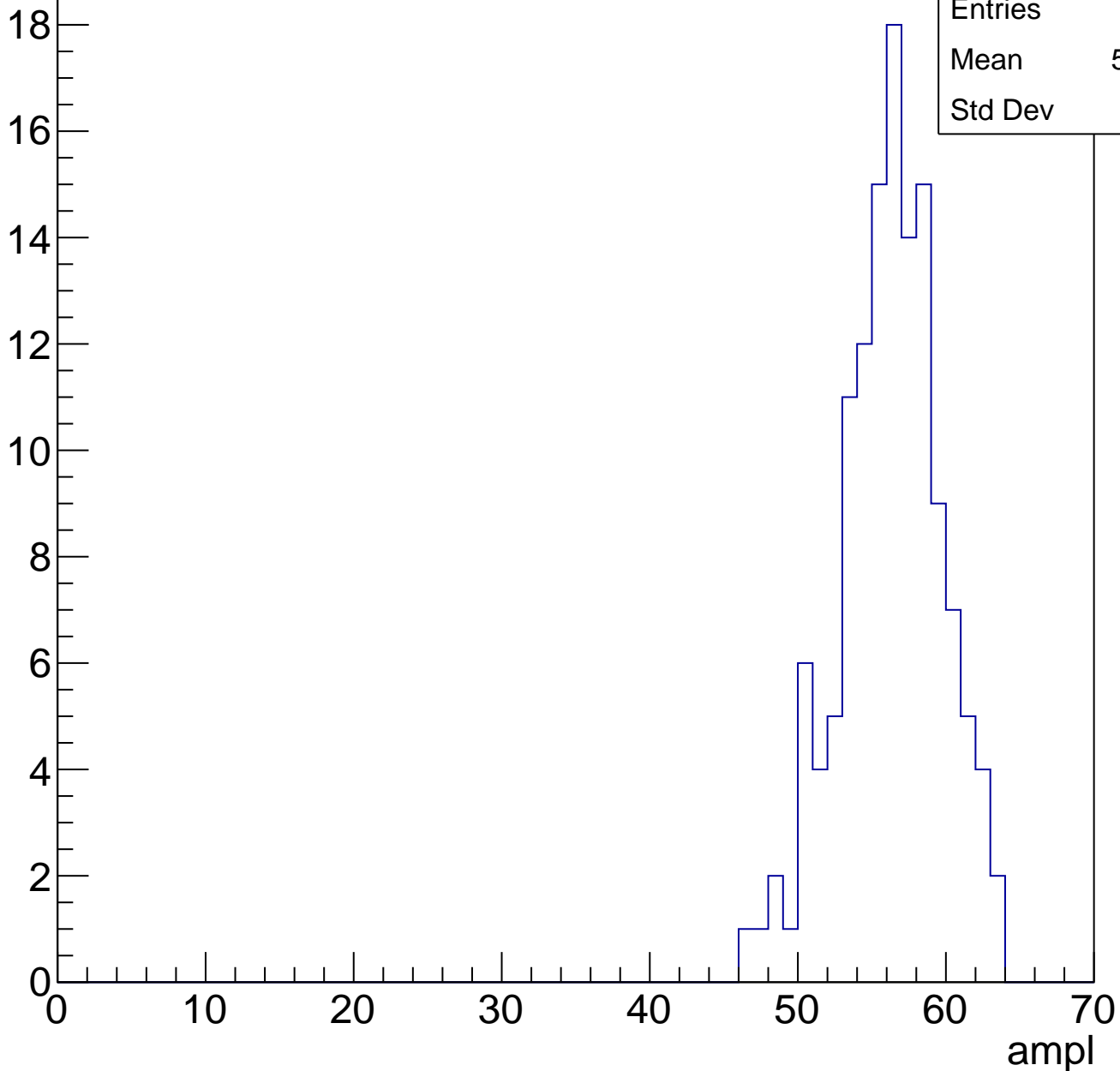
ampl



# B0L101S, U16-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

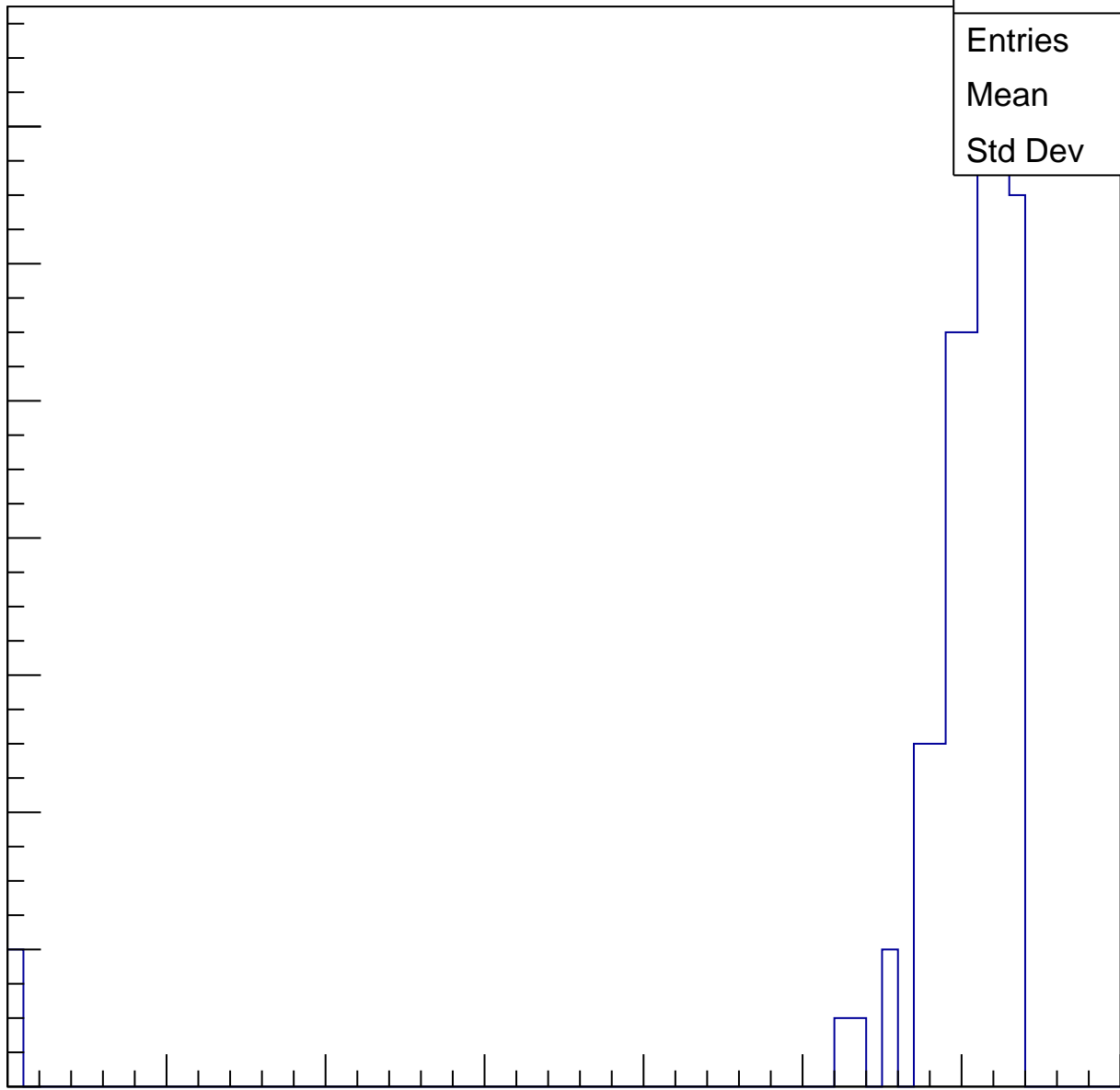
2

0

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 58.8  |
| Std Dev | 9.632 |

ampl

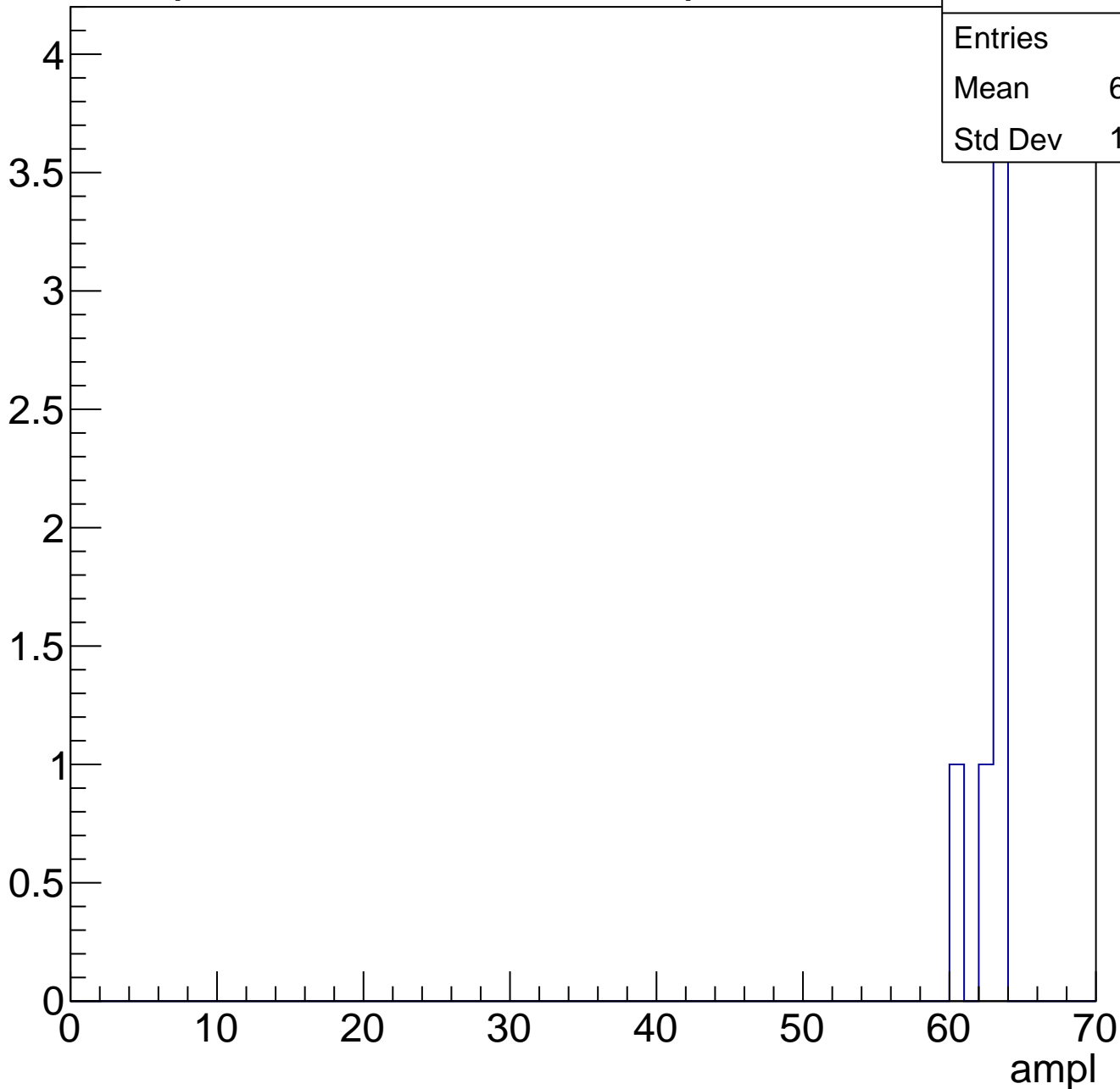
0 10 20 30 40 50 60 70



# B0L101S, U16-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

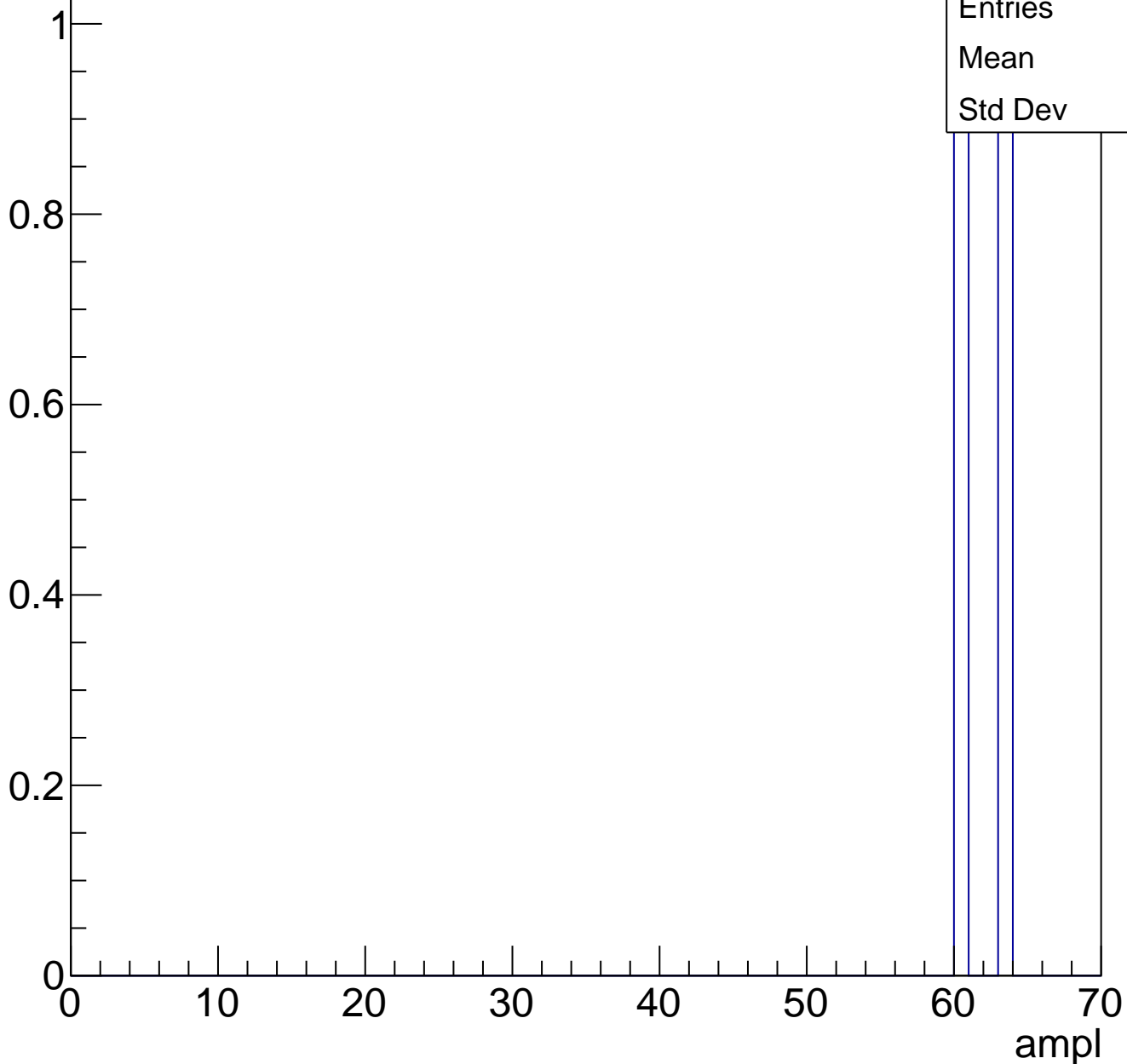




# B0L101S, U16-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch100, adc0

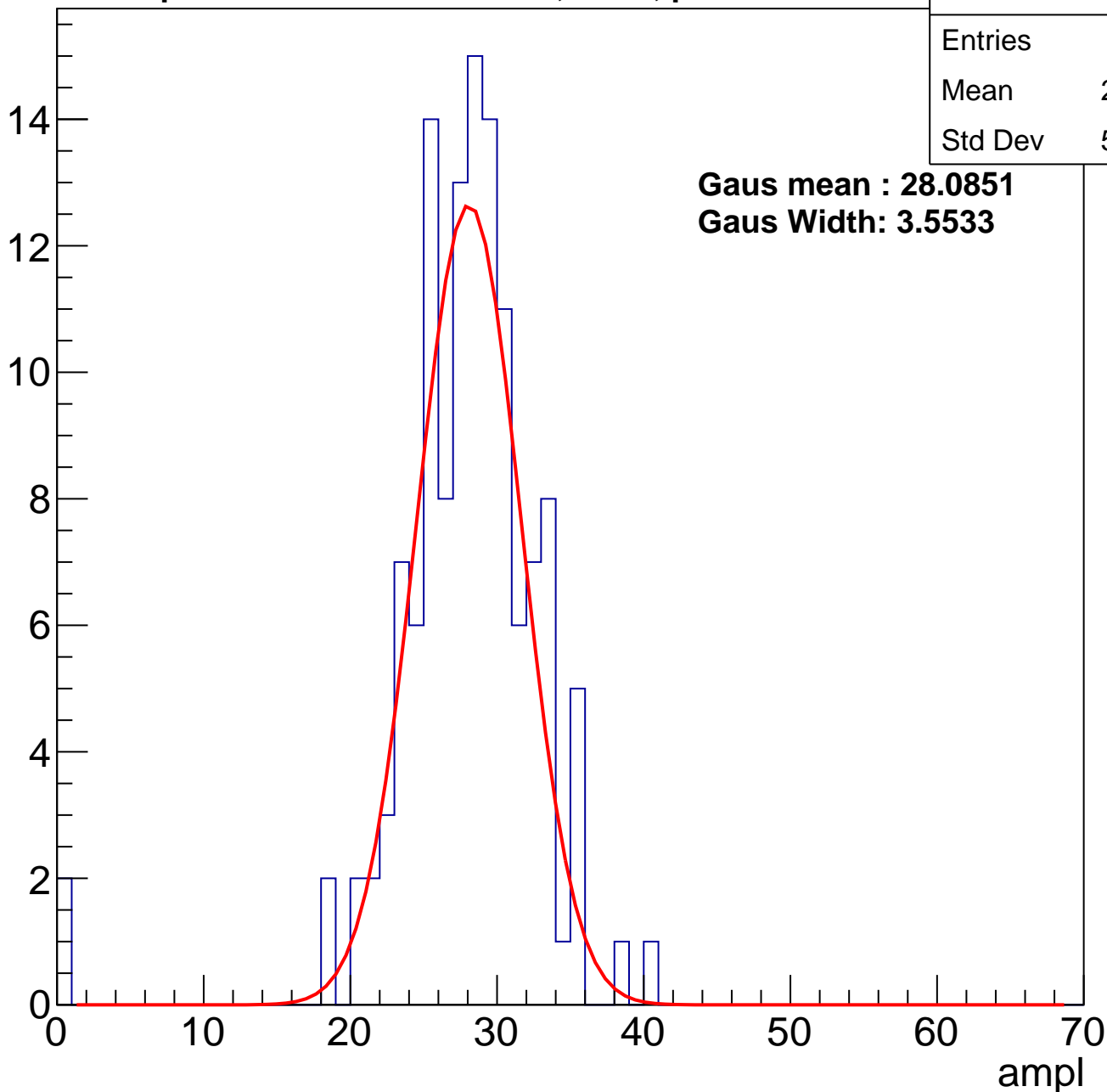
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 27.46 |
| Std Dev | 5.196 |

**Gaus mean : 28.0851**

**Gaus Width: 3.5533**

Entry



# B0L101S, U16-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 151   |
| Mean    | 34.82 |
| Std Dev | 4.768 |

**Gaus mean : 35.2628**

**Gaus Width: 3.7722**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

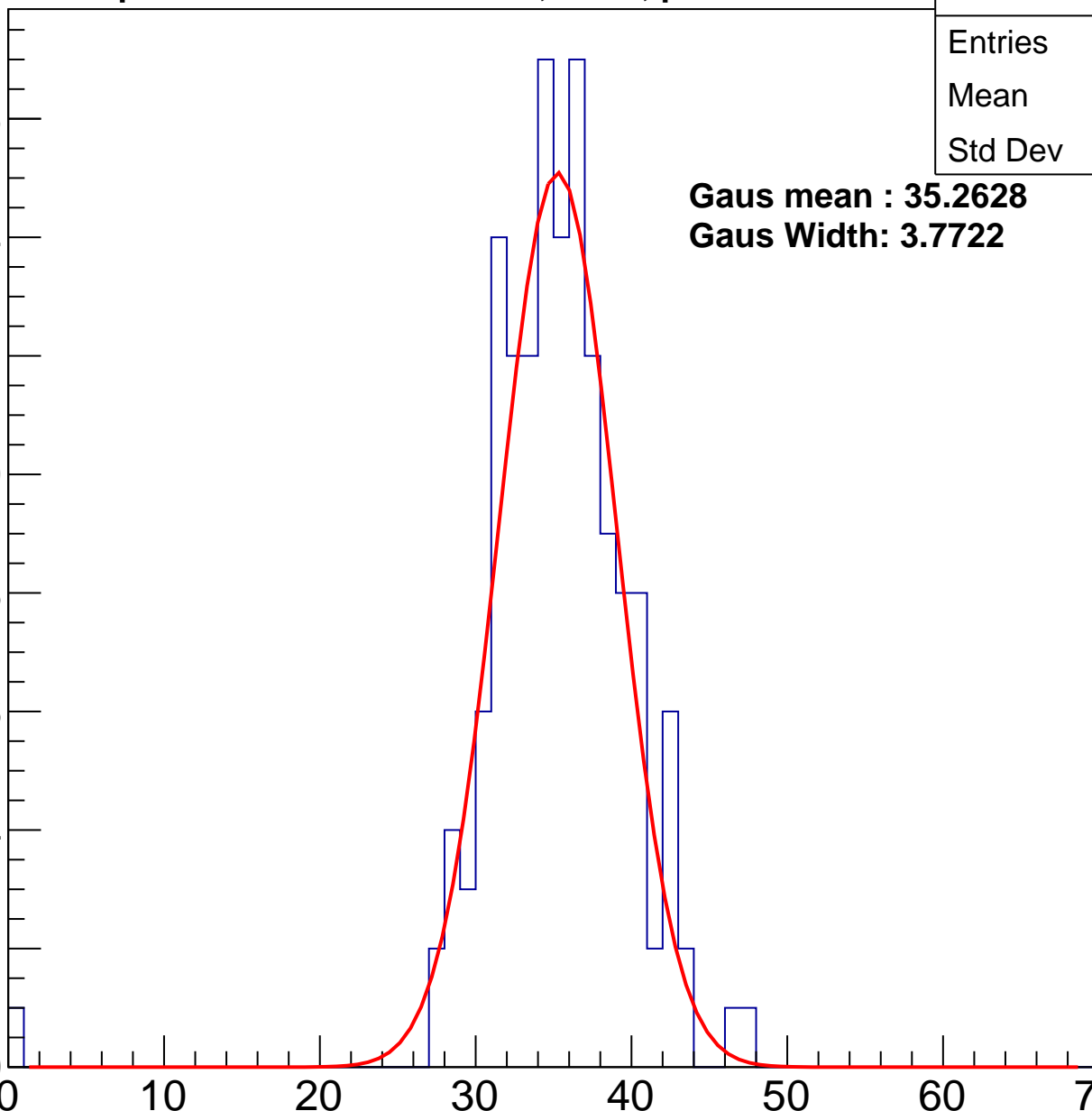
40

50

60

70

ampl



# B0L101S, U16-ch100, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

127

Mean

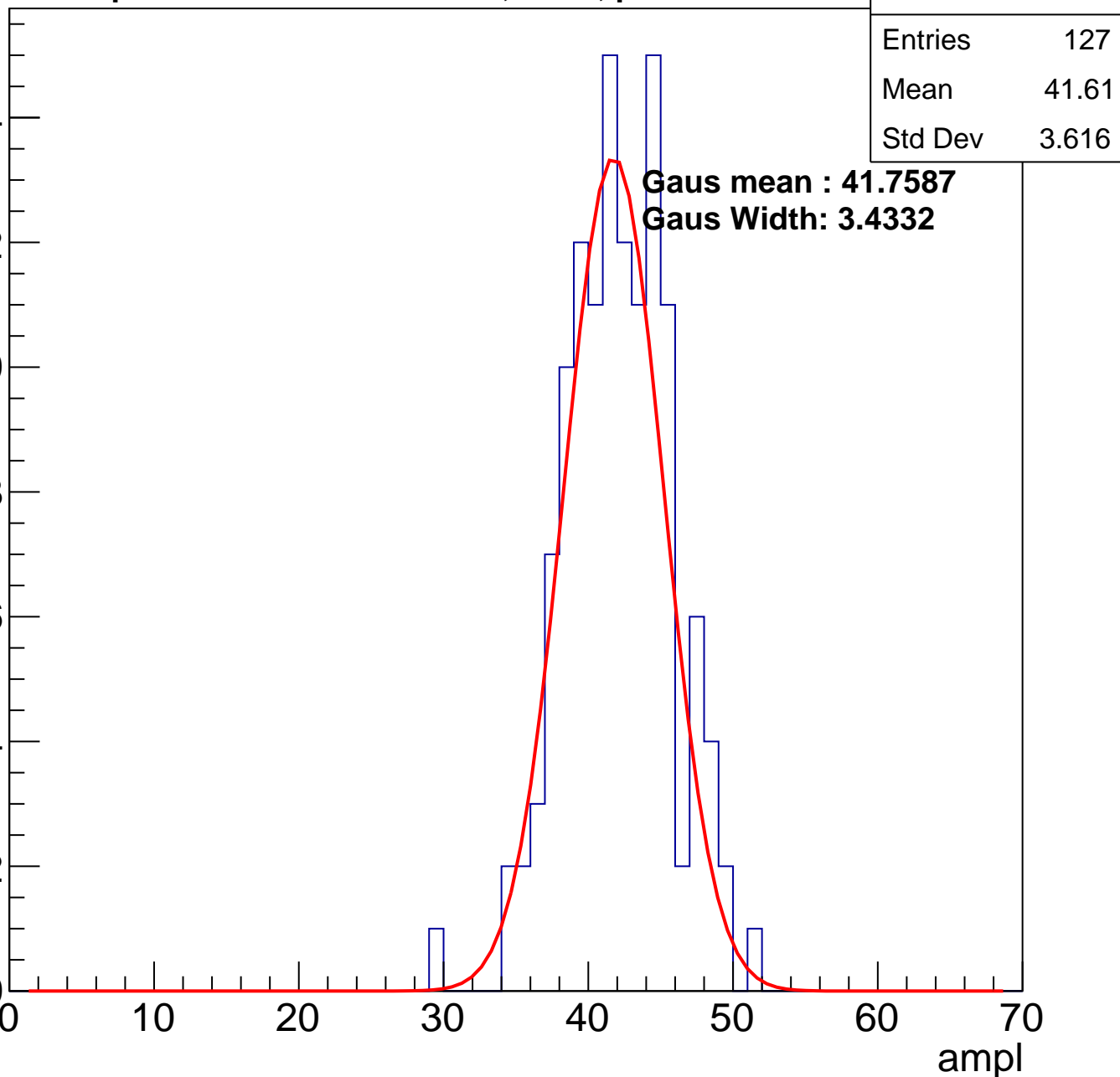
41.61

Std Dev

3.616

**Gaus mean : 41.7587**

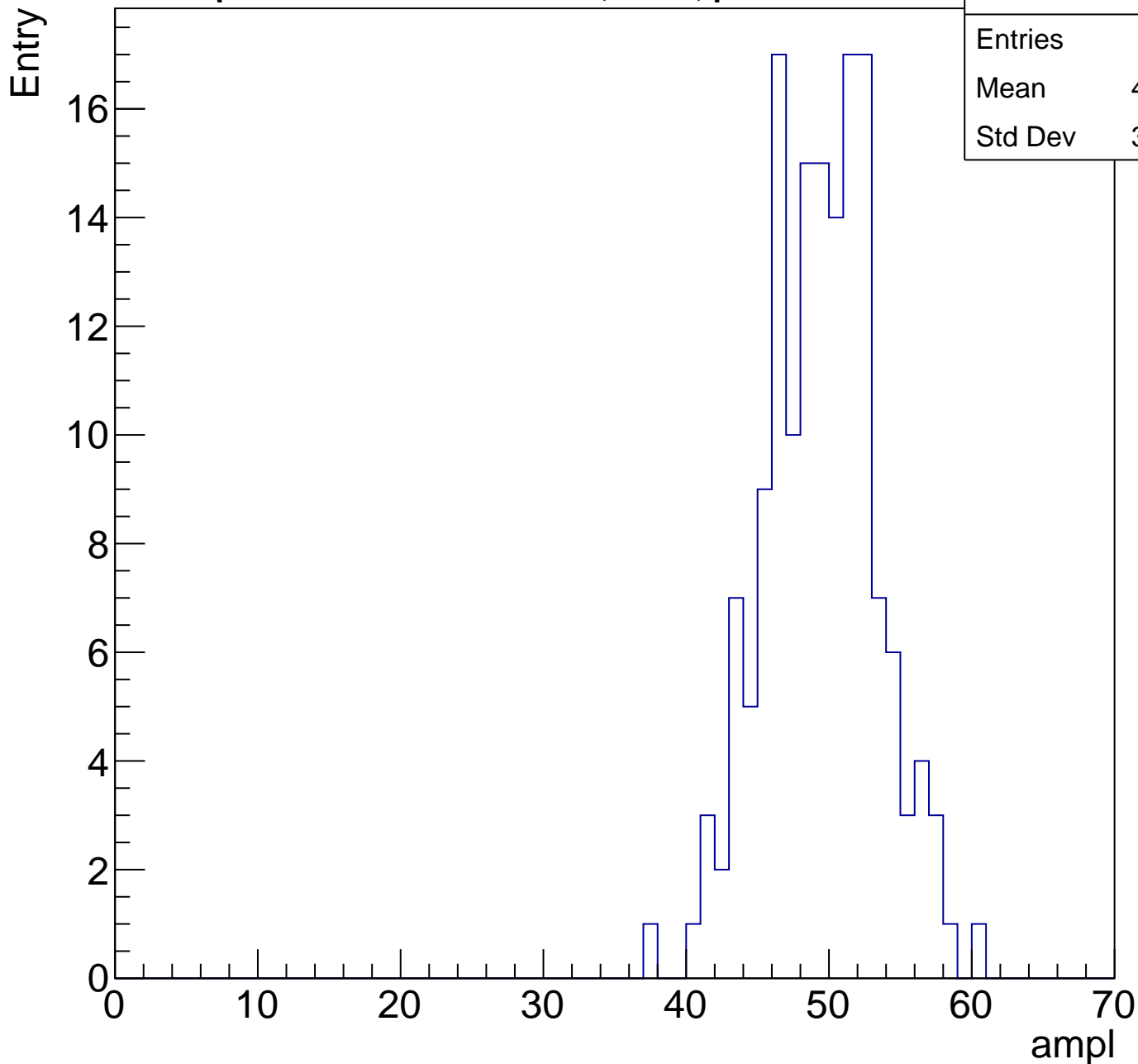
**Gaus Width: 3.4332**



# B0L101S, U16-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

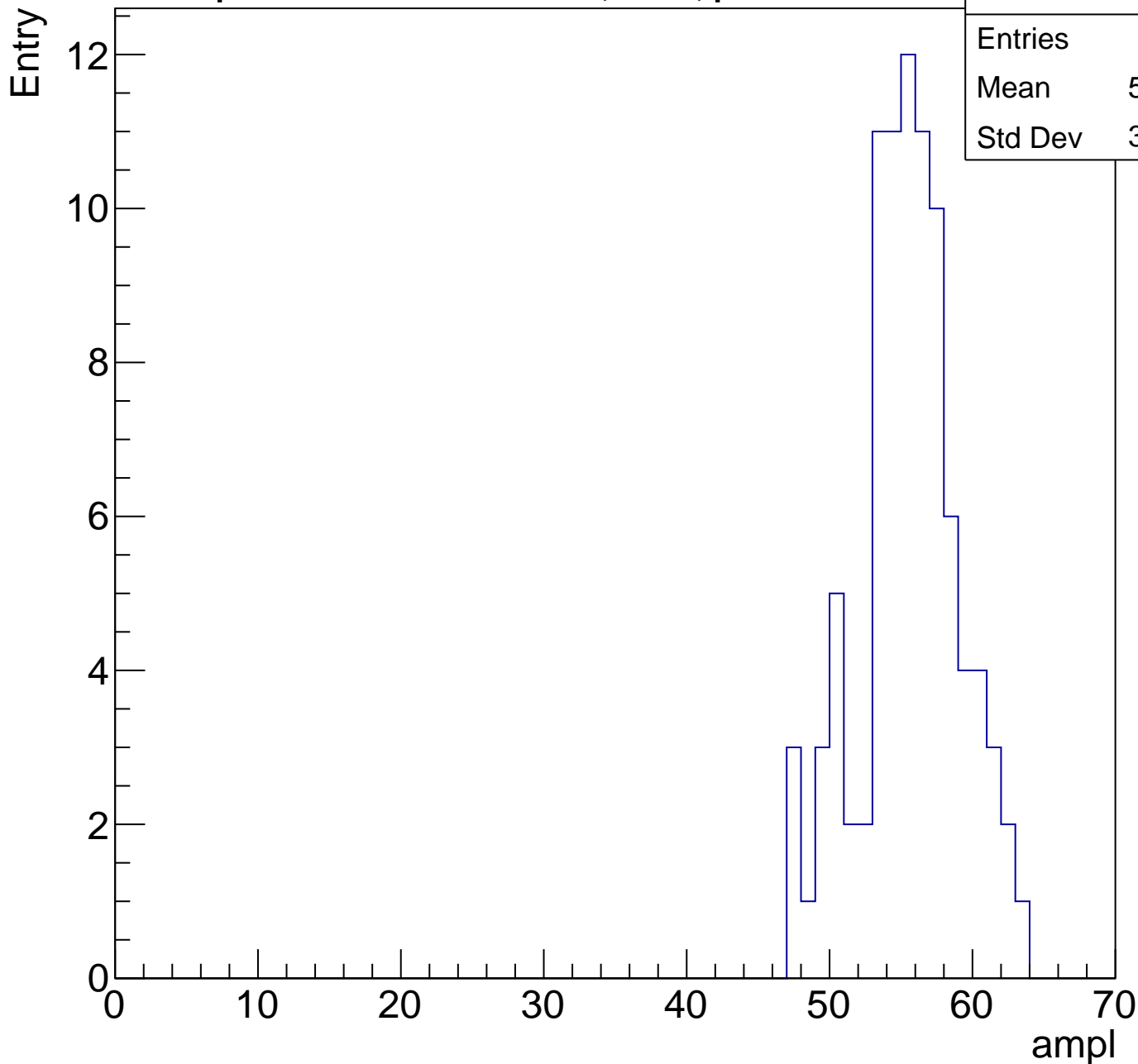
|         |       |
|---------|-------|
| Entries | 158   |
| Mean    | 48.99 |
| Std Dev | 3.929 |



# B0L101S, U16-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 91    |
| Mean    | 55.04 |
| Std Dev | 3.505 |



# B0L101S, U16-ch100, adc5

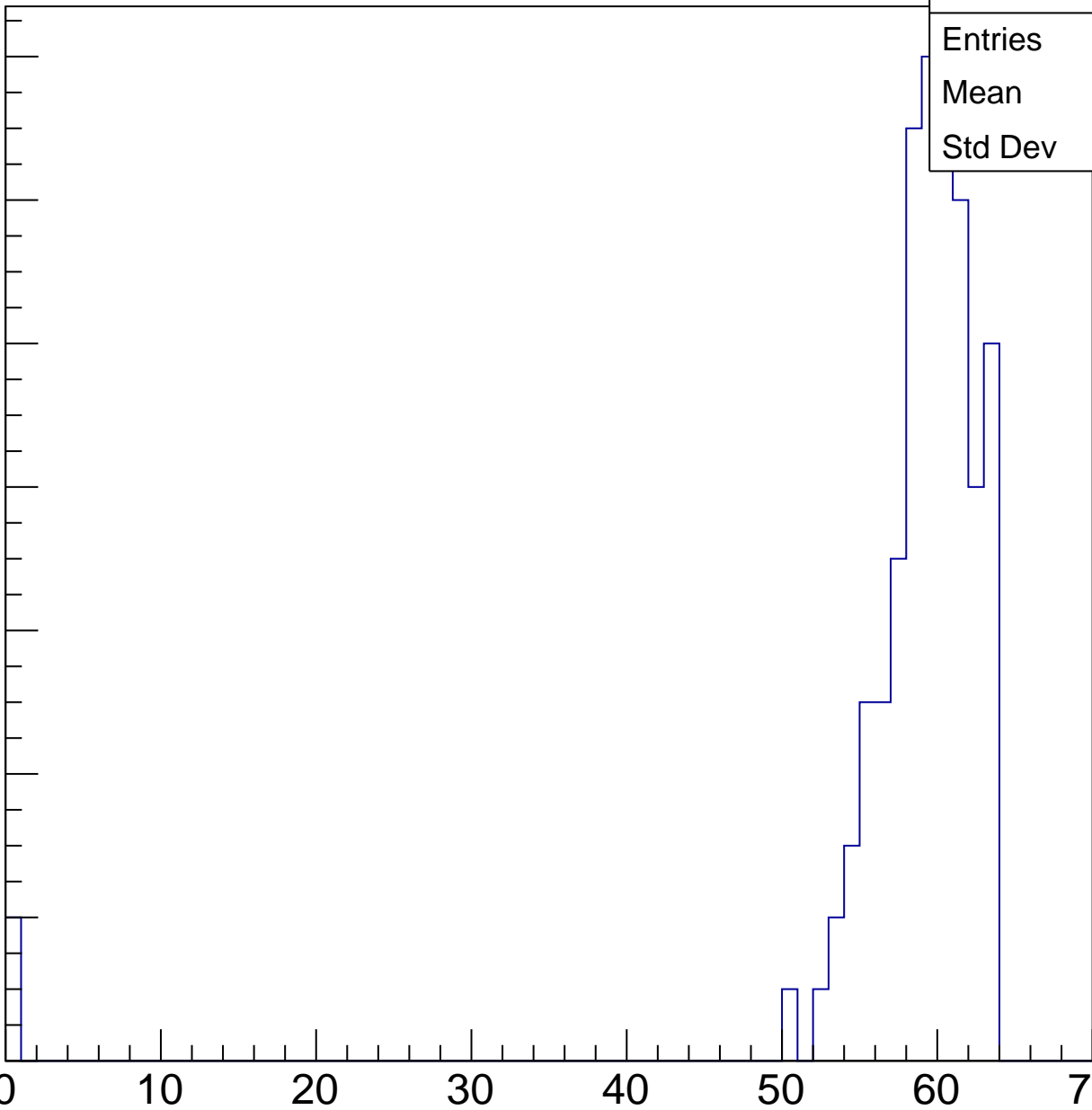
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 97    |
| Mean    | 57.75 |
| Std Dev | 8.821 |

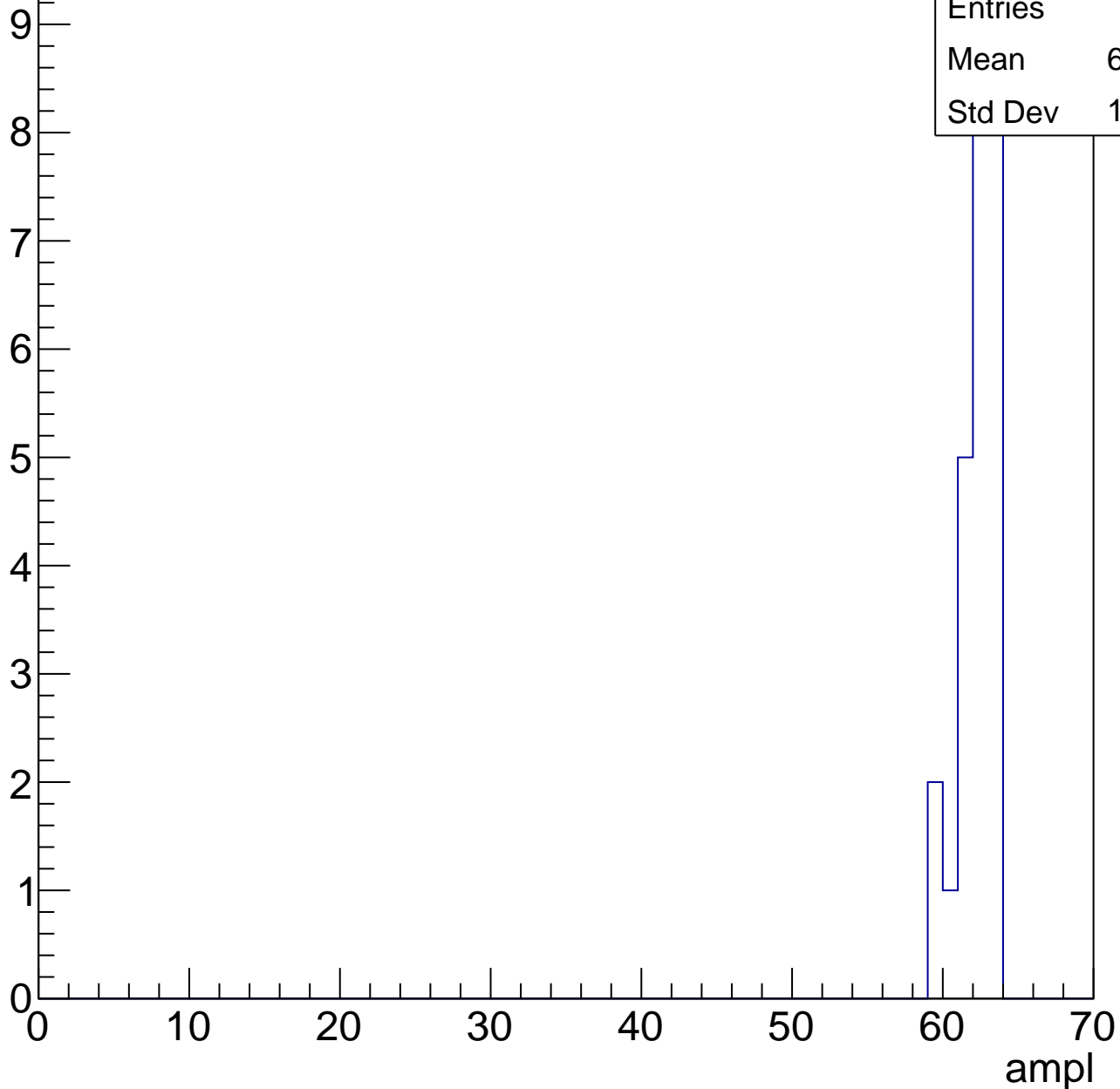
ampl



# B0L101S, U16-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 28.23 |
| Std Dev | 3.22  |

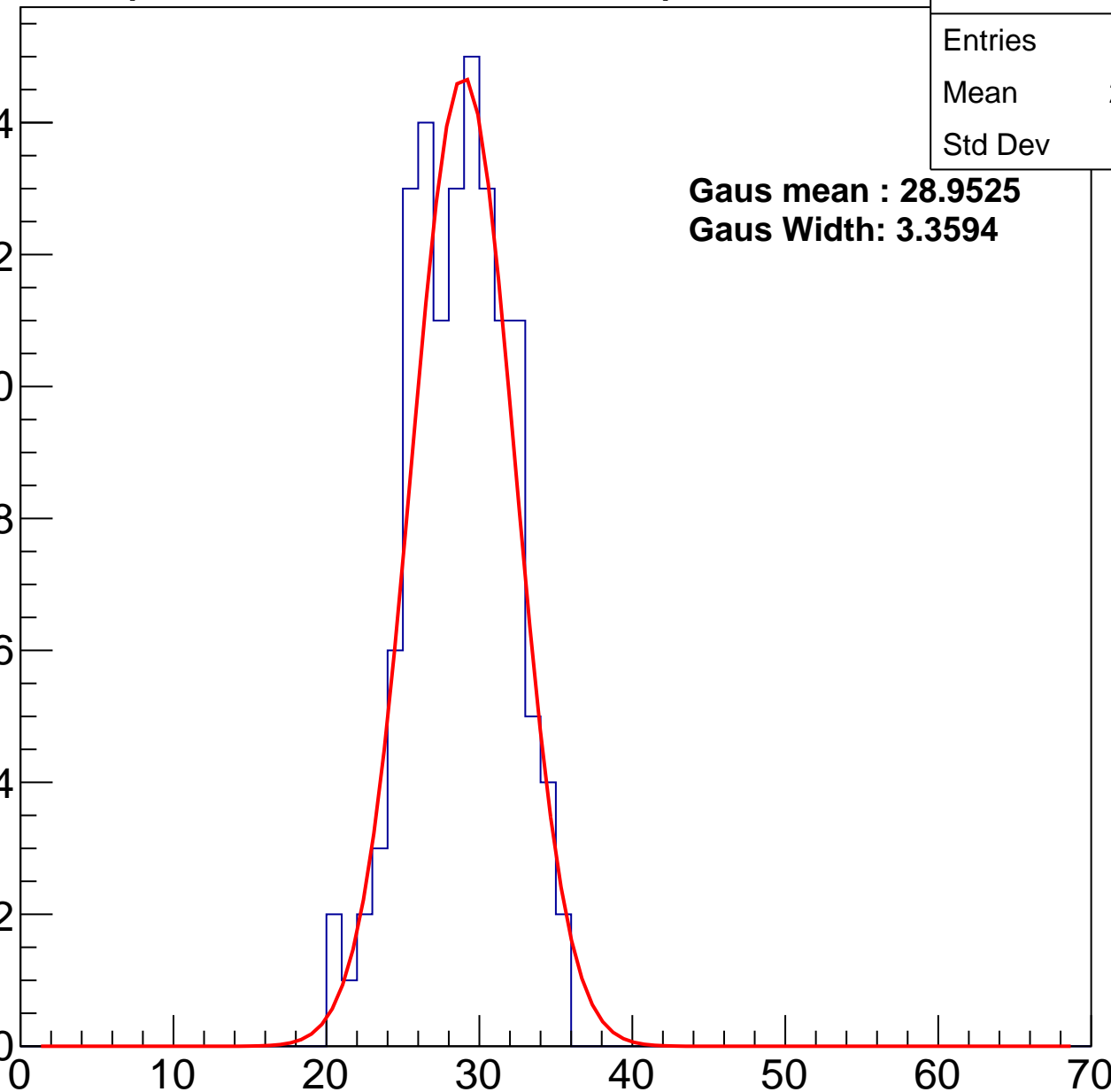
**Gaus mean : 28.9525**

**Gaus Width: 3.3594**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



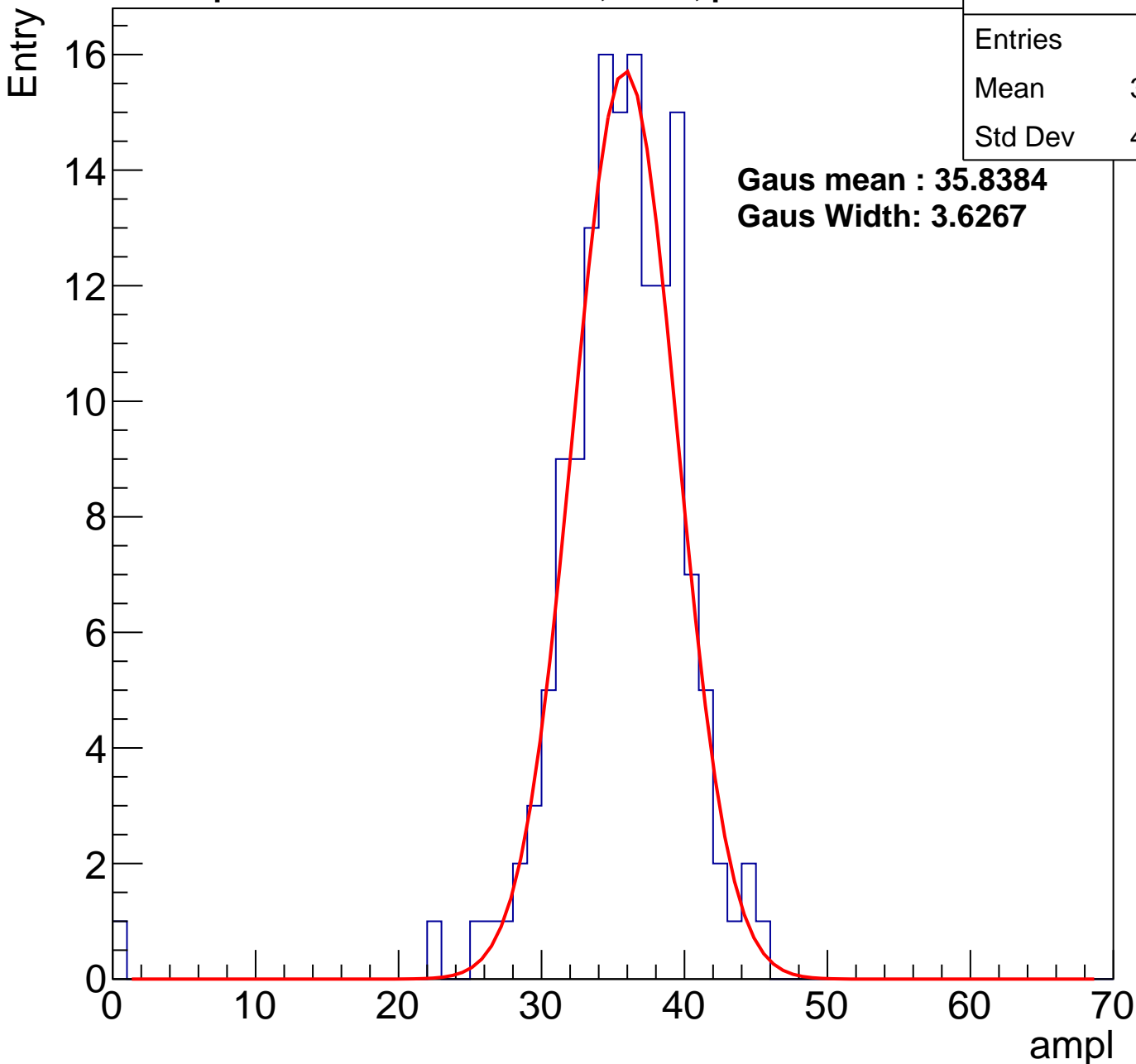
# B0L101S, U16-ch101, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 150   |
| Mean    | 35.09 |
| Std Dev | 4.798 |

**Gaus mean : 35.8384**

**Gaus Width: 3.6267**



# B0L101S, U16-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

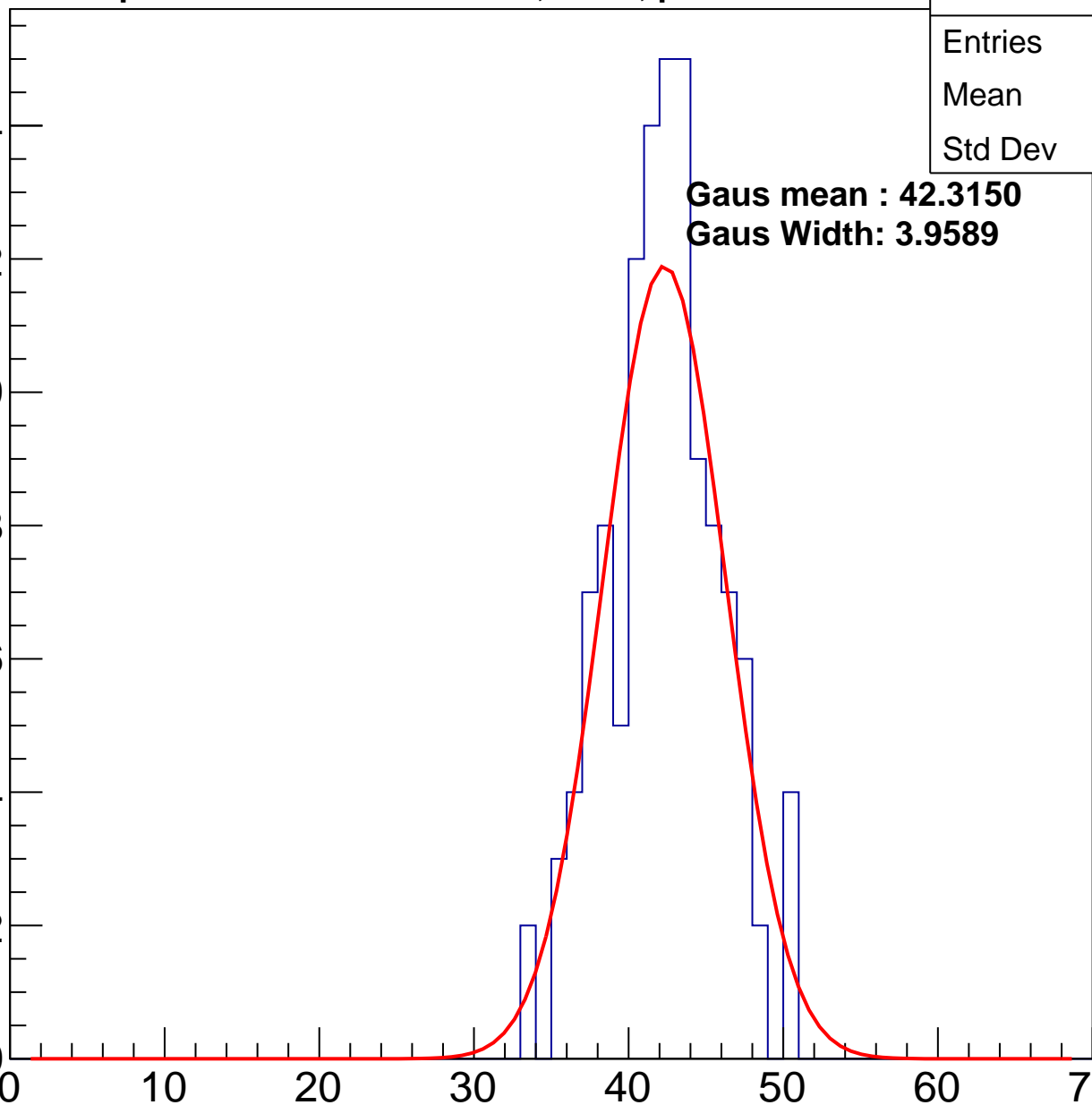
70

ampl

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 41.8  |
| Std Dev | 3.604 |

**Gaus mean : 42.3150**

**Gaus Width: 3.9589**



# B0L101S, U16-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 152   |
| Mean    | 48.85 |
| Std Dev | 3.795 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

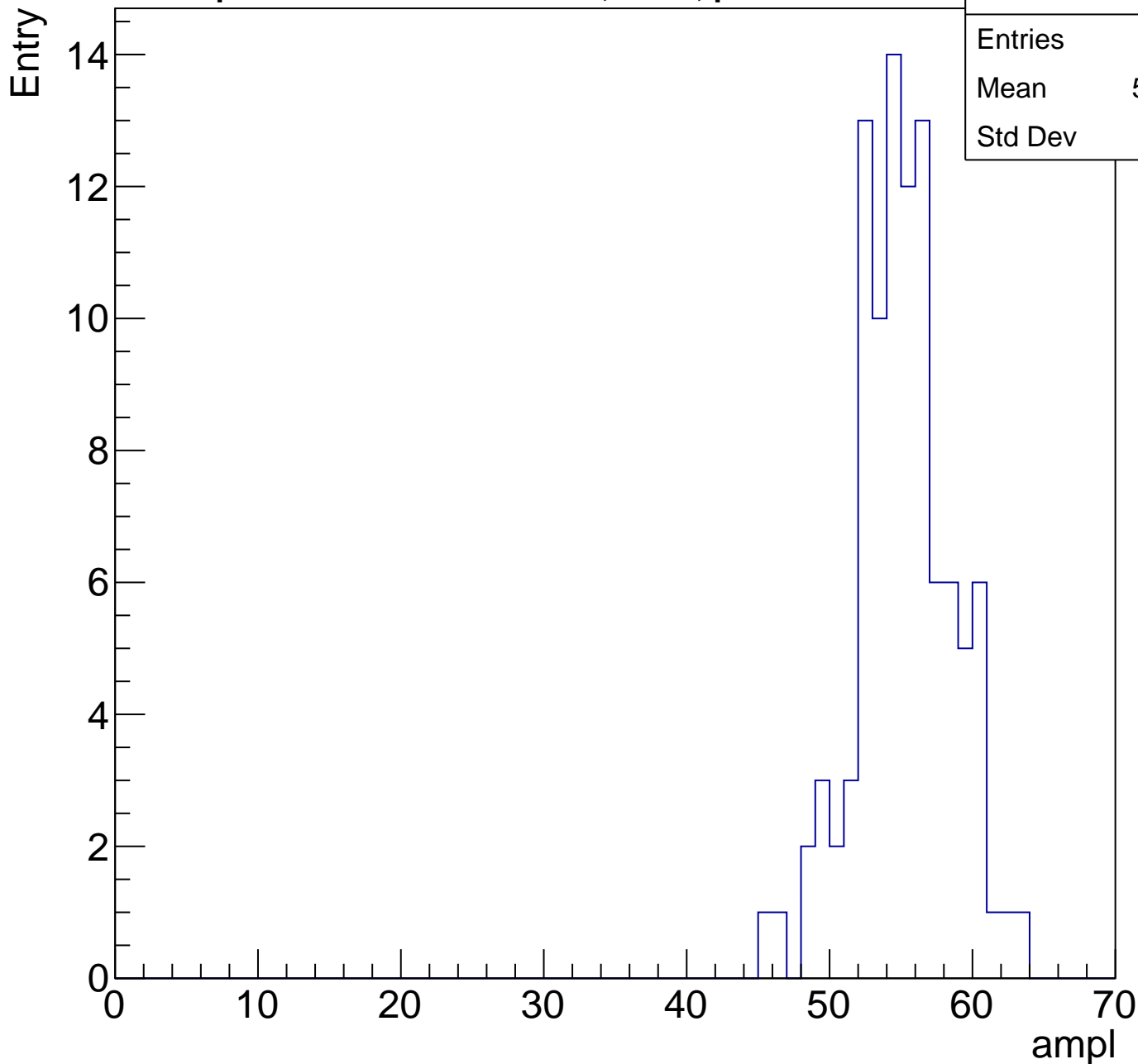
ampl

70

# B0L101S, U16-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 100   |
| Mean    | 54.68 |
| Std Dev | 3.34  |



# B0L101S, U16-ch101, adc5

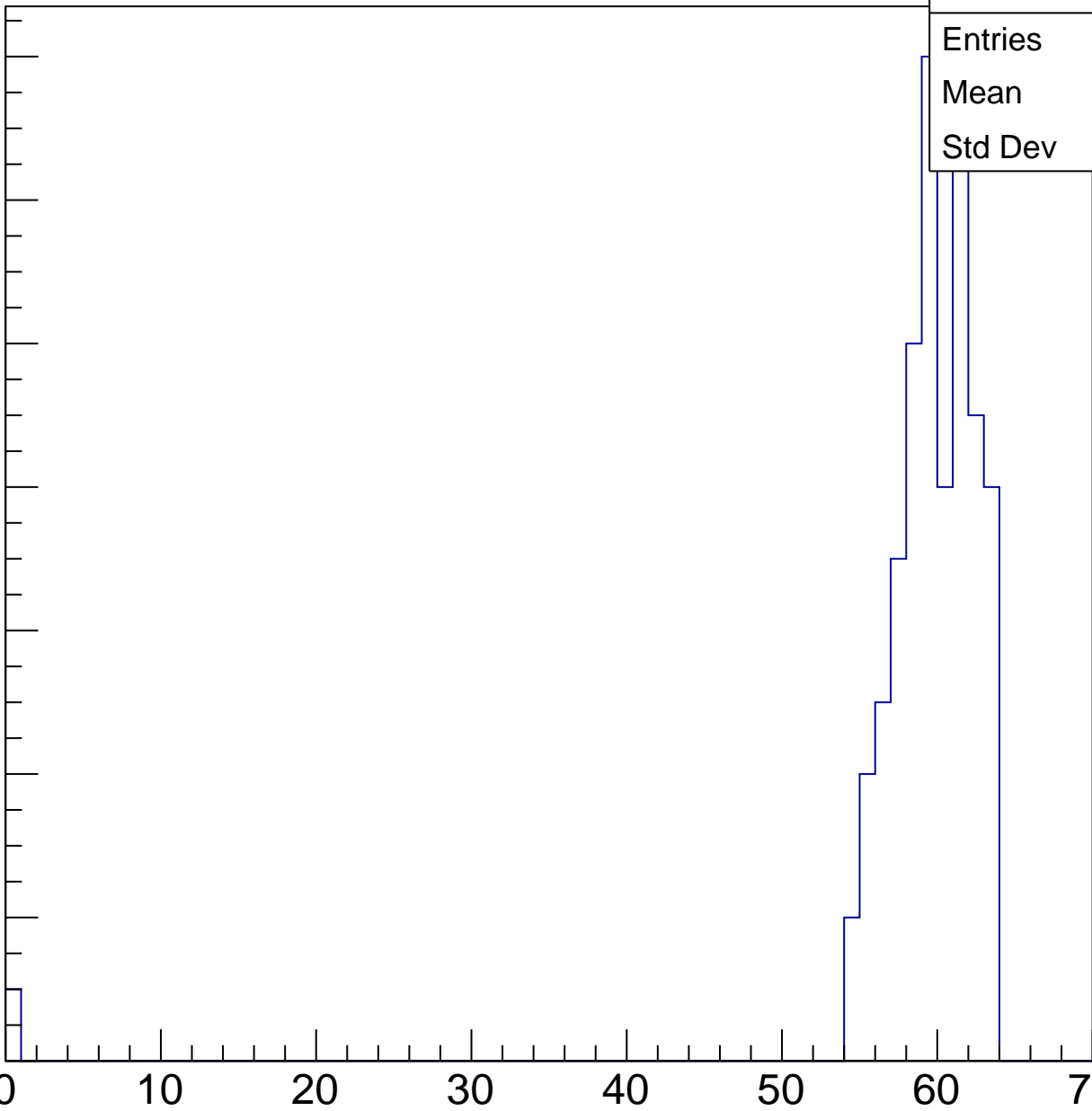
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 58.65 |
| Std Dev | 6.932 |

ampl



# B0L101S, U16-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 59.62 |
| Std Dev | 9.831 |

Entry

10

8

6

4

2

0

0

10

20

30

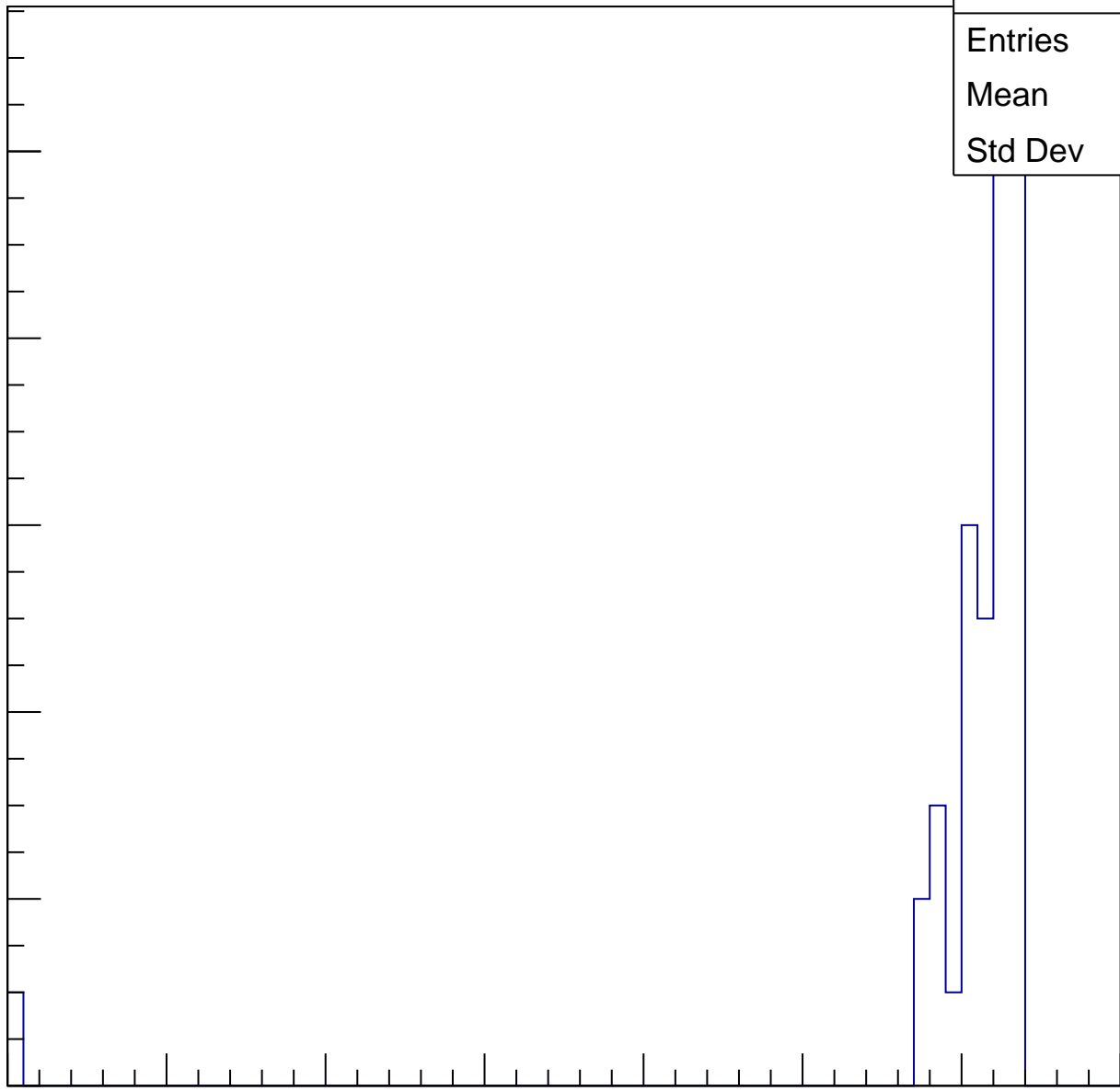
40

50

60

70

ampl

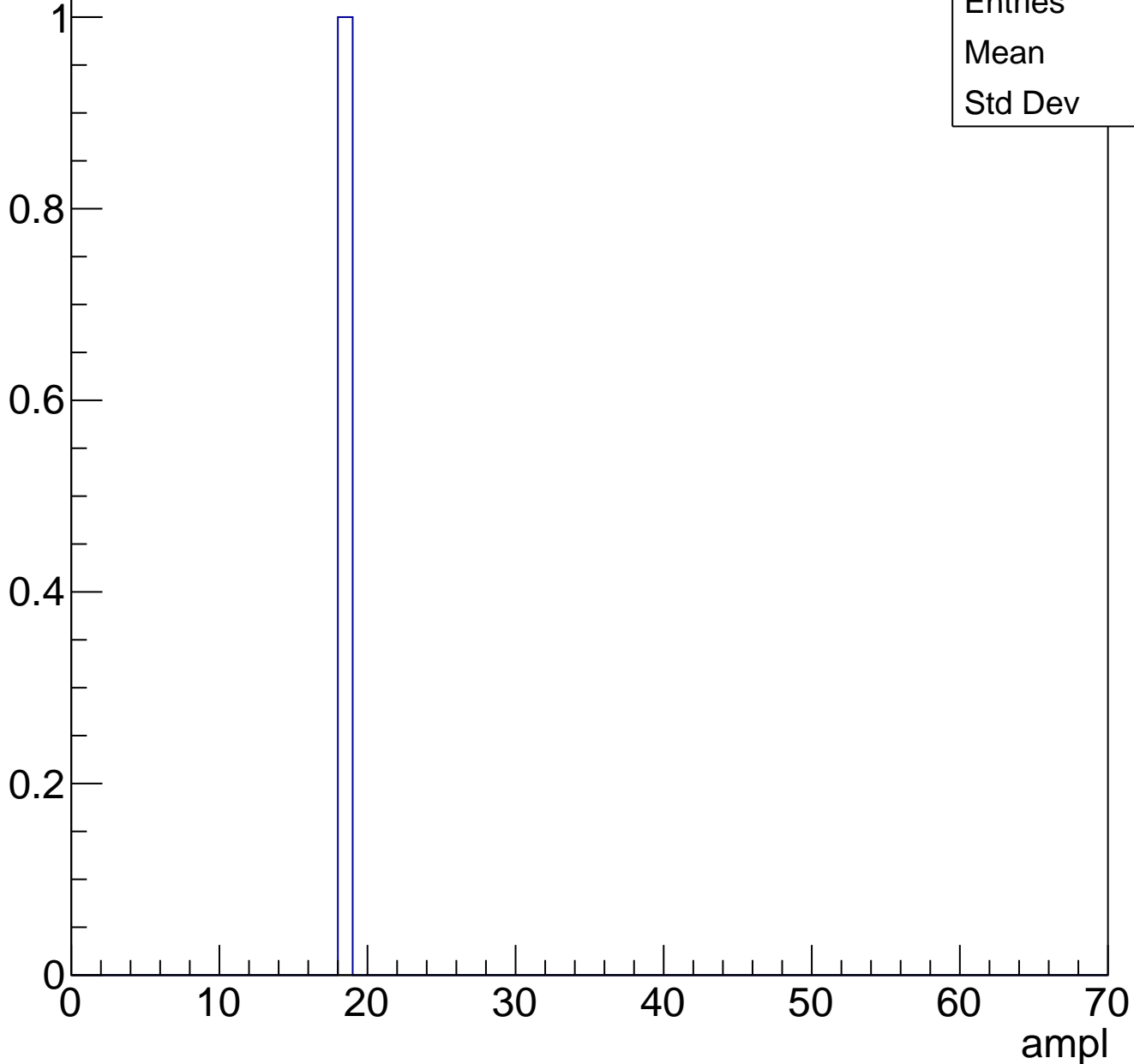




# B0L101S, U16-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



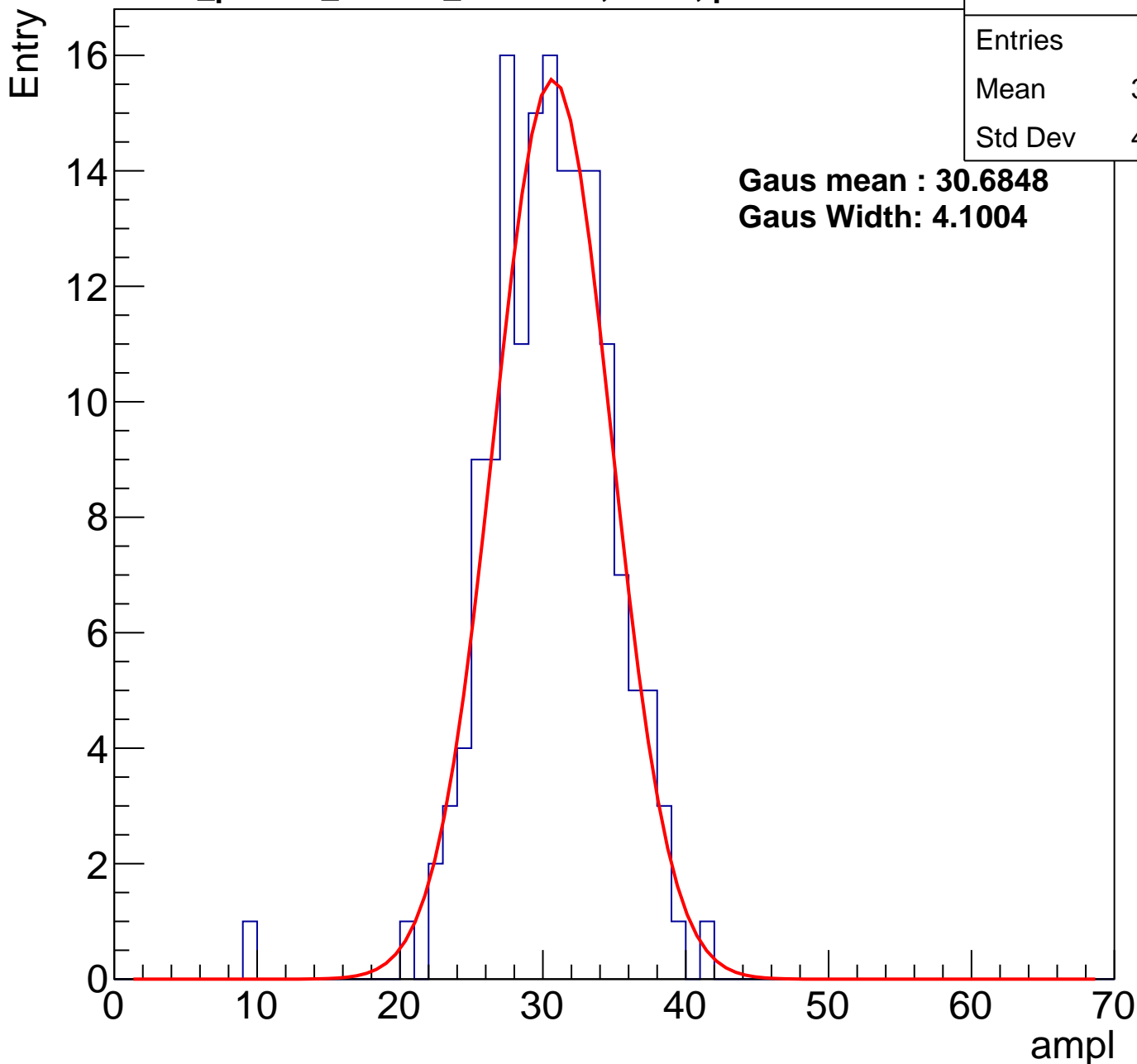
# B0L101S, U16-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 162   |
| Mean    | 30.09 |
| Std Dev | 4.229 |

**Gaus mean : 30.6848**

**Gaus Width: 4.1004**



# B0L101S, U16-ch102, adc1

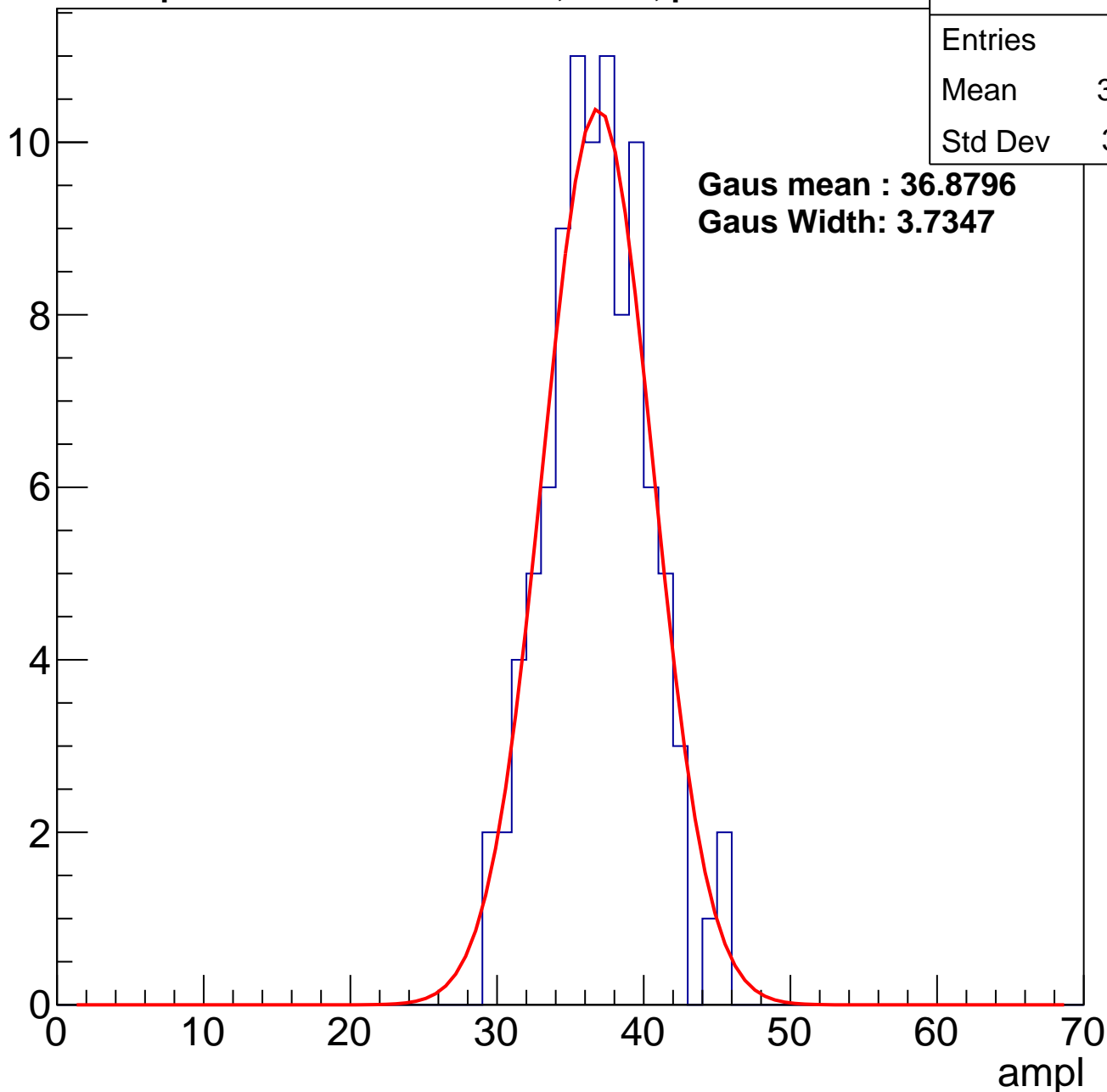
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 36.39 |
| Std Dev | 3.441 |

**Gaus mean : 36.8796**

**Gaus Width: 3.7347**

Entry



# B0L101S, U16-ch102, adc2

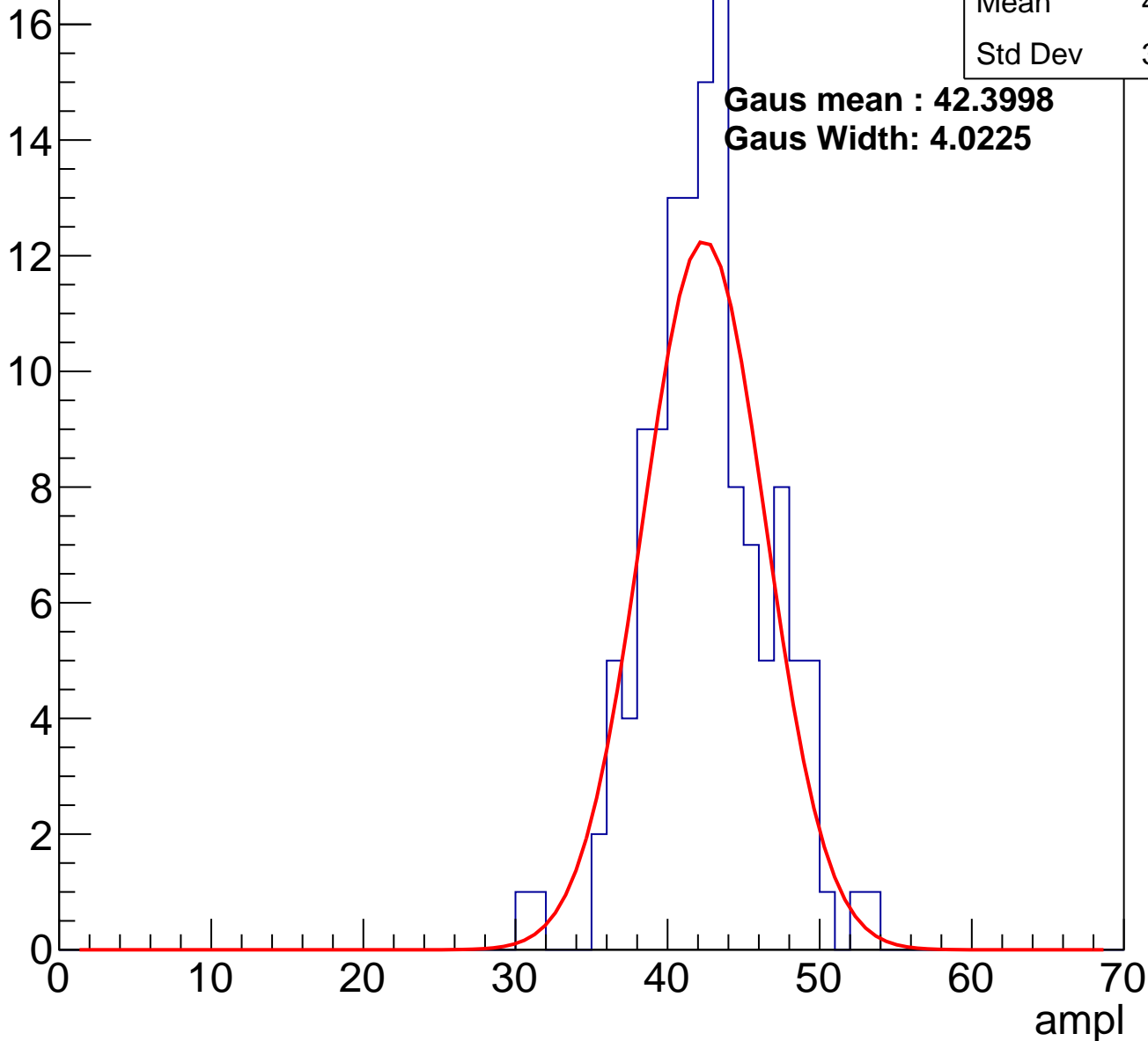
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 130   |
| Mean    | 42.15 |
| Std Dev | 3.954 |

**Gaus mean : 42.3998**

**Gaus Width: 4.0225**



# B0L101S, U16-ch102, adc3

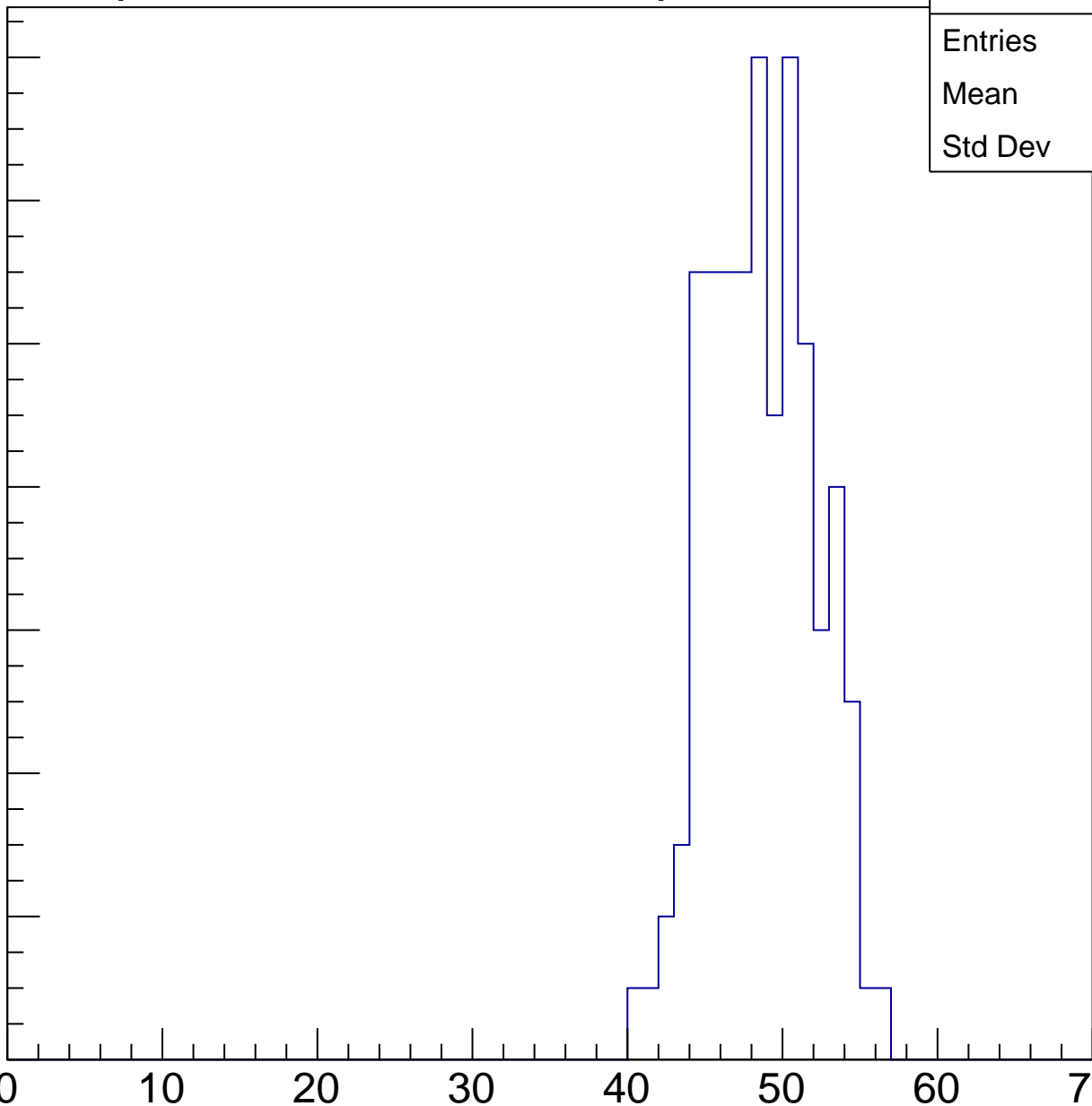
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 48.2  |
| Std Dev | 3.342 |

ampl



# B0L101S, U16-ch102, adc4

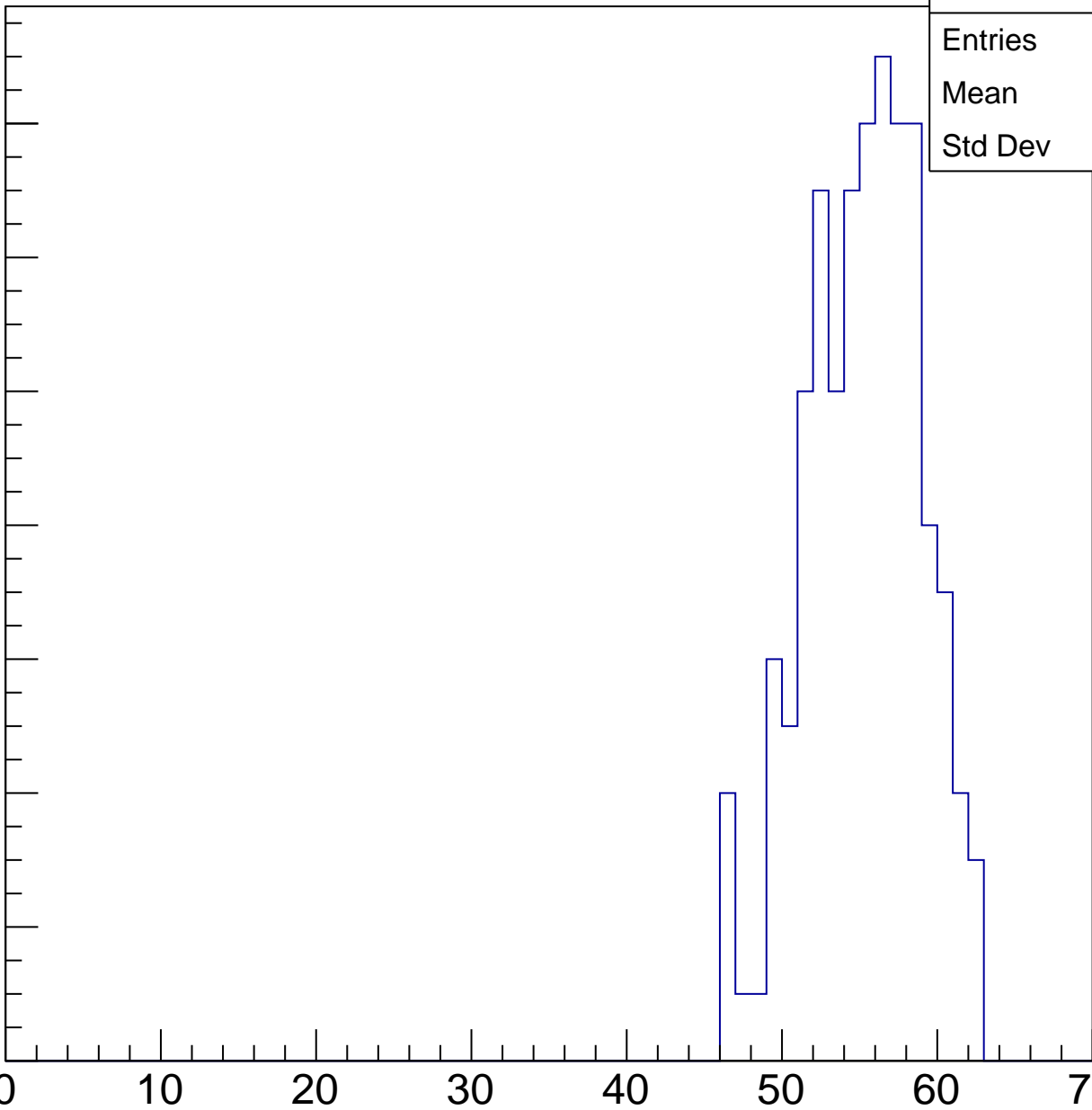
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 54.81 |
| Std Dev | 3.652 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch102, adc5

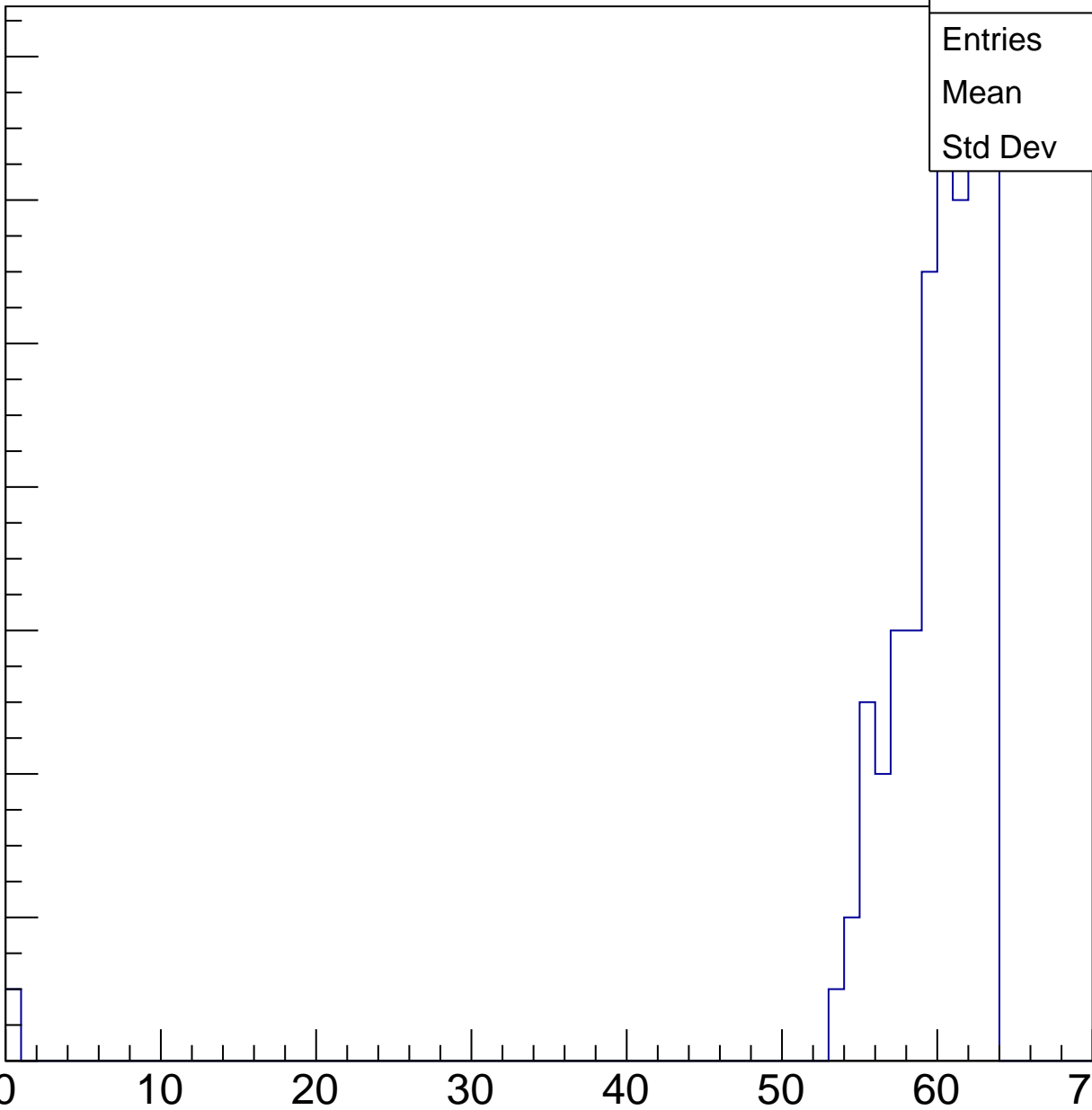
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 59.08 |
| Std Dev | 6.838 |

ampl

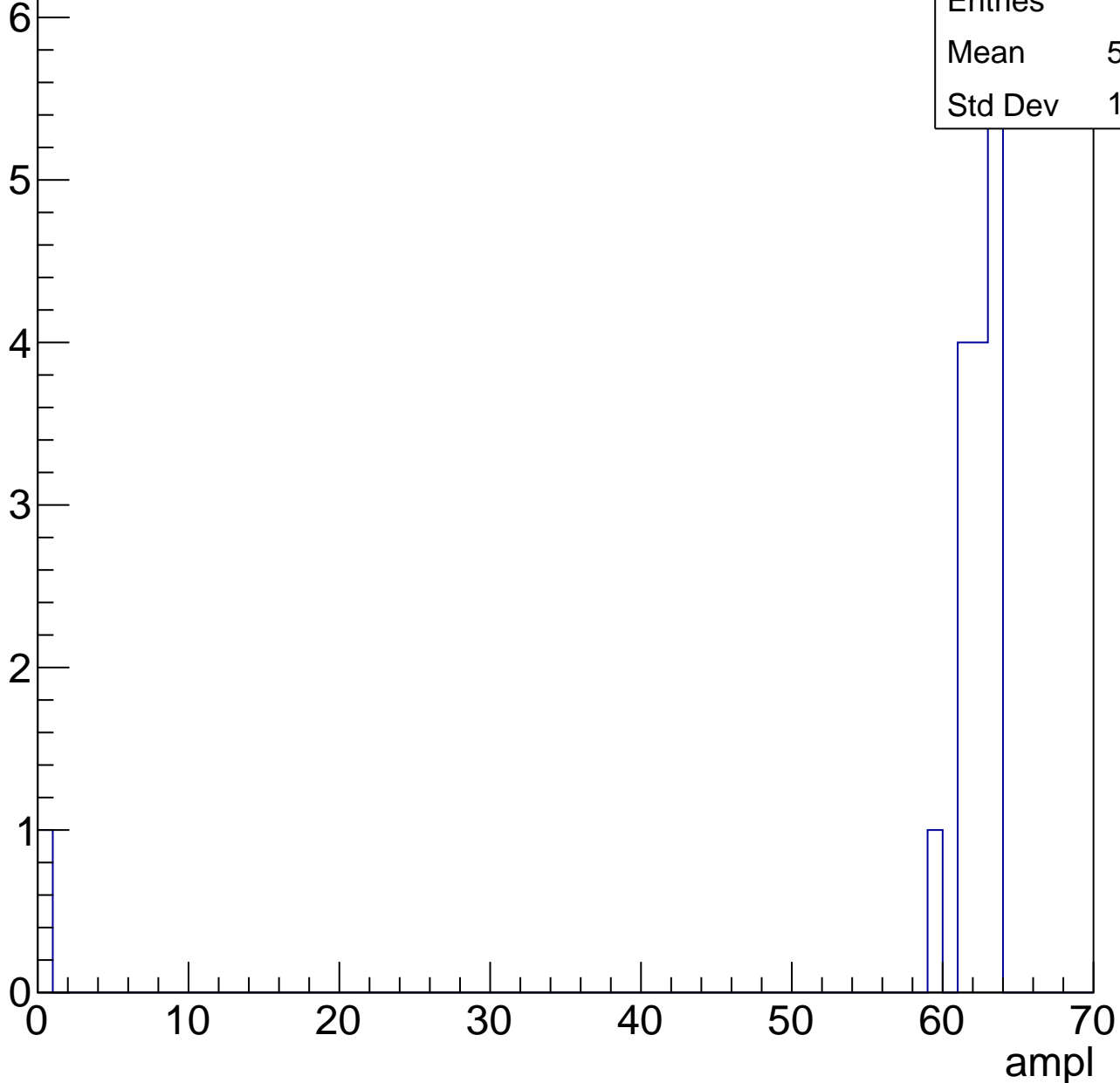


# B0L101S, U16-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 16    |
| Mean    | 58.06 |
| Std Dev | 15.03 |





# B0L101S, U16-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch103, adc0

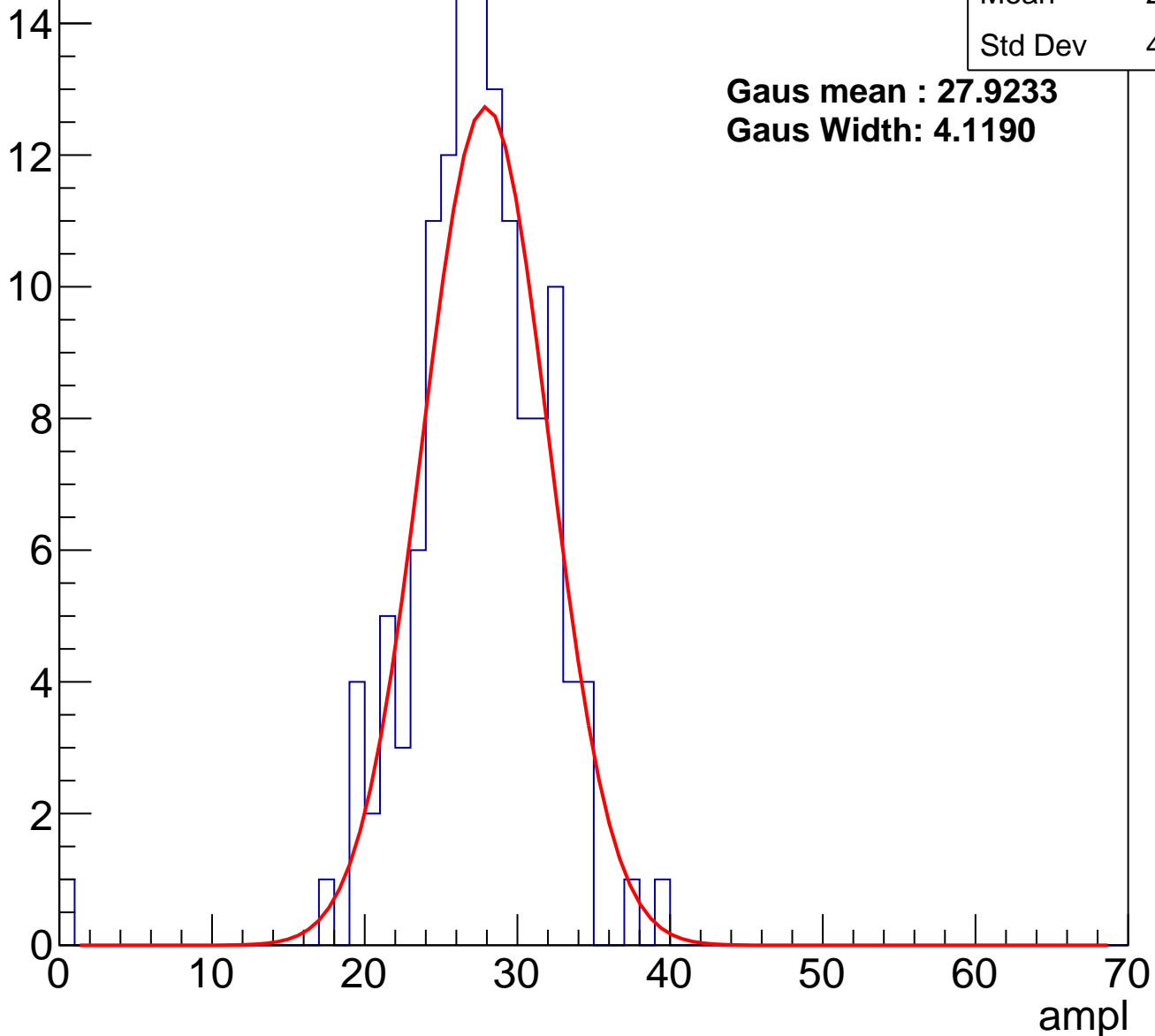
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 26.93 |
| Std Dev | 4.543 |

**Gaus mean : 27.9233**

**Gaus Width: 4.1190**

Entry



# B0L101S, U16-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 34.44 |
| Std Dev | 3.813 |

**Gaus mean : 35.1521**

**Gaus Width: 3.5659**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

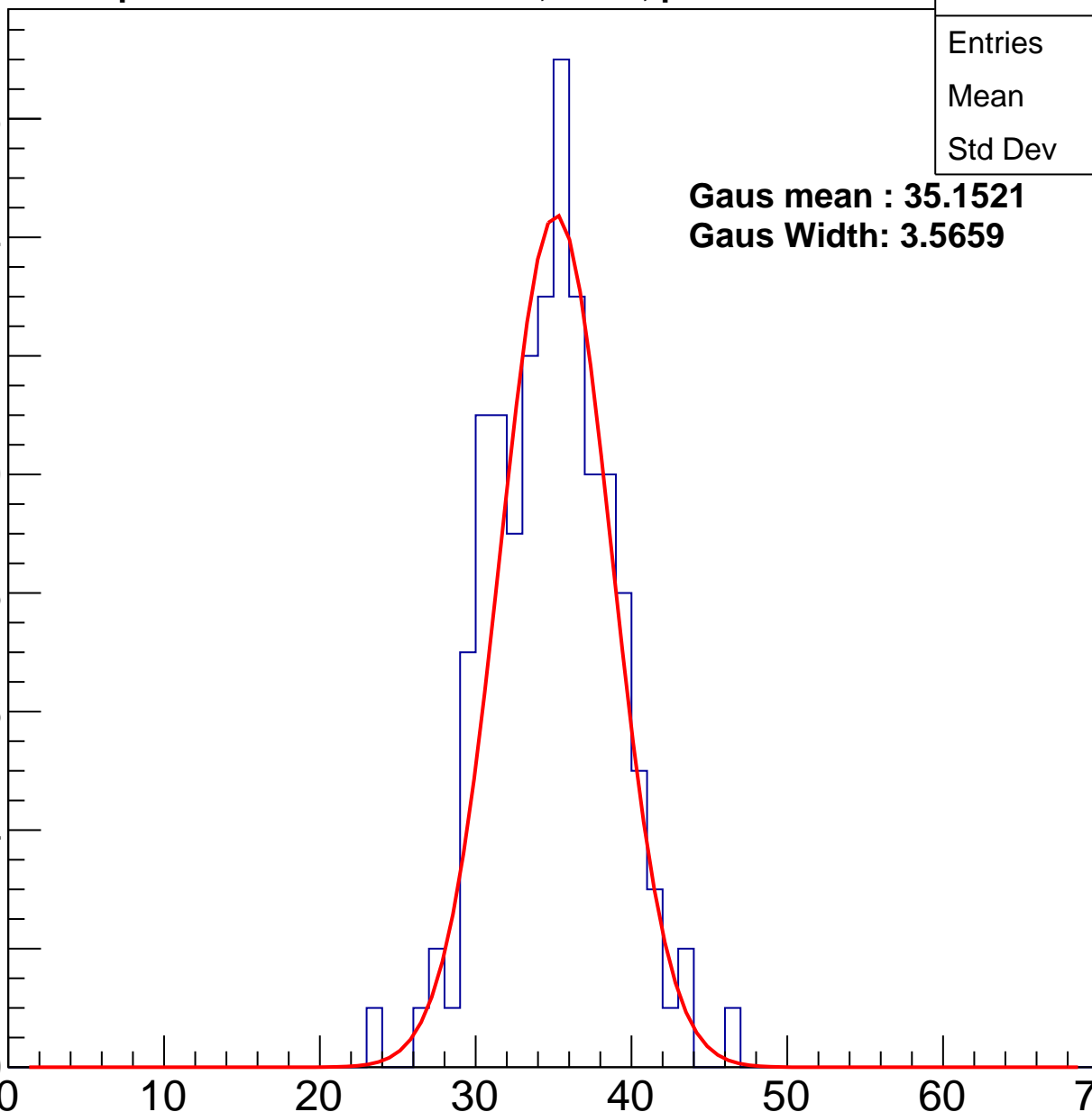
40

50

60

70

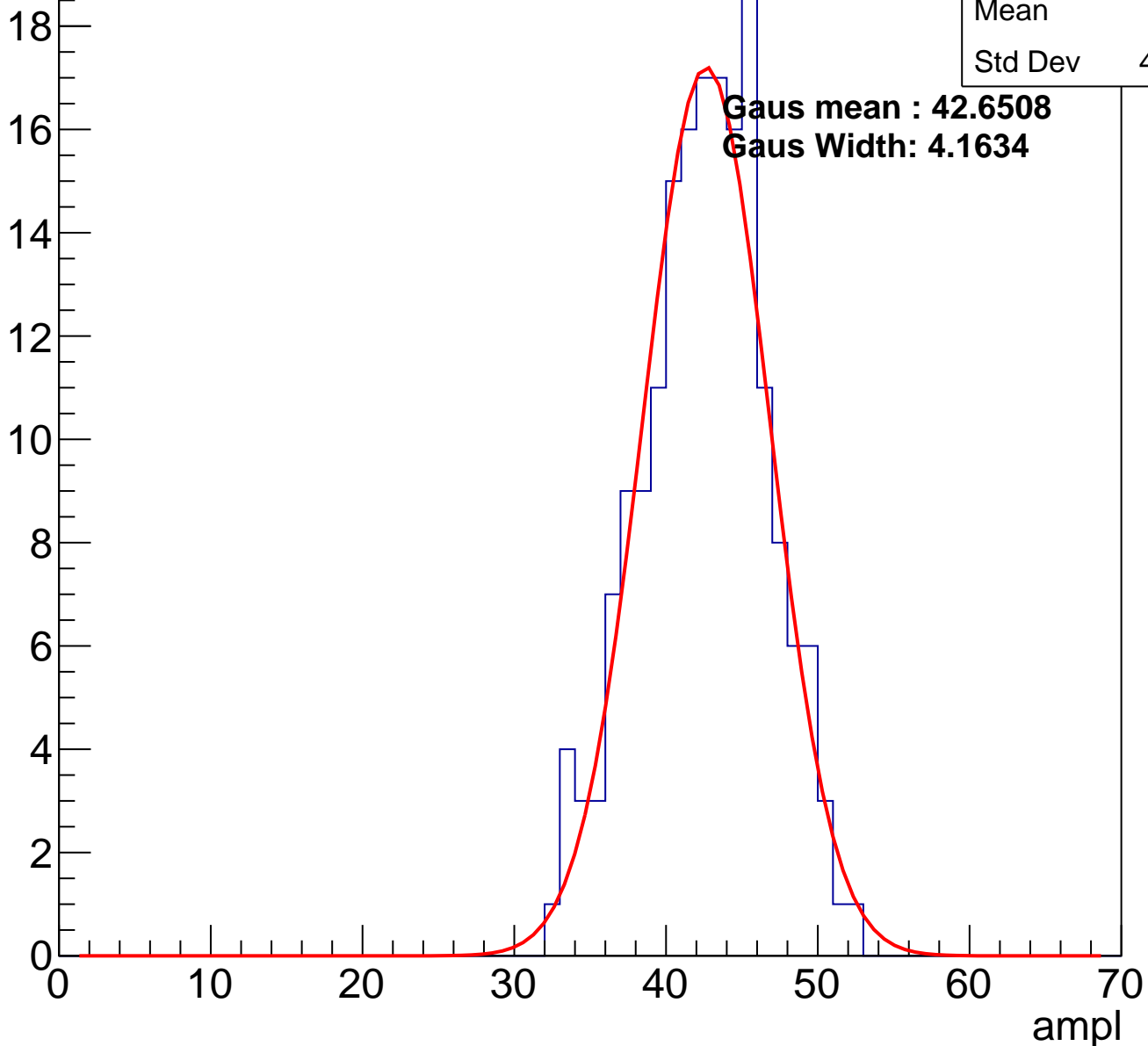
ampl



# B0L101S, U16-ch103, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch103, adc3

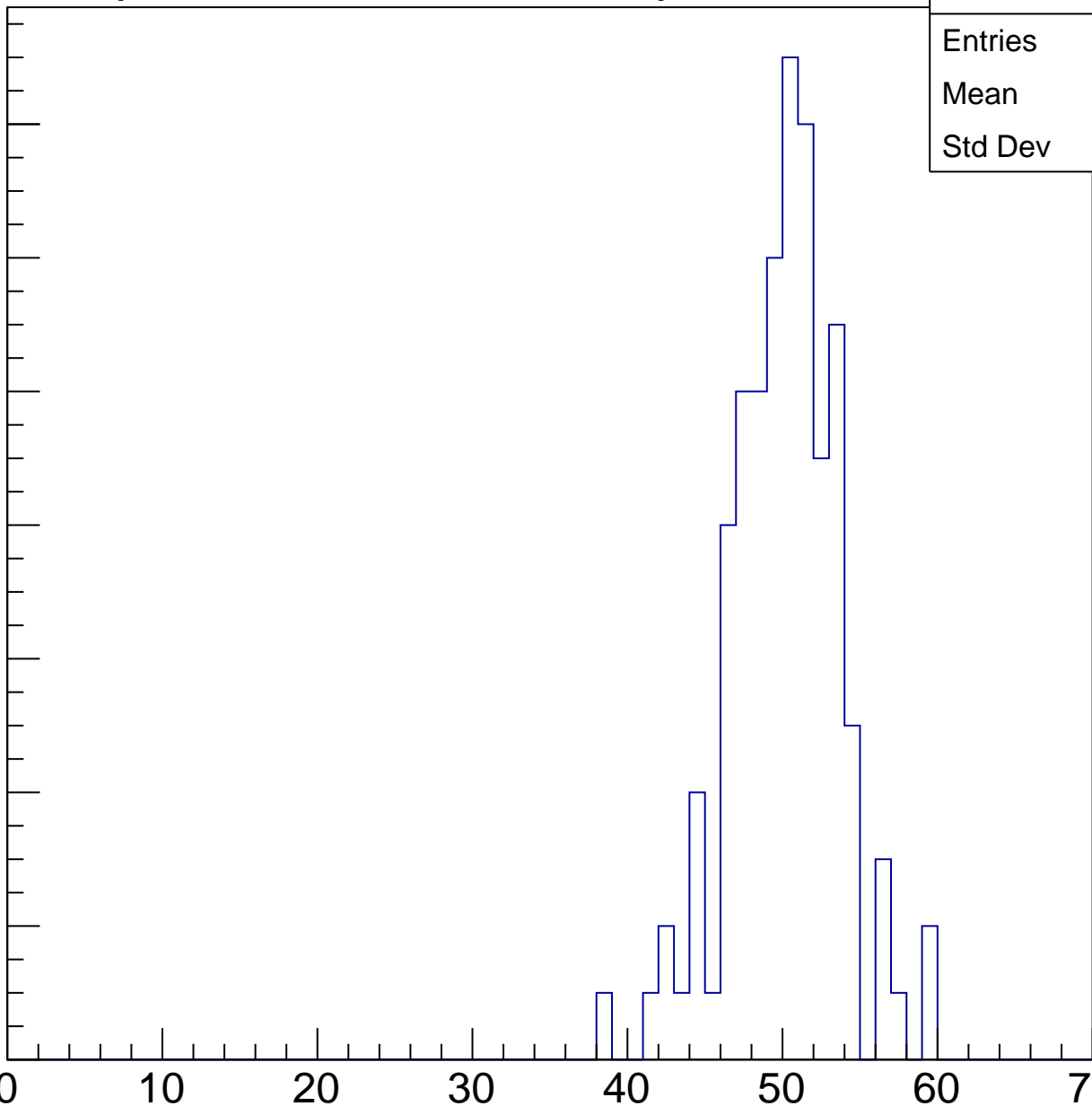
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 49.65 |
| Std Dev | 3.536 |

ampl



# B0L101S, U16-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 54.82 |
| Std Dev | 3.44  |

Entry

10

8

6

4

2

0

0

10

20

30

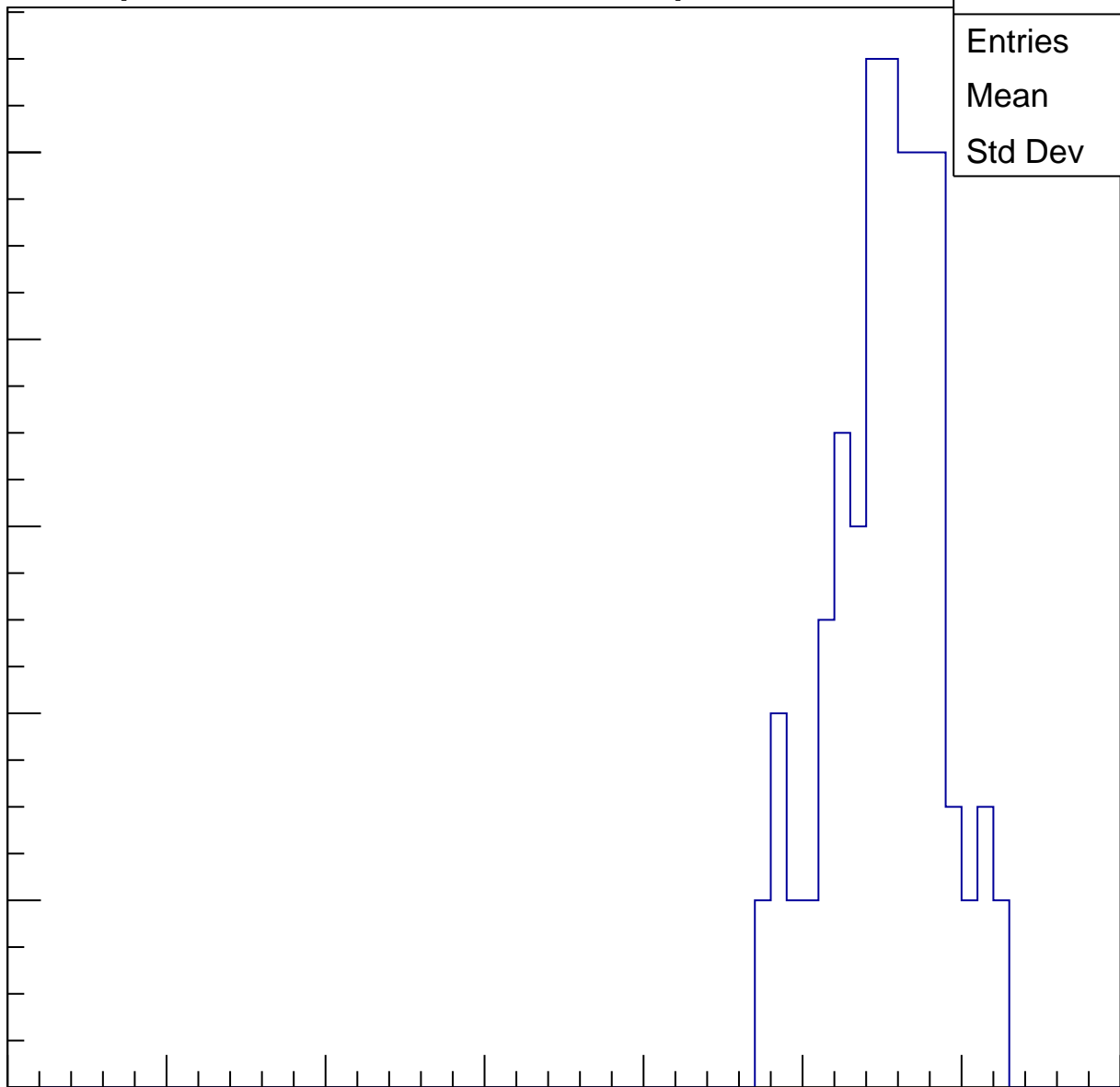
40

50

60

70

ampl



# B0L101S, U16-ch103, adc5

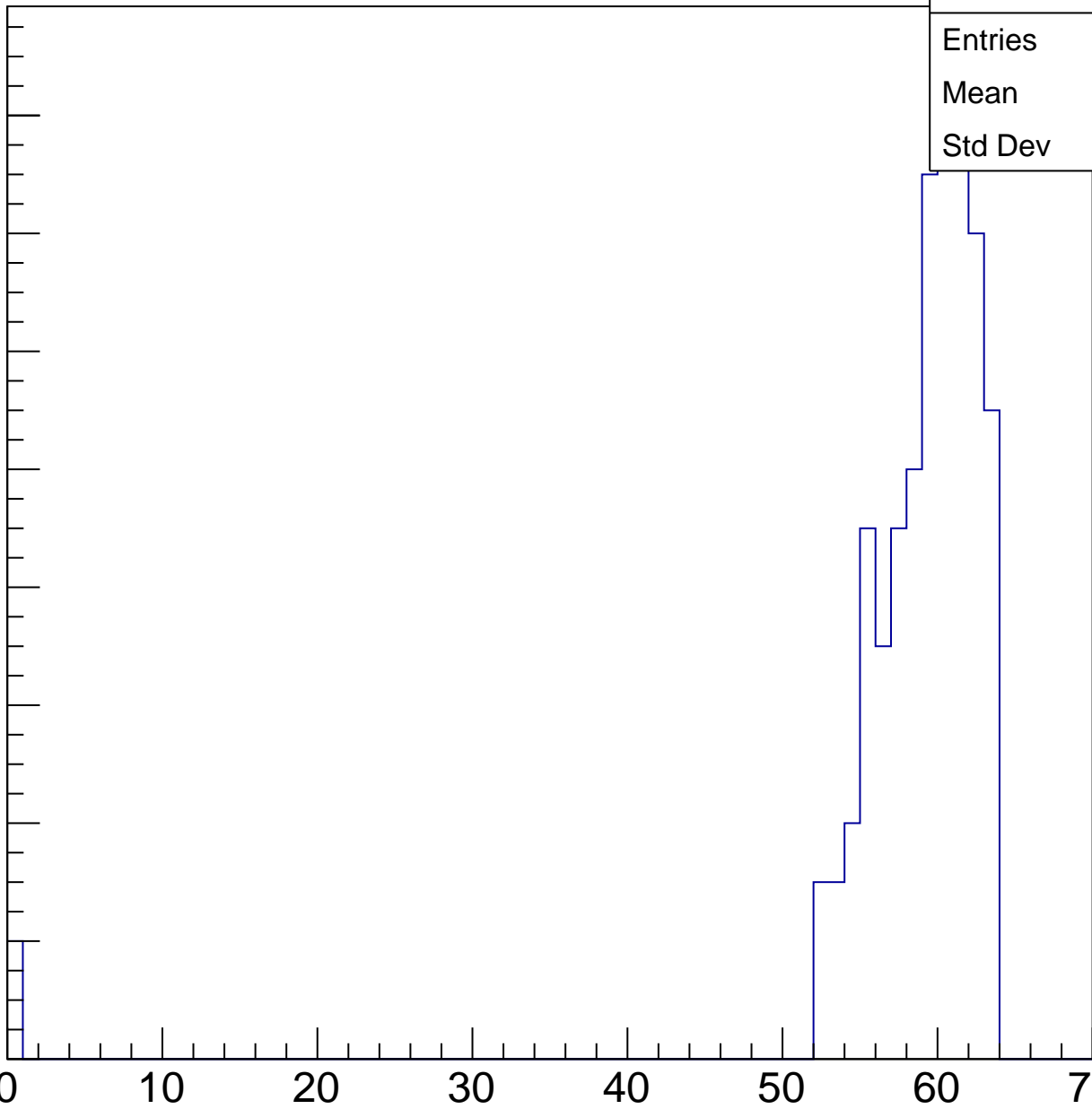
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 120   |
| Mean    | 57.94 |
| Std Dev | 8.072 |

ampl

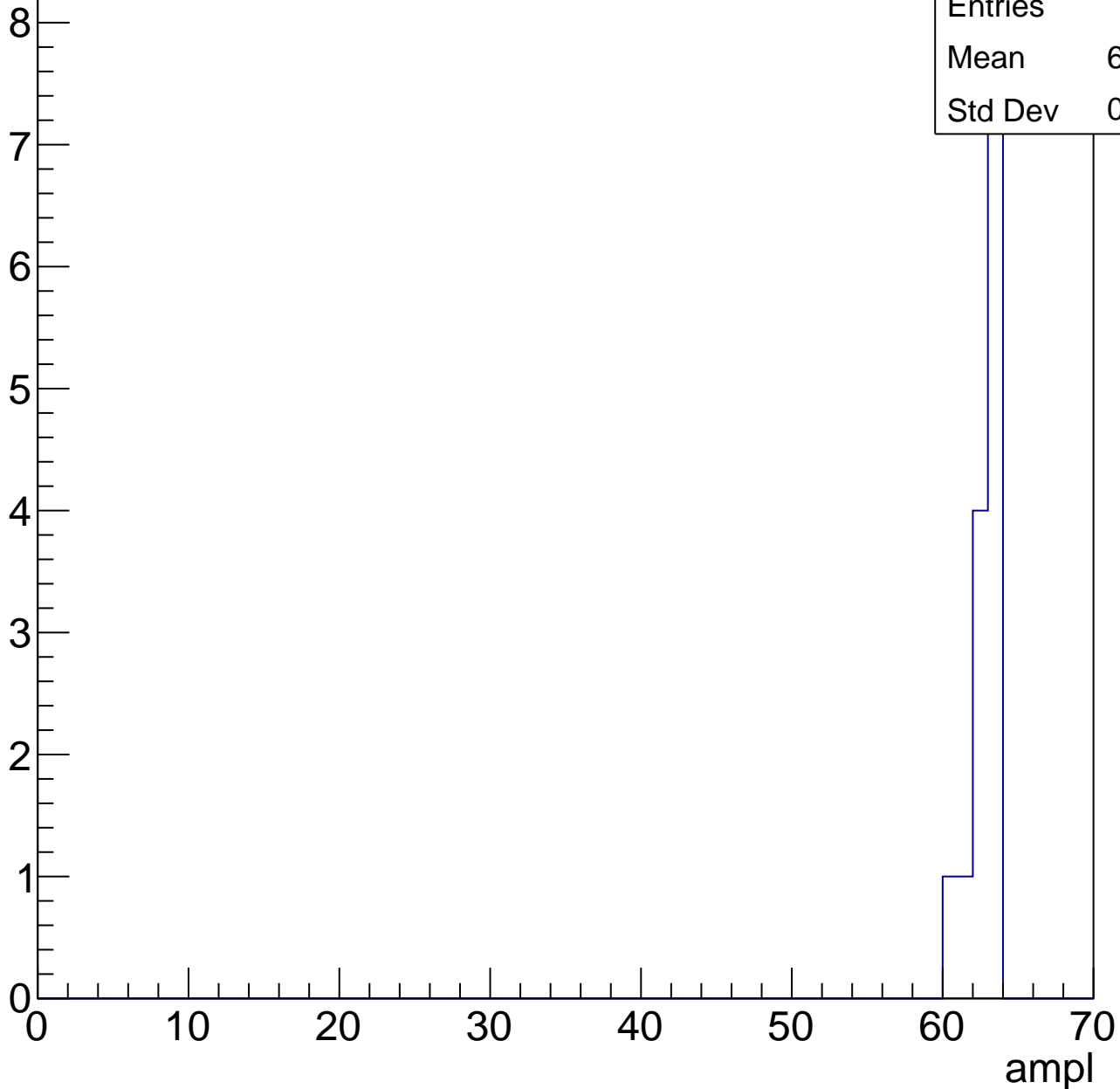


# B0L101S, U16-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 62.36 |
| Std Dev | 0.895 |

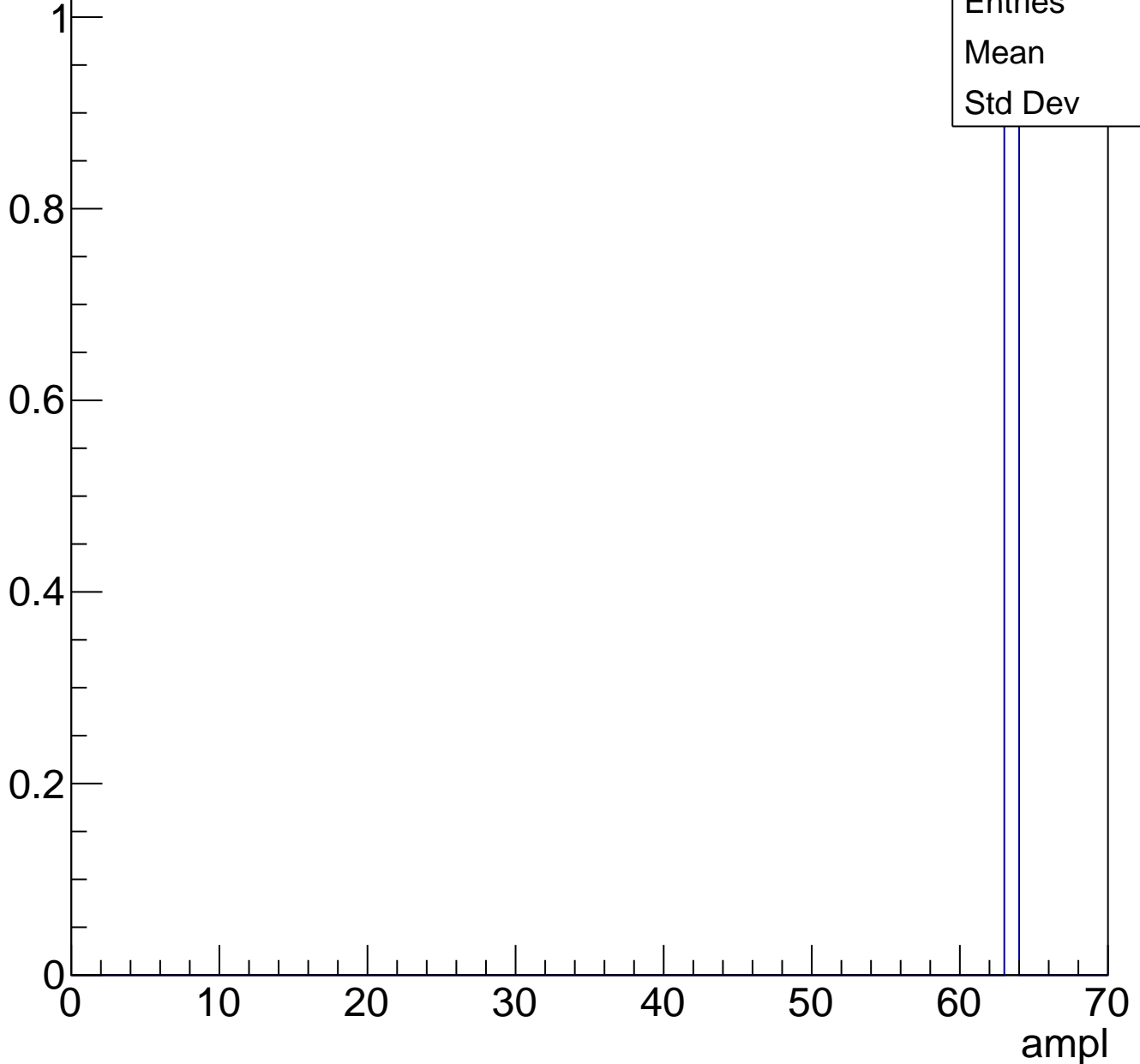




# B0L101S, U16-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch104, adc0

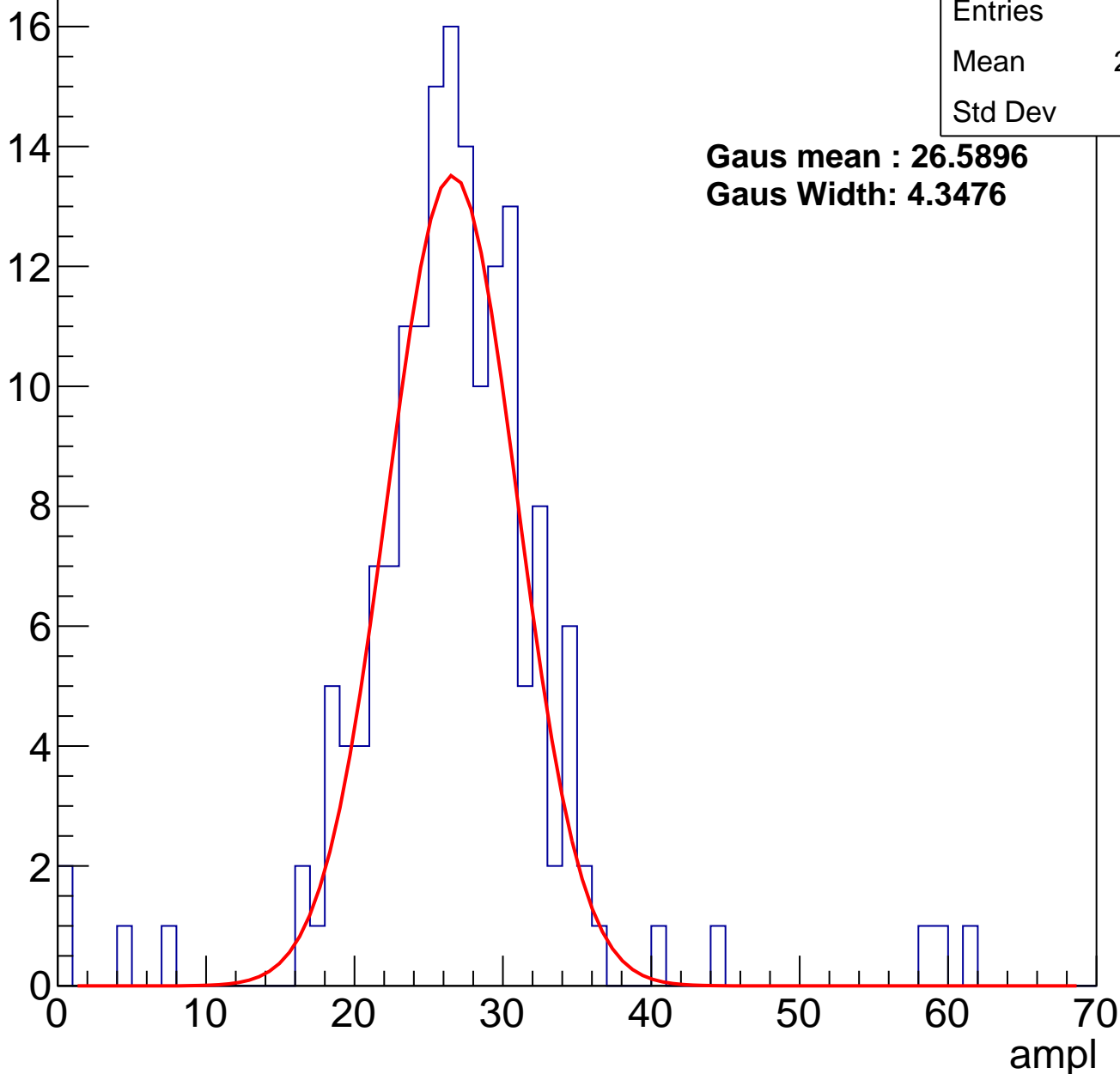
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 165   |
| Mean    | 26.42 |
| Std Dev | 7.36  |

**Gaus mean : 26.5896**

**Gaus Width: 4.3476**

Entry



# B0L101S, U16-ch104, adc1

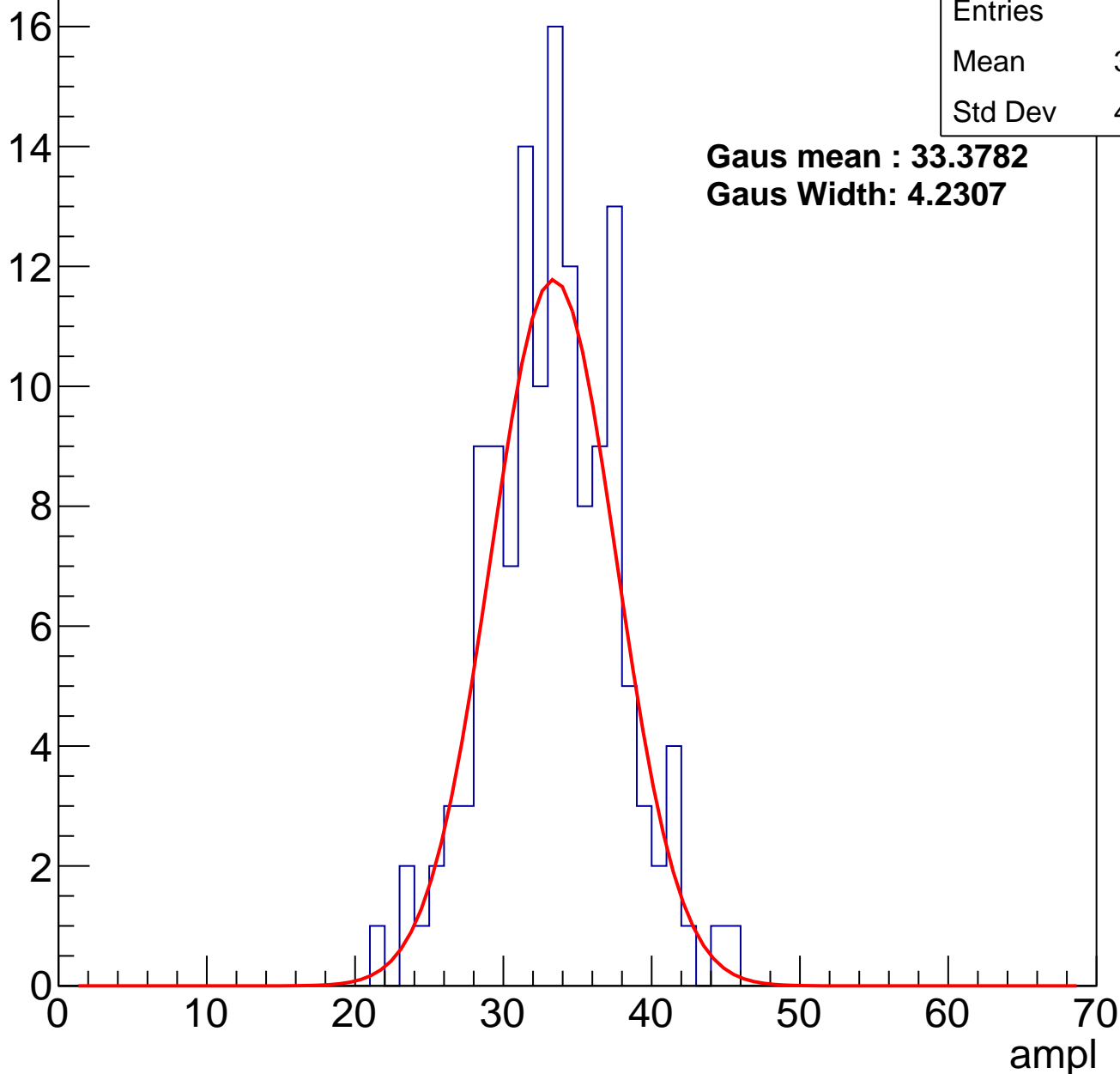
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 32.94 |
| Std Dev | 4.348 |

**Gaus mean : 33.3782**

**Gaus Width: 4.2307**

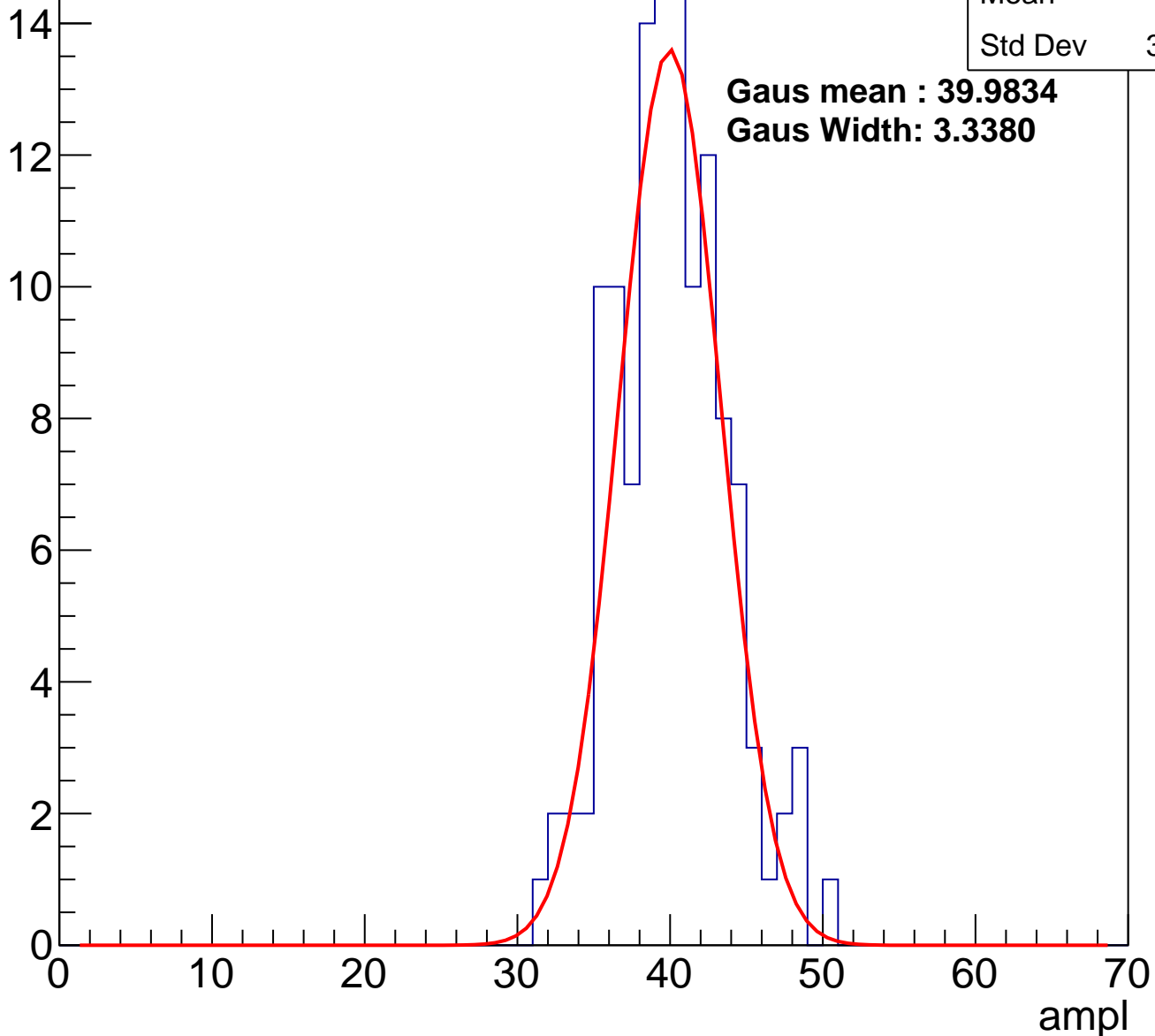
Entry



# B0L101S, U16-ch104, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch104, adc3

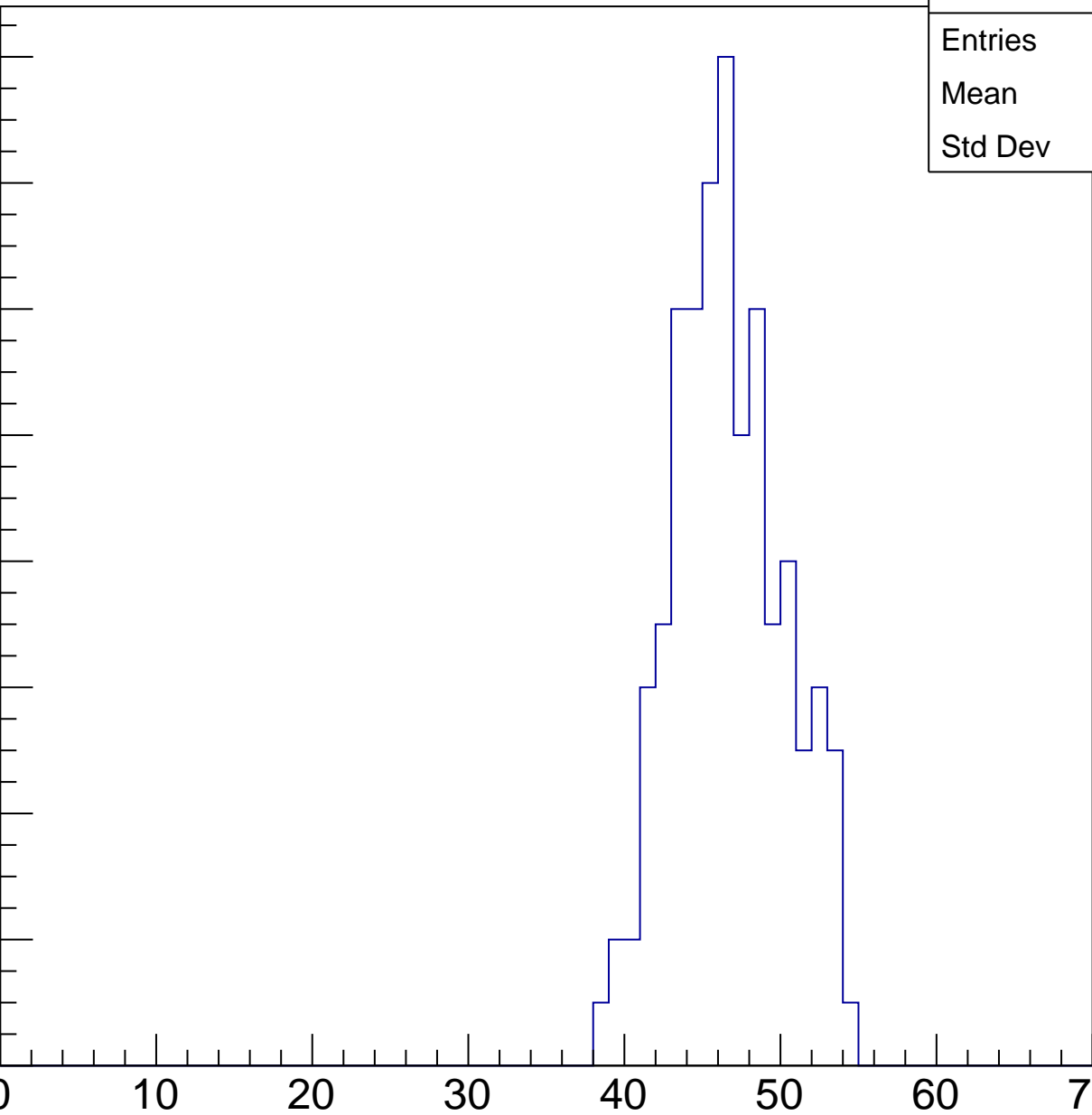
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 46.2  |
| Std Dev | 3.519 |

ampl



# B0L101S, U16-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

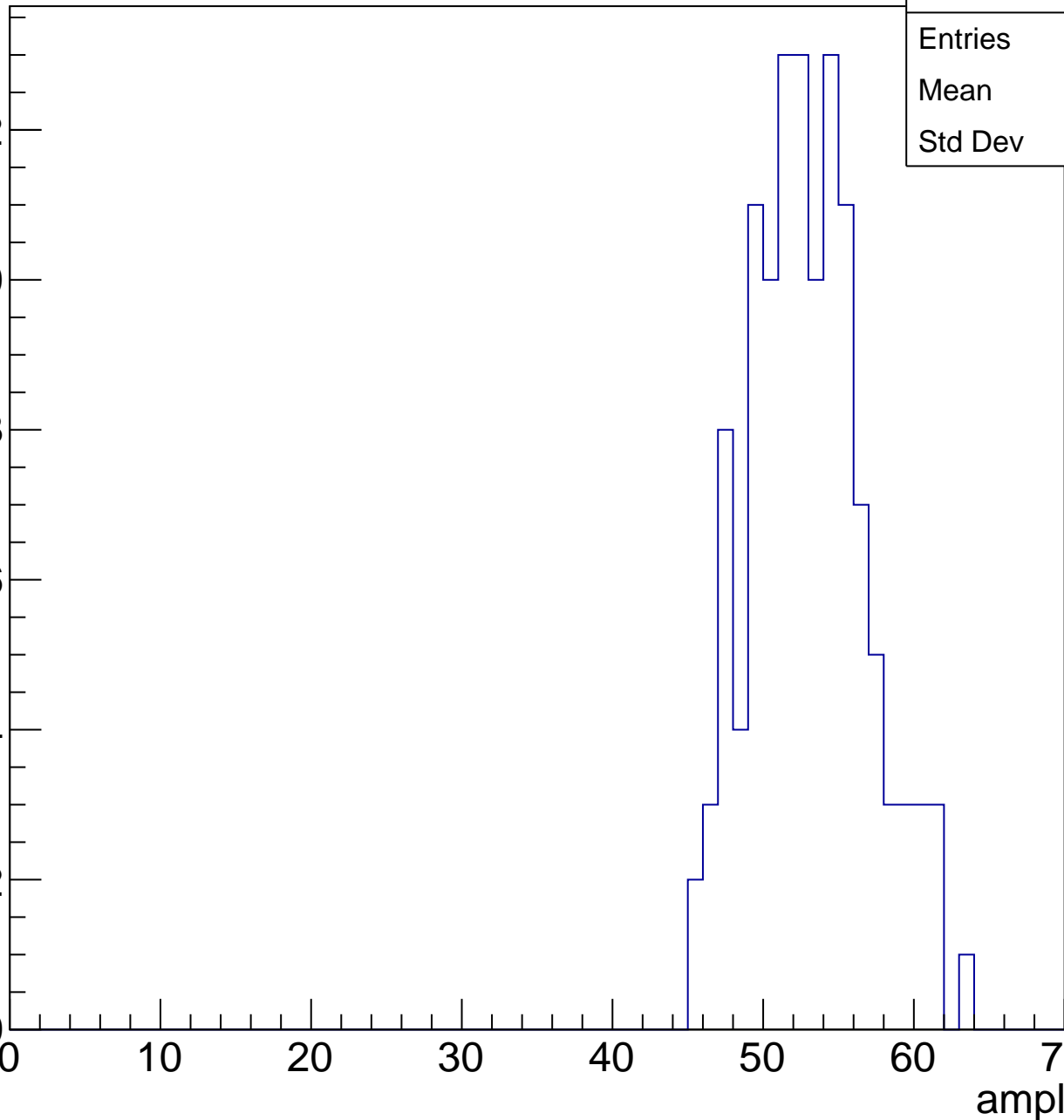
40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 52.56 |
| Std Dev | 3.817 |



# B0L101S, U16-ch104, adc5

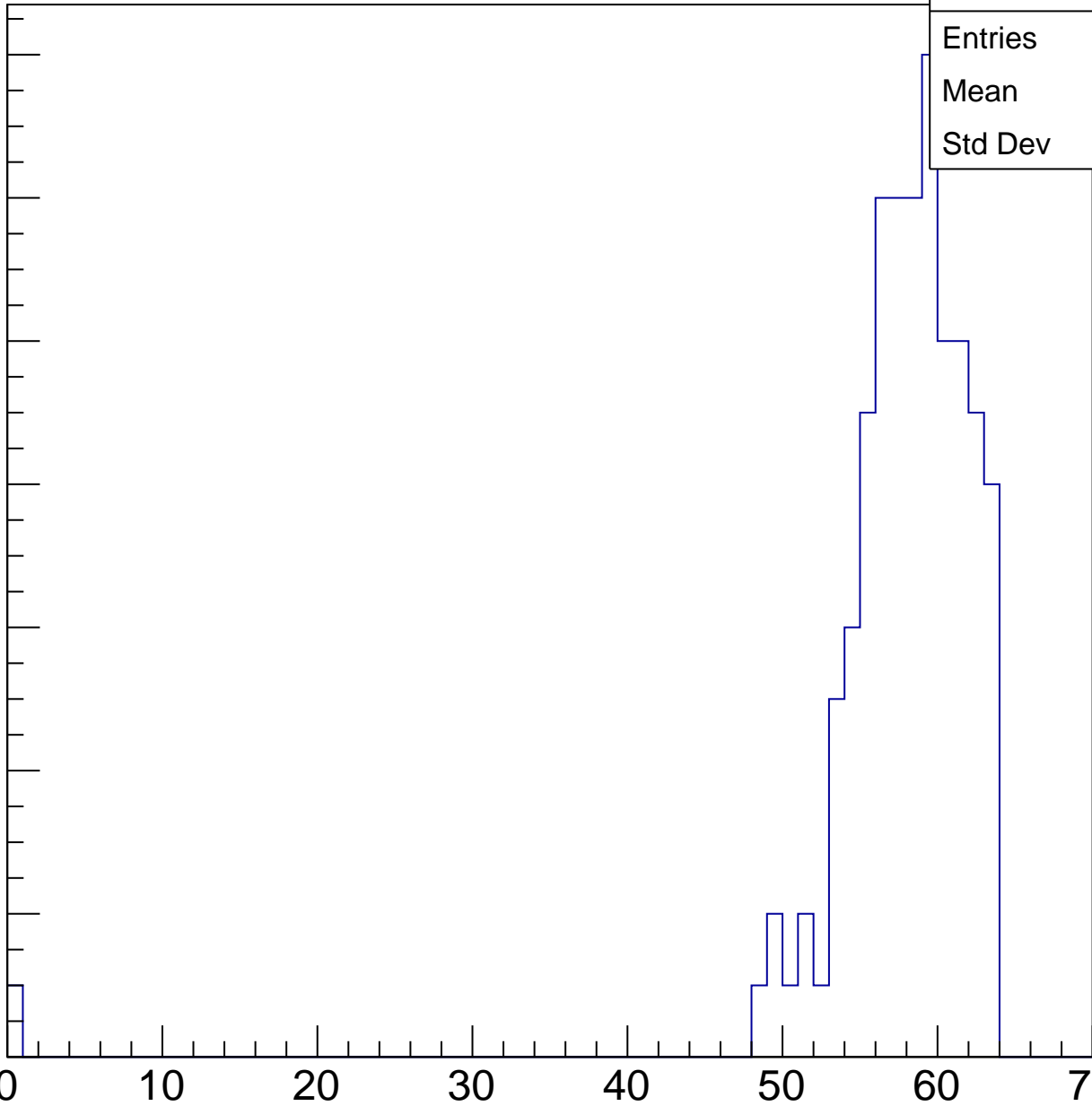
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 57.25 |
| Std Dev | 6.331 |

ampl



# B0L101S, U16-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 59.4  |
| Std Dev | 8.979 |

ampl

0

10

20

30

40

50

60

70



# B0L101S, U16-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



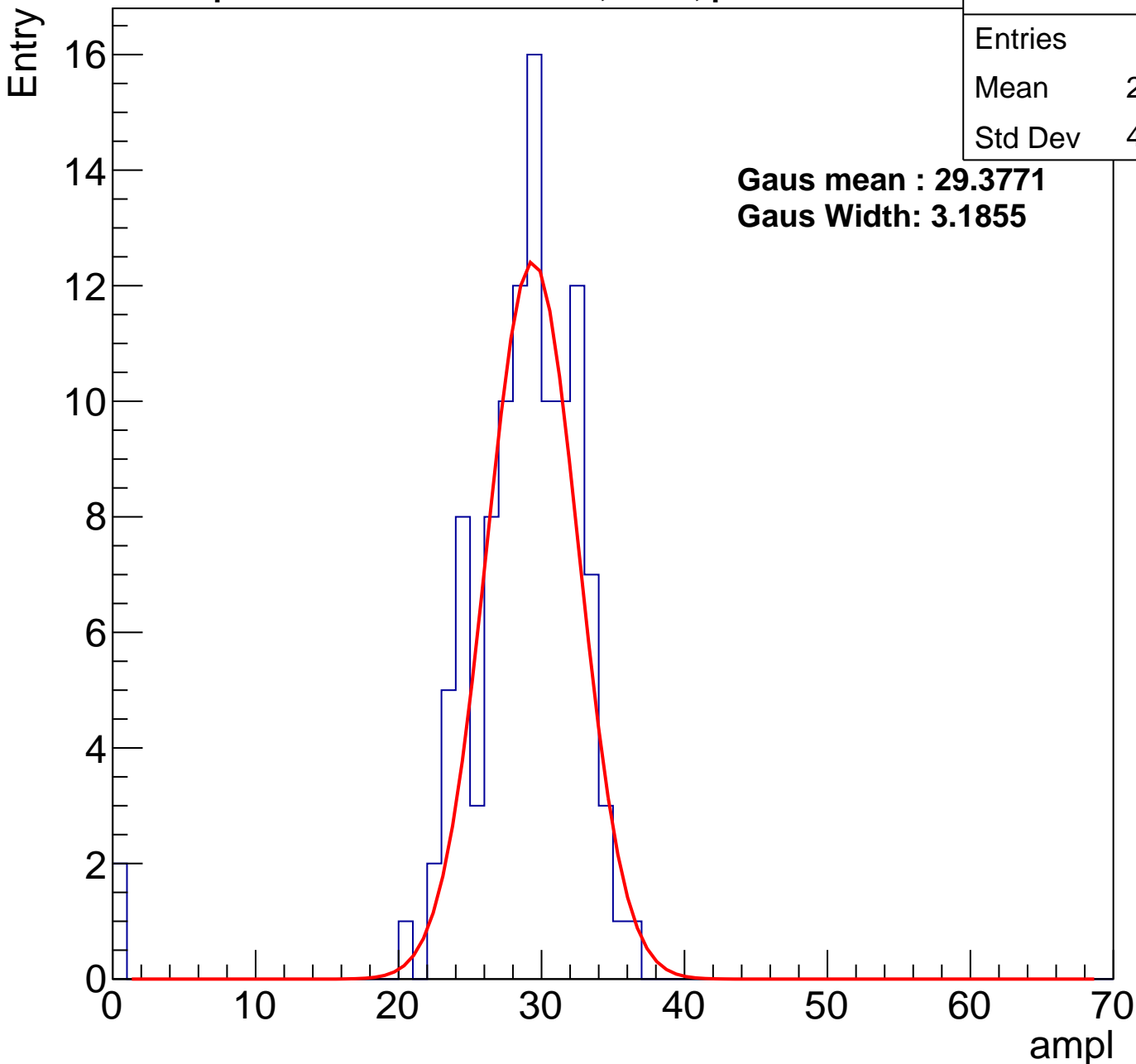
# B0L101S, U16-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 28.13 |
| Std Dev | 4.985 |

**Gaus mean : 29.3771**

**Gaus Width: 3.1855**



# B0L101S, U16-ch105, adc1

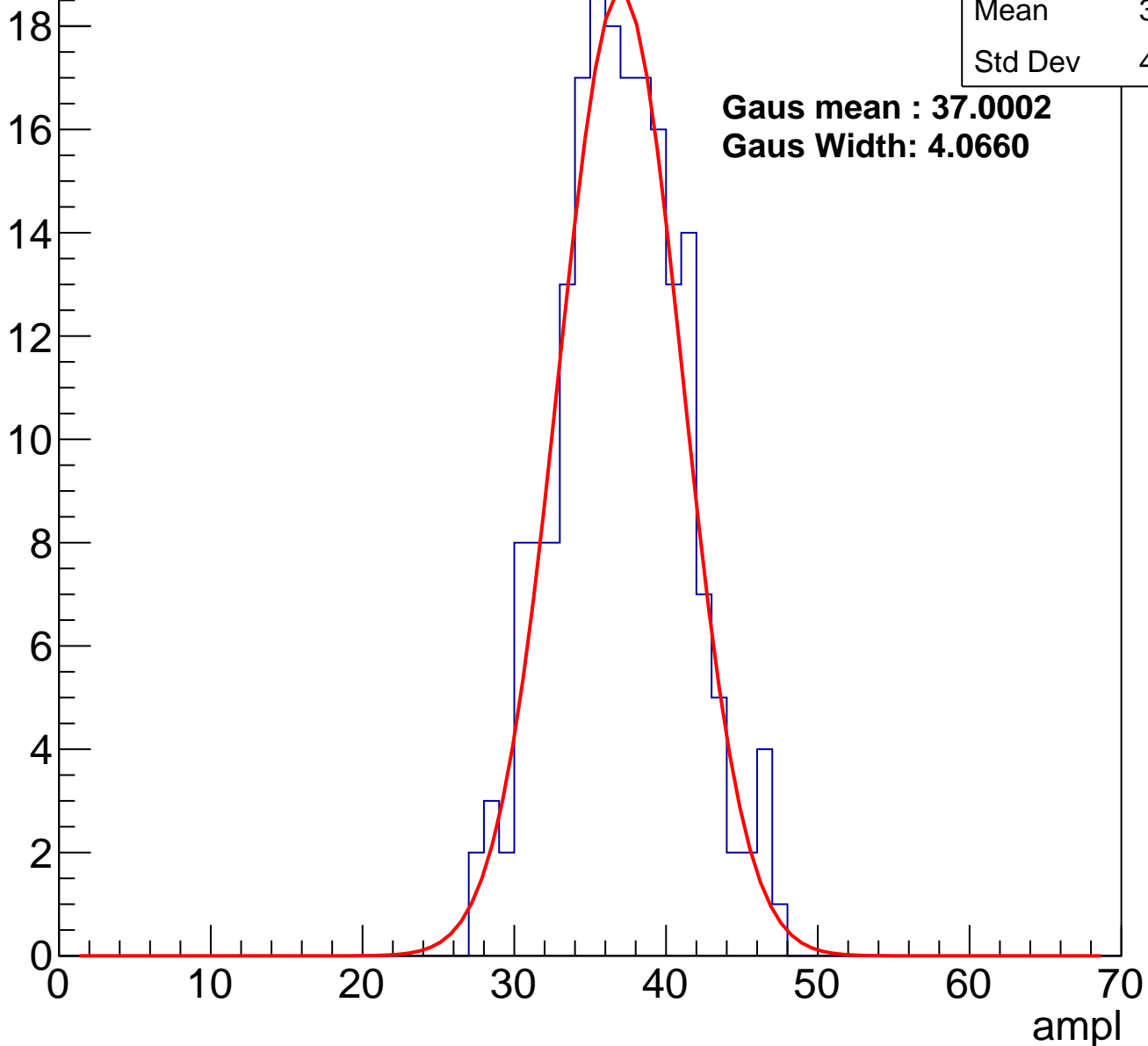
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 196   |
| Mean    | 36.59 |
| Std Dev | 4.103 |

**Gaus mean : 37.0002**

**Gaus Width: 4.0660**

Entry



# B0L101S, U16-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

112

Mean

43.94

Std Dev

3.418

**Gaus mean : 44.4944**

**Gaus Width: 3.6714**

0

10

20

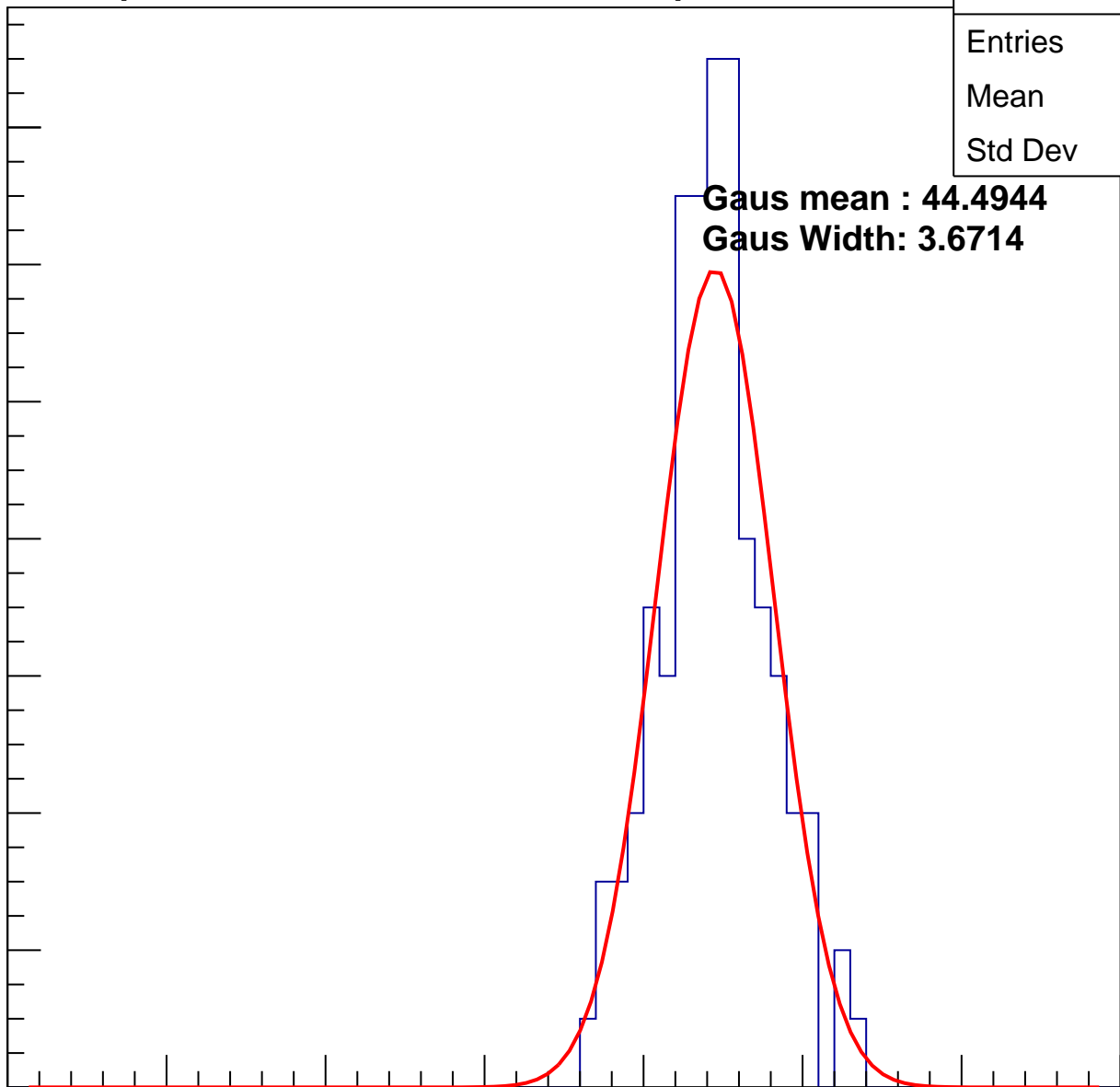
30

40

50

60

ampl



# B0L101S, U16-ch105, adc3

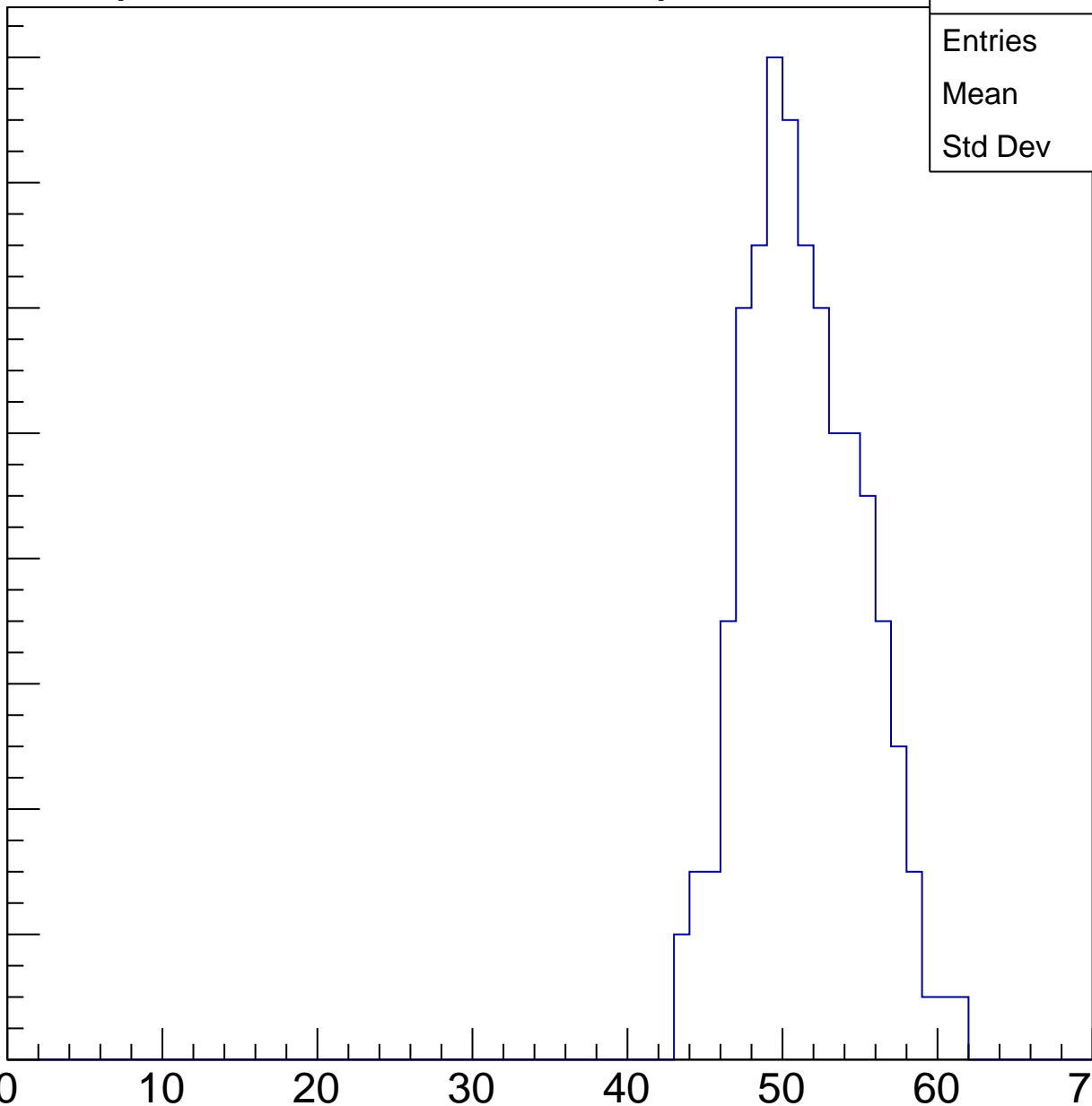
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 50.91 |
| Std Dev | 3.719 |

ampl



# B0L101S, U16-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

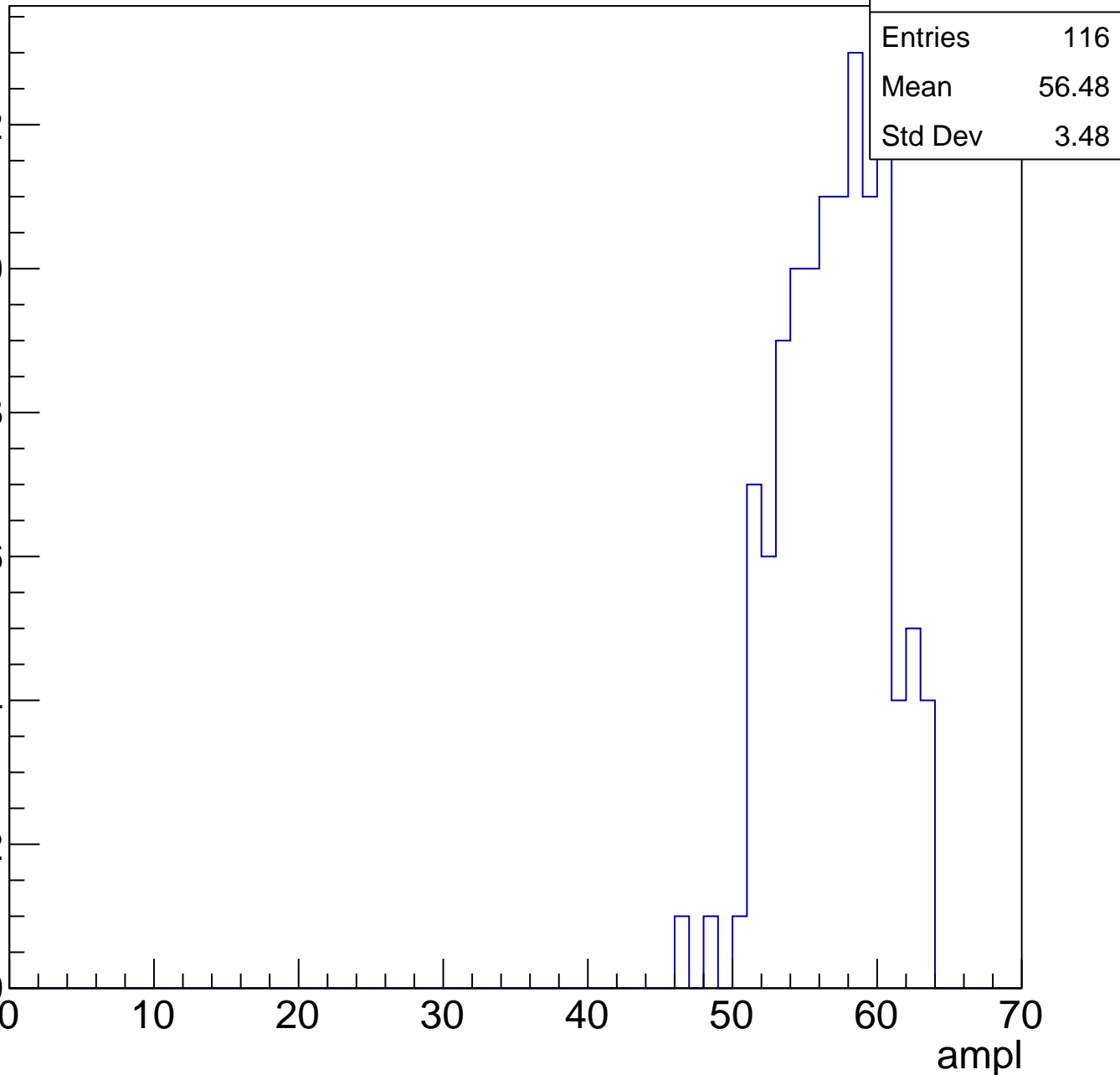
116

Mean

56.48

Std Dev

3.48



# B0L101S, U16-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

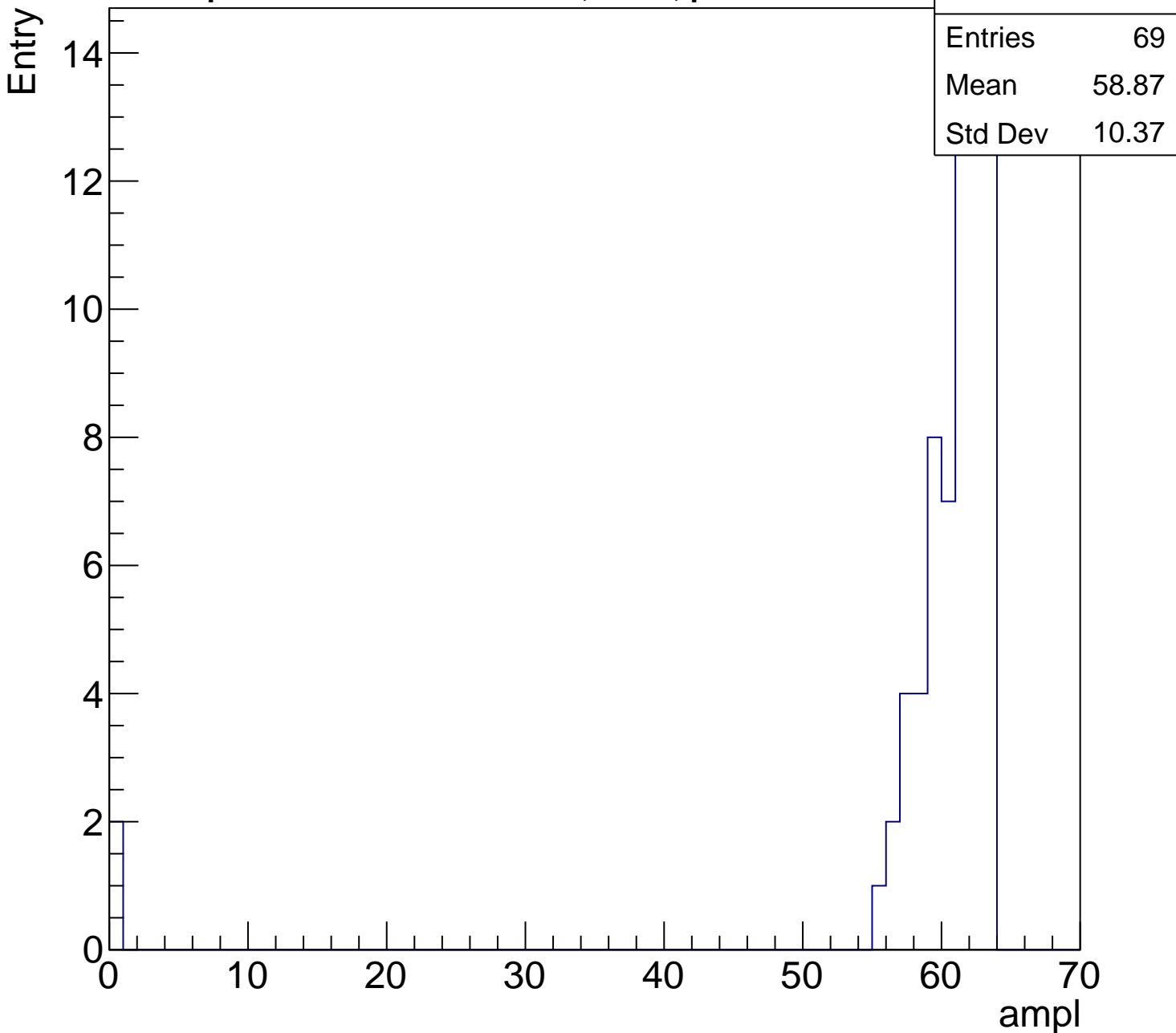
|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 58.87 |
| Std Dev | 10.37 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 5      |
| Mean    | 62     |
| Std Dev | 0.8944 |

ampl

0 10 20 30 40 50 60 70

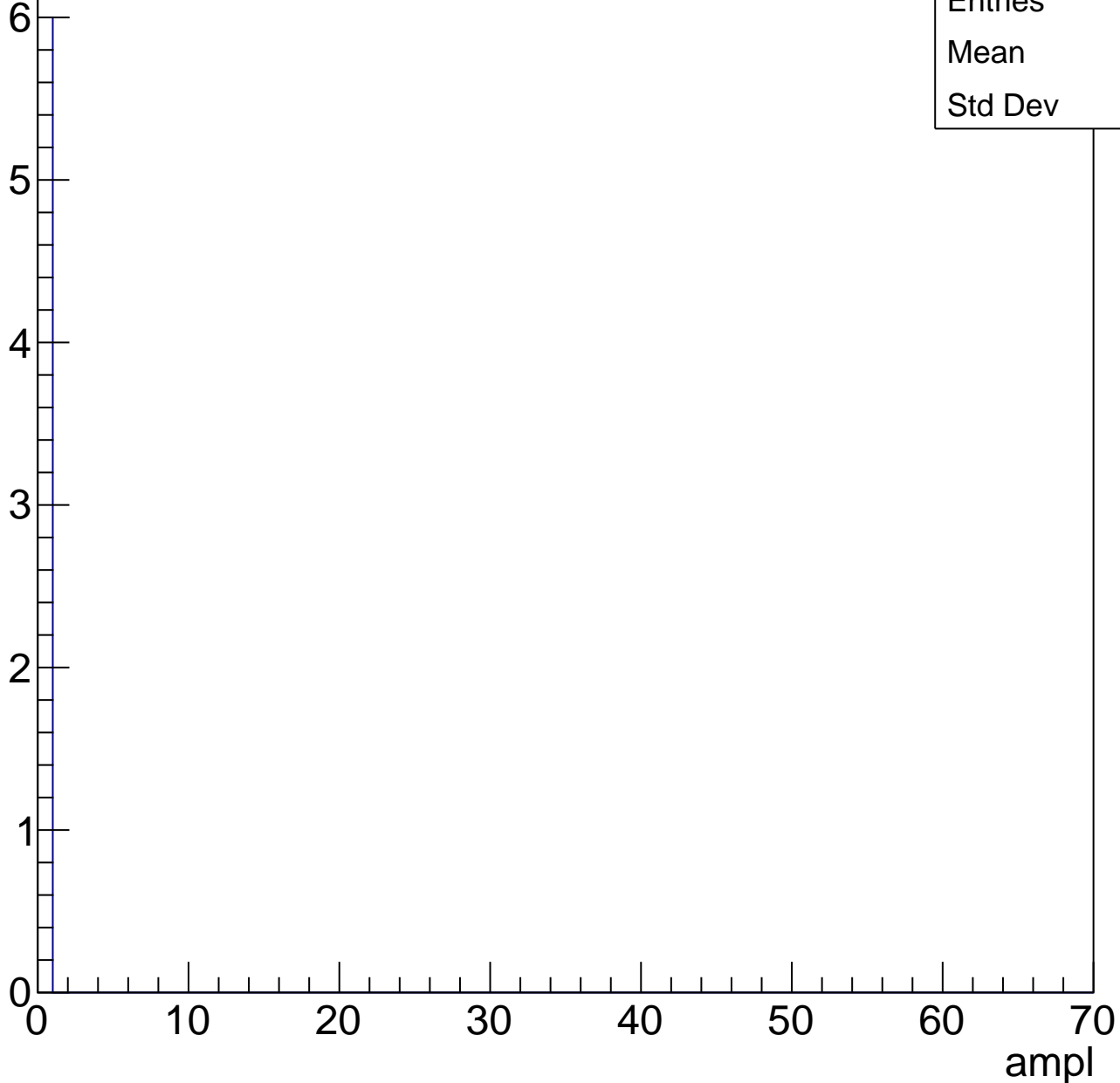


# B0L101S, U16-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |   |
|---------|---|
| Entries | 6 |
| Mean    | 0 |
| Std Dev | 0 |



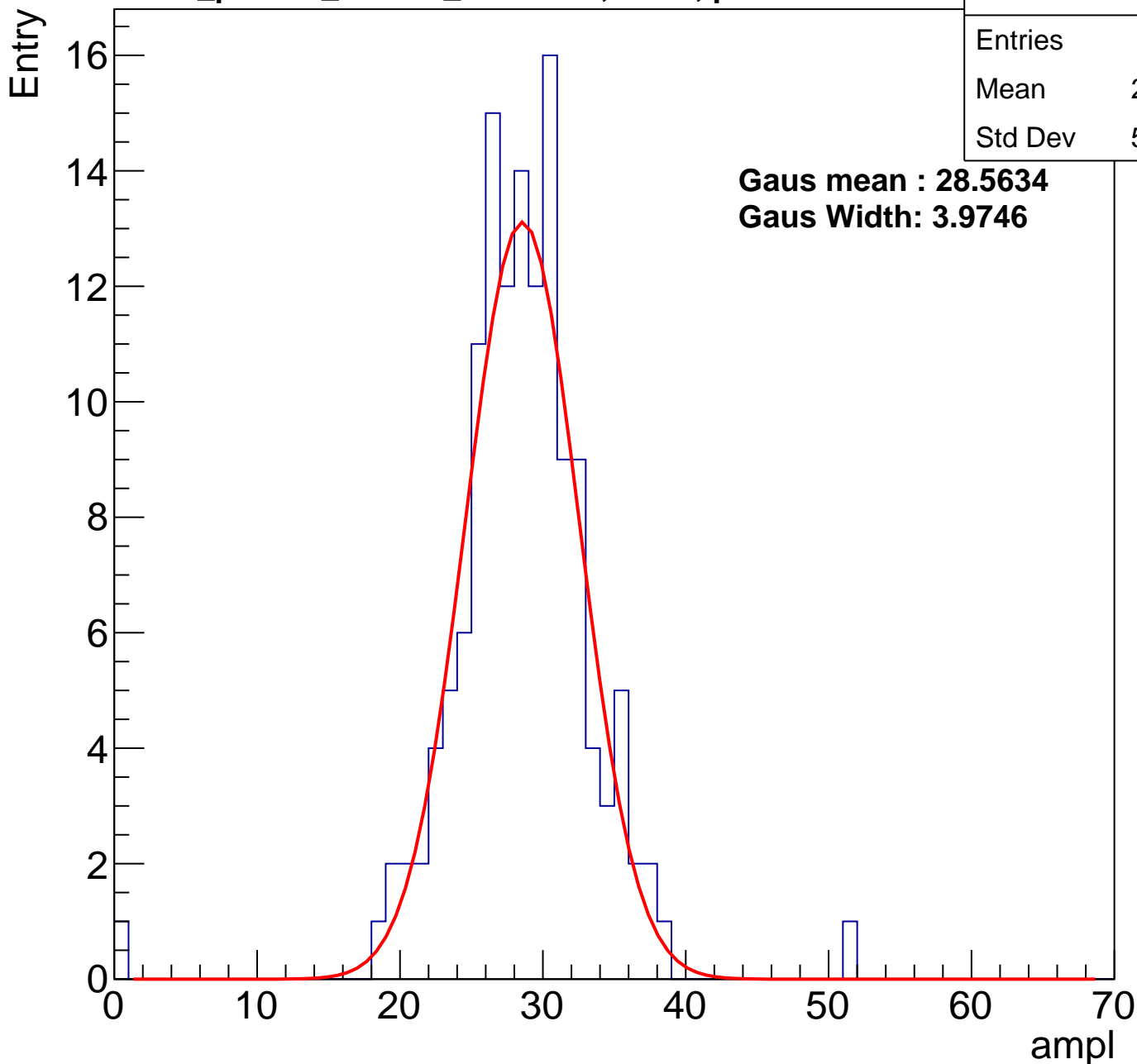
# B0L101S, U16-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 28.09 |
| Std Dev | 5.003 |

**Gaus mean : 28.5634**

**Gaus Width: 3.9746**



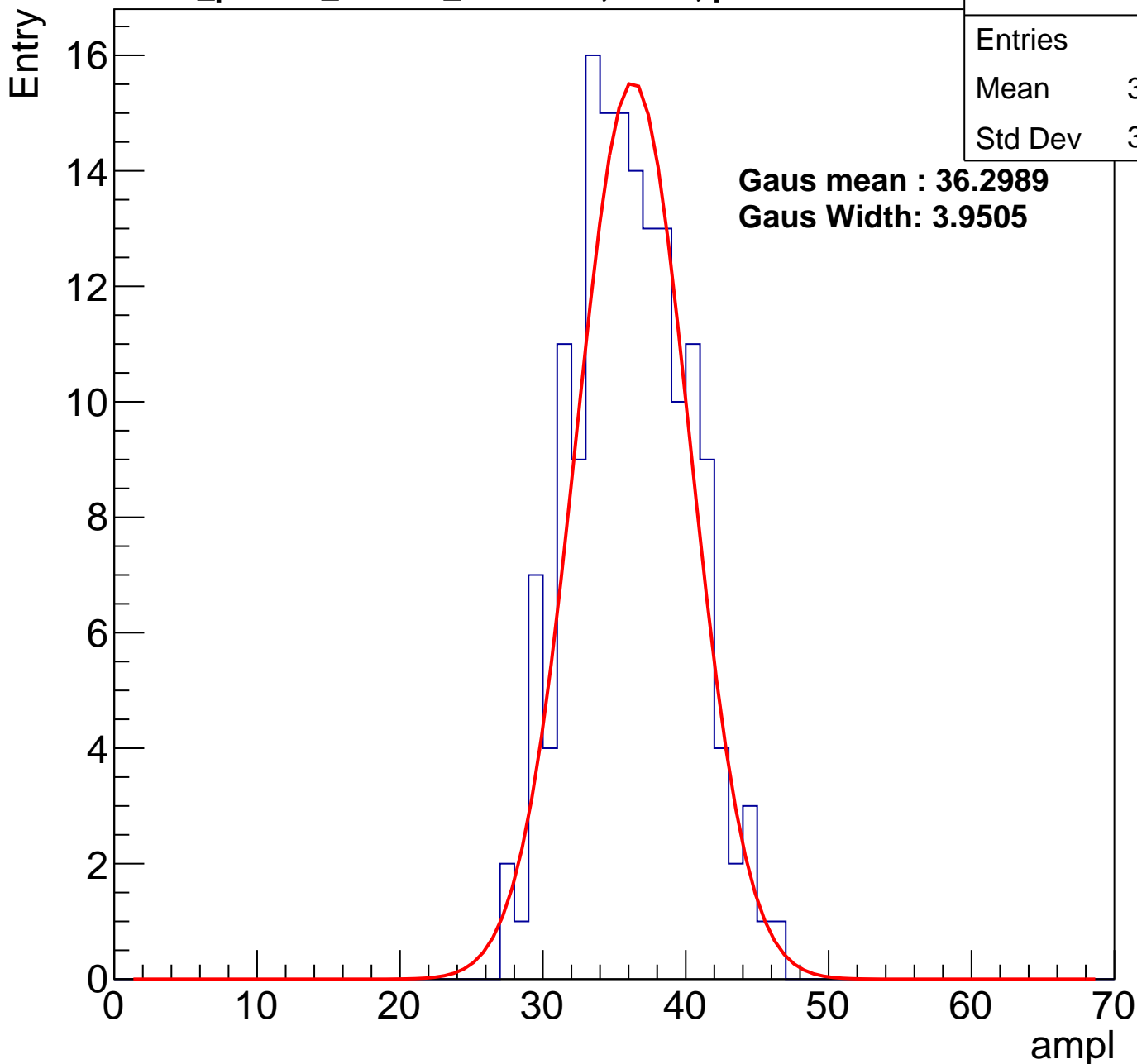
# B0L101S, U16-ch106, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 161   |
| Mean    | 35.73 |
| Std Dev | 3.948 |

**Gaus mean : 36.2989**

**Gaus Width: 3.9505**



# B0L101S, U16-ch106, adc2

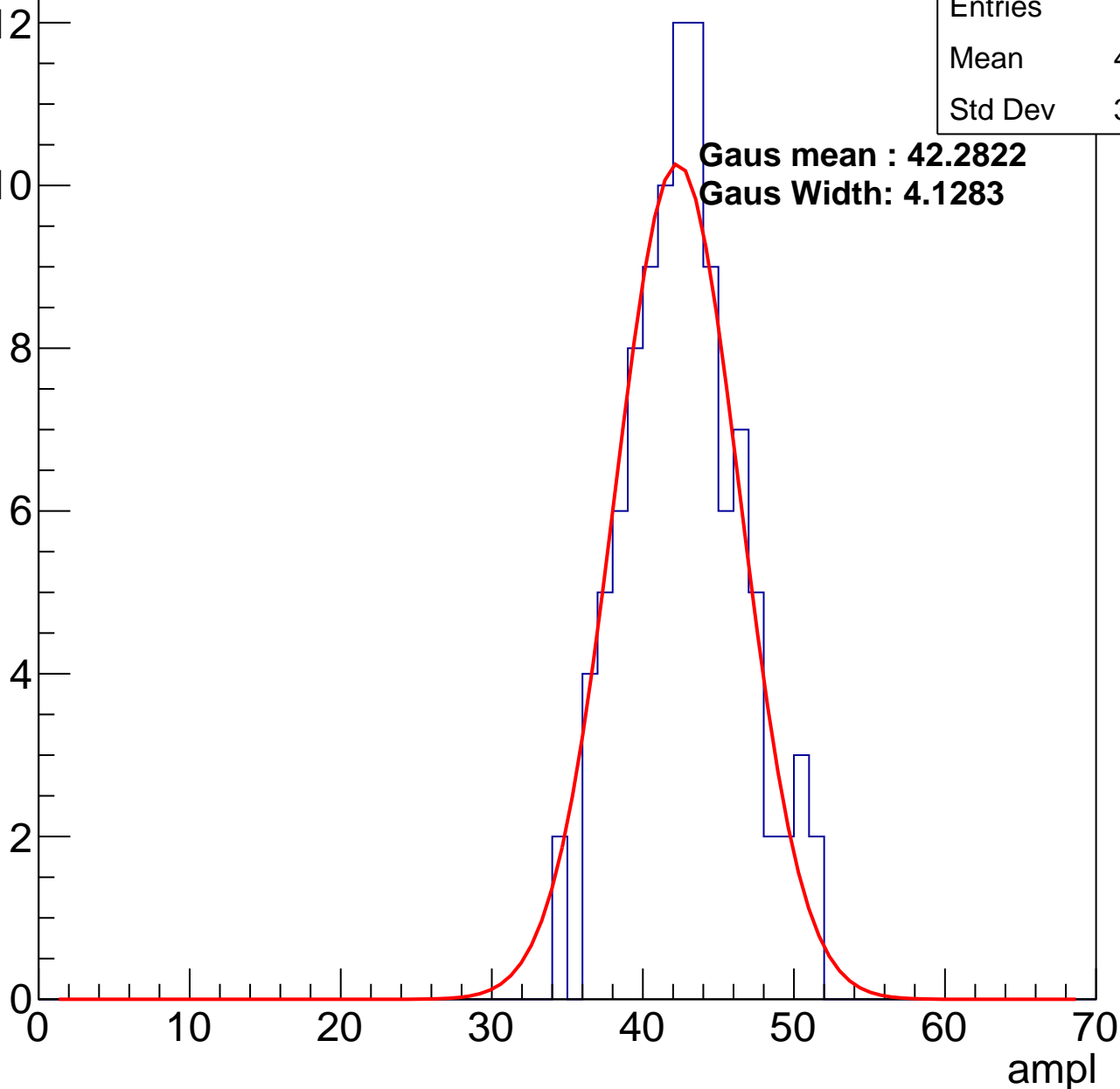
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 42.27 |
| Std Dev | 3.755 |

**Gaus mean : 42.2822**

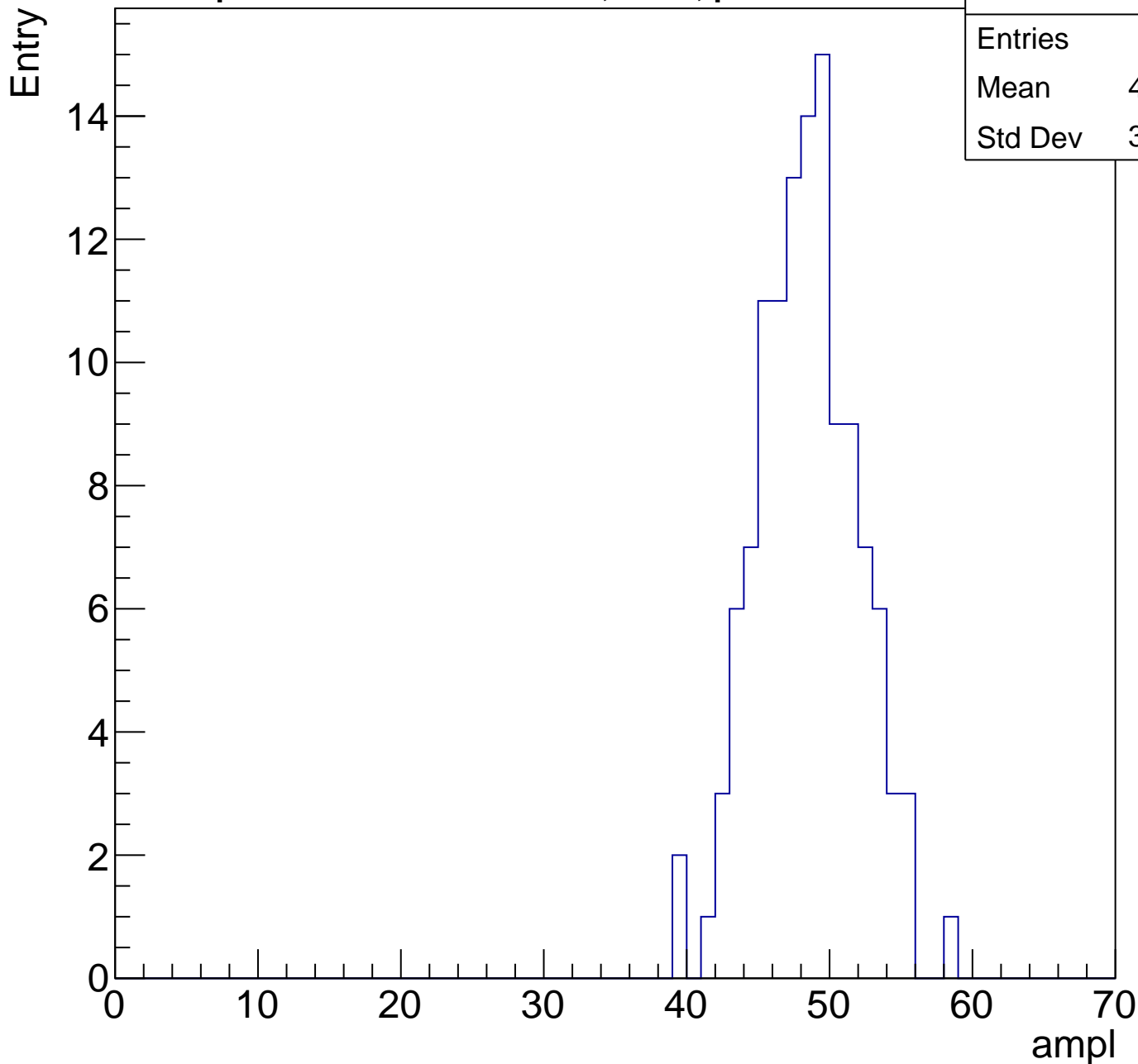
**Gaus Width: 4.1283**



# B0L101S, U16-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

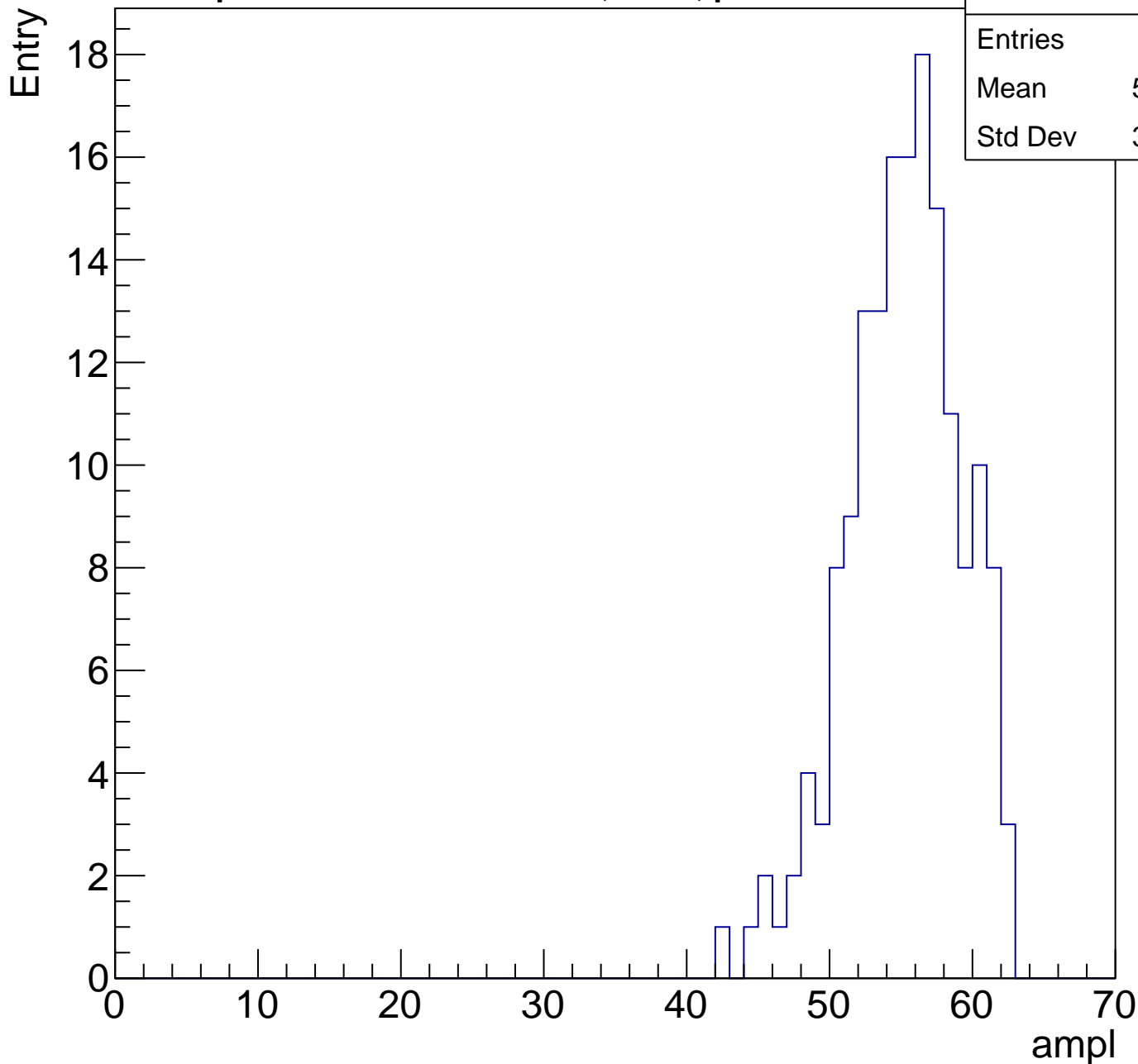
|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 47.98 |
| Std Dev | 3.504 |



# B0L101S, U16-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 162   |
| Mean    | 54.75 |
| Std Dev | 3.938 |



# B0L101S, U16-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

71

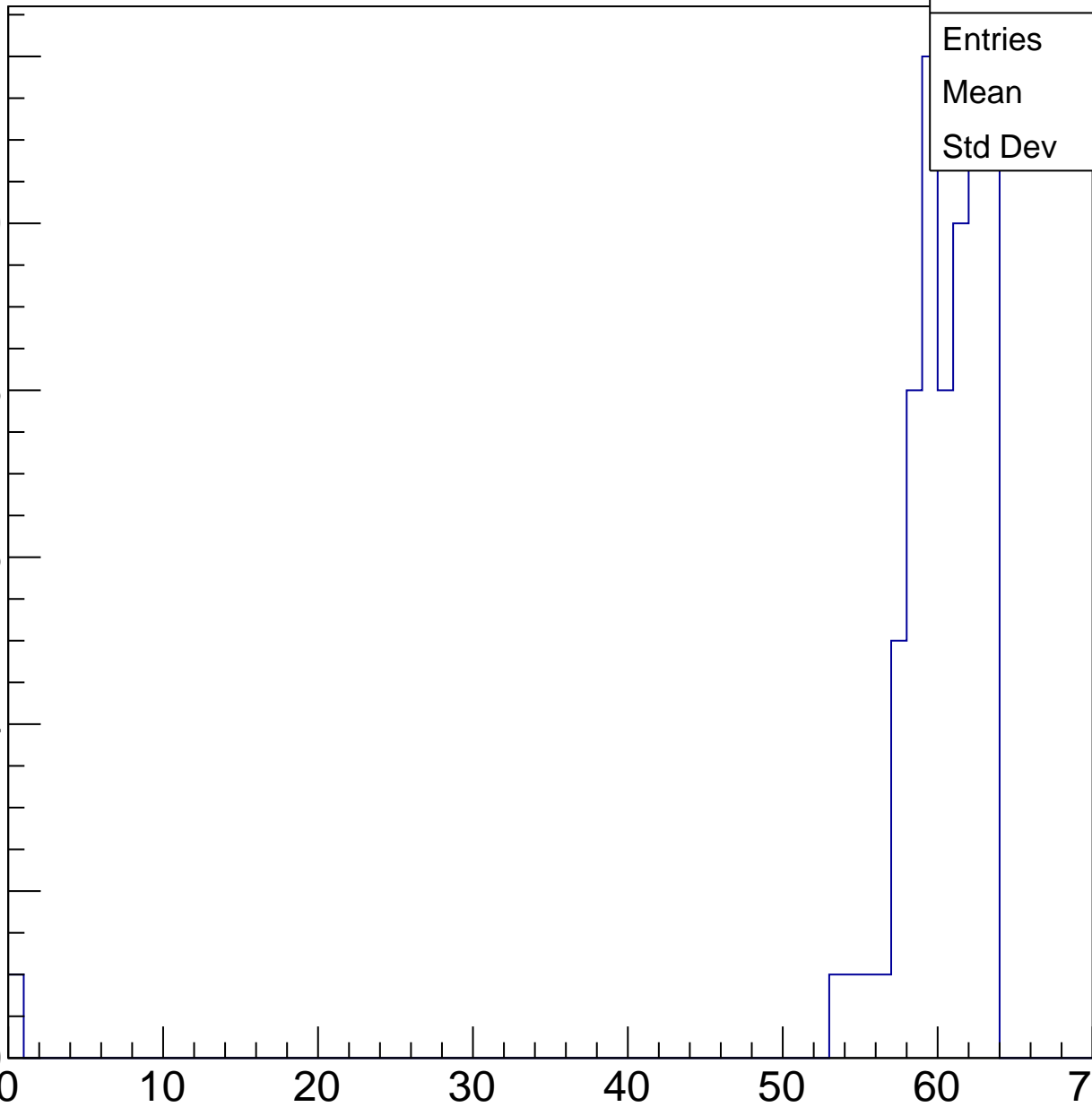
Mean

59.2

Std Dev

7.441

ampl

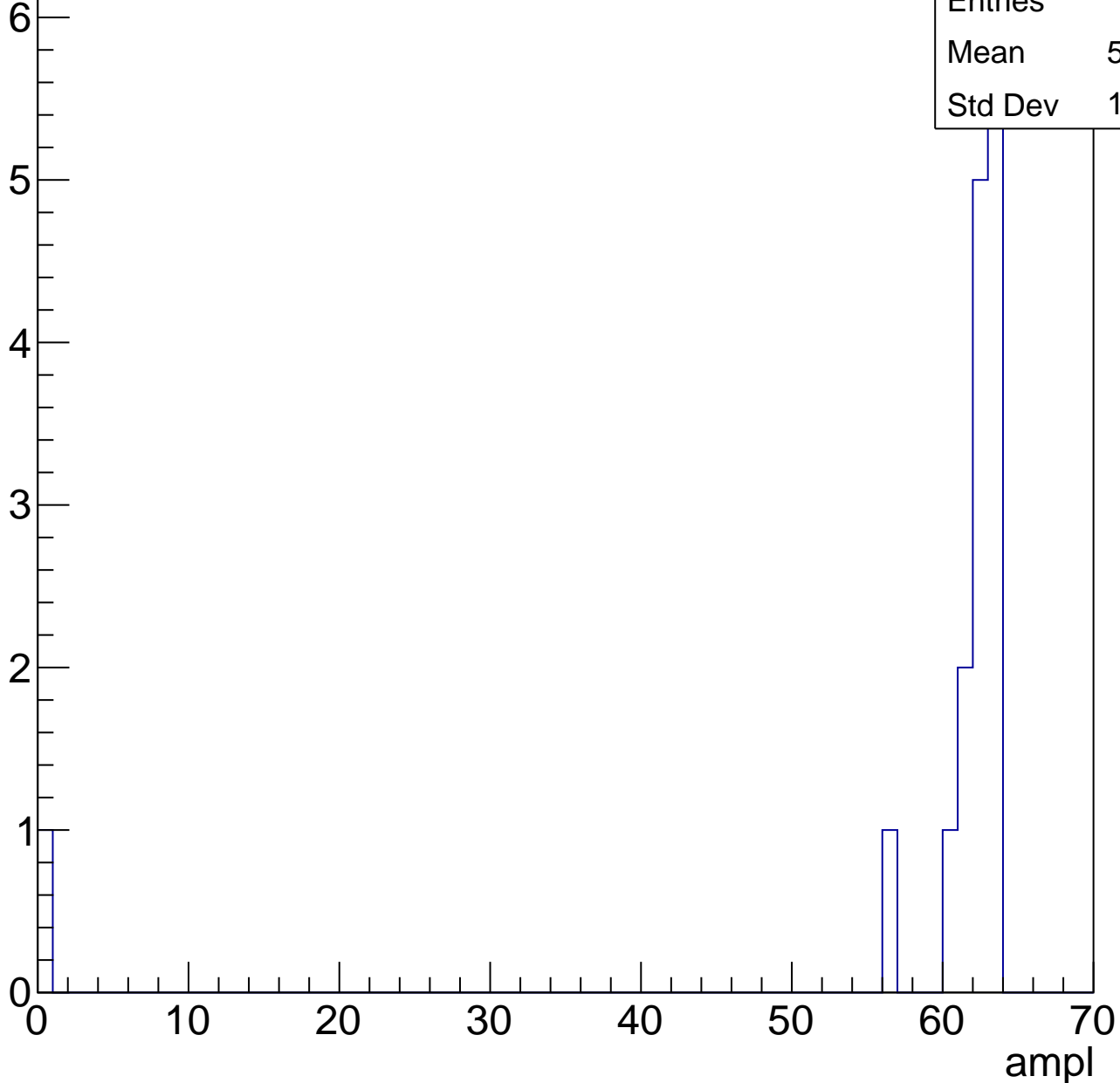


# B0L101S, U16-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 16    |
| Mean    | 57.88 |
| Std Dev | 15.04 |





# B0L101S, U16-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

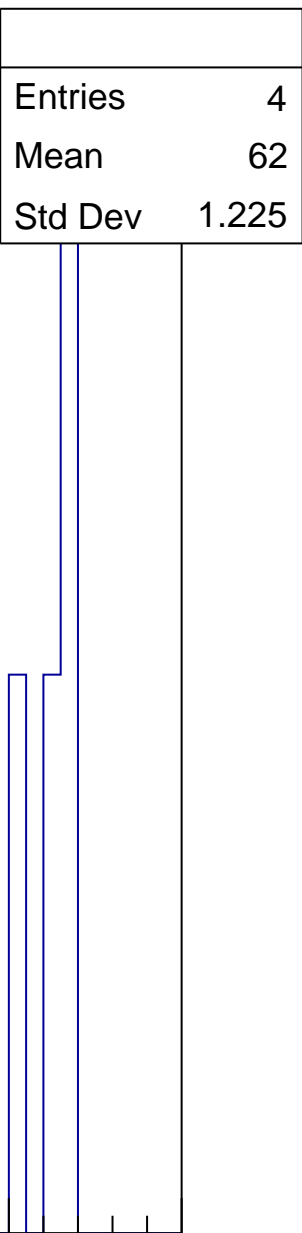
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 62    |
| Std Dev | 1.225 |

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

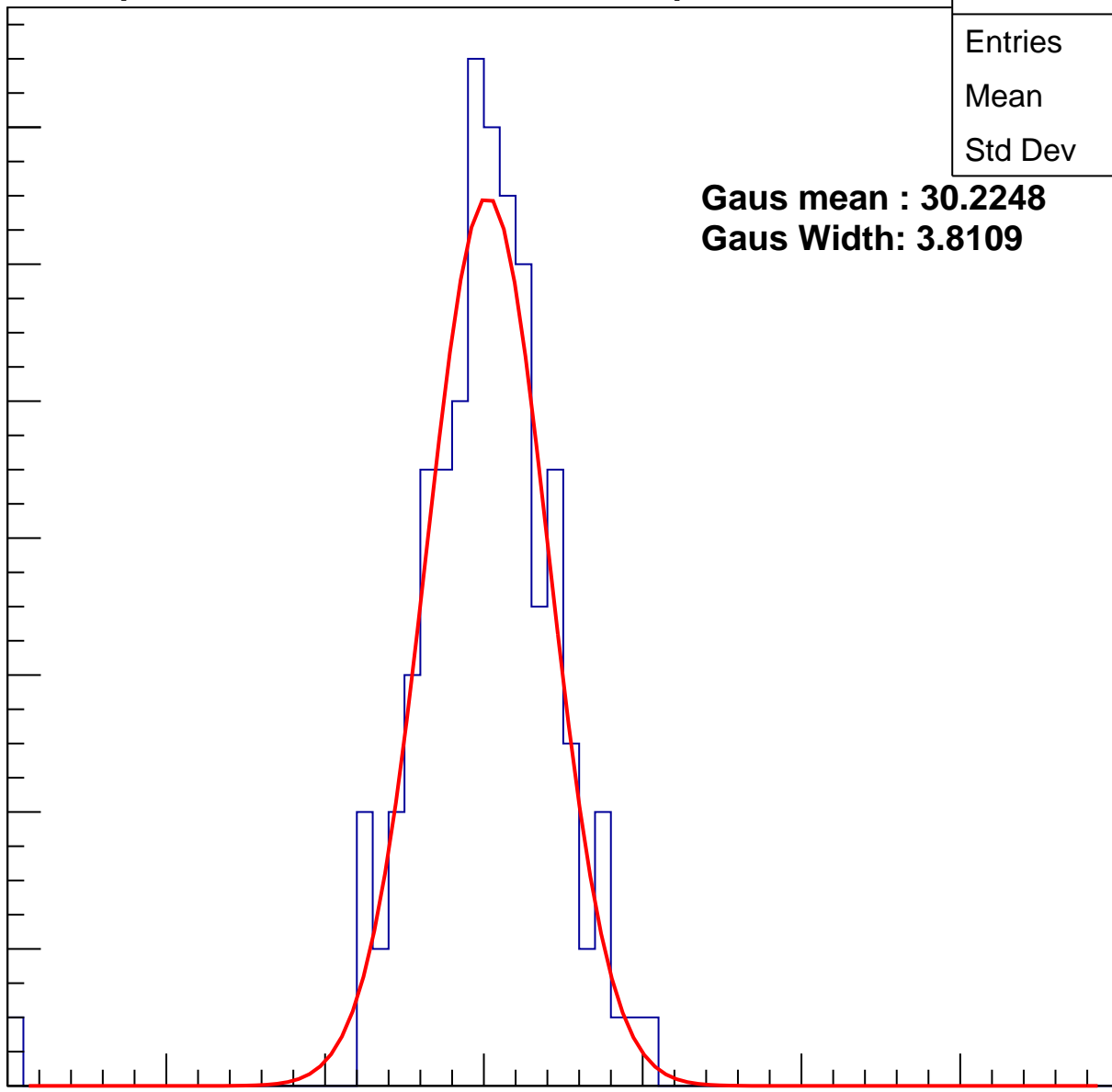
70

ampl

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 29.67 |
| Std Dev | 4.592 |

**Gaus mean : 30.2248**

**Gaus Width: 3.8109**



# B0L101S, U16-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 144   |
| Mean    | 36.9  |
| Std Dev | 4.037 |

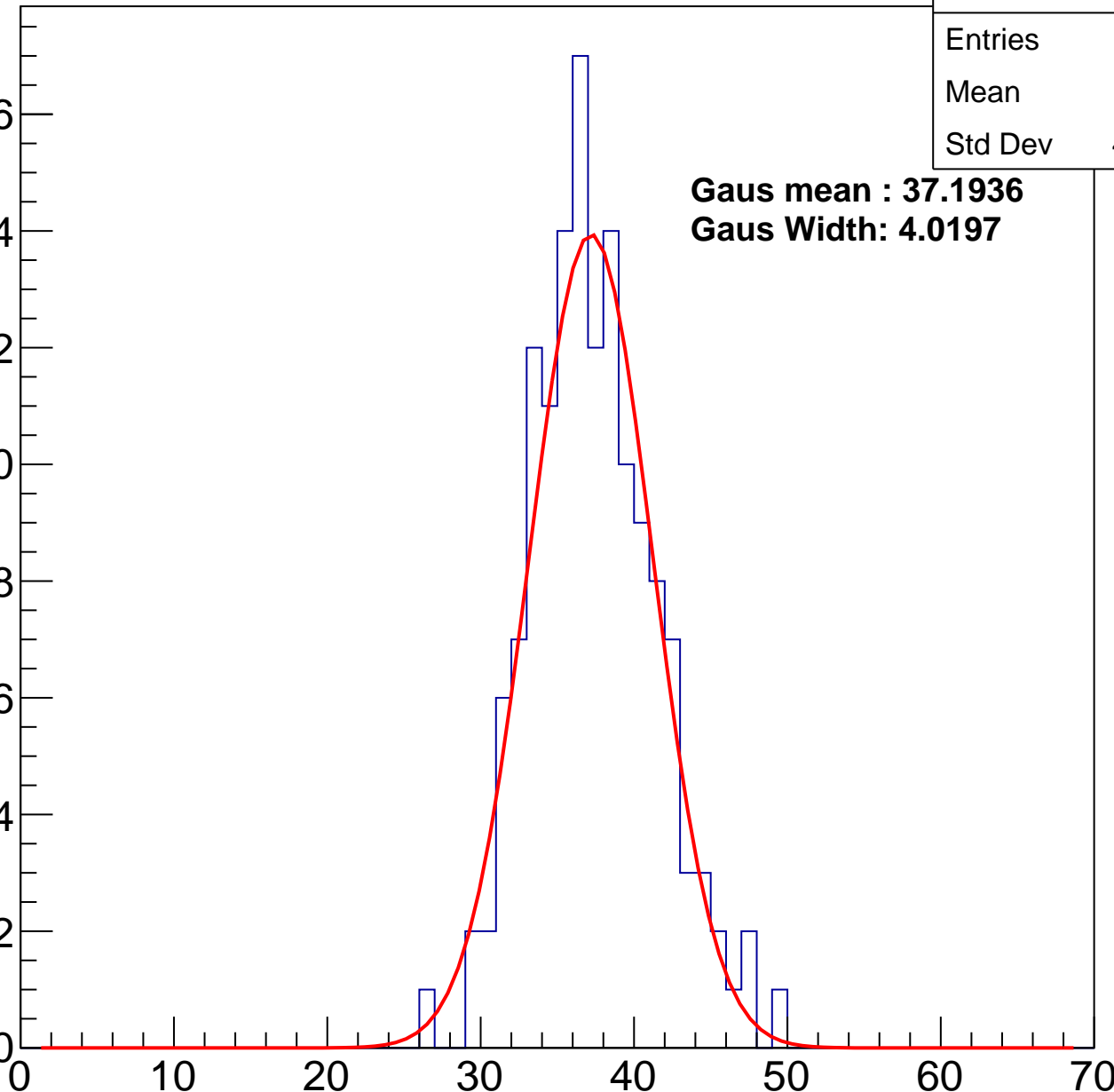
**Gaus mean : 37.1936**

**Gaus Width: 4.0197**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch107, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

|         |       |
|---------|-------|
| Entries | 99    |
| Mean    | 42.54 |
| Std Dev | 3.22  |

**Gaus mean : 43.0122**

**Gaus Width: 3.3253**

0

10

20

30

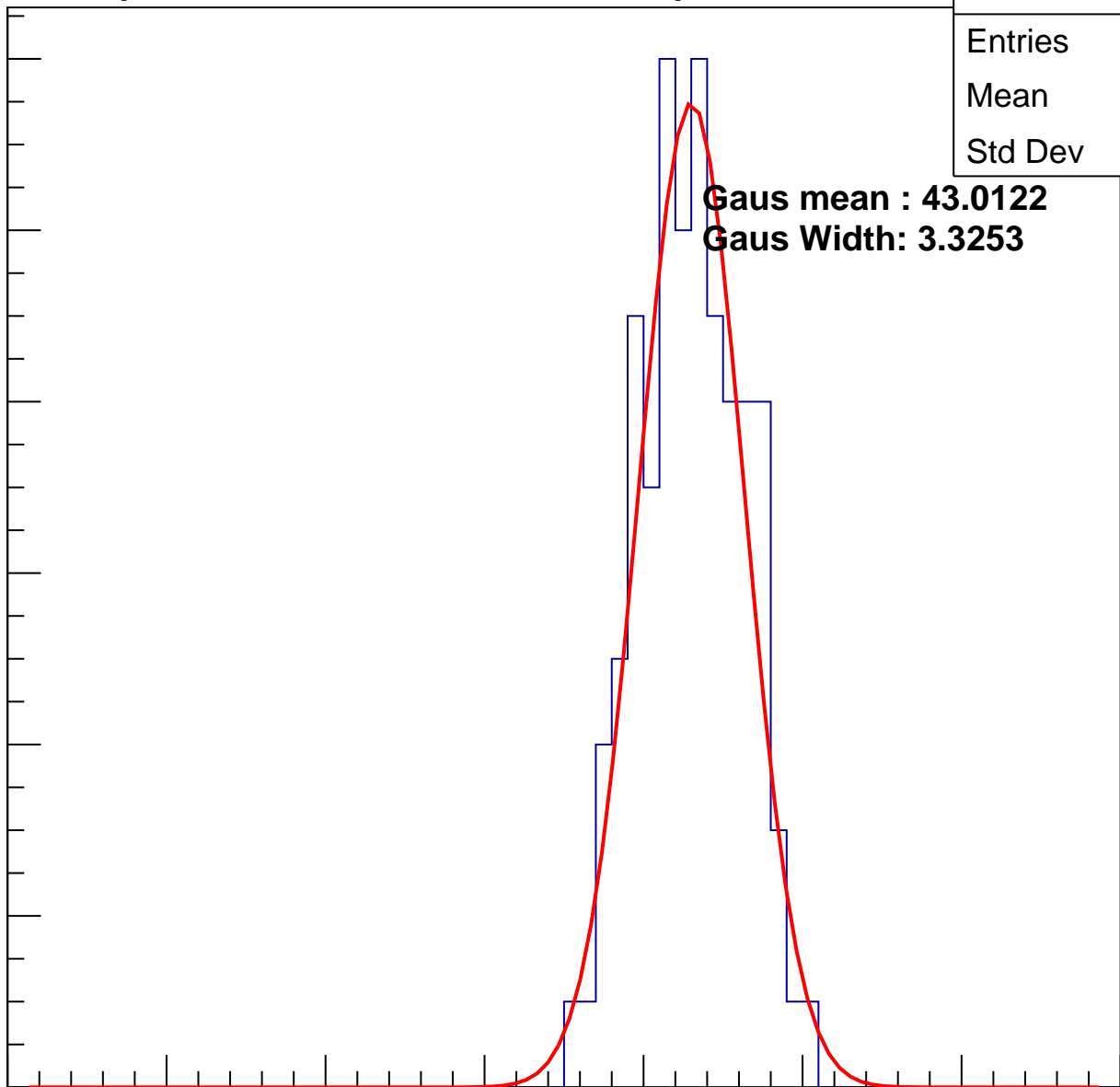
40

50

60

70

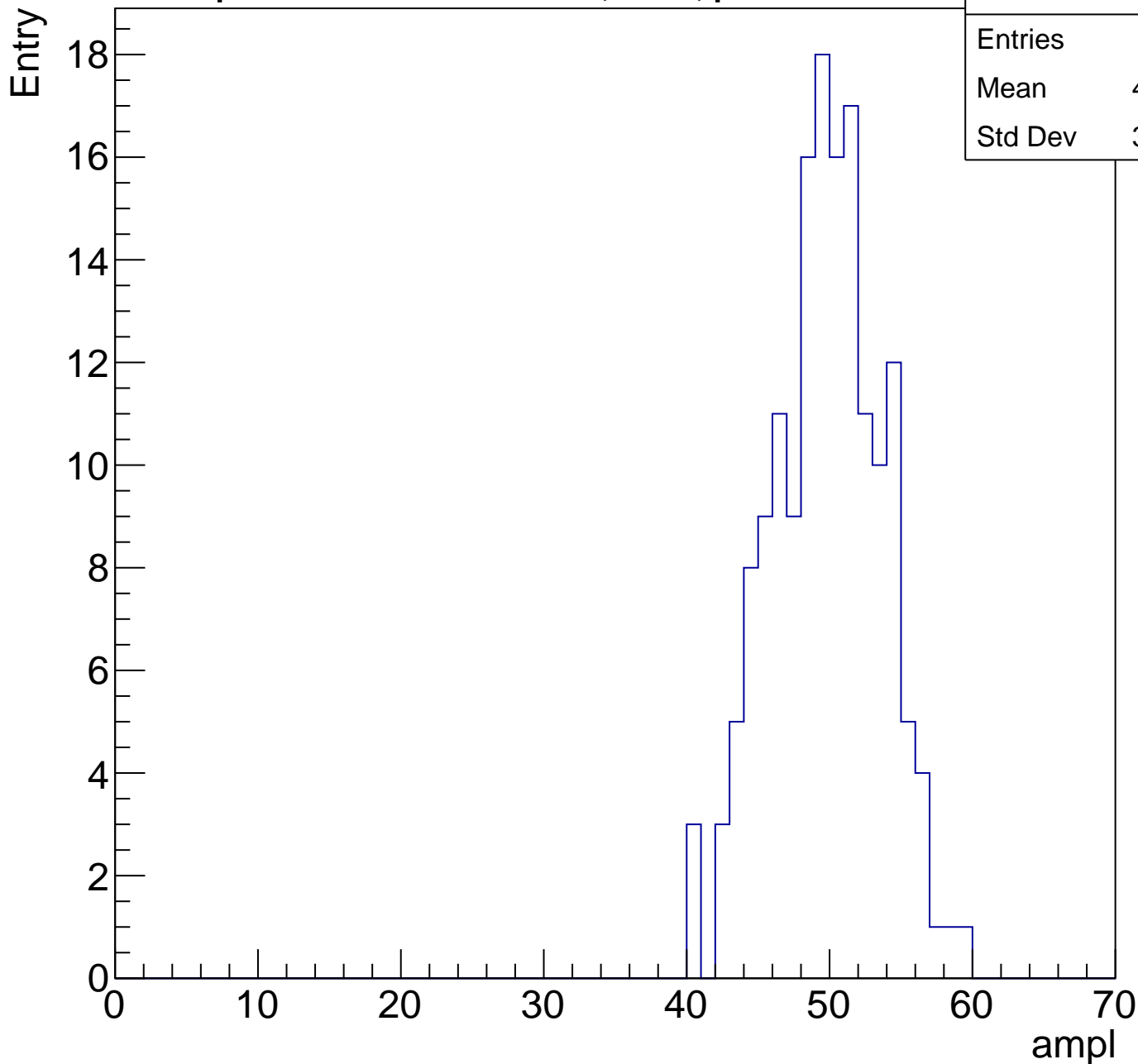
ampl



# B0L101S, U16-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

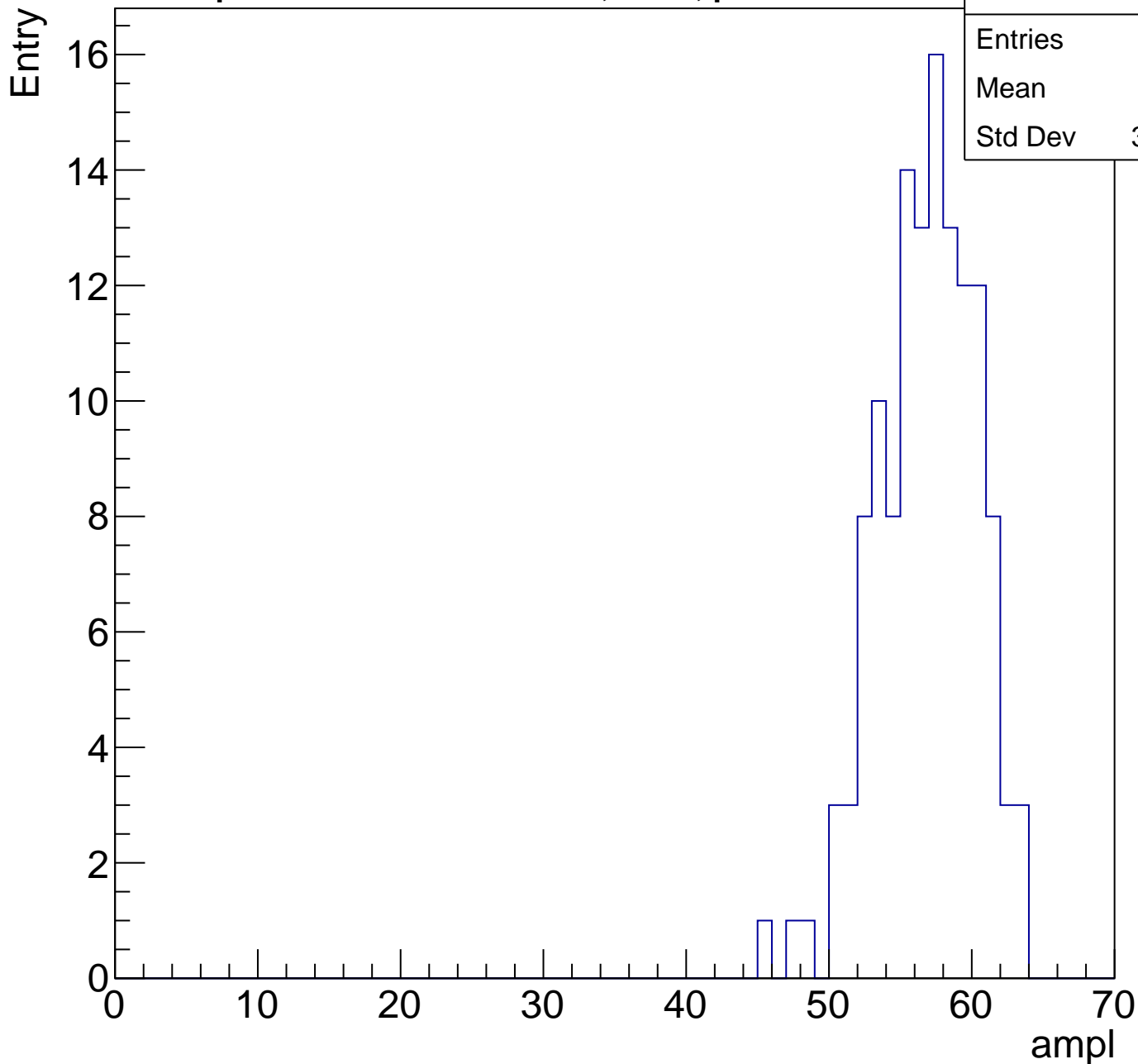
|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 49.29 |
| Std Dev | 3.804 |



# B0L101S, U16-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 56.4  |
| Std Dev | 3.427 |



# B0L101S, U16-ch107, adc5

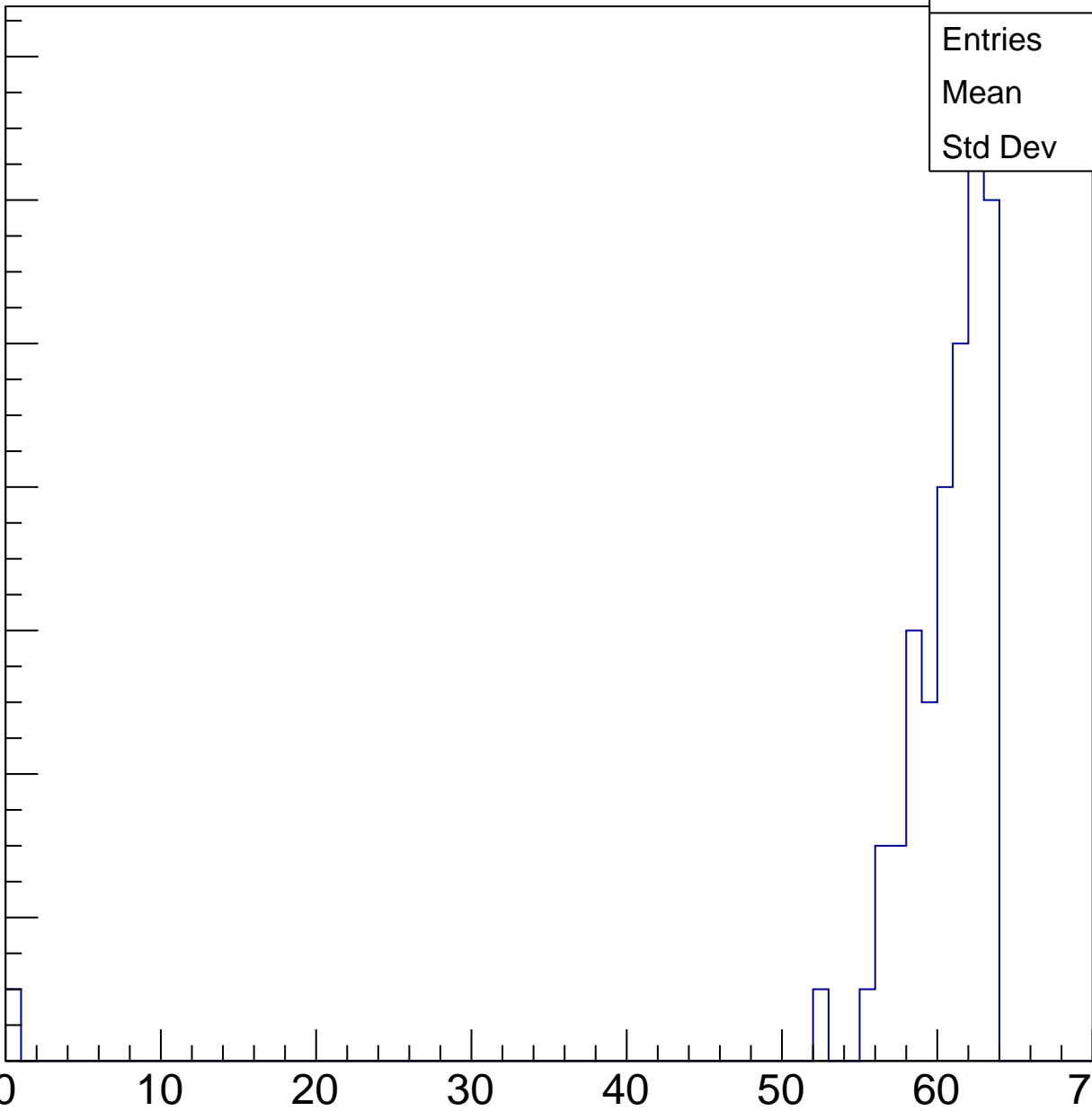
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 59.42 |
| Std Dev | 7.852 |

ampl

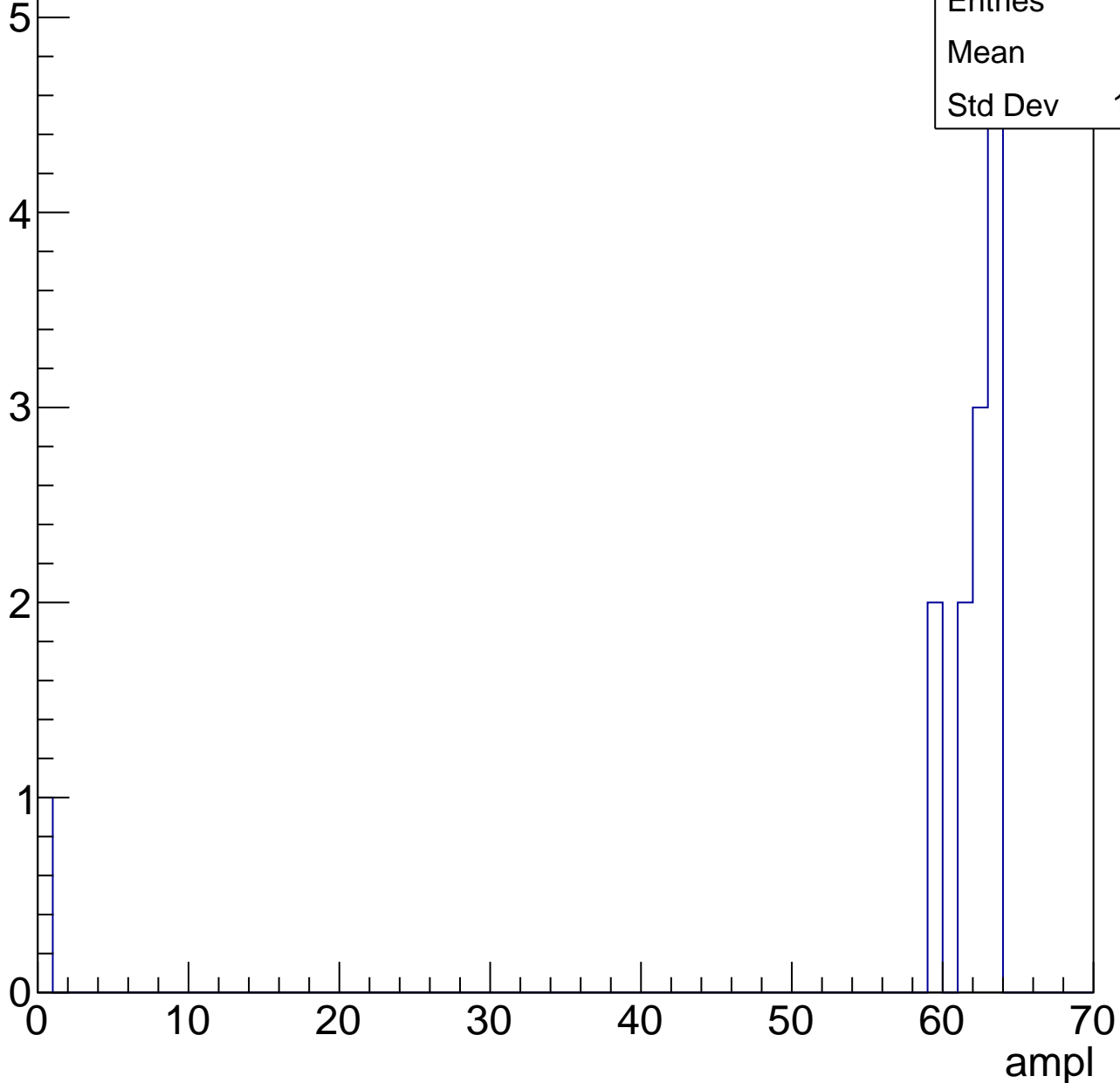


# B0L101S, U16-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 13    |
| Mean    | 57    |
| Std Dev | 16.51 |





# B0L101S, U16-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch108, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 28.33 |
| Std Dev | 4.826 |

**Gaus mean : 28.0113**

**Gaus Width: 3.8069**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

136

Mean

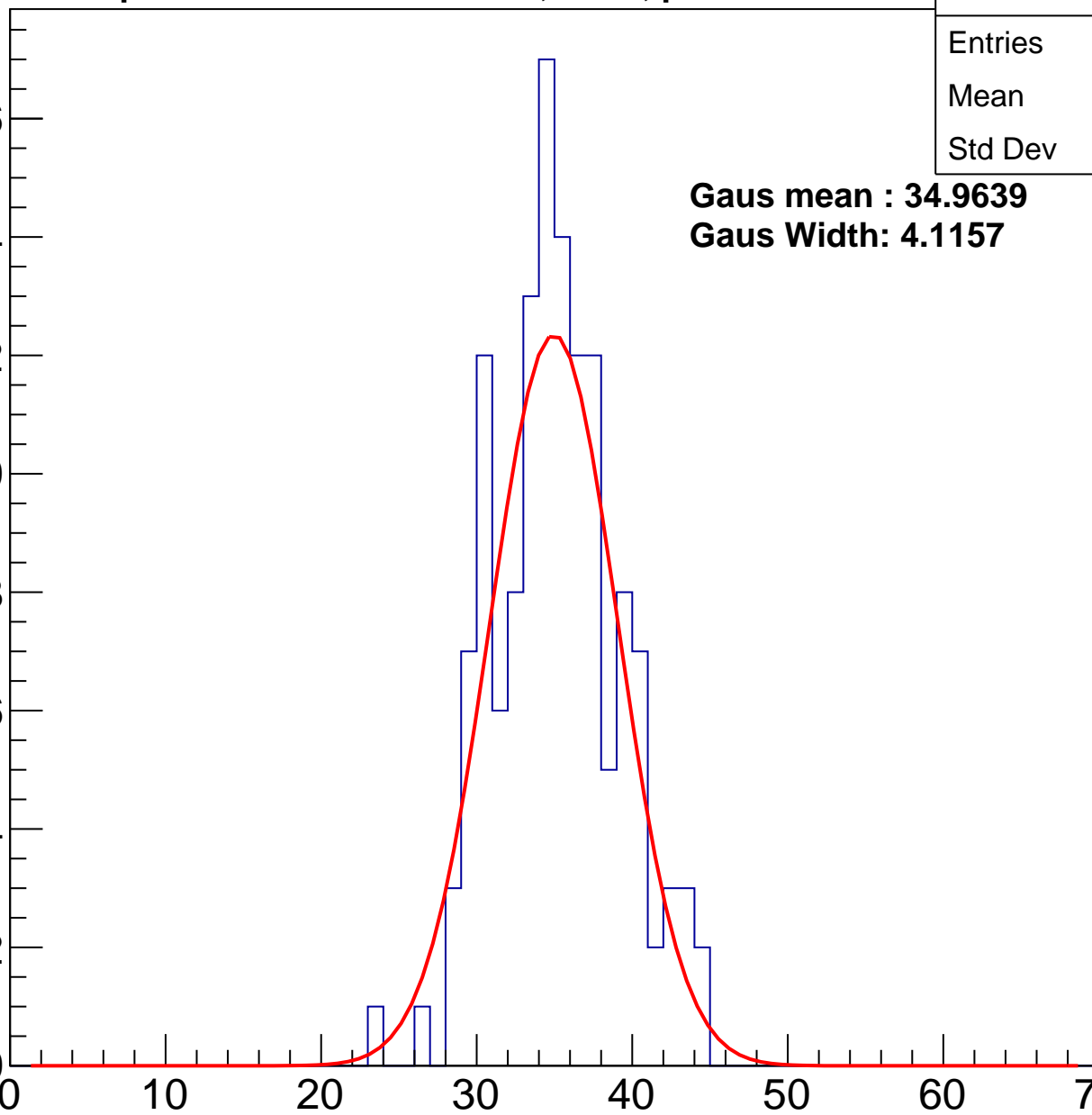
34.69

Std Dev

3.957

**Gaus mean : 34.9639**

**Gaus Width: 4.1157**



# B0L101S, U16-ch108, adc2

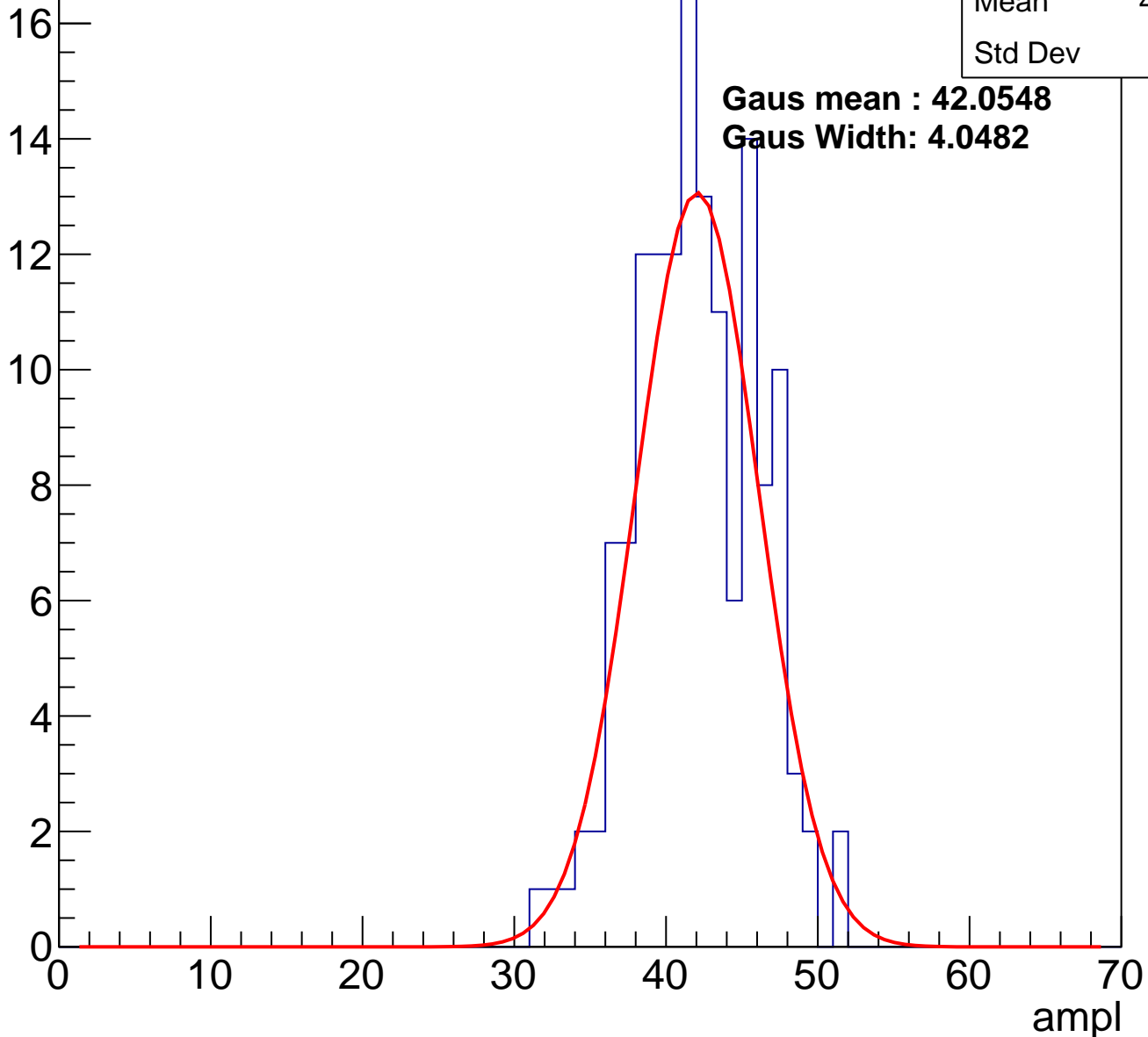
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 143   |
| Mean    | 41.55 |
| Std Dev | 3.92  |

**Gaus mean : 42.0548**

**Gaus Width: 4.0482**

Entry



# B0L101S, U16-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 48.18 |
| Std Dev | 3.651 |

Entry

12

10

8

6

4

2

0

0

10

20

30

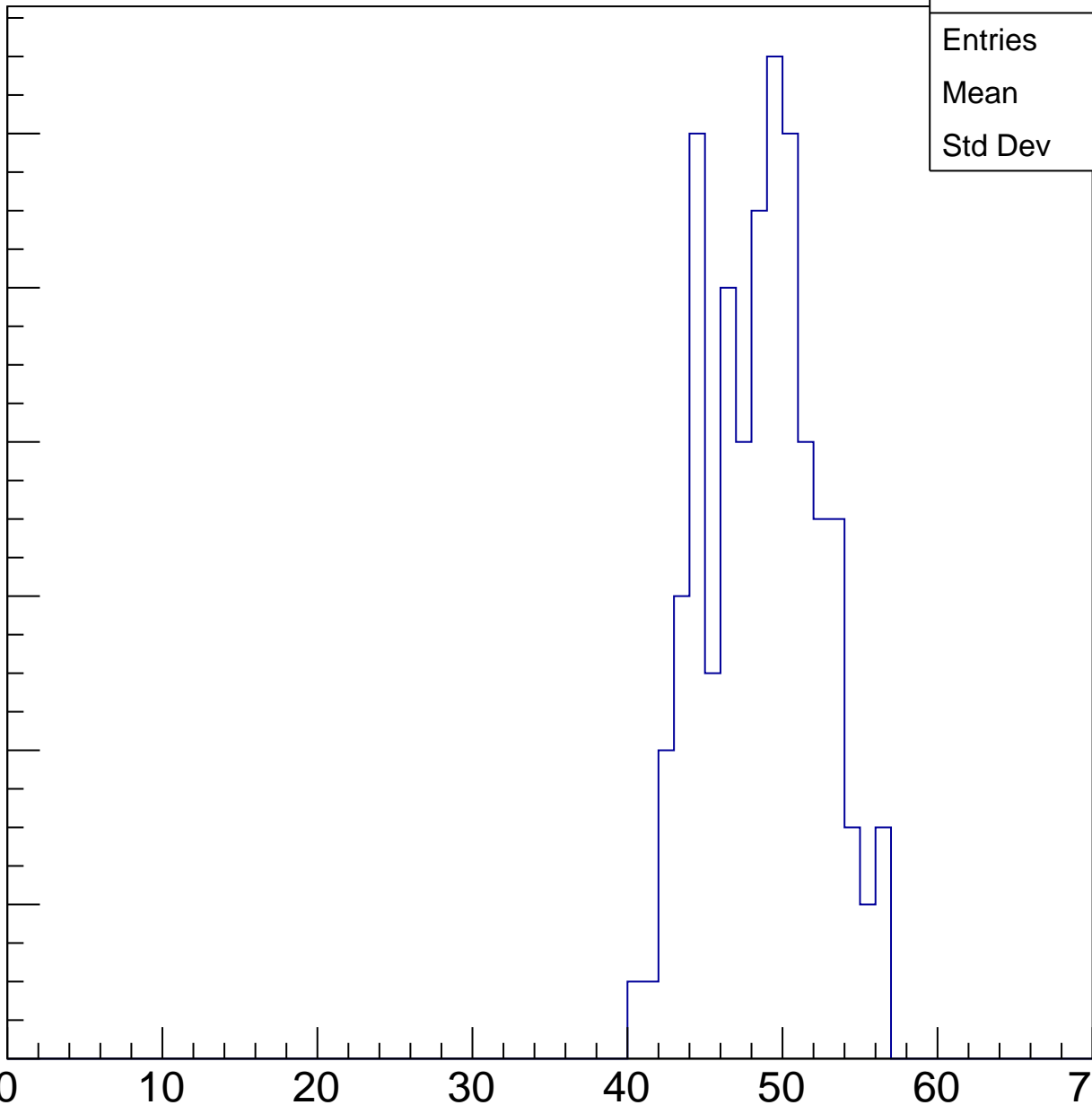
40

50

60

70

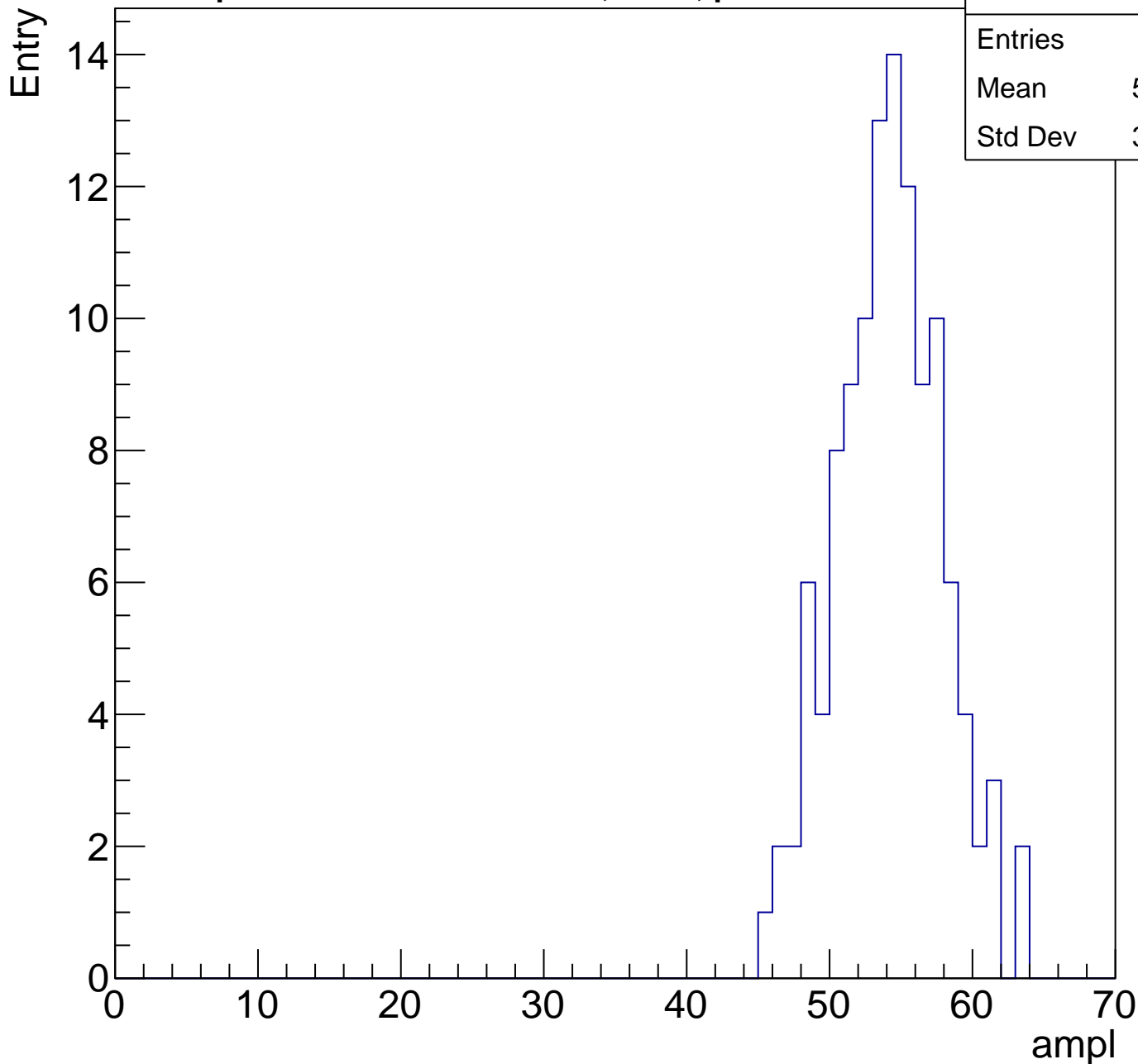
ampl



# B0L101S, U16-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 117   |
| Mean    | 53.73 |
| Std Dev | 3.688 |



# B0L101S, U16-ch108, adc5

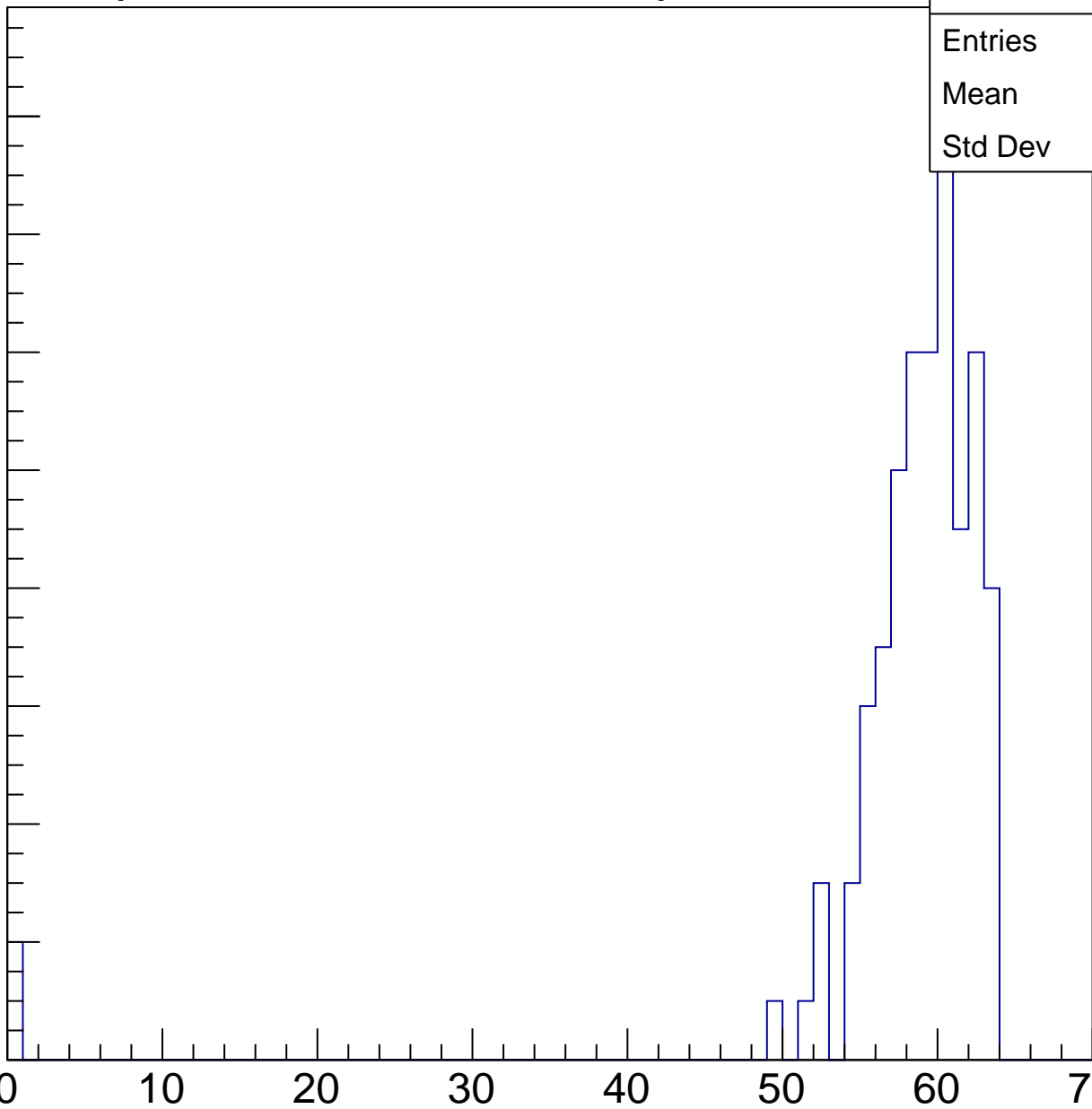
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 103   |
| Mean    | 57.58 |
| Std Dev | 8.613 |

ampl

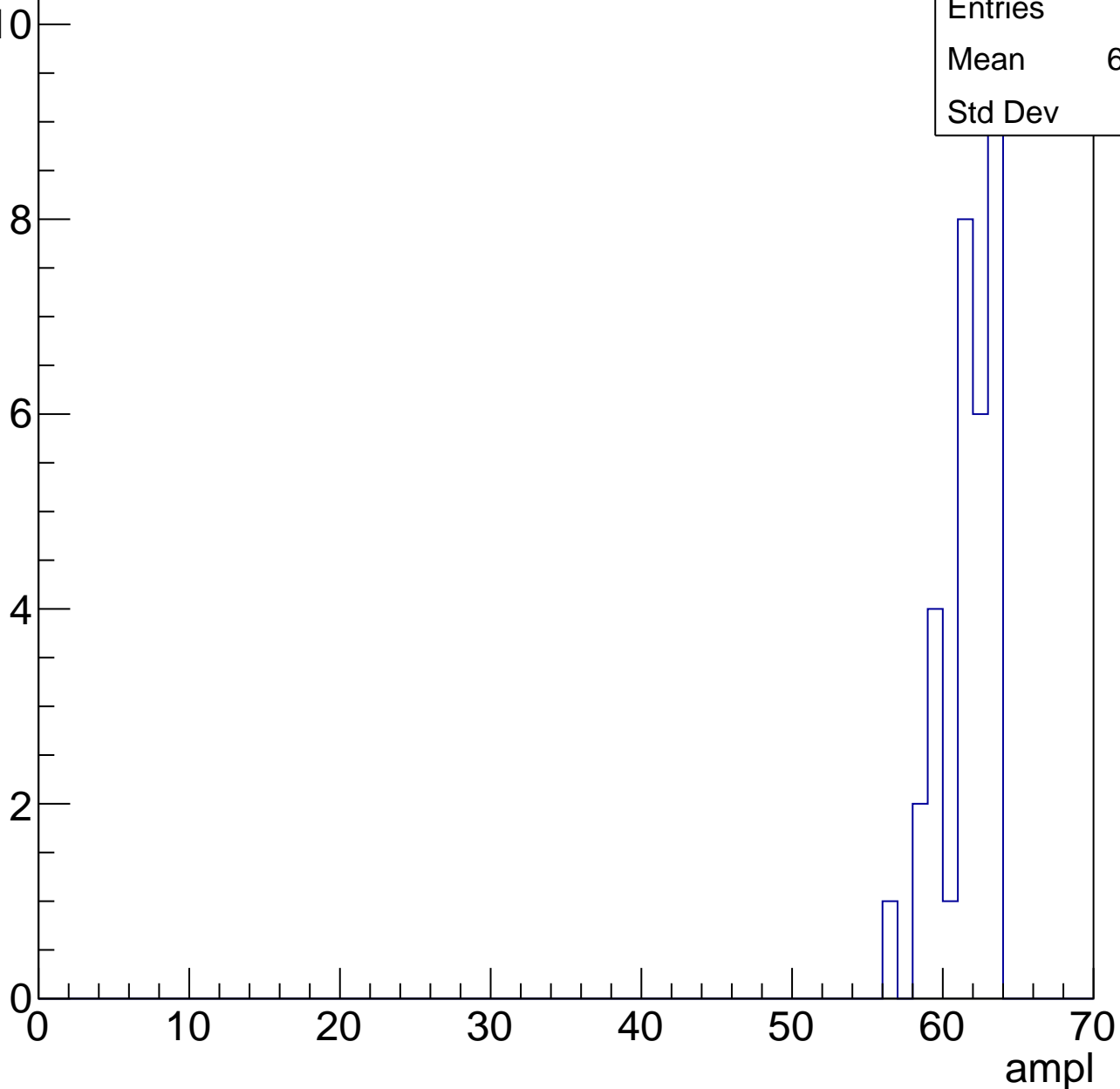


# B0L101S, U16-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 32    |
| Mean    | 61.19 |
| Std Dev | 1.81  |

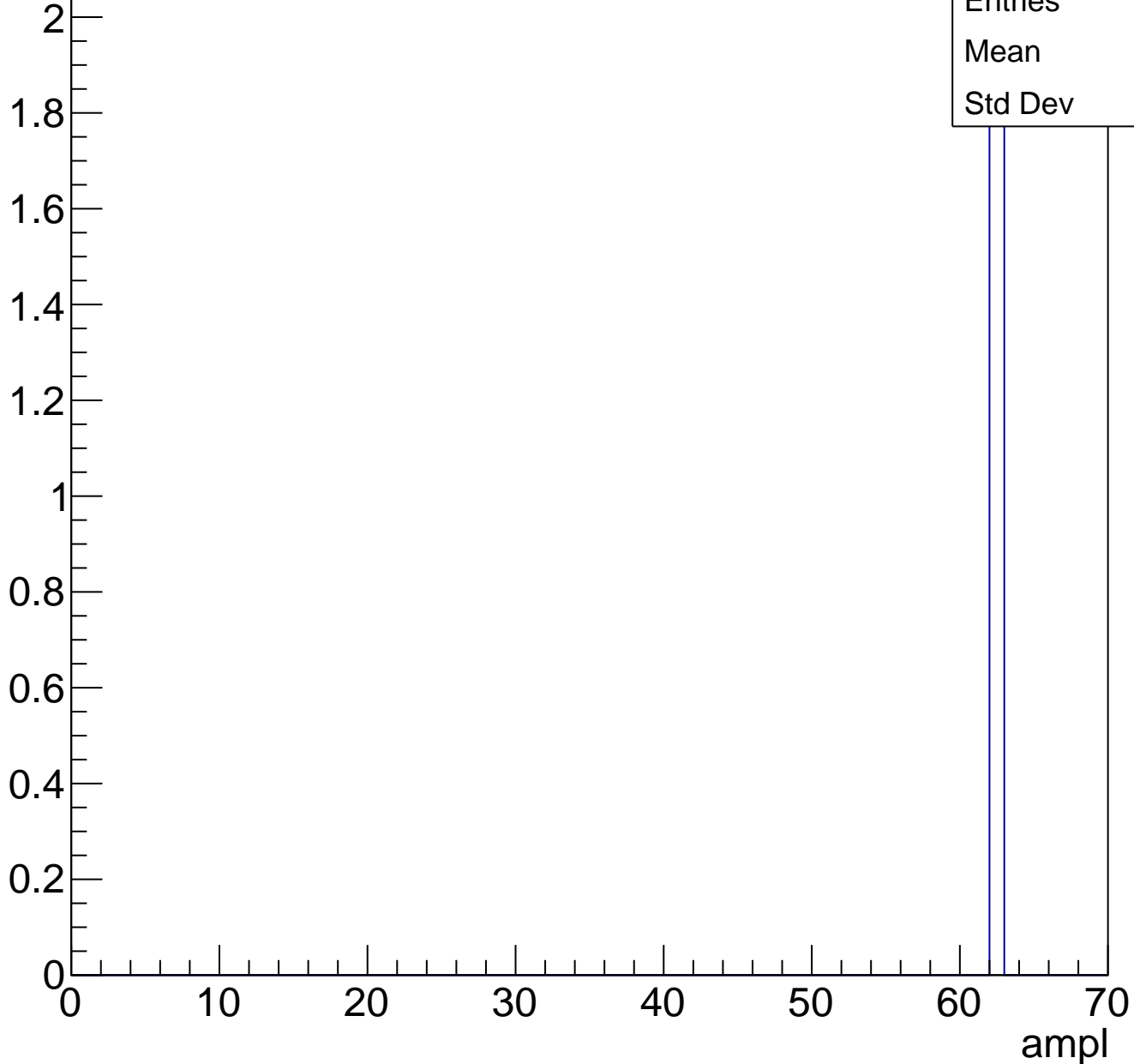




# B0L101S, U16-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 28.44 |
| Std Dev | 4.22  |

**Gaus mean : 29.2859**

**Gaus Width: 3.6010**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

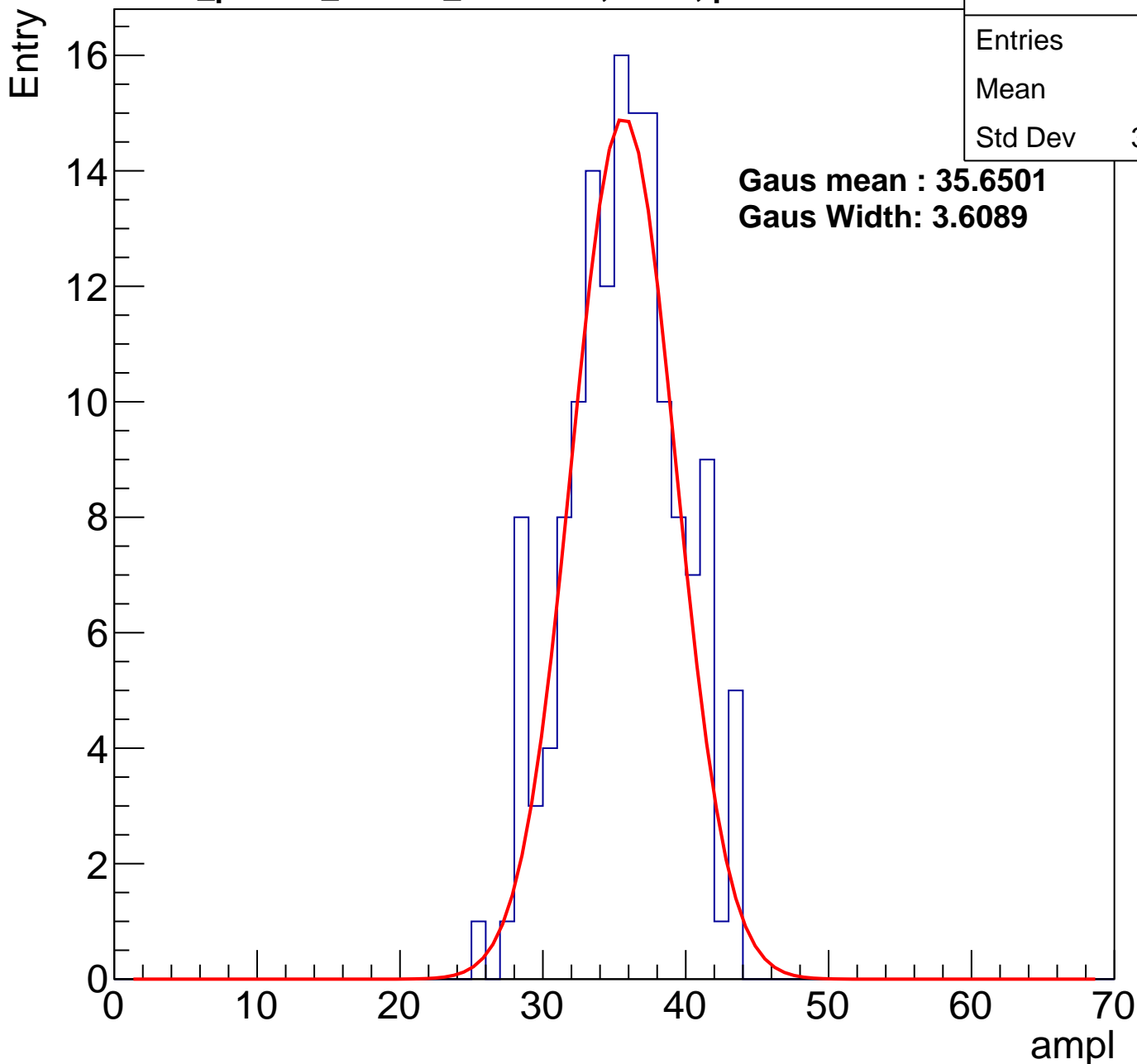
# B0L101S, U16-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 35.2  |
| Std Dev | 3.873 |

**Gaus mean : 35.6501**

**Gaus Width: 3.6089**



# B0L101S, U16-ch109, adc2

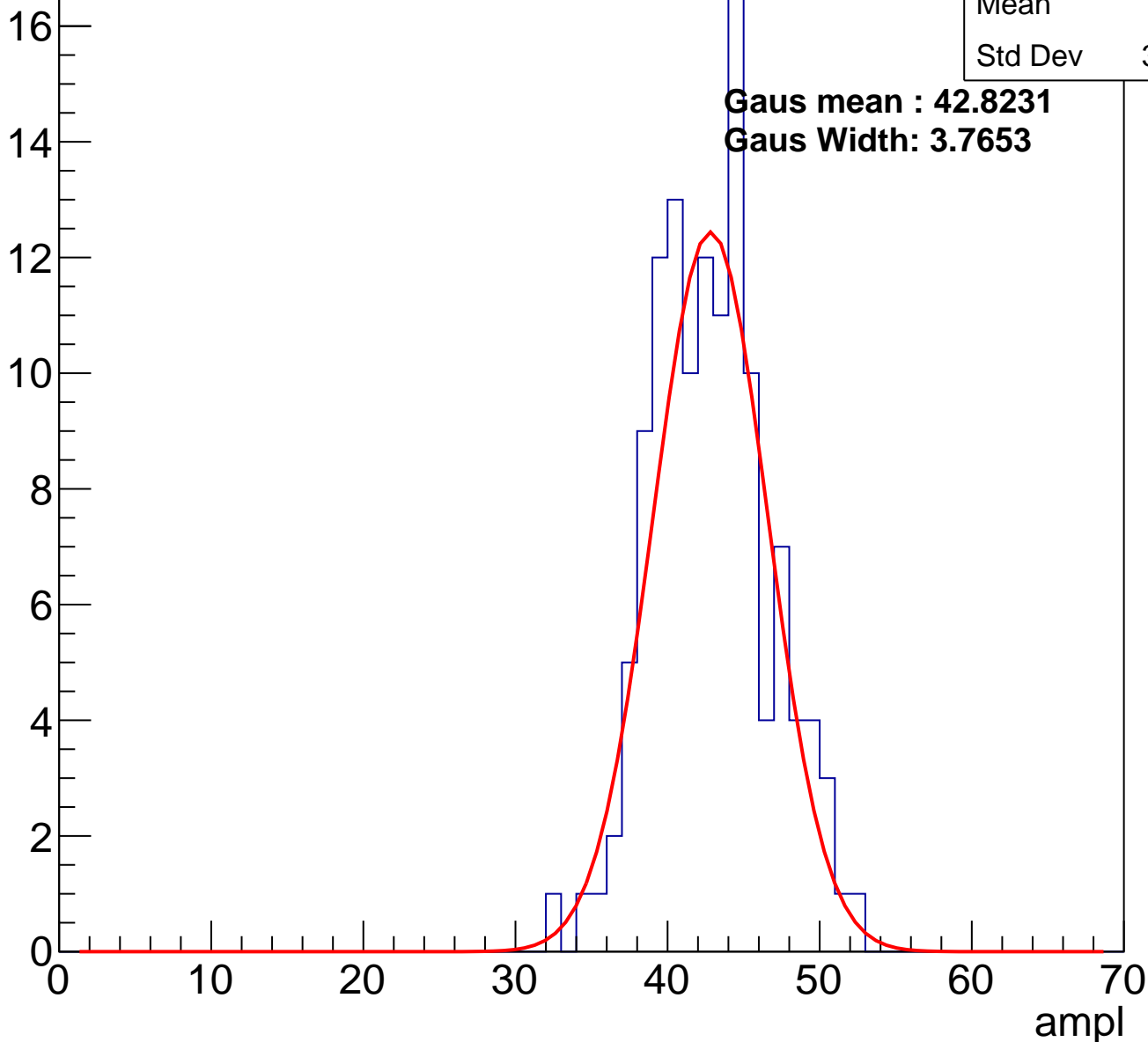
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 128   |
| Mean    | 42.4  |
| Std Dev | 3.776 |

**Gaus mean : 42.8231**

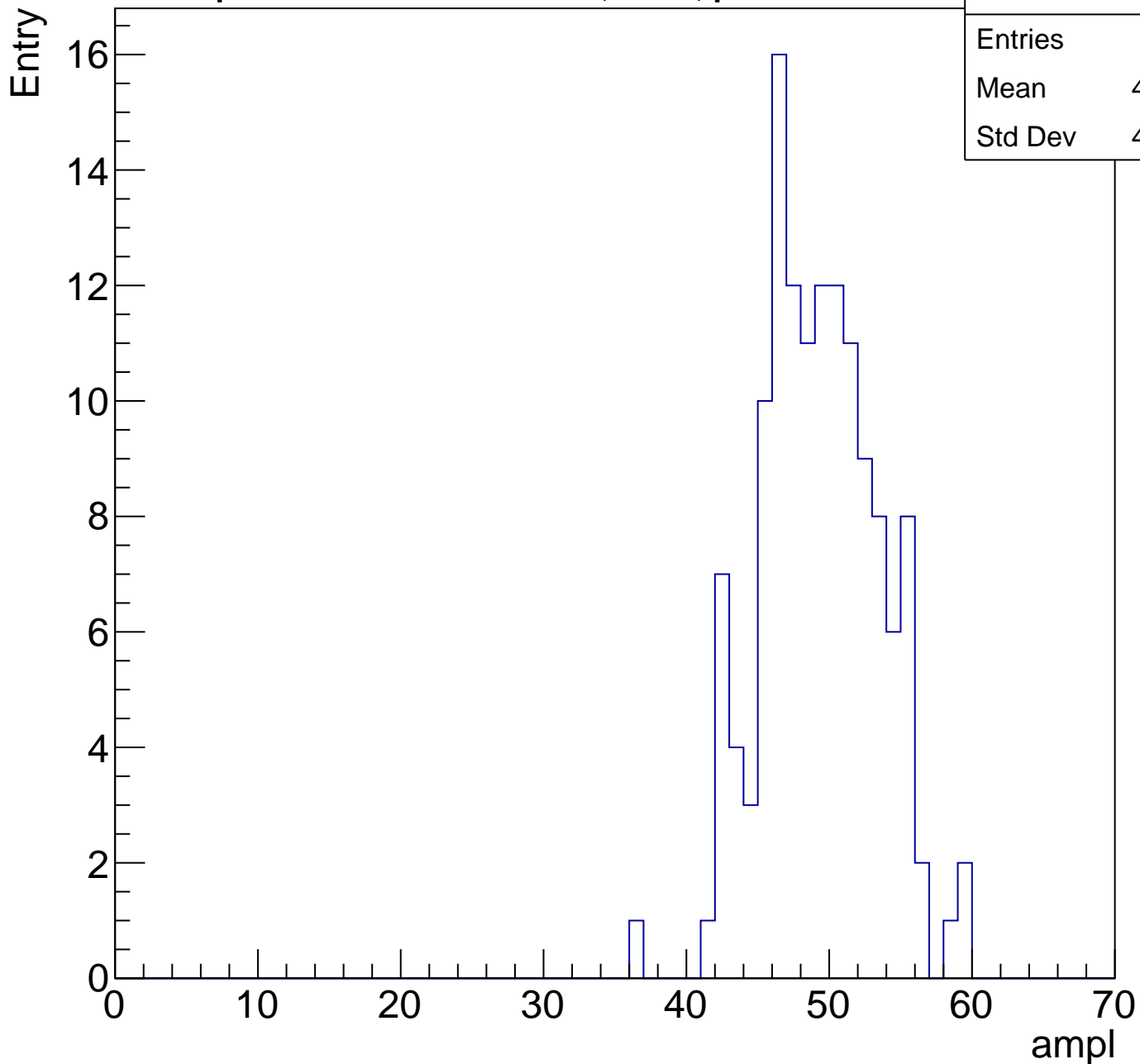
**Gaus Width: 3.7653**



# B0L101S, U16-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

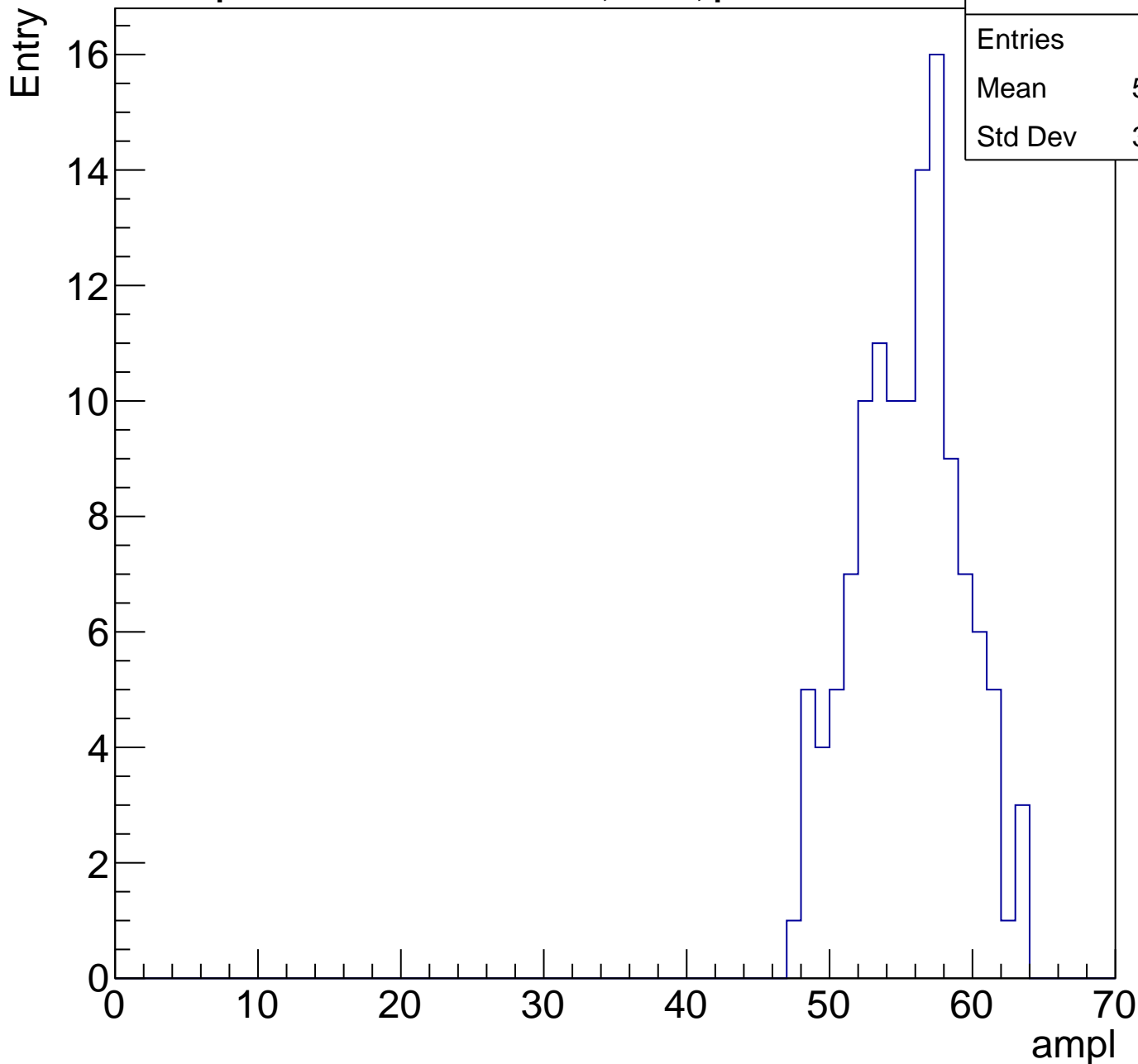
|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 48.87 |
| Std Dev | 4.056 |



# B0L101S, U16-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 55.08 |
| Std Dev | 3.676 |



# B0L101S, U16-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

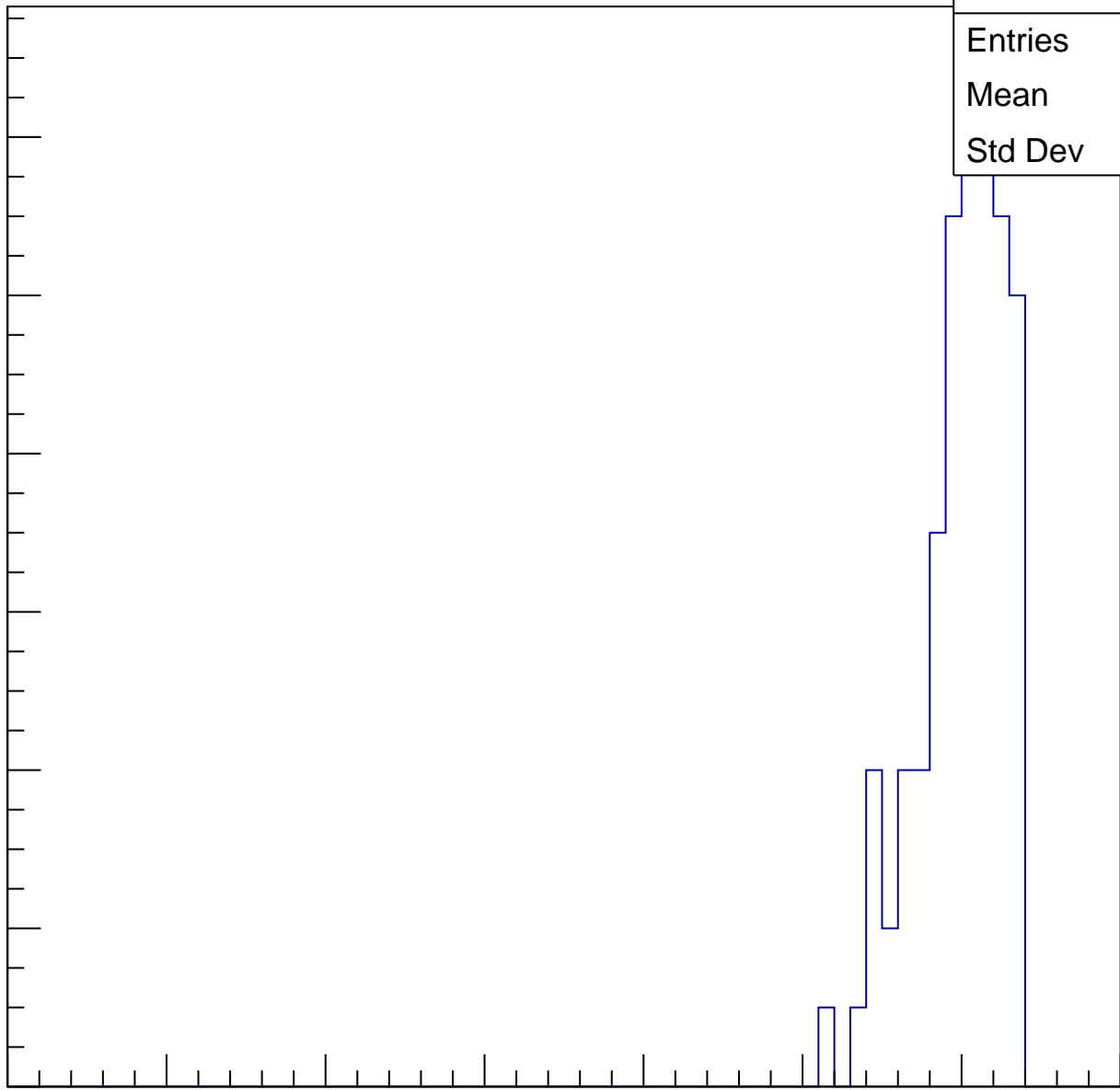
2

0

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 59.52 |
| Std Dev | 2.72  |

ampl

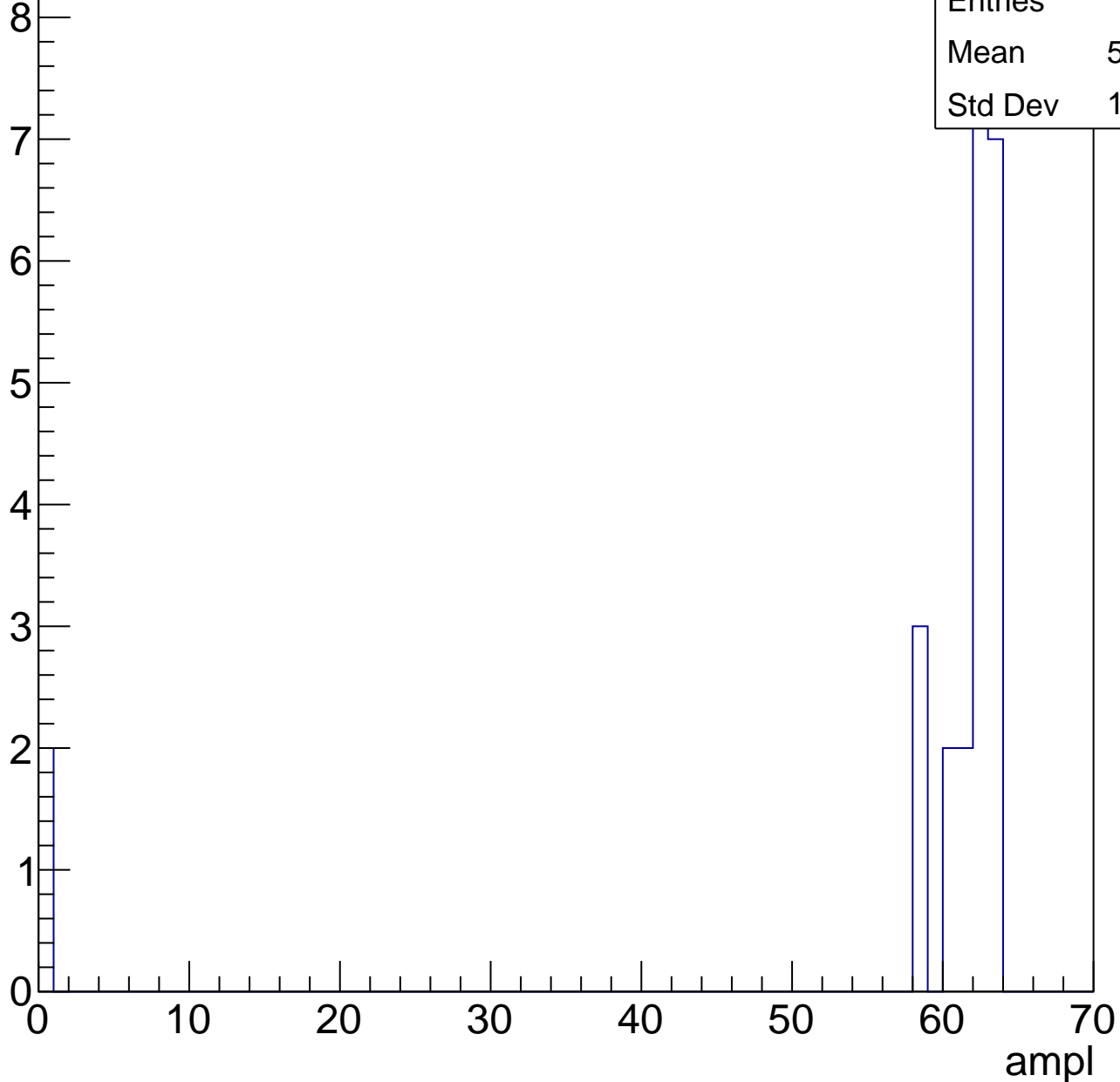
0 10 20 30 40 50 60 70



# B0L101S, U16-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

124

Mean

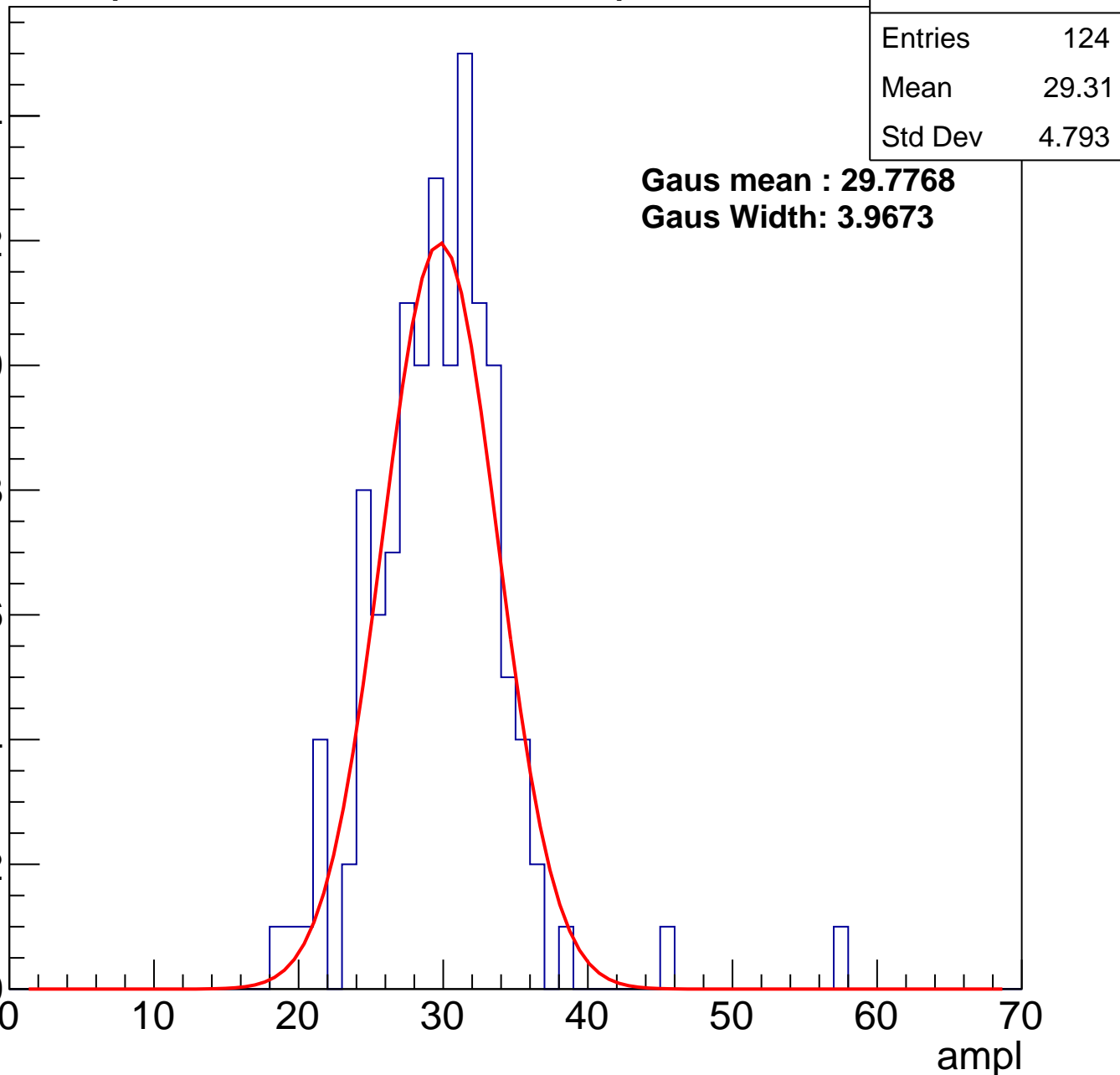
29.31

Std Dev

4.793

**Gaus mean : 29.7768**

**Gaus Width: 3.9673**



# B0L101S, U16-ch110, adc1

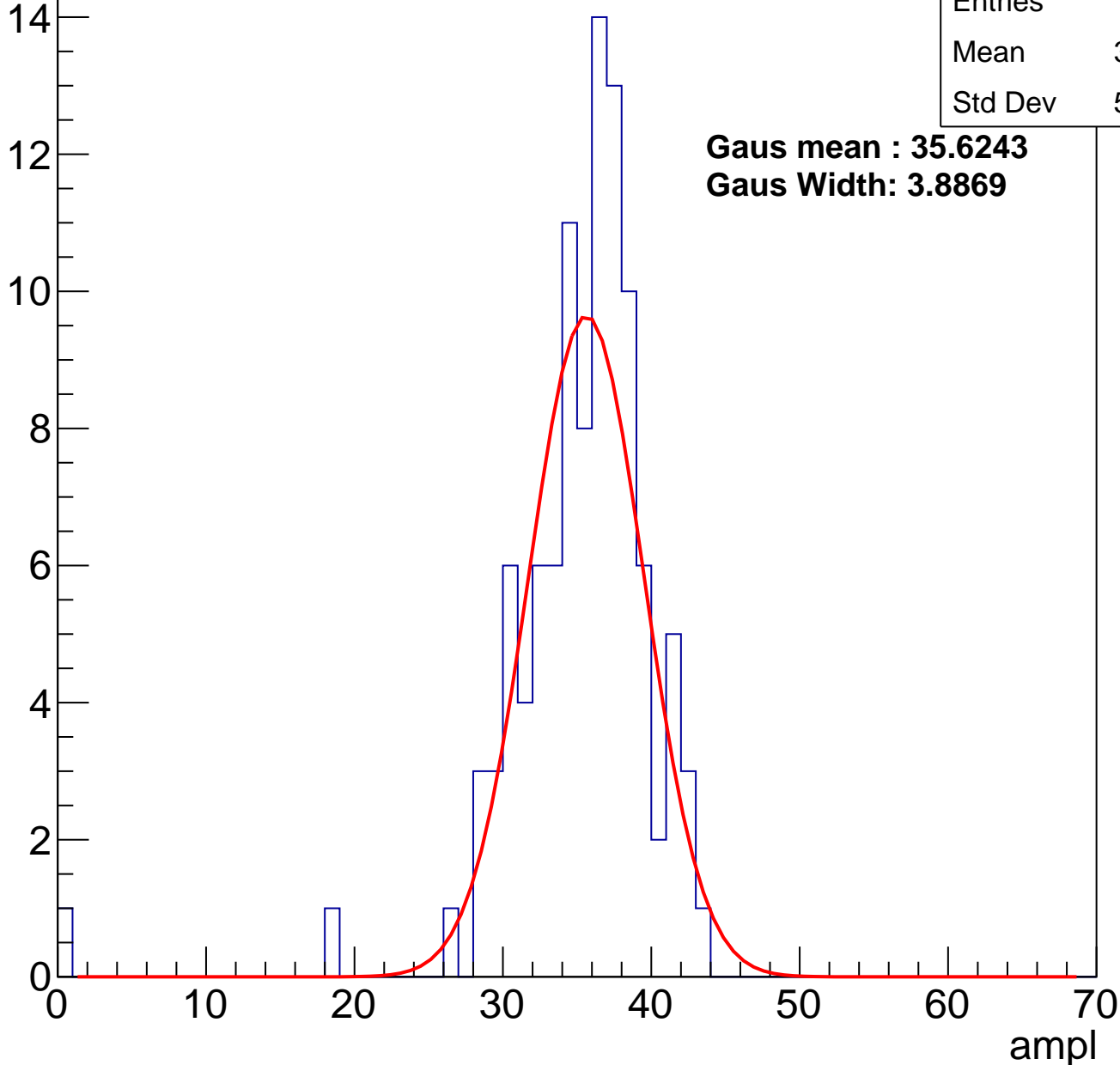
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 104   |
| Mean    | 34.77 |
| Std Dev | 5.226 |

**Gaus mean : 35.6243**

**Gaus Width: 3.8869**

Entry



# B0L101S, U16-ch110, adc2

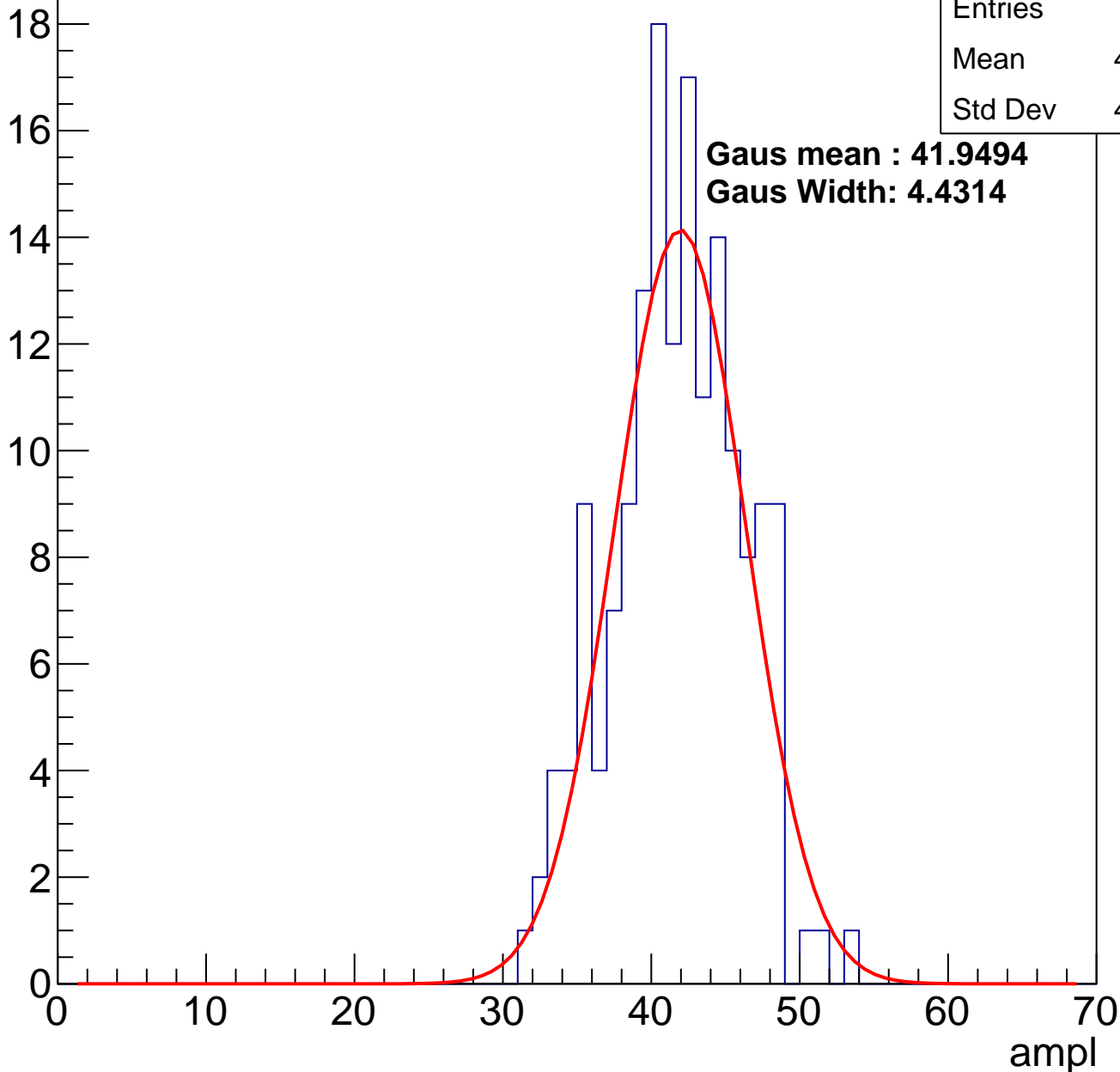
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 164   |
| Mean    | 41.29 |
| Std Dev | 4.294 |

**Gaus mean : 41.9494**

**Gaus Width: 4.4314**

Entry

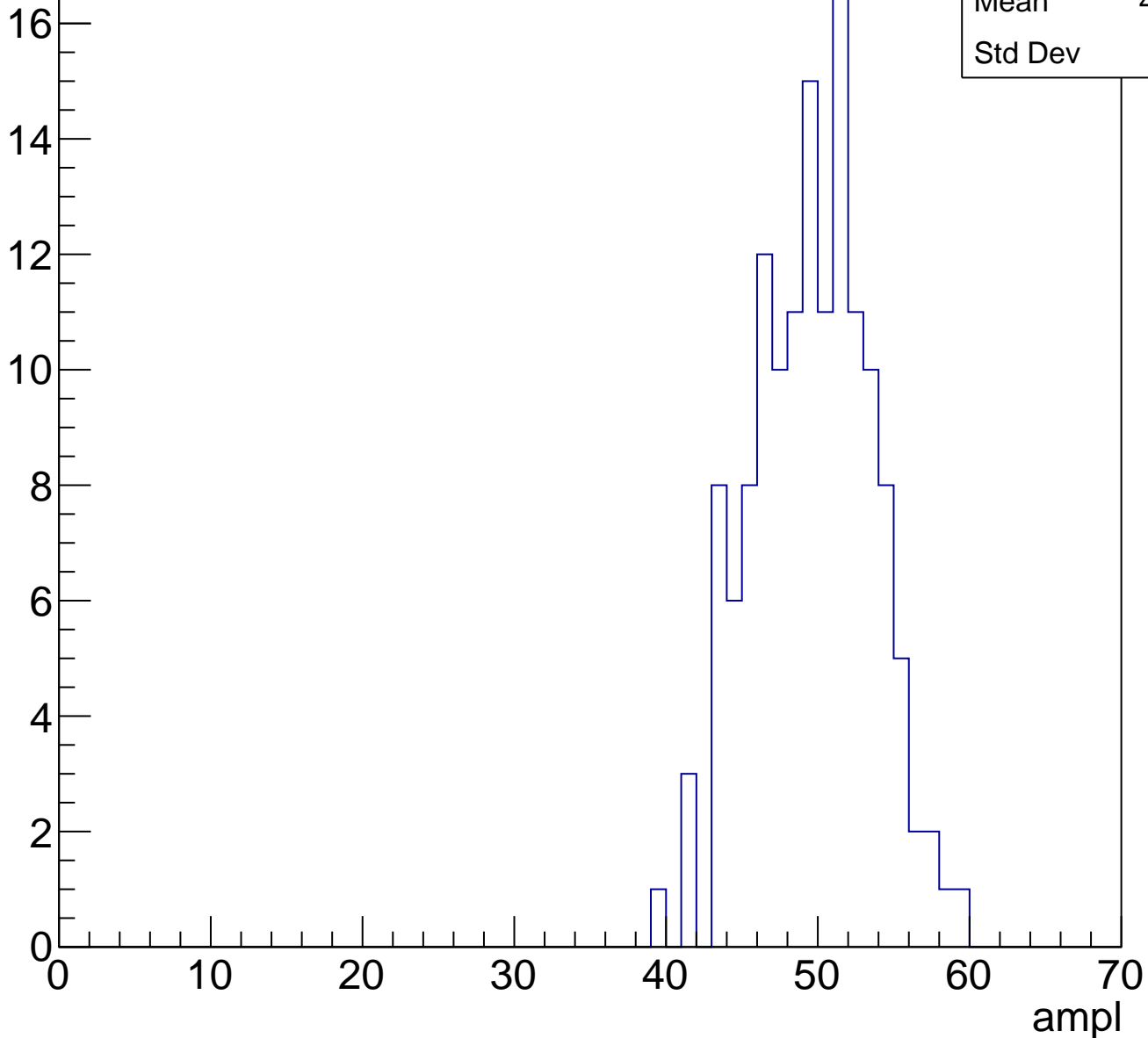


# B0L101S, U16-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 49.18 |
| Std Dev | 3.88  |



# B0L101S, U16-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 98    |
| Mean    | 54.9  |
| Std Dev | 3.649 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

2

4

8

8

14

12

9

1

1

1

1

1

1

1

1

1

# B0L101S, U16-ch110, adc5

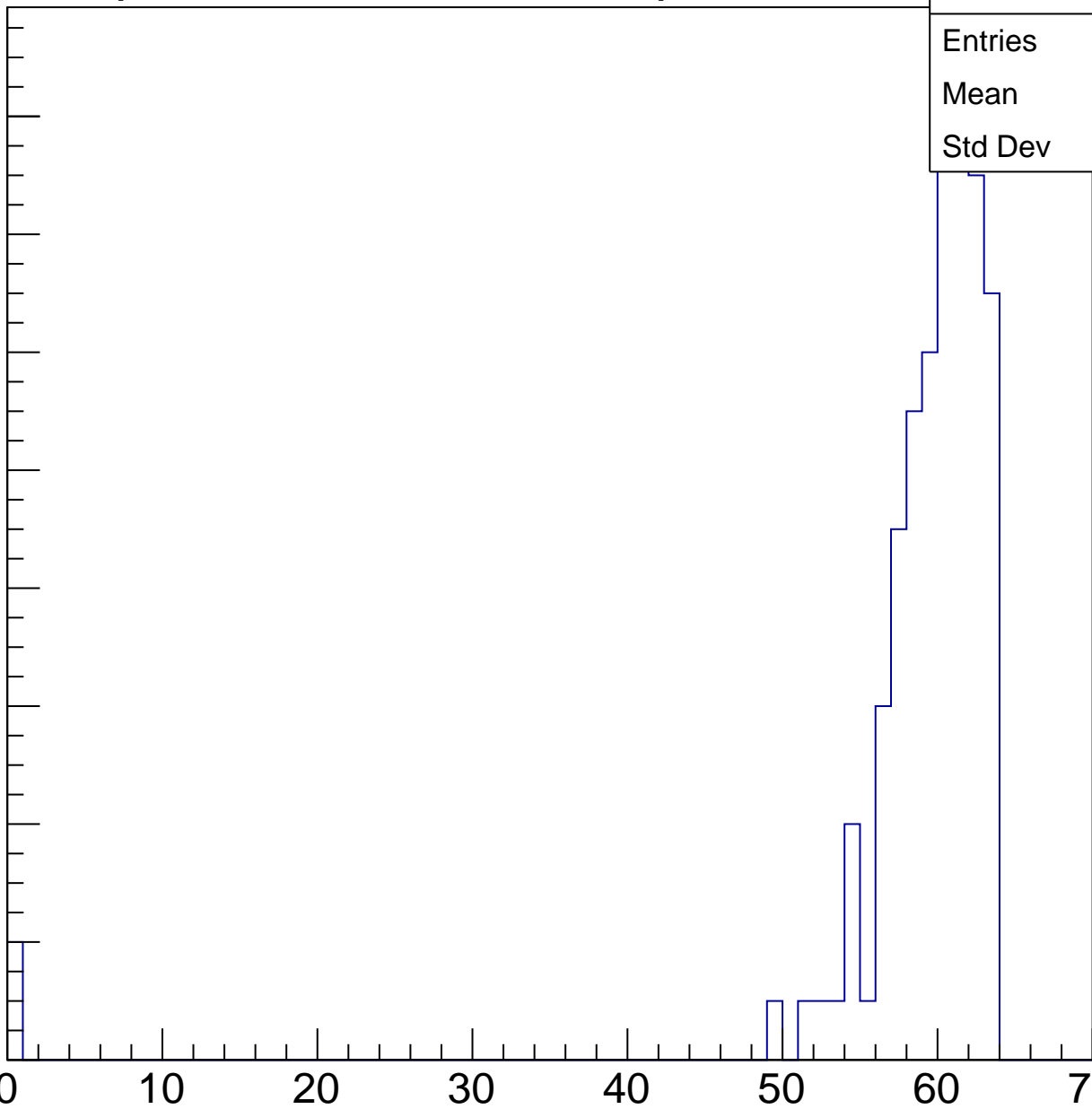
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 110   |
| Mean    | 58.34 |
| Std Dev | 8.424 |

ampl

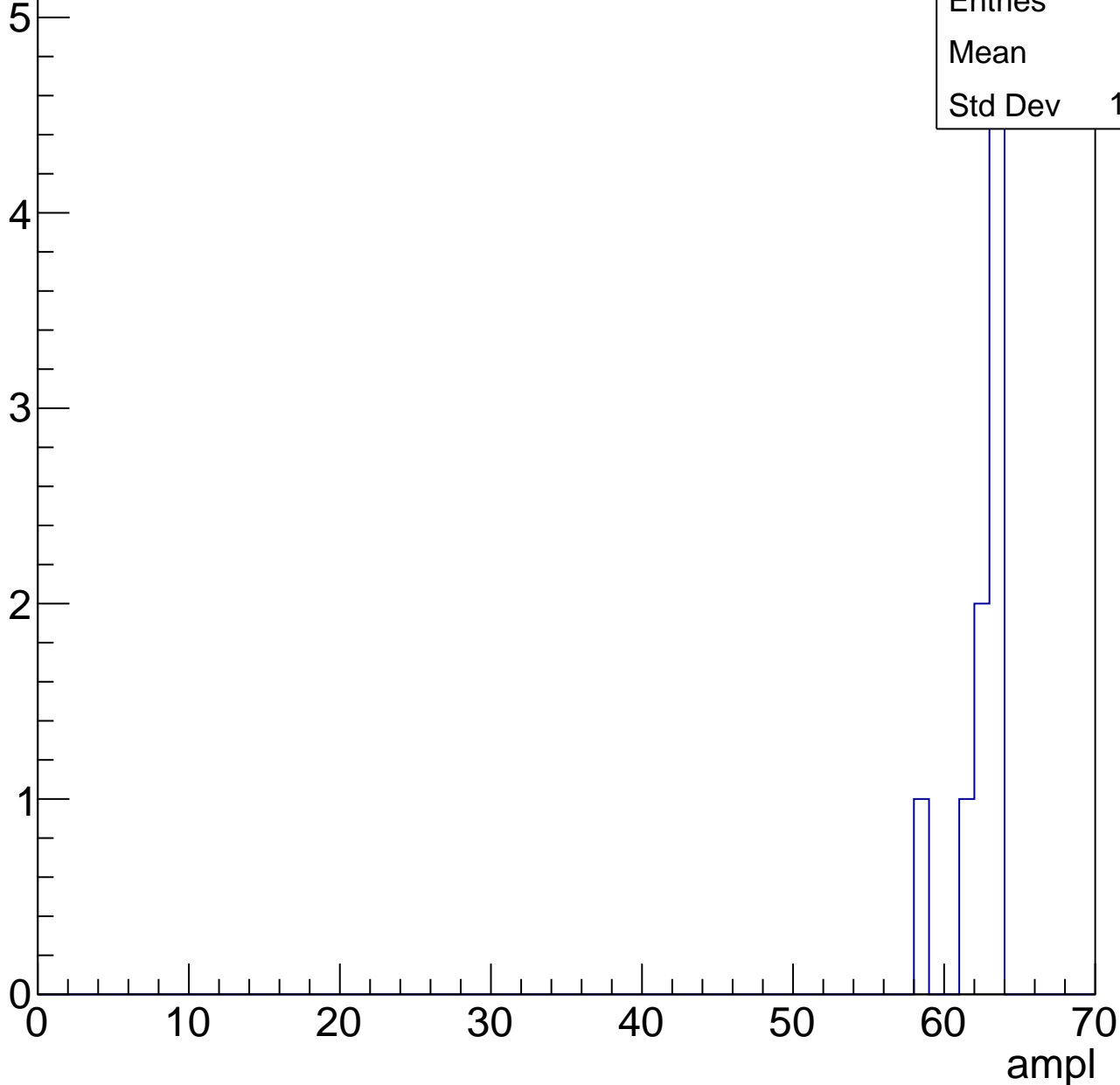


# B0L101S, U16-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 9     |
| Mean    | 62    |
| Std Dev | 1.563 |

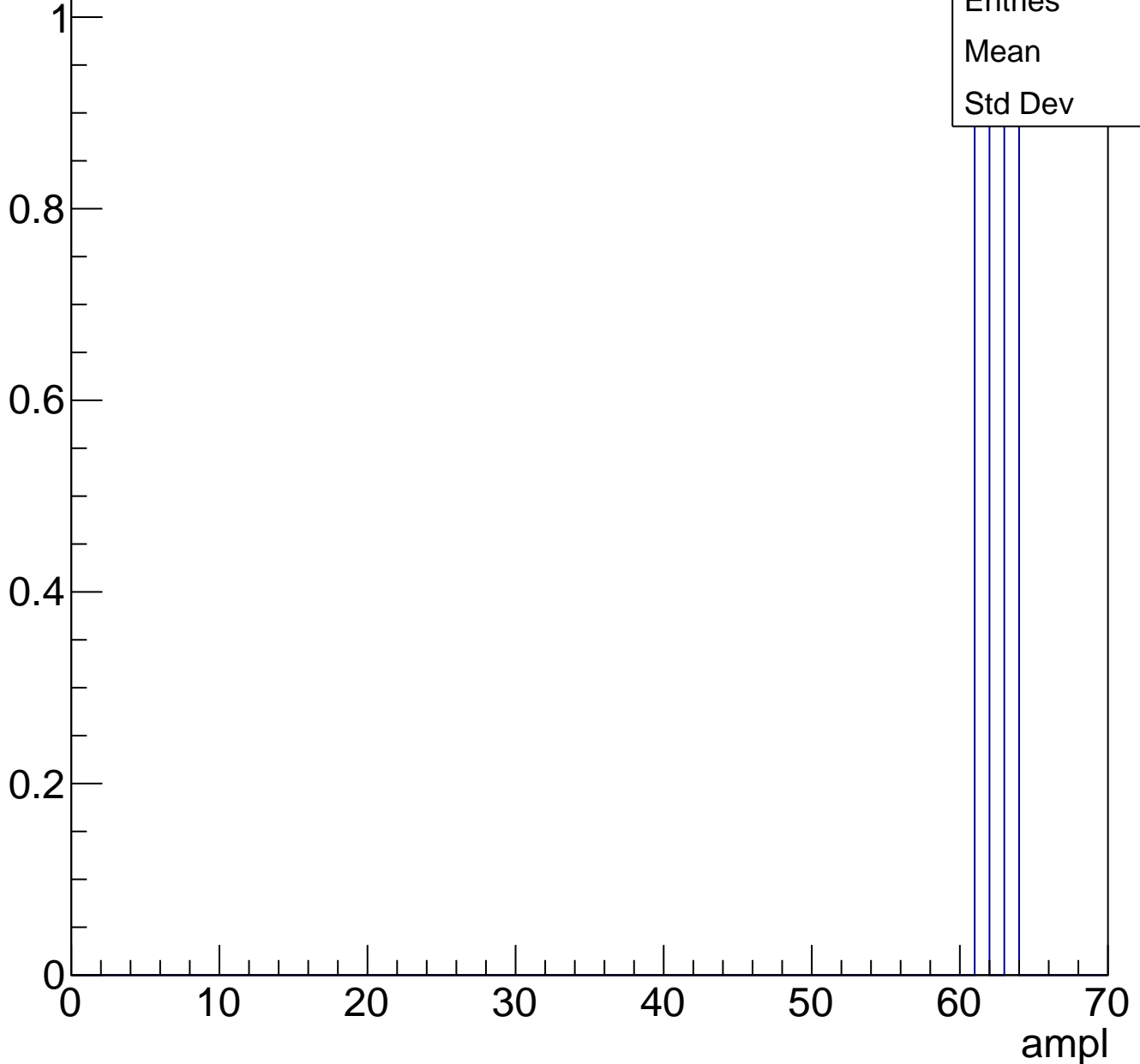




# B0L101S, U16-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 28.86 |
| Std Dev | 4.443 |

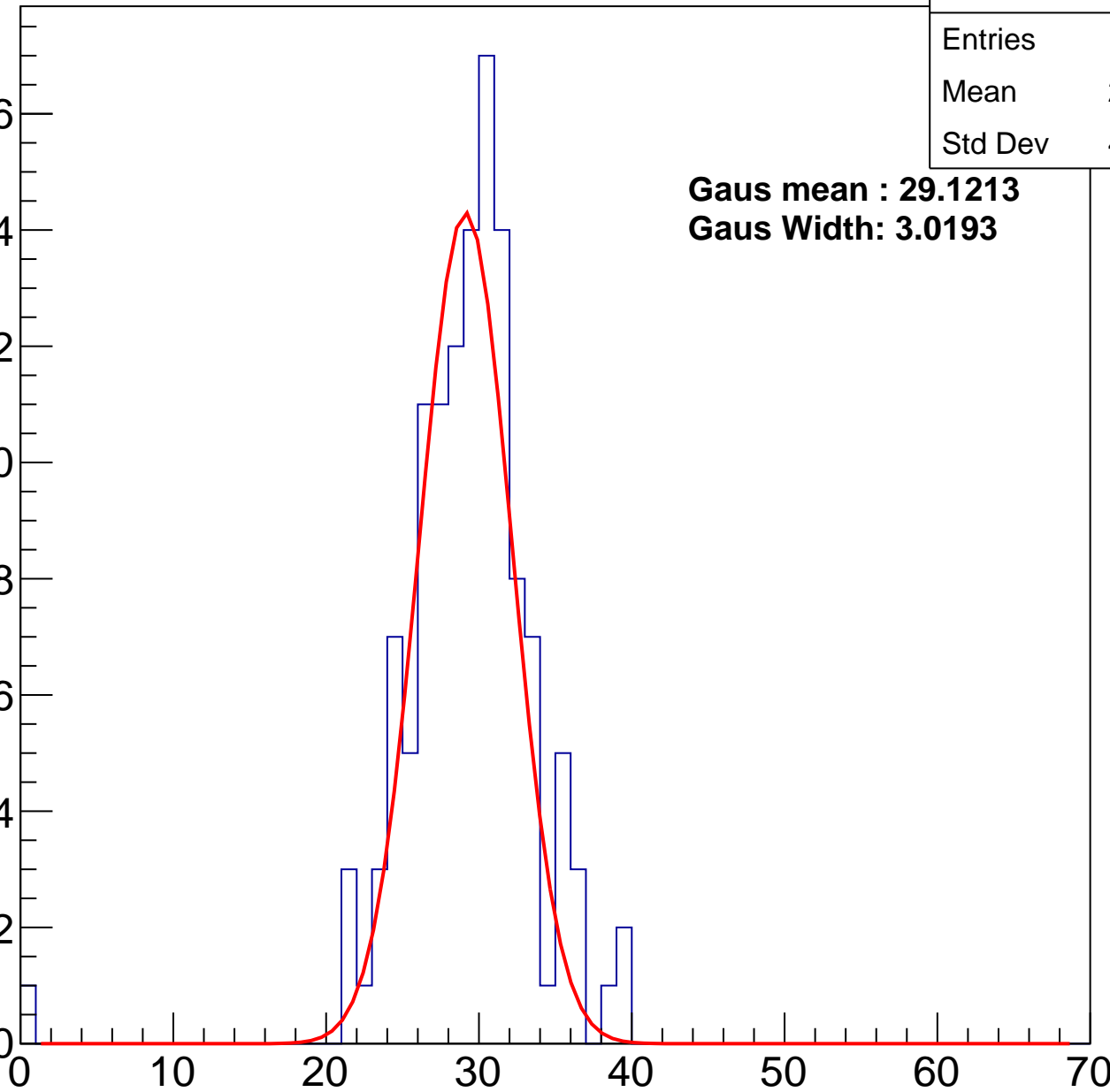
**Gaus mean : 29.1213**

**Gaus Width: 3.0193**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch111, adc1

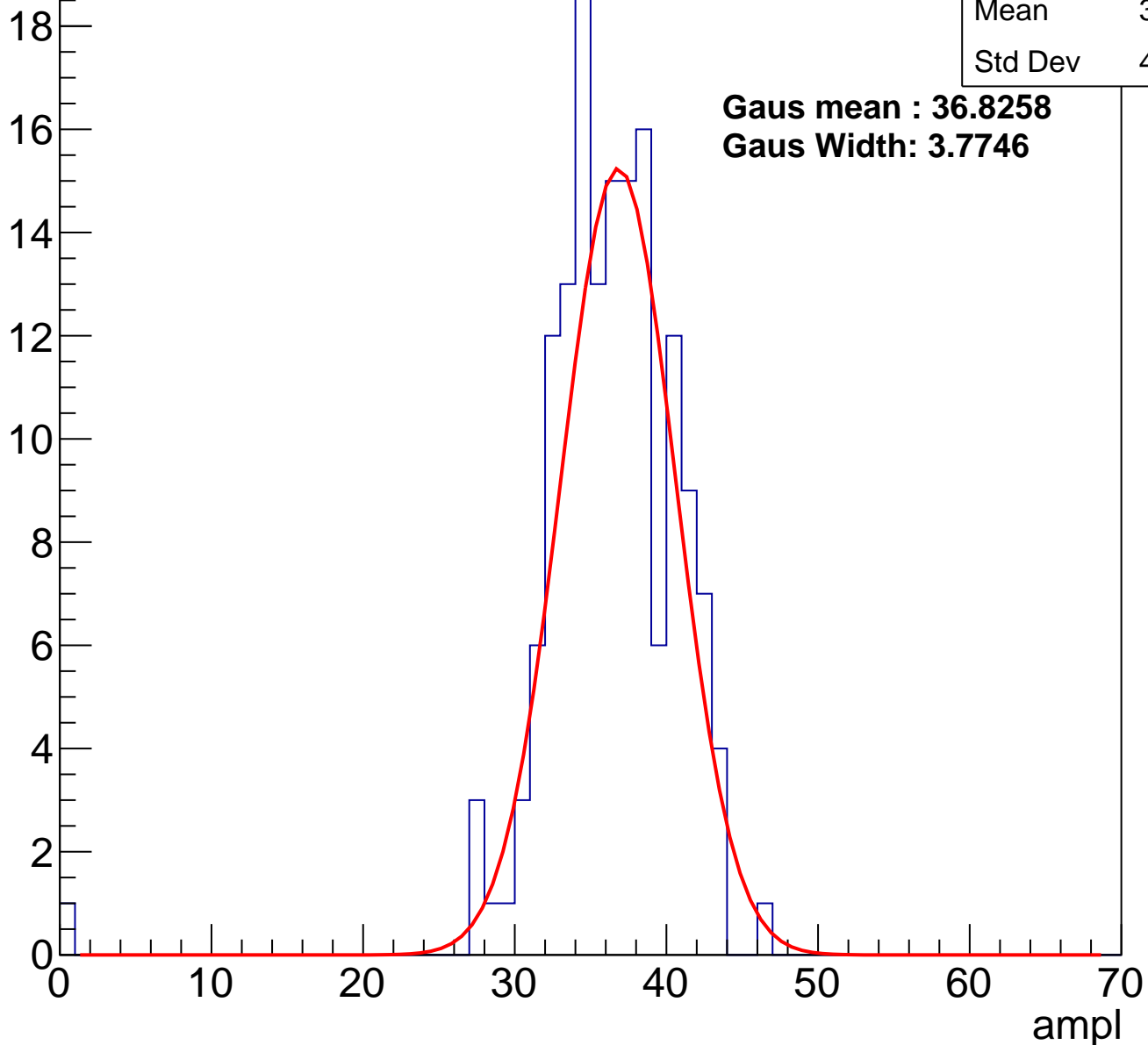
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 157   |
| Mean    | 35.83 |
| Std Dev | 4.655 |

**Gaus mean : 36.8258**

**Gaus Width: 3.7746**

Entry



# B0L101S, U16-ch111, adc2

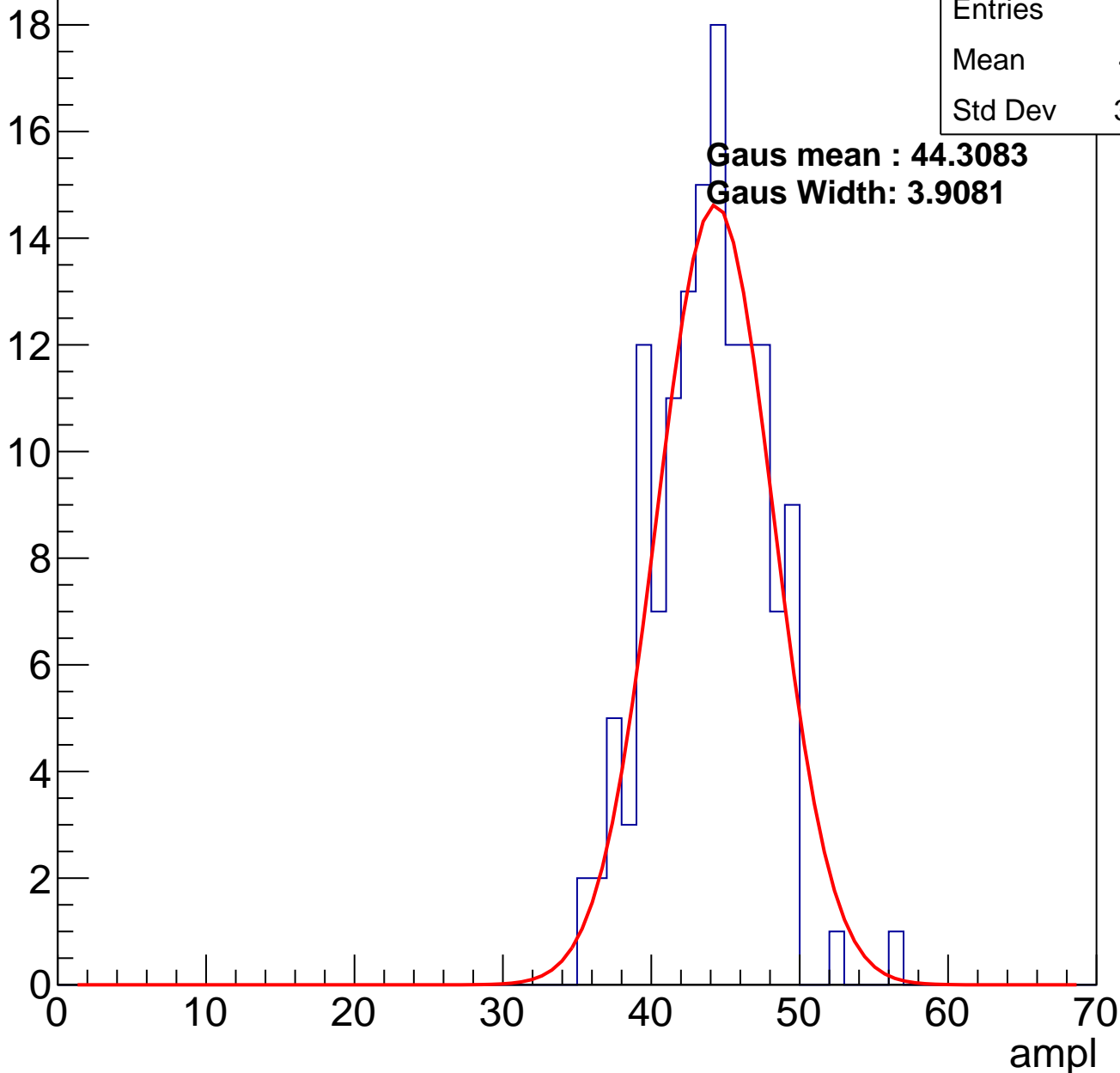
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 43.41 |
| Std Dev | 3.643 |

**Gaus mean : 44.3083**

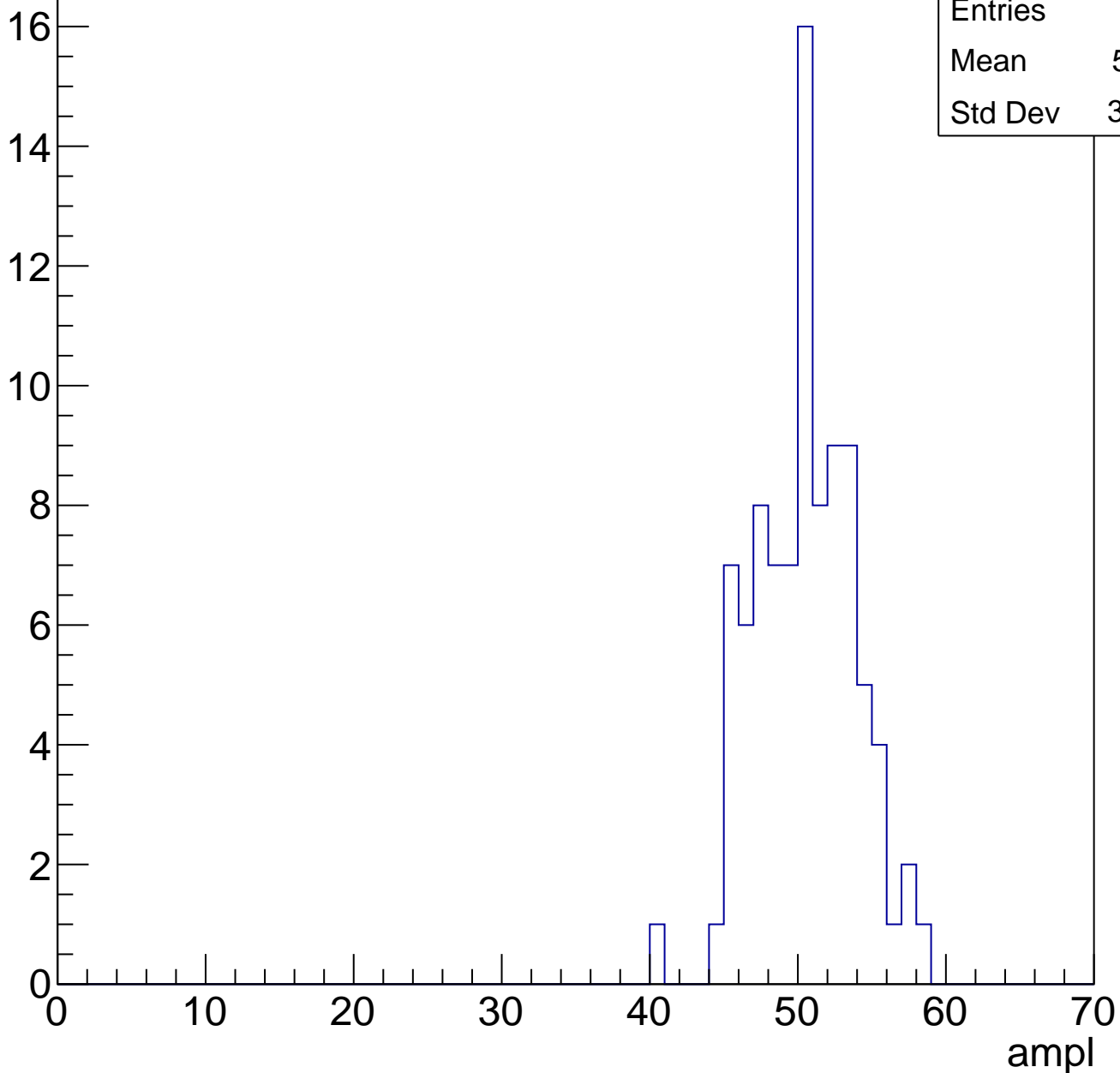
**Gaus Width: 3.9081**



# B0L101S, U16-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 50.01 |
| Std Dev | 3.325 |

# B0L101S, U16-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 54.72 |
| Std Dev | 3.897 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

# B0L101S, U16-ch111, adc5

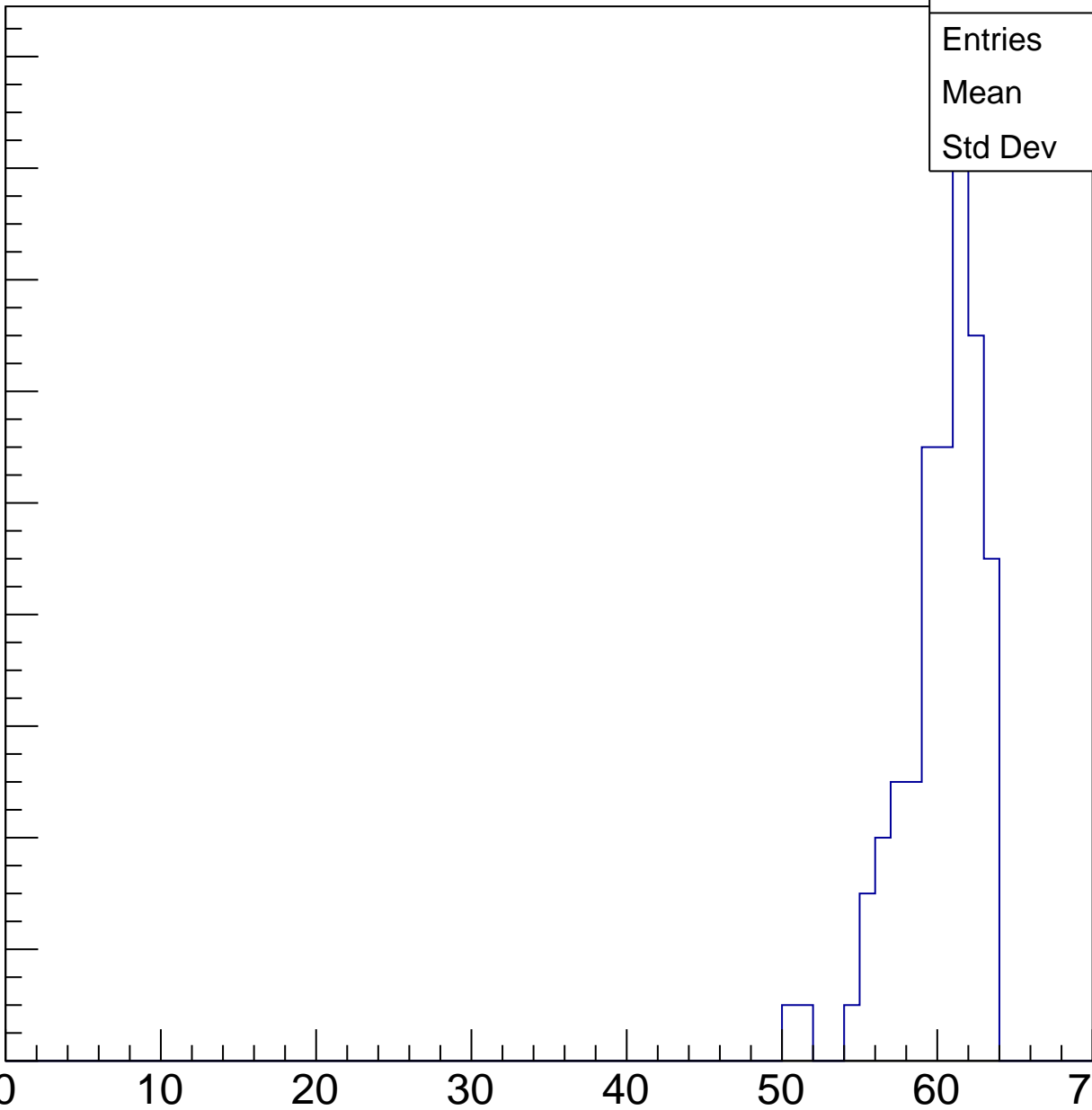
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 59.74 |
| Std Dev | 2.654 |

ampl

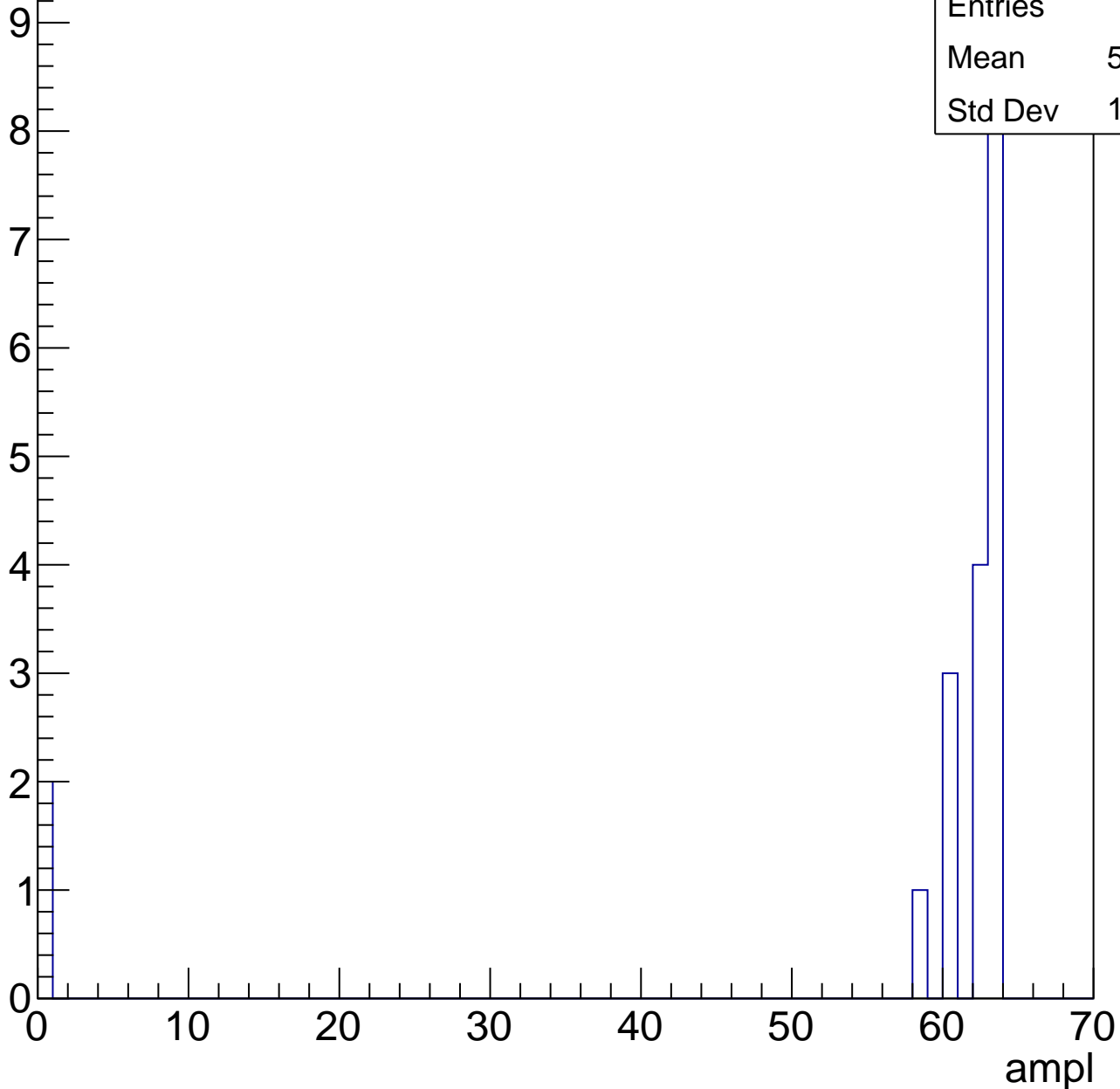


# B0L101S, U16-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 19    |
| Mean    | 55.42 |
| Std Dev | 19.06 |





# B0L101S, U16-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch112, adc0

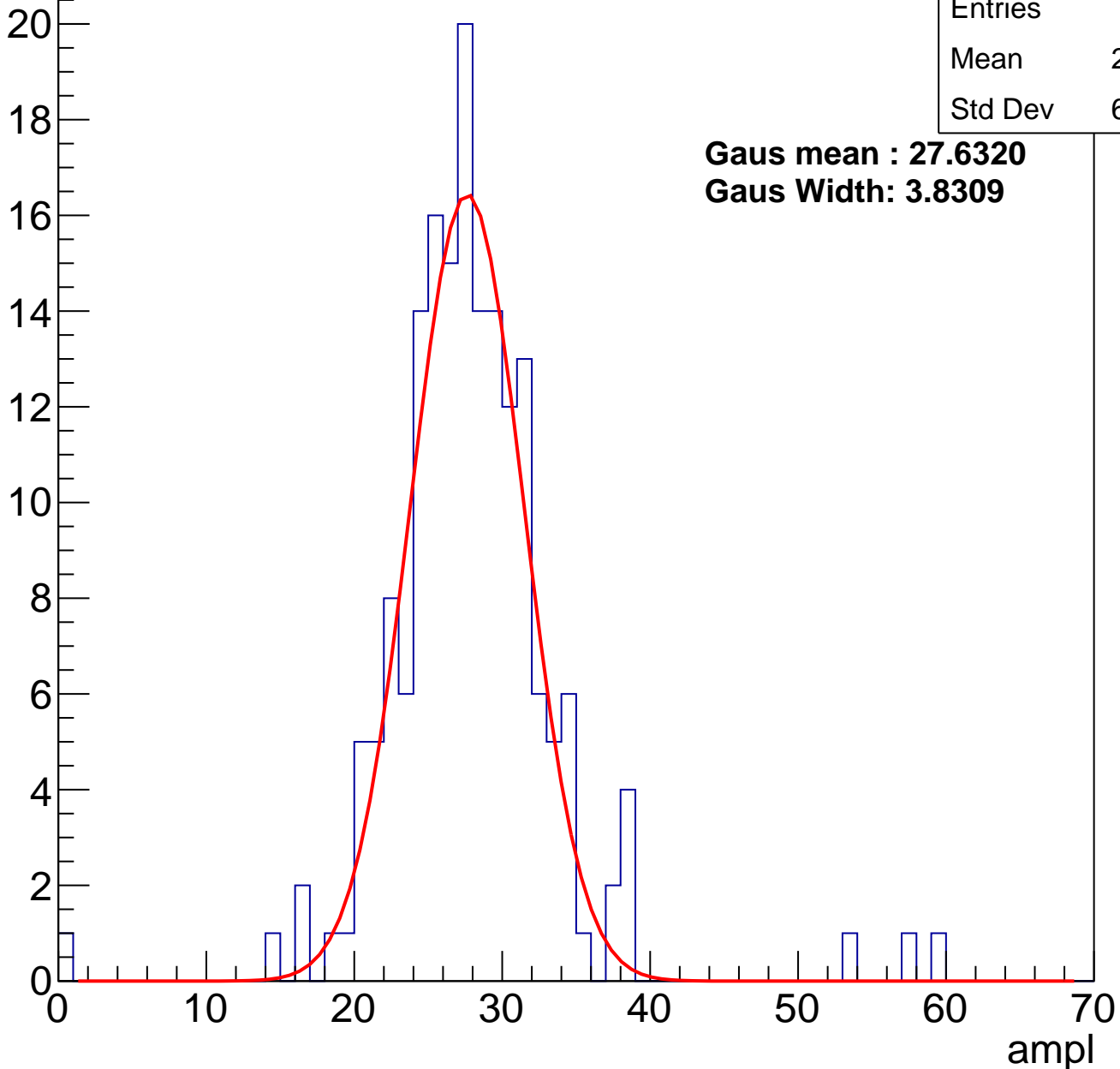
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 175   |
| Mean    | 27.54 |
| Std Dev | 6.103 |

**Gaus mean : 27.6320**

**Gaus Width: 3.8309**

Entry



# B0L101S, U16-ch112, adc1

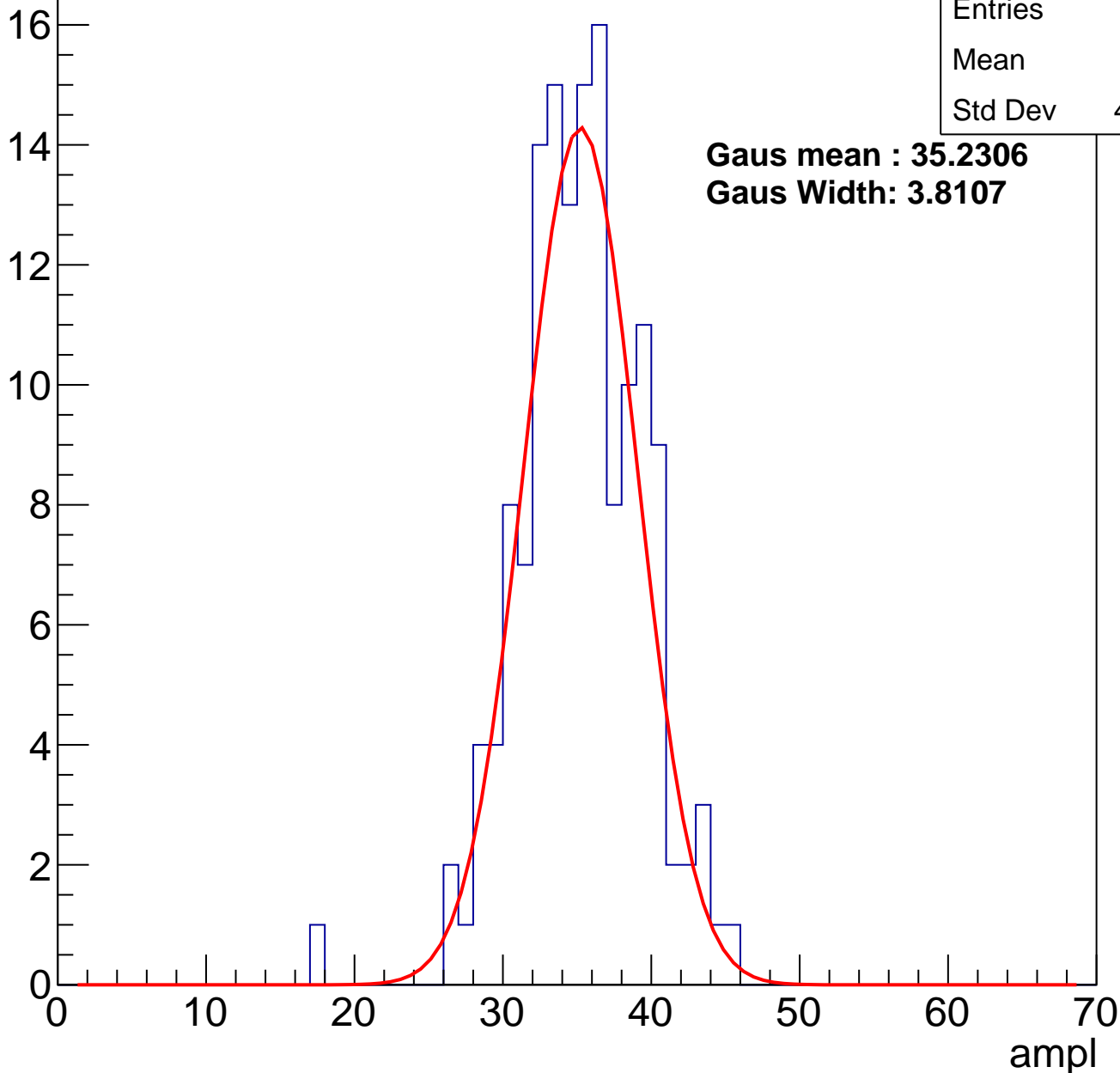
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 147   |
| Mean    | 34.8  |
| Std Dev | 4.105 |

**Gaus mean : 35.2306**

**Gaus Width: 3.8107**

Entry



# B0L101S, U16-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

105

Mean

41.1

Std Dev

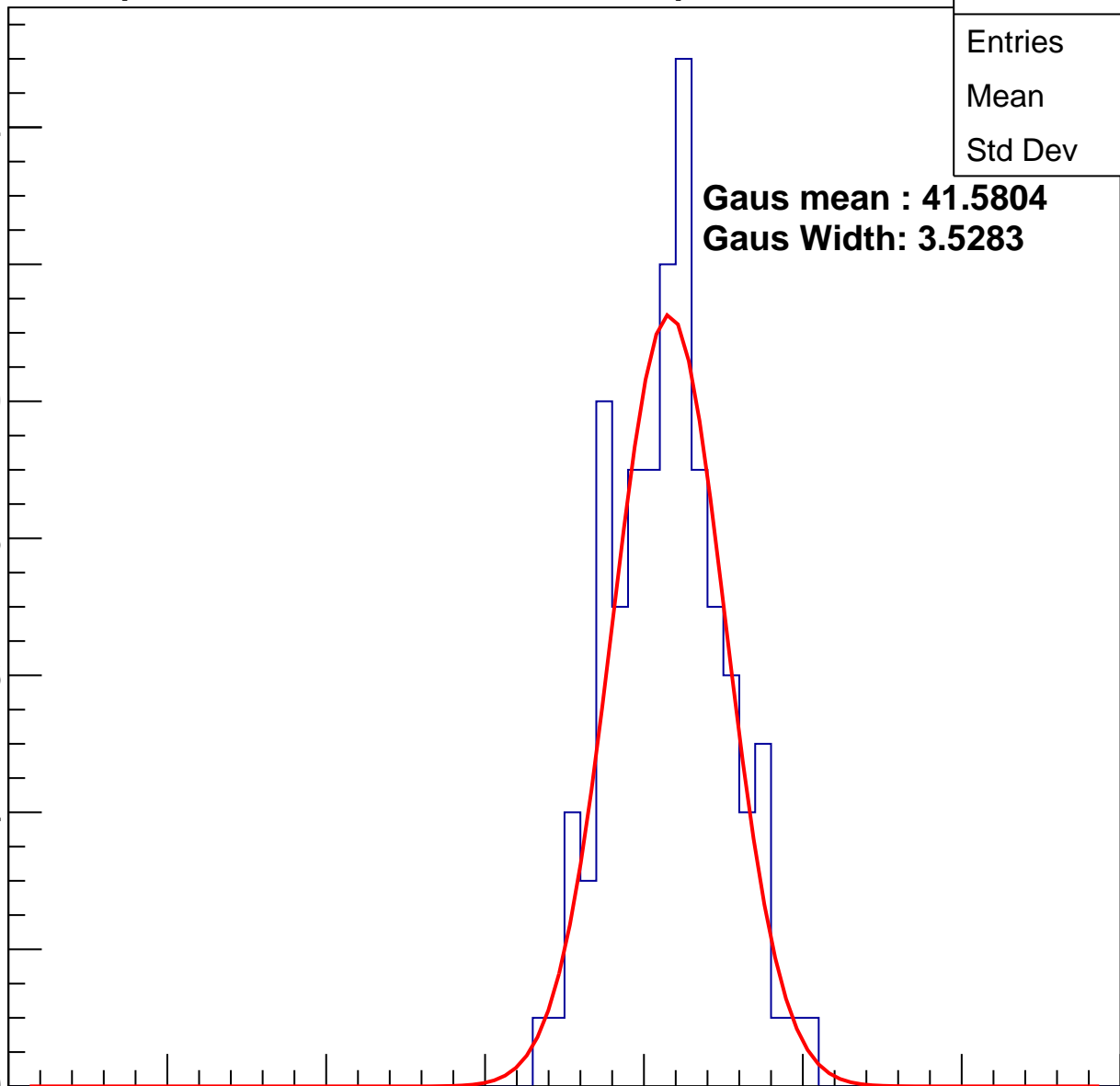
3.496

**Gaus mean : 41.5804**

**Gaus Width: 3.5283**

0 10 20 30 40 50 60 70

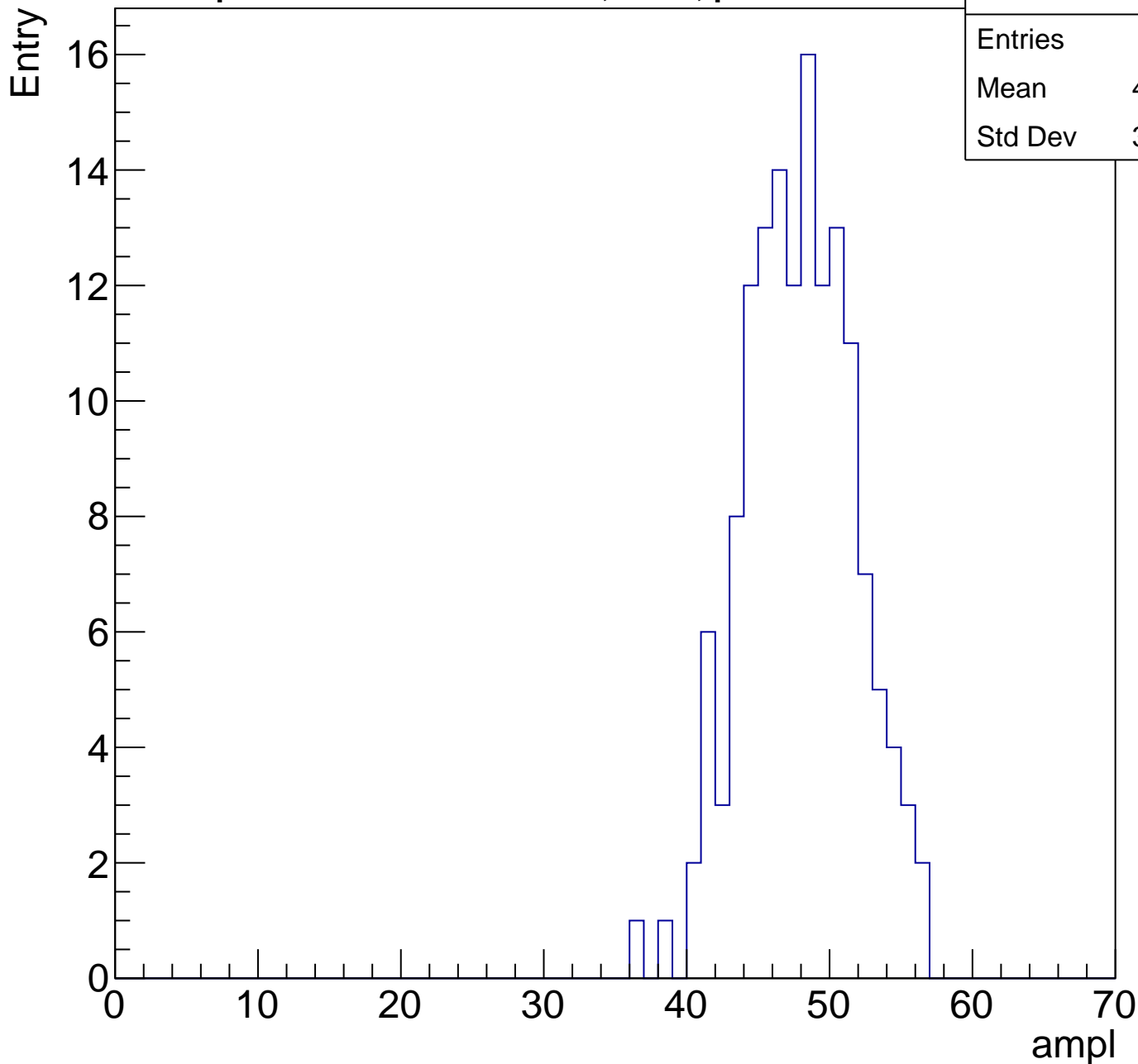
ampl



# B0L101S, U16-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

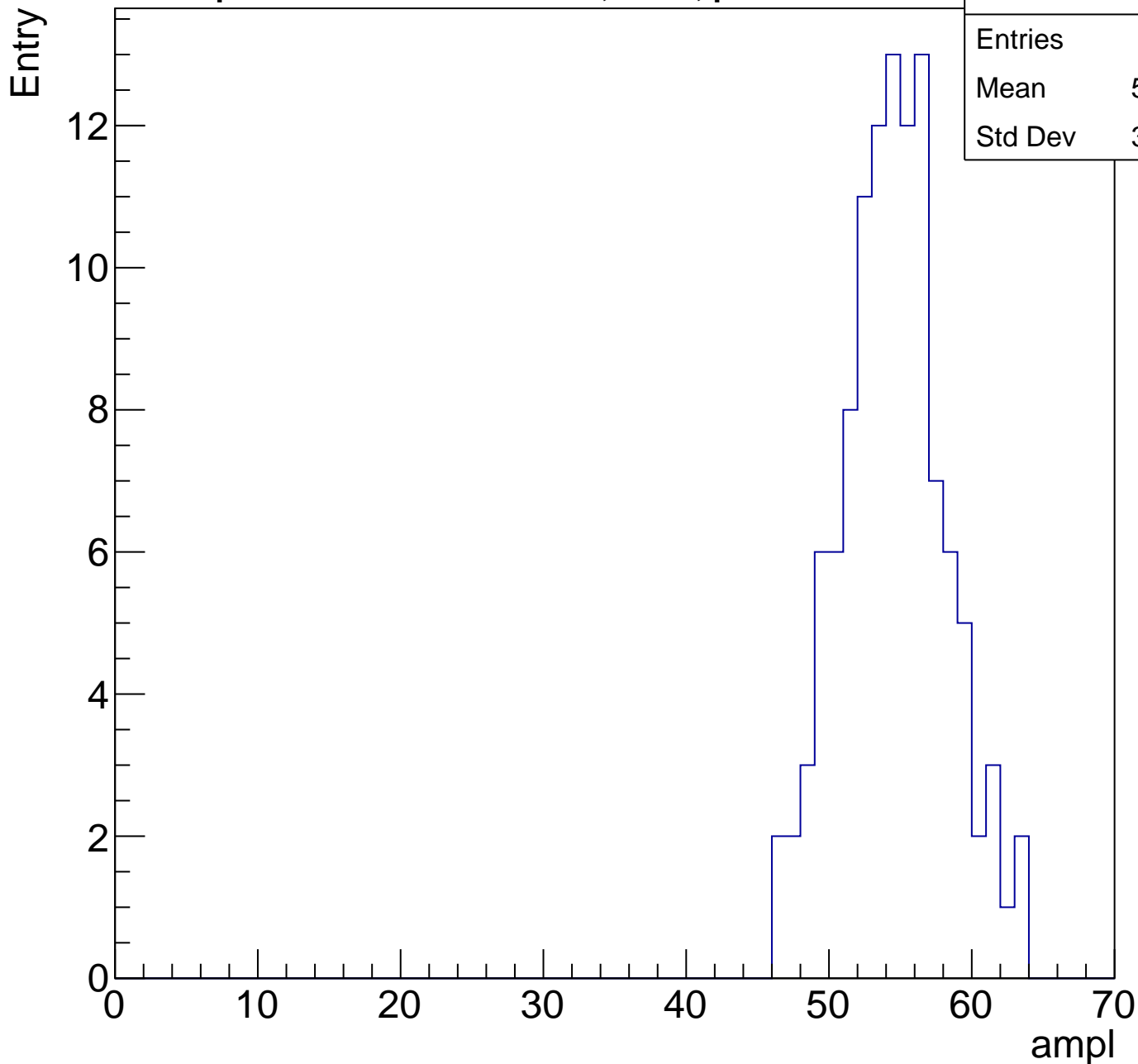
|         |       |
|---------|-------|
| Entries | 145   |
| Mean    | 47.45 |
| Std Dev | 3.829 |



# B0L101S, U16-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

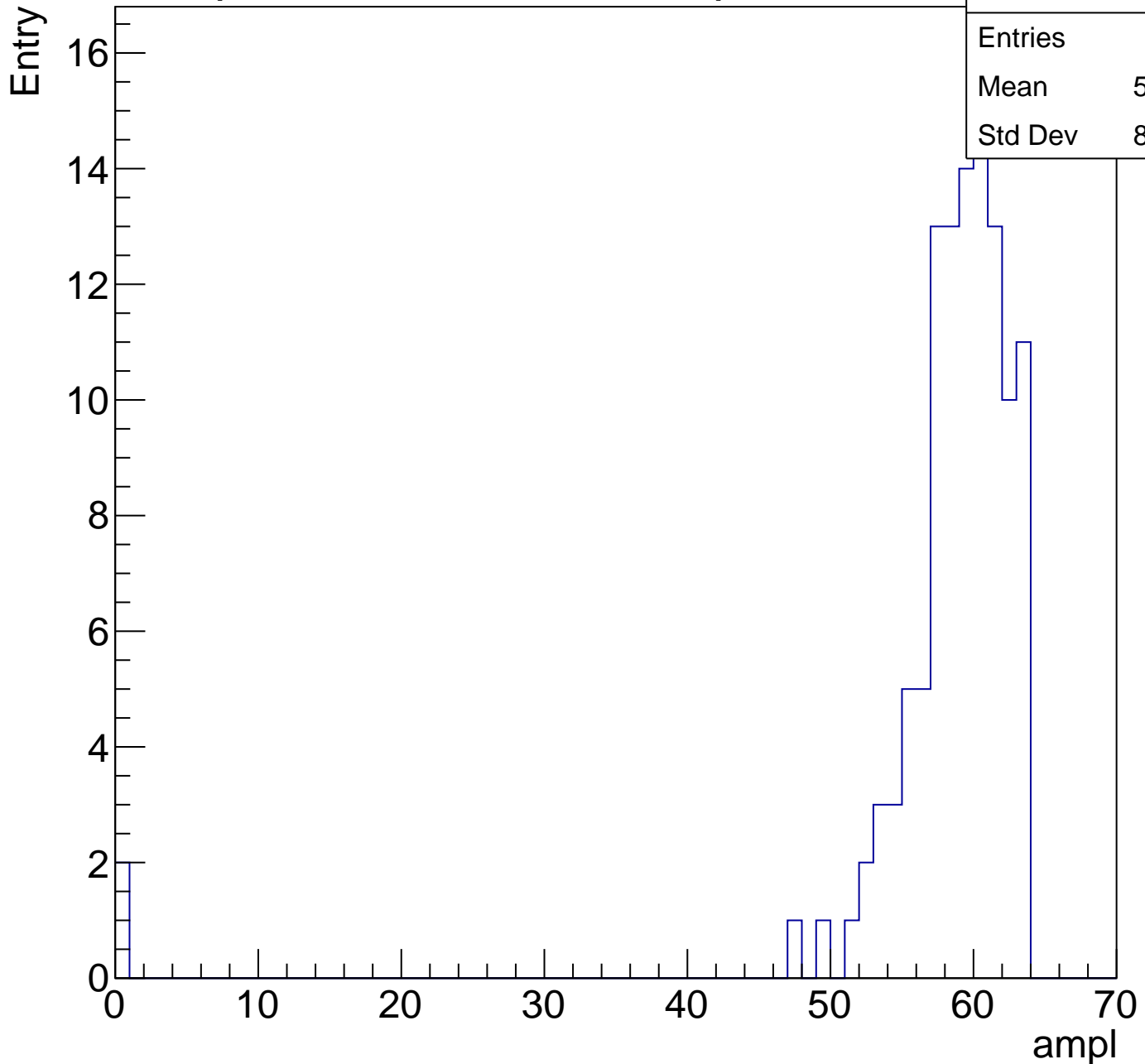
|         |       |
|---------|-------|
| Entries | 114   |
| Mean    | 54.06 |
| Std Dev | 3.628 |



# B0L101S, U16-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 113   |
| Mean    | 57.65 |
| Std Dev | 8.344 |

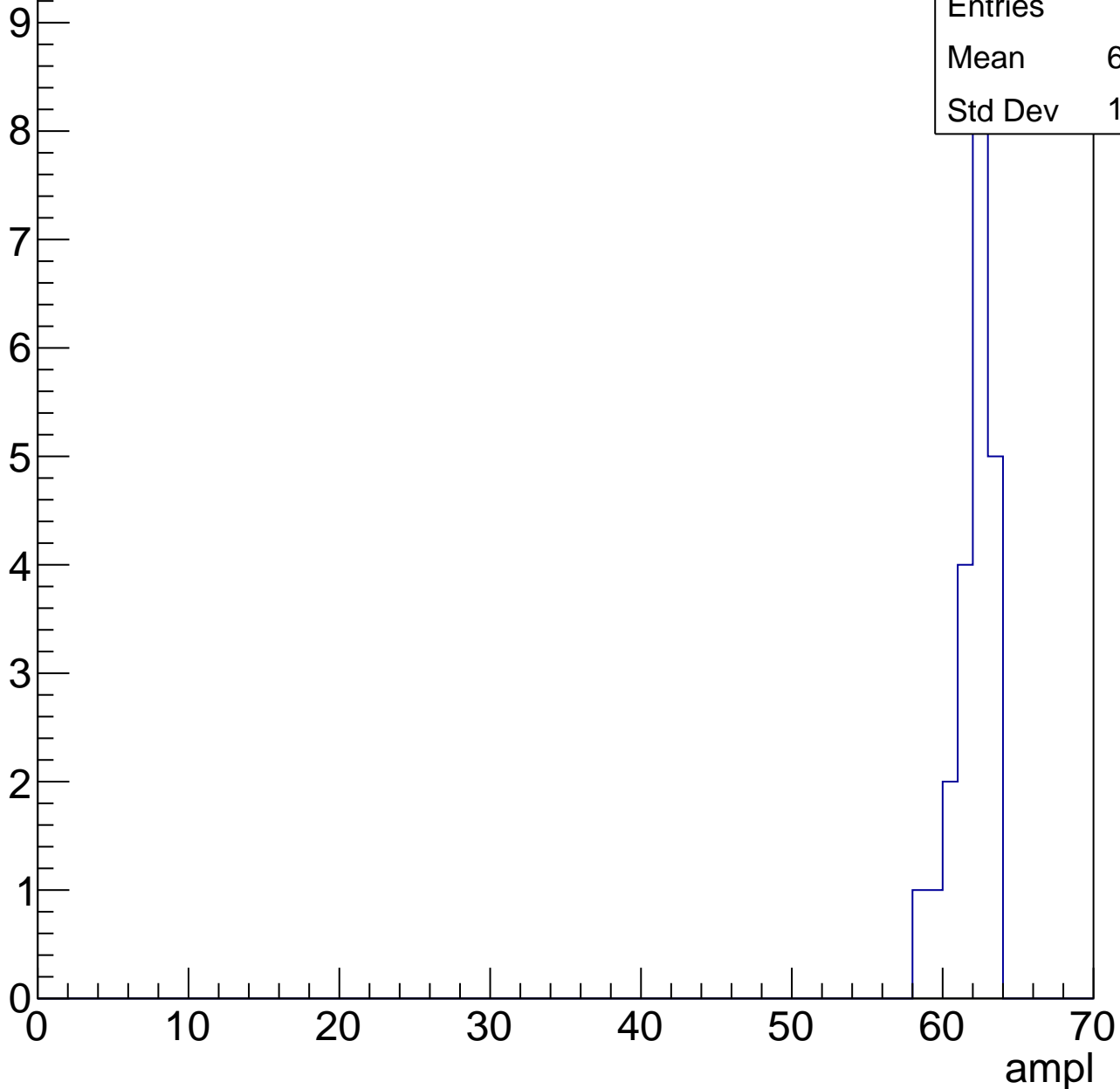


# B0L101S, U16-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 22    |
| Mean    | 61.55 |
| Std Dev | 1.305 |

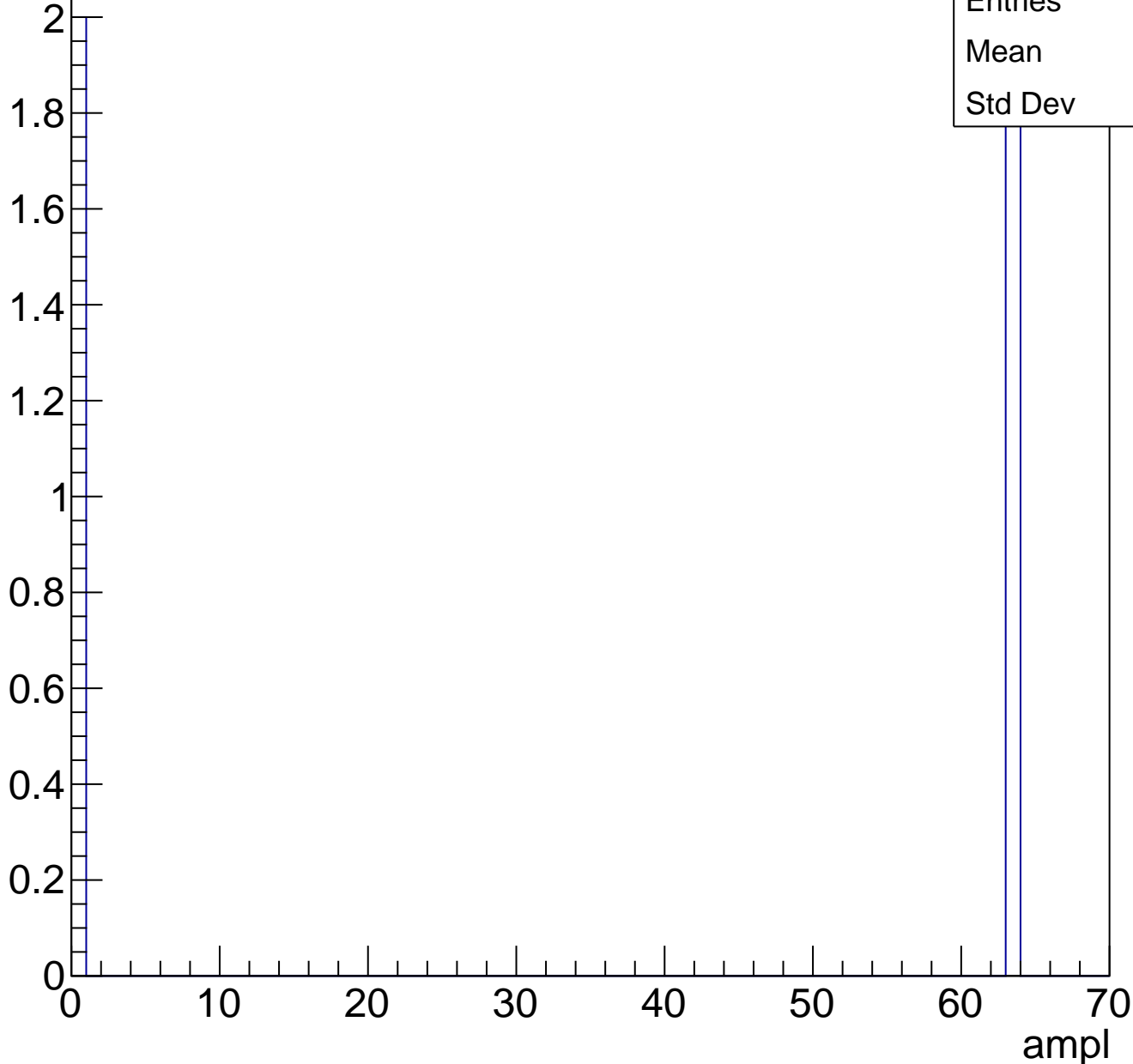




# B0L101S, U16-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch113, adc0

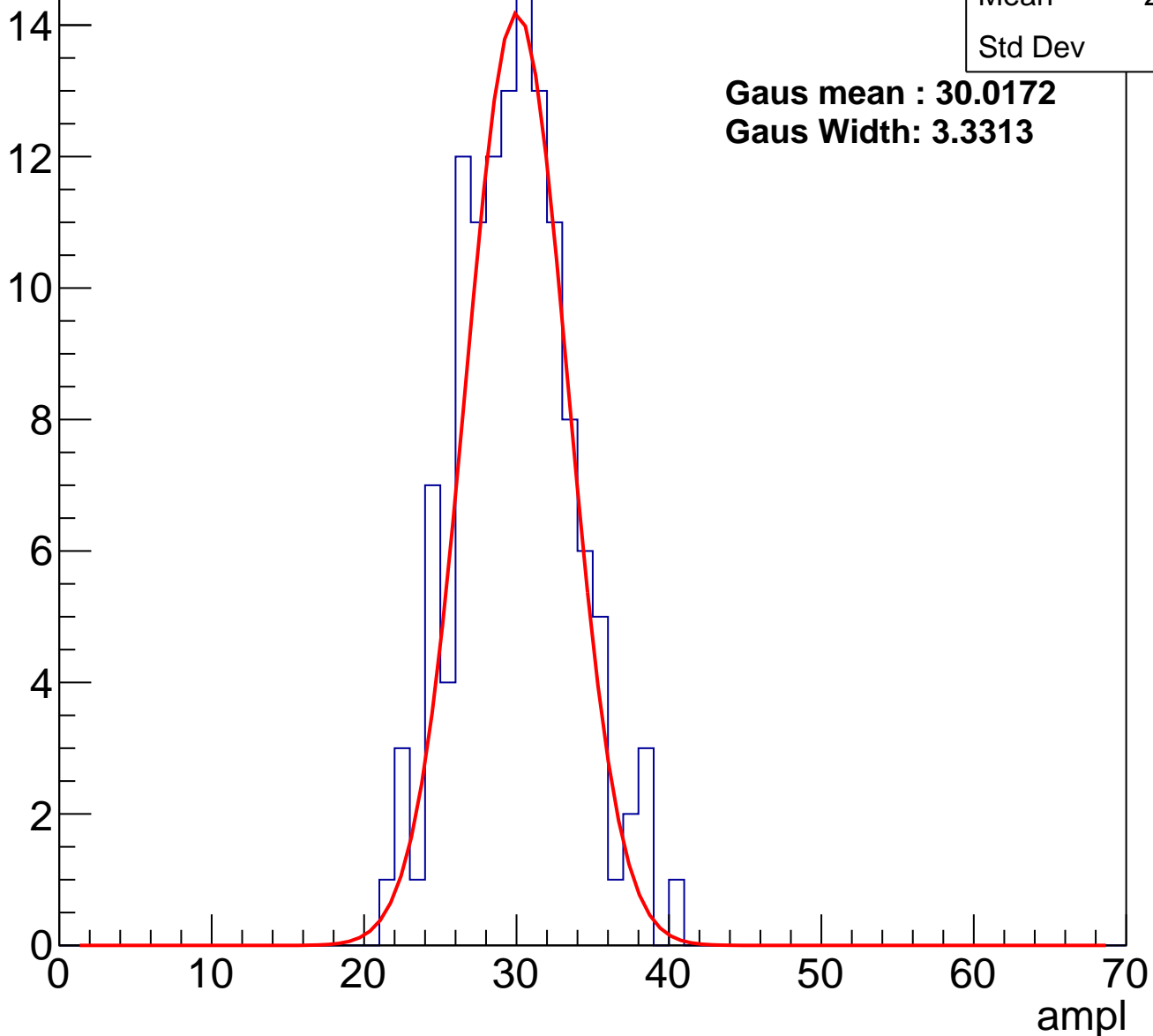
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 29.55 |
| Std Dev | 3.71  |

**Gaus mean : 30.0172**

**Gaus Width: 3.3313**

Entry



# B0L101S, U16-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 153   |
| Mean    | 36.24 |
| Std Dev | 4.154 |

**Gaus mean : 36.8314**

**Gaus Width: 4.0941**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

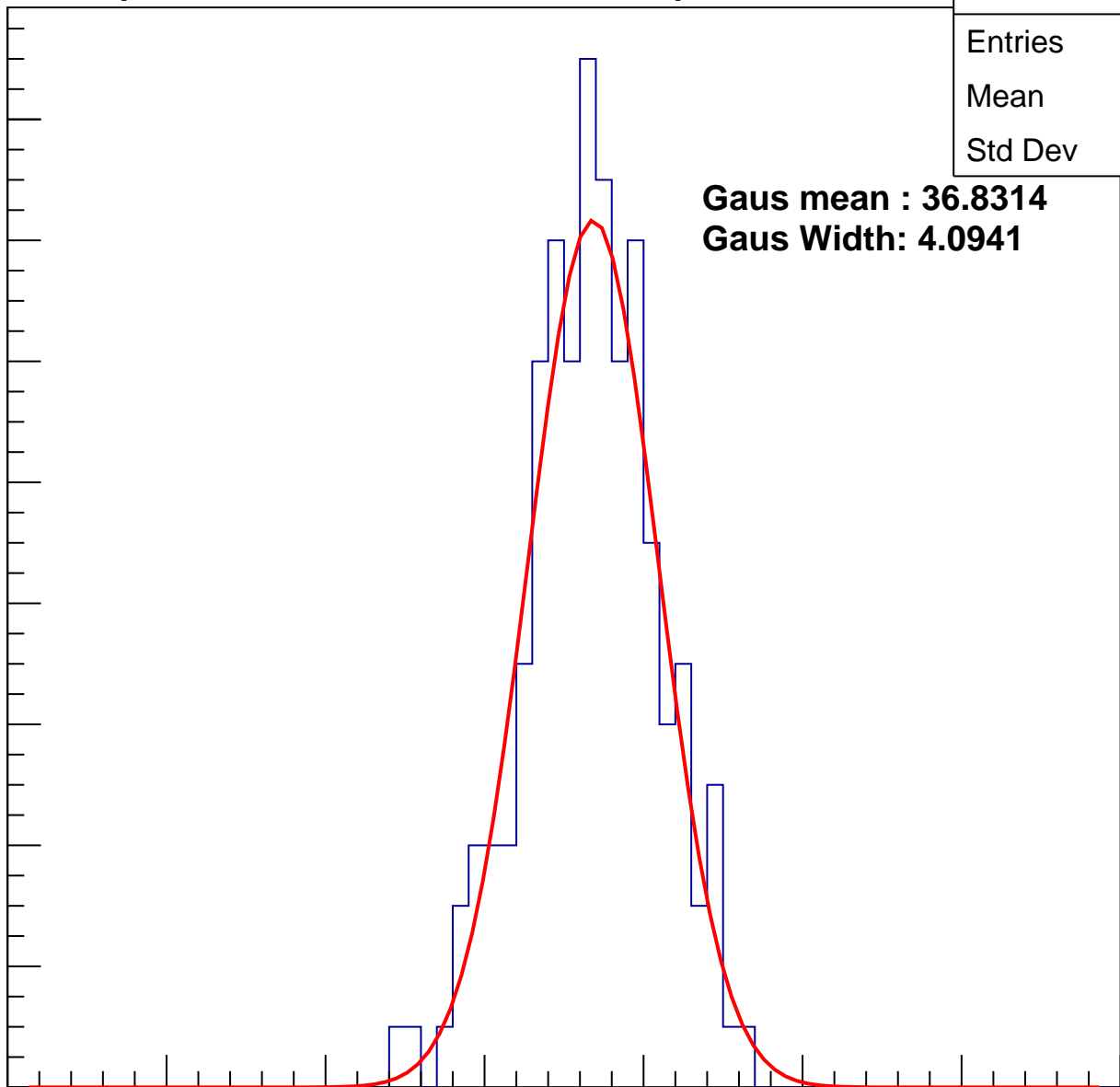
40

50

60

70

ampl



# B0L101S, U16-ch113, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

43.02

Std Dev

3.471

**Gaus mean : 43.4378**

**Gaus Width: 3.6261**

0

10

20

30

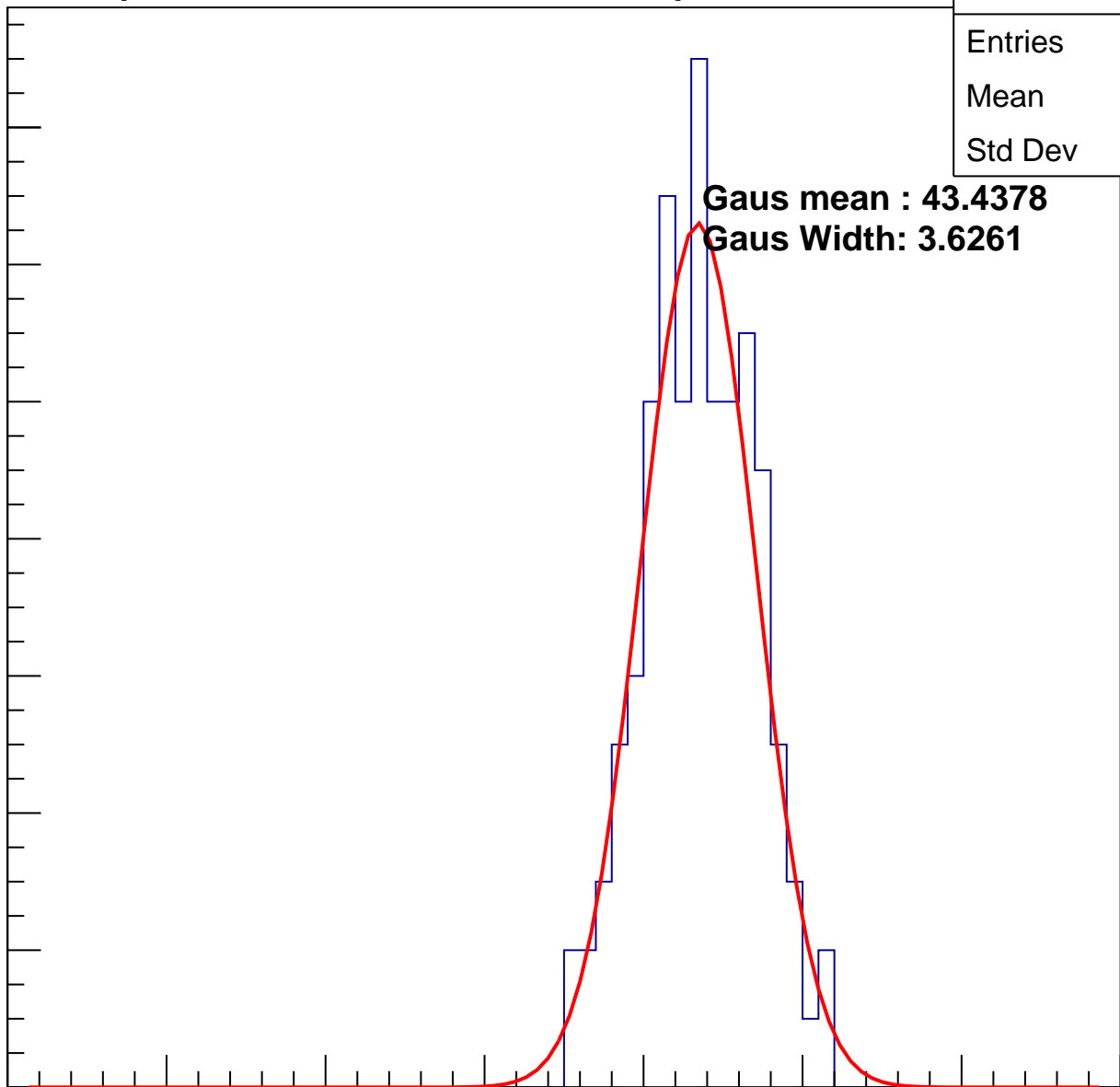
40

50

60

70

ampl



# B0L101S, U16-ch113, adc3

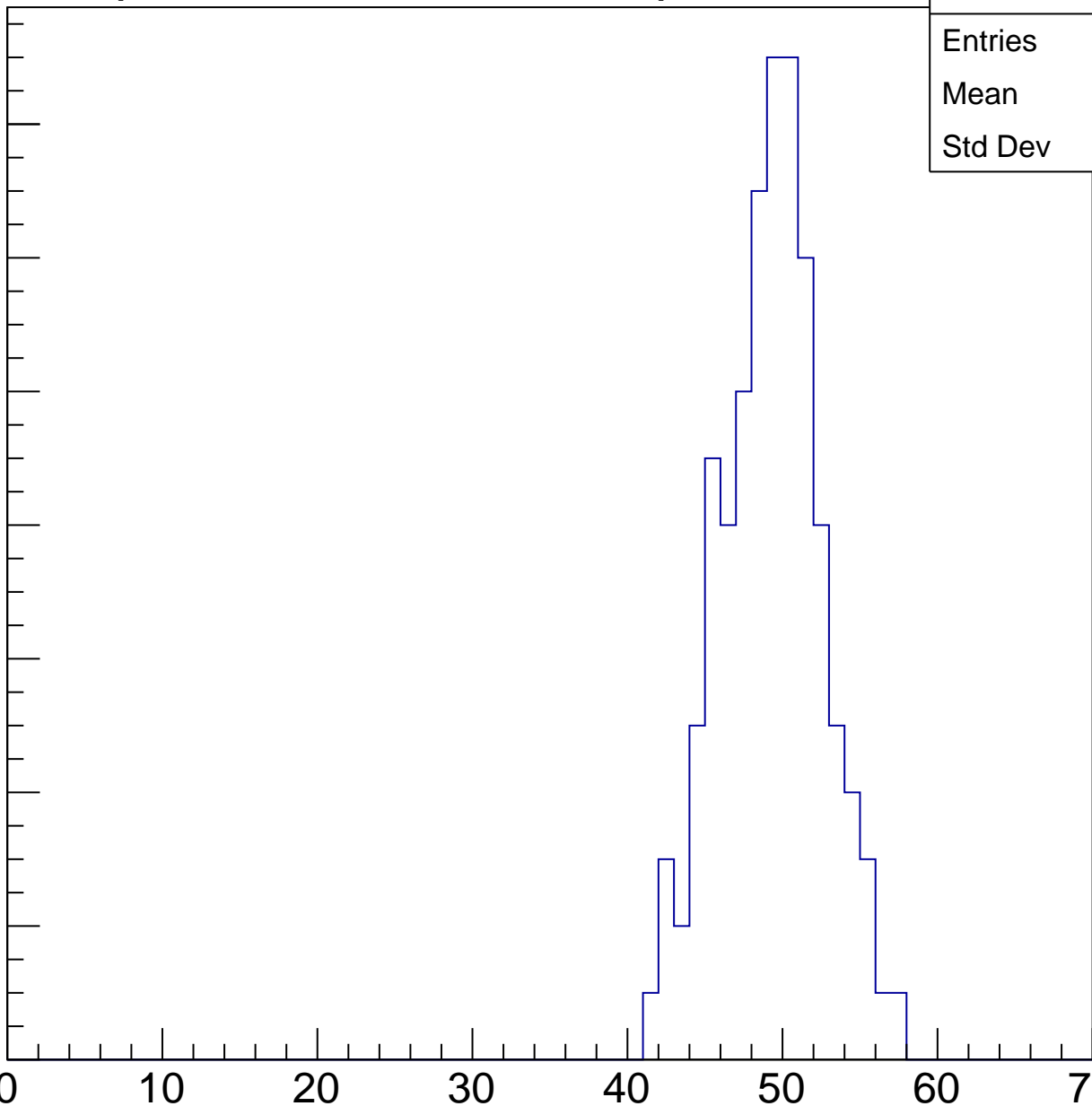
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 48.8  |
| Std Dev | 3.242 |

ampl



# B0L101S, U16-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 132   |
| Mean    | 55.32 |
| Std Dev | 3.239 |

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

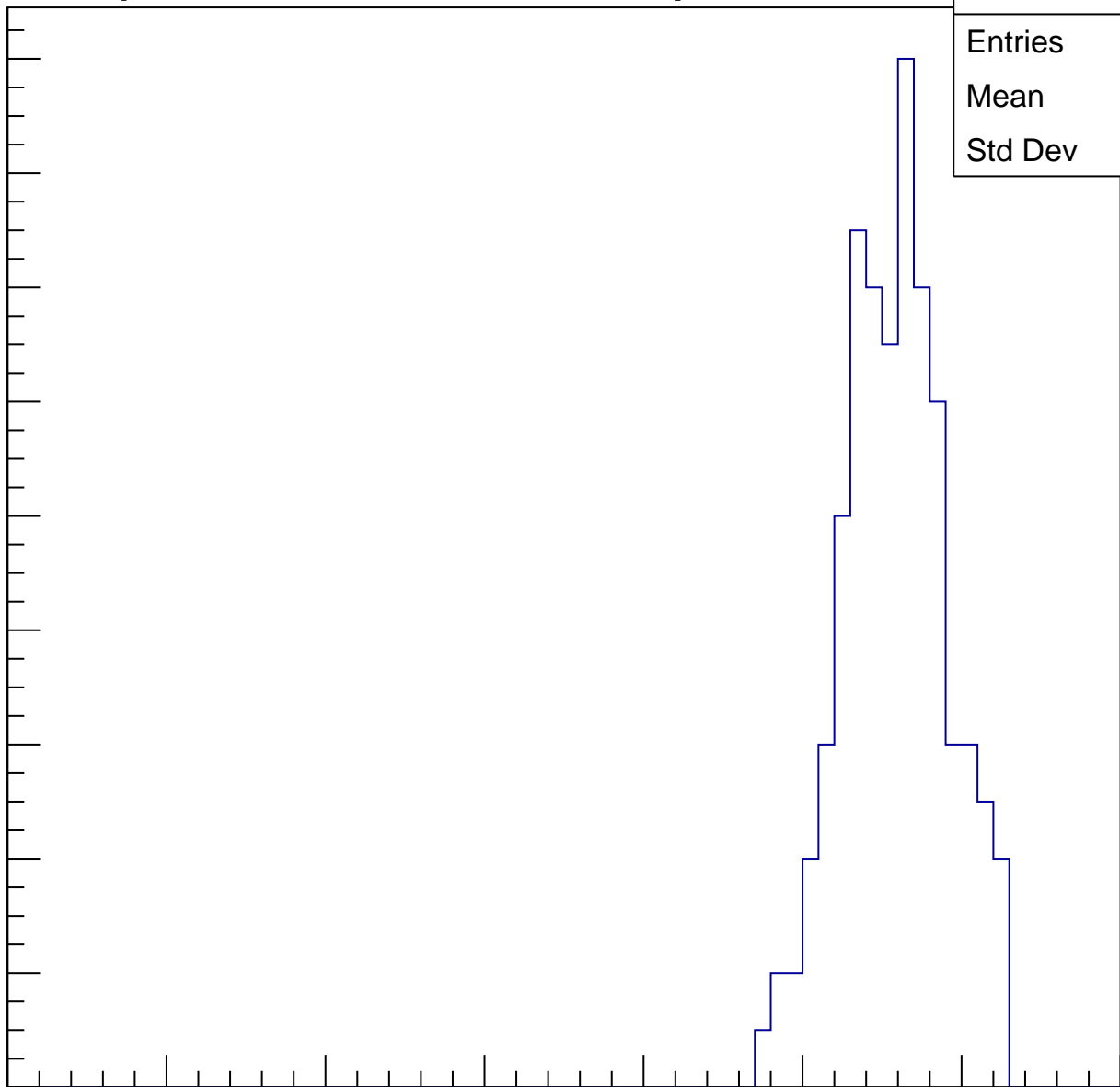
40

50

60

70

ampl



# B0L101S, U16-ch113, adc5

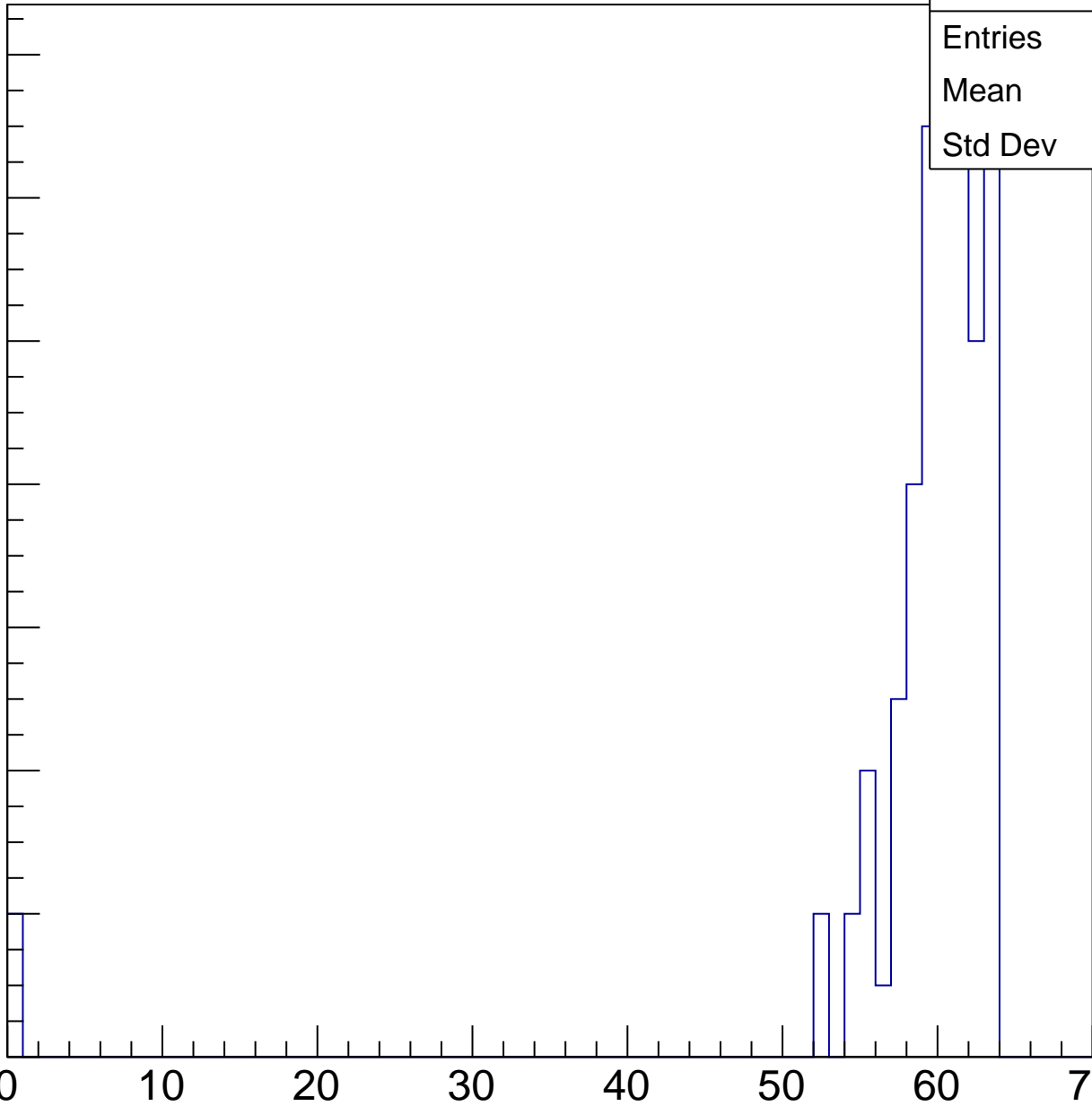
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 58.41 |
| Std Dev | 9.271 |

ampl

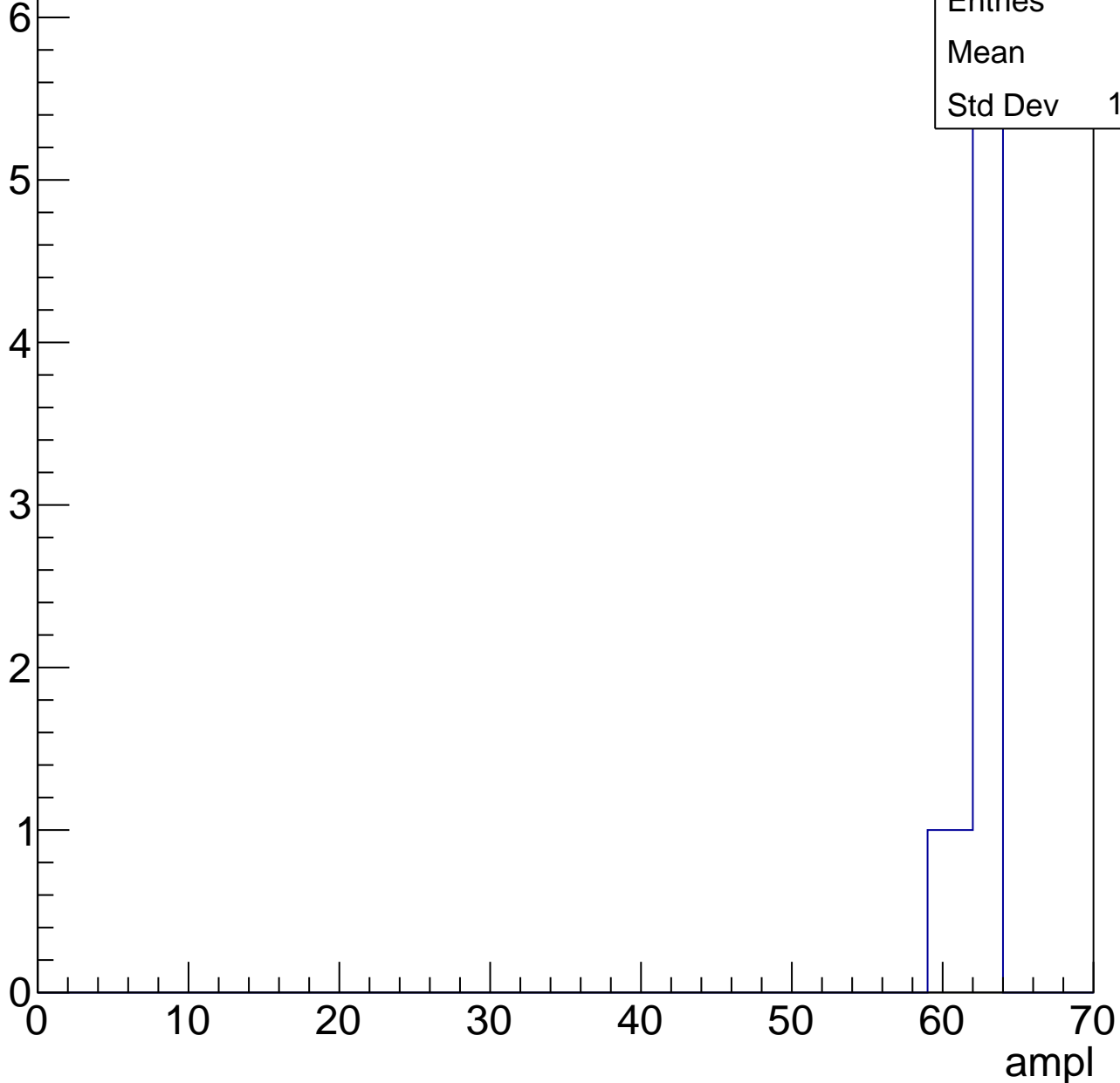


# B0L101S, U16-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 15    |
| Mean    | 62    |
| Std Dev | 1.155 |

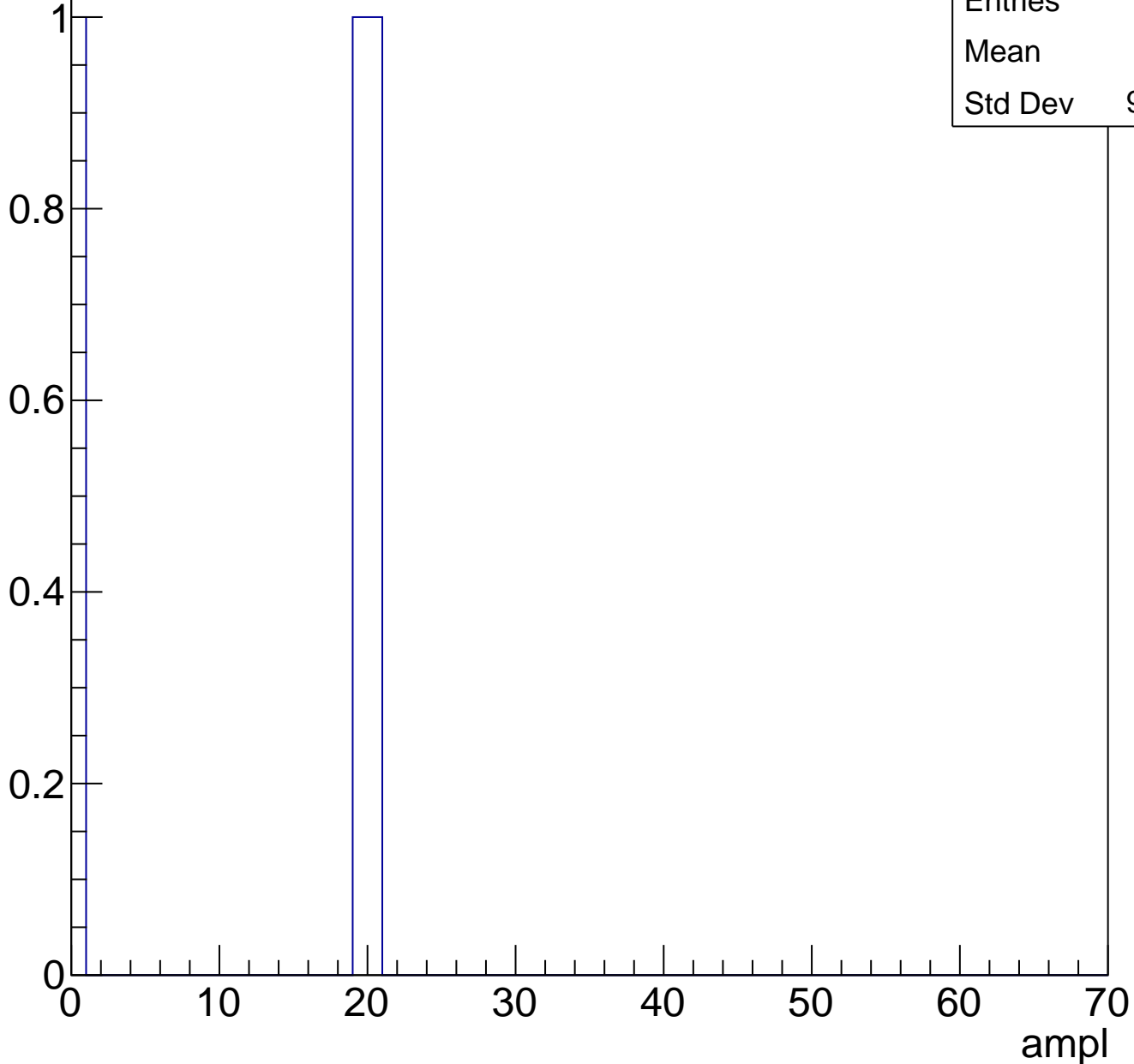




# B0L101S, U16-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch114, adc0

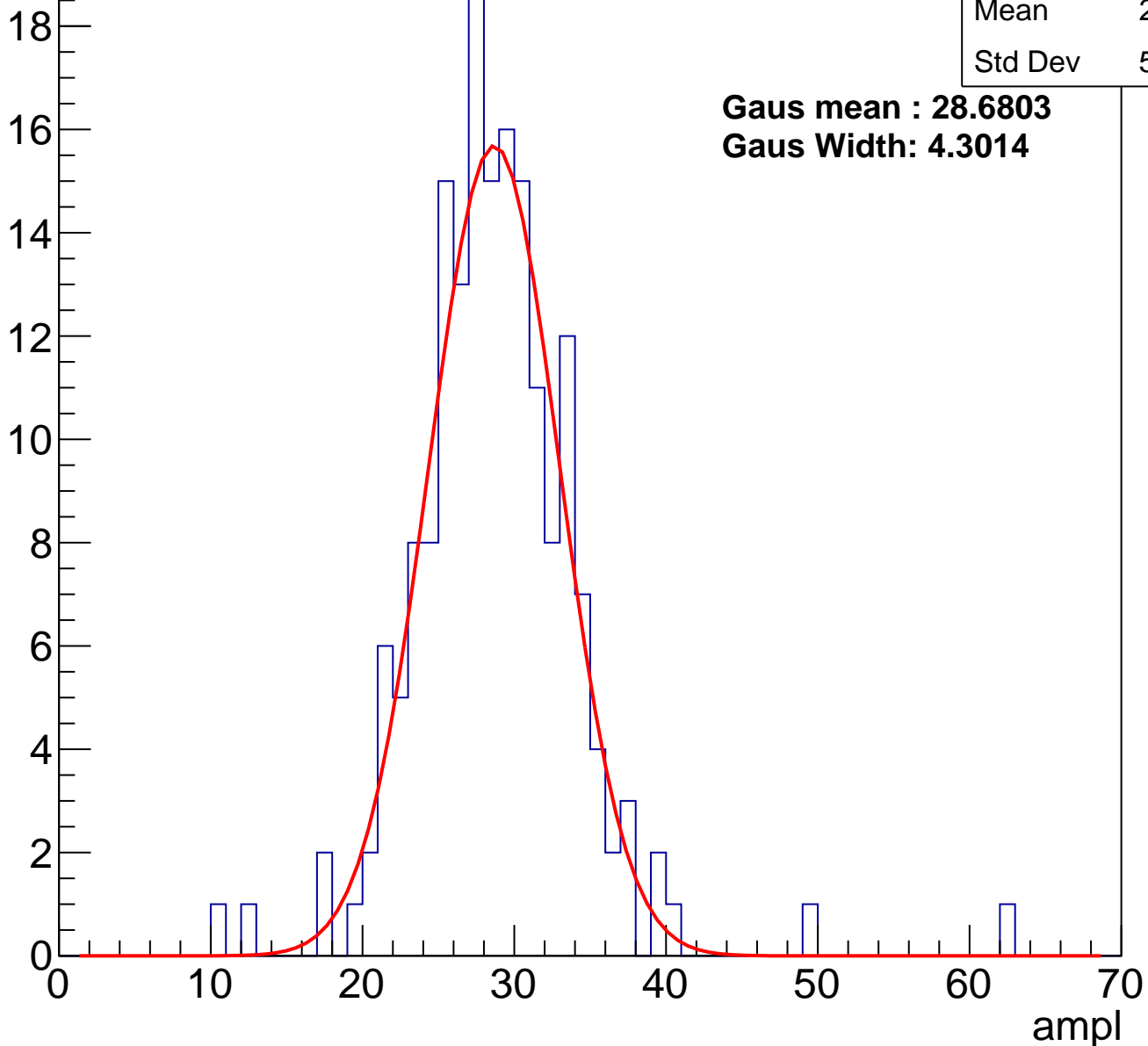
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 179   |
| Mean    | 28.32 |
| Std Dev | 5.505 |

**Gaus mean : 28.6803**

**Gaus Width: 4.3014**

Entry



# B0L101S, U16-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 35.59 |
| Std Dev | 3.784 |

**Gaus mean : 36.2057**

**Gaus Width: 3.5986**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

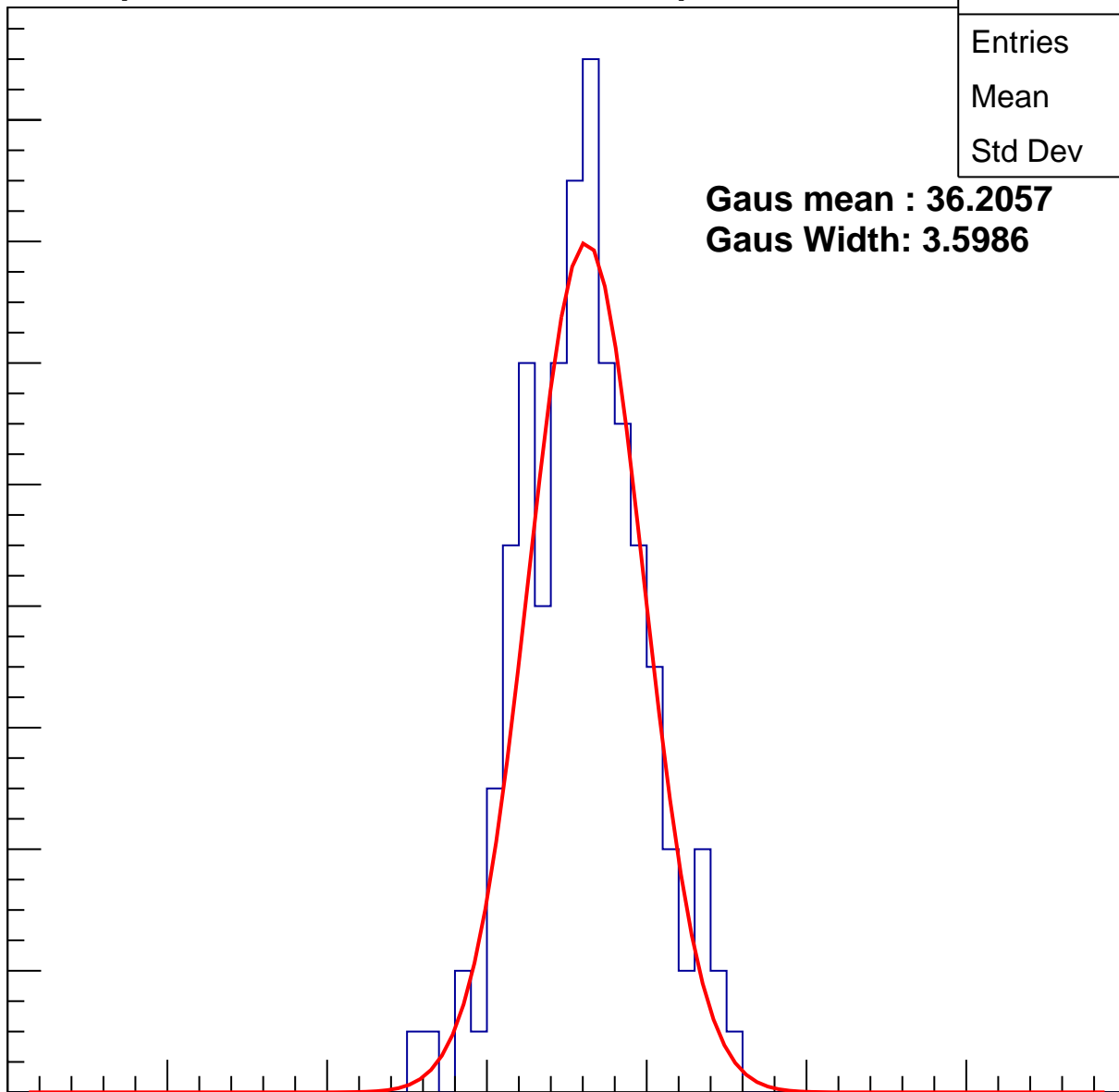
40

50

60

70

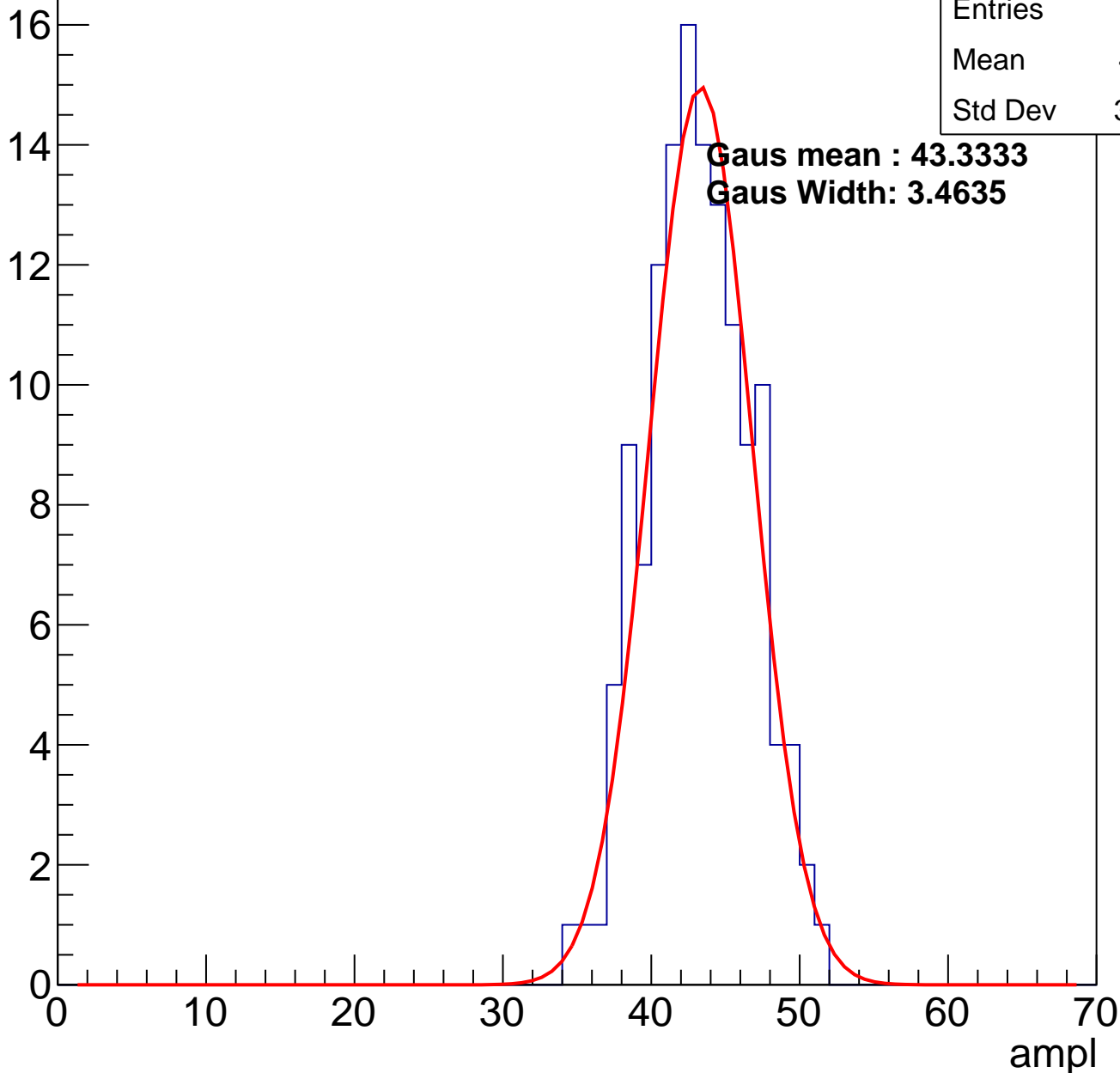
ampl



# B0L101S, U16-ch114, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

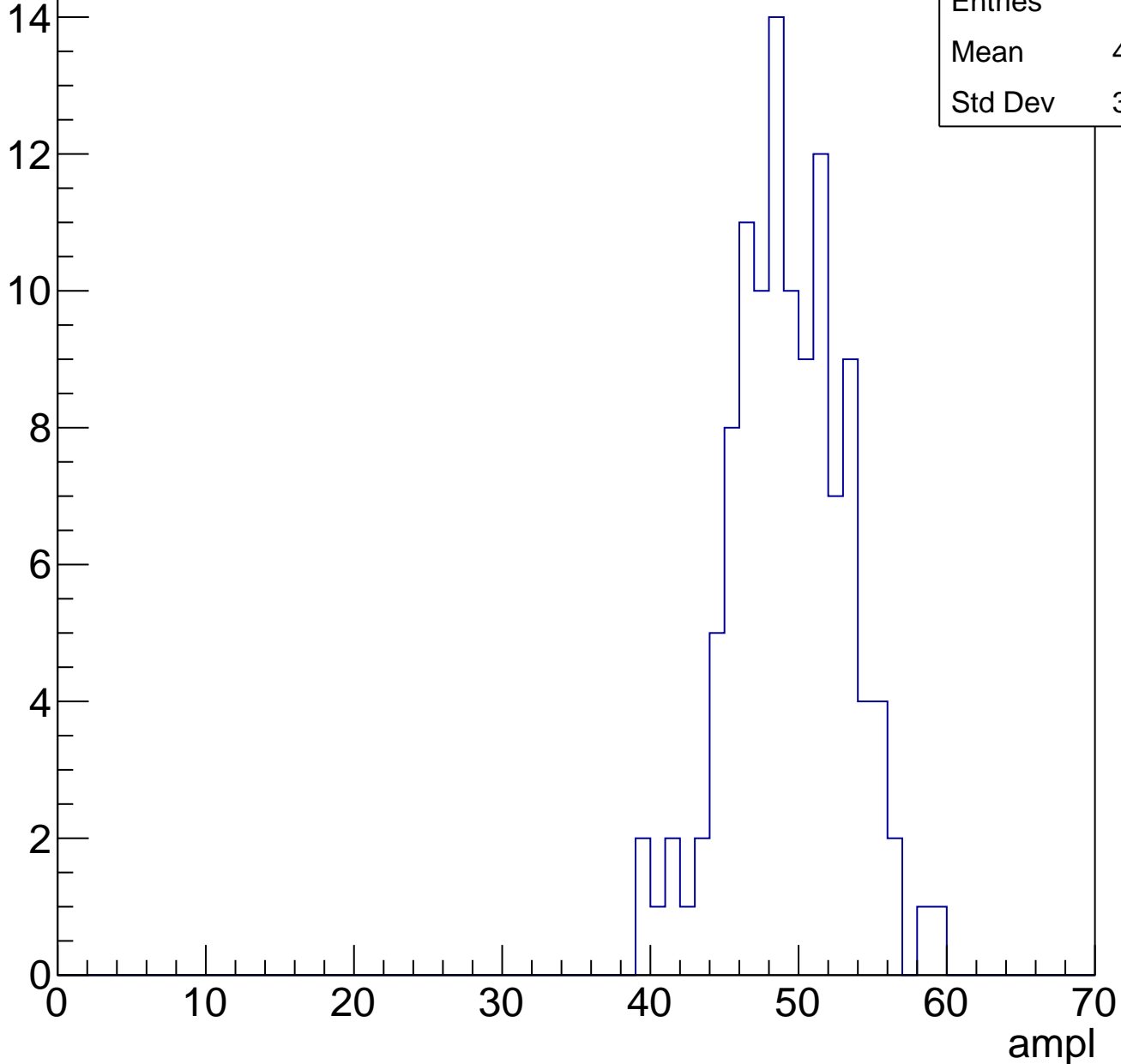


# B0L101S, U16-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 48.82 |
| Std Dev | 3.866 |



# B0L101S, U16-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 154   |
| Mean    | 55.31 |
| Std Dev | 3.778 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

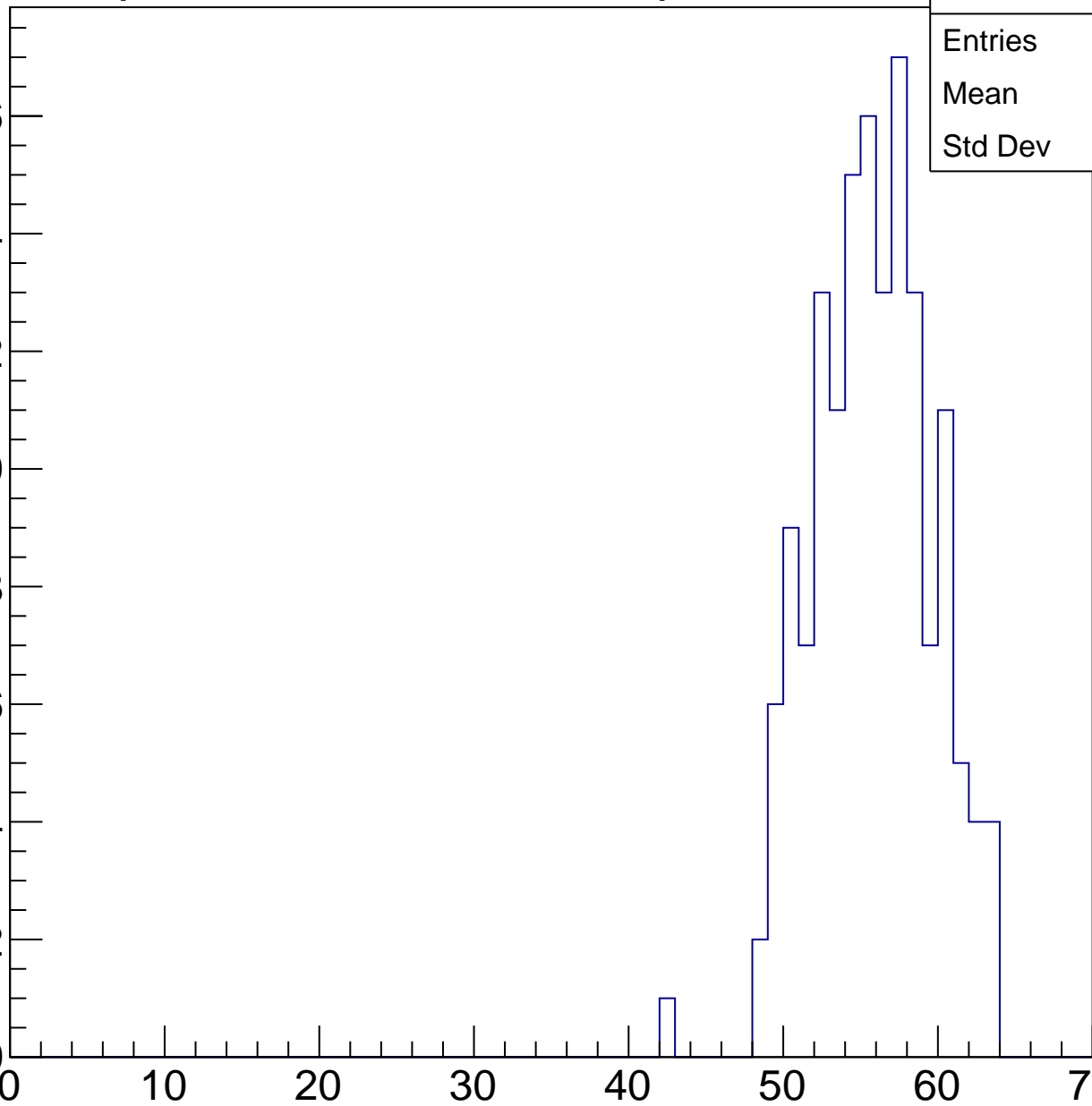
30

40

50

60

ampl



# B0L101S, U16-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

Entries 58

Mean 59.09

Std Dev 8.114

8

6

4

2

0

0

10

20

30

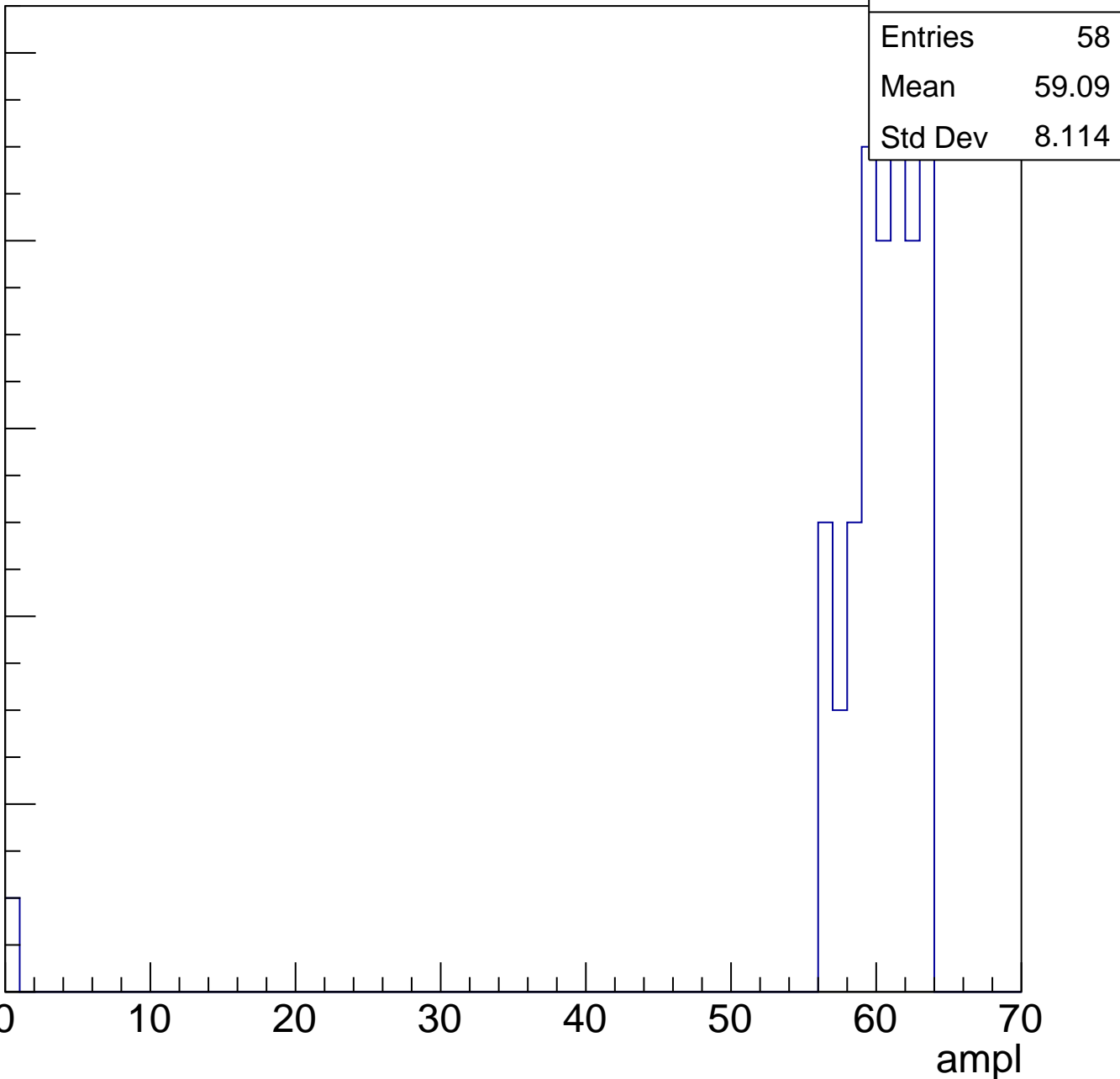
40

50

60

70

ampl

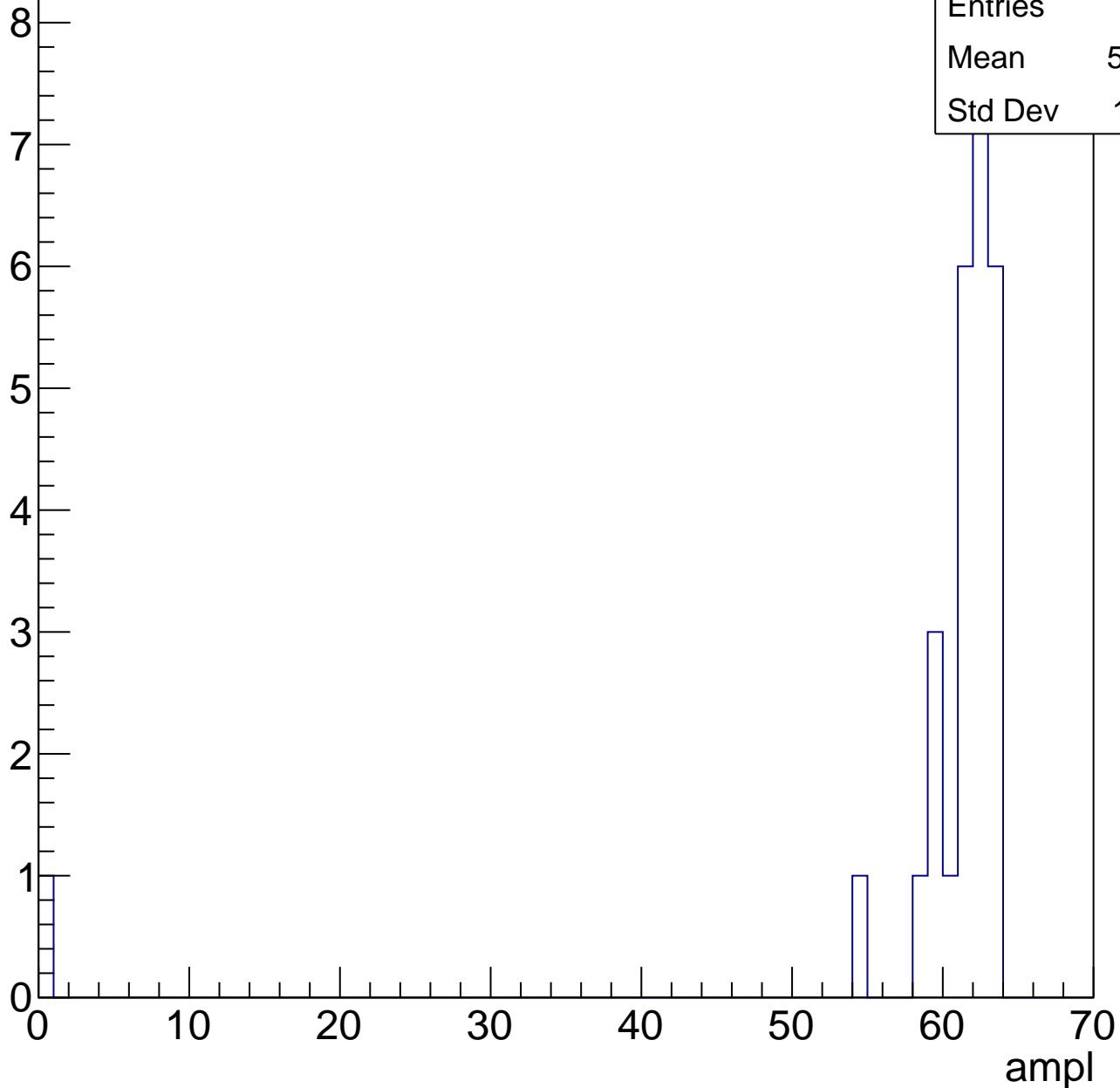


# B0L101S, U16-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 27    |
| Mean    | 58.85 |
| Std Dev | 11.71 |





# B0L101S, U16-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 3 |
| Mean    | 0 |
| Std Dev | 0 |

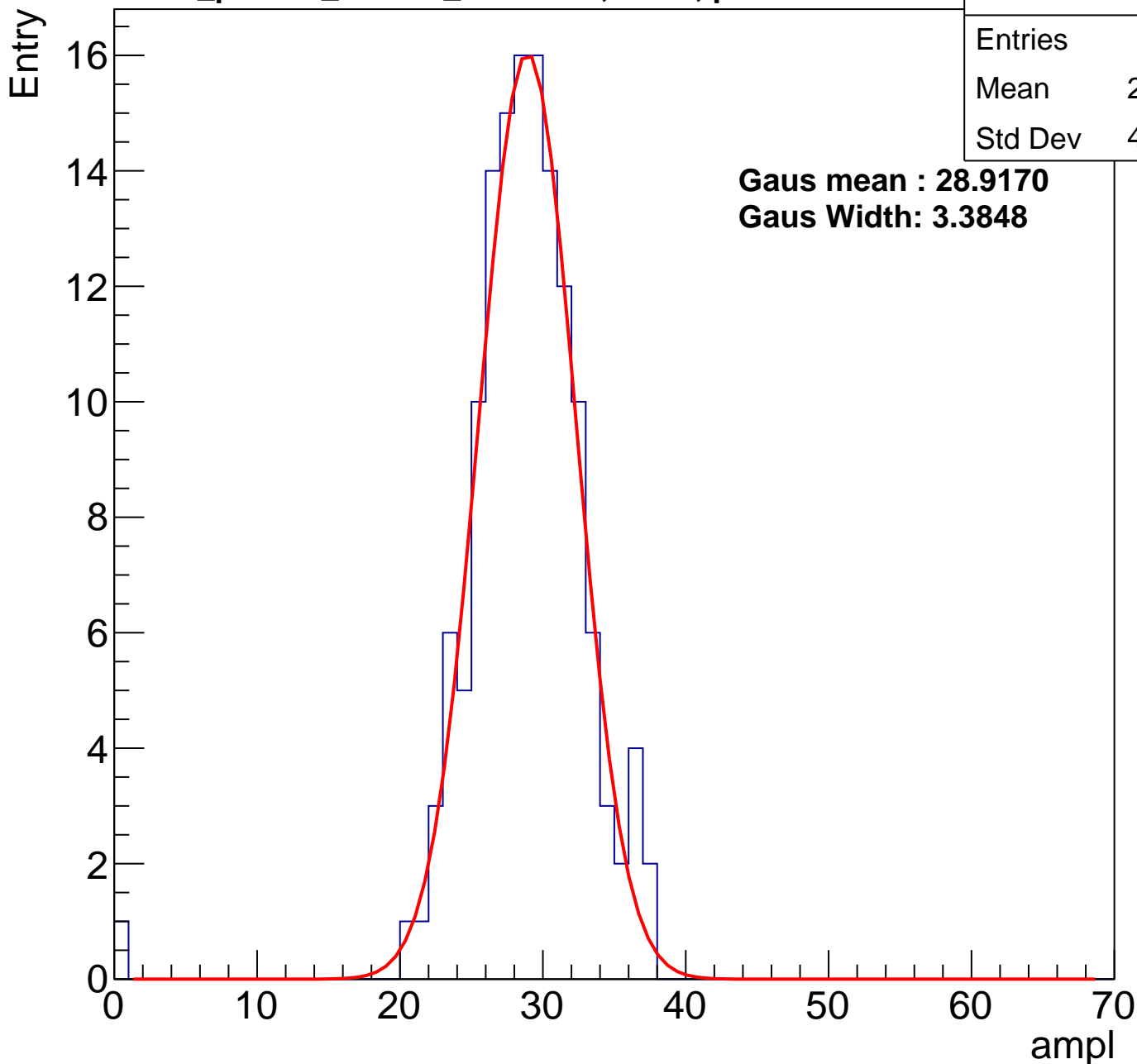
# B0L101S, U16-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 141   |
| Mean    | 28.34 |
| Std Dev | 4.205 |

**Gaus mean : 28.9170**

**Gaus Width: 3.3848**



# B0L101S, U16-ch115, adc1

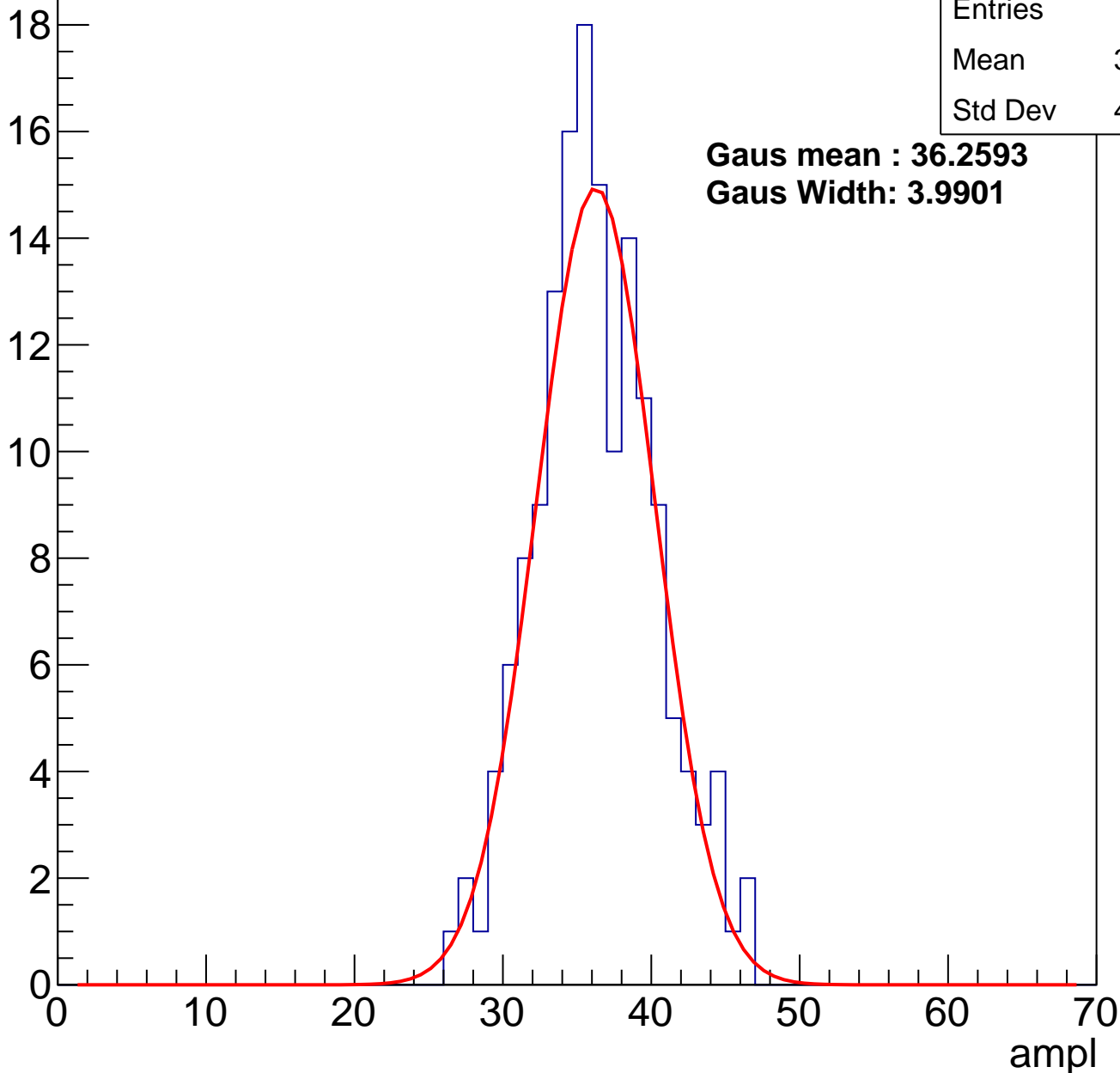
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 156   |
| Mean    | 35.83 |
| Std Dev | 4.034 |

**Gaus mean : 36.2593**

**Gaus Width: 3.9901**

Entry



# B0L101S, U16-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

114

Mean

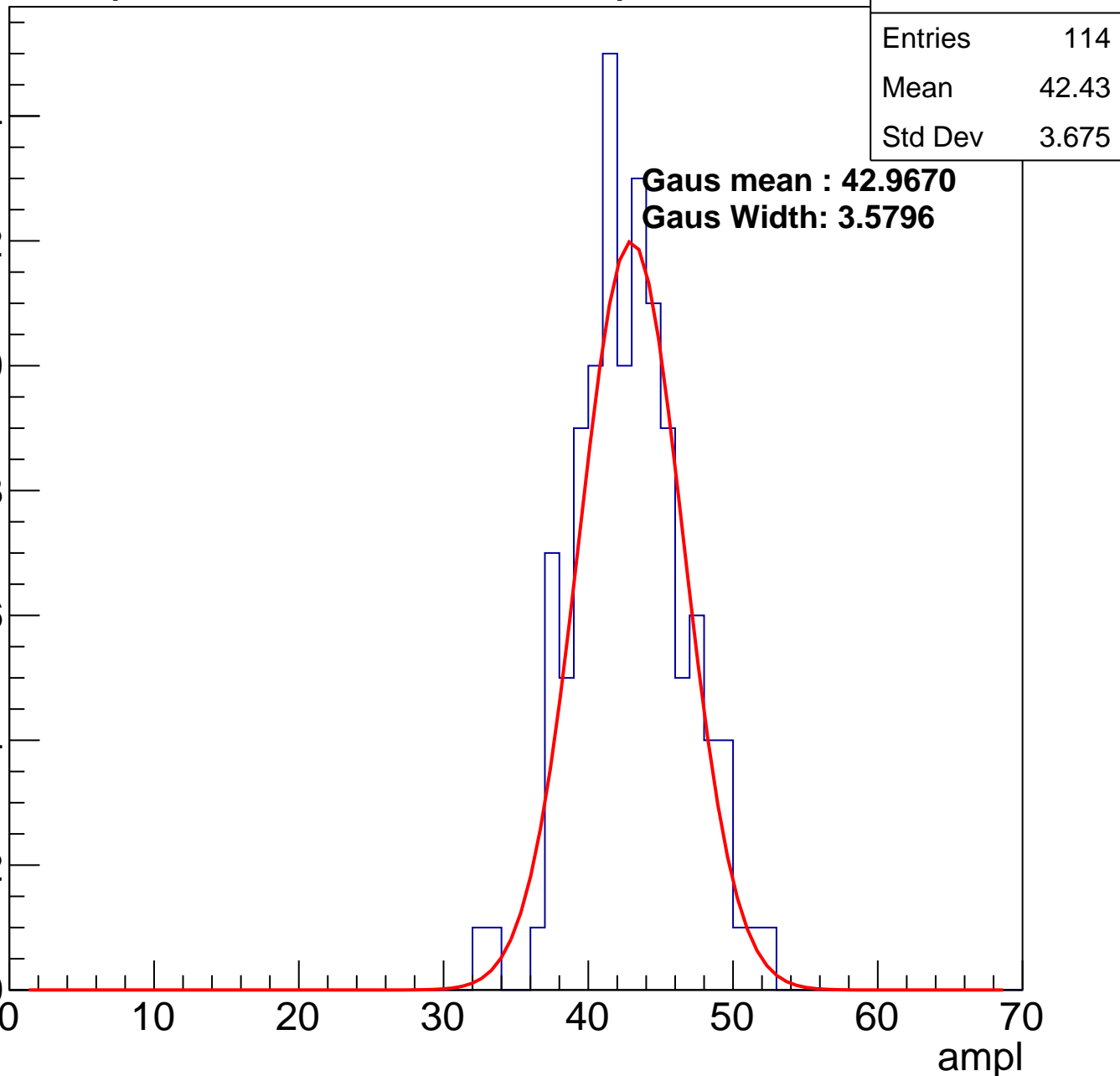
42.43

Std Dev

3.675

**Gaus mean : 42.9670**

**Gaus Width: 3.5796**



# B0L101S, U16-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

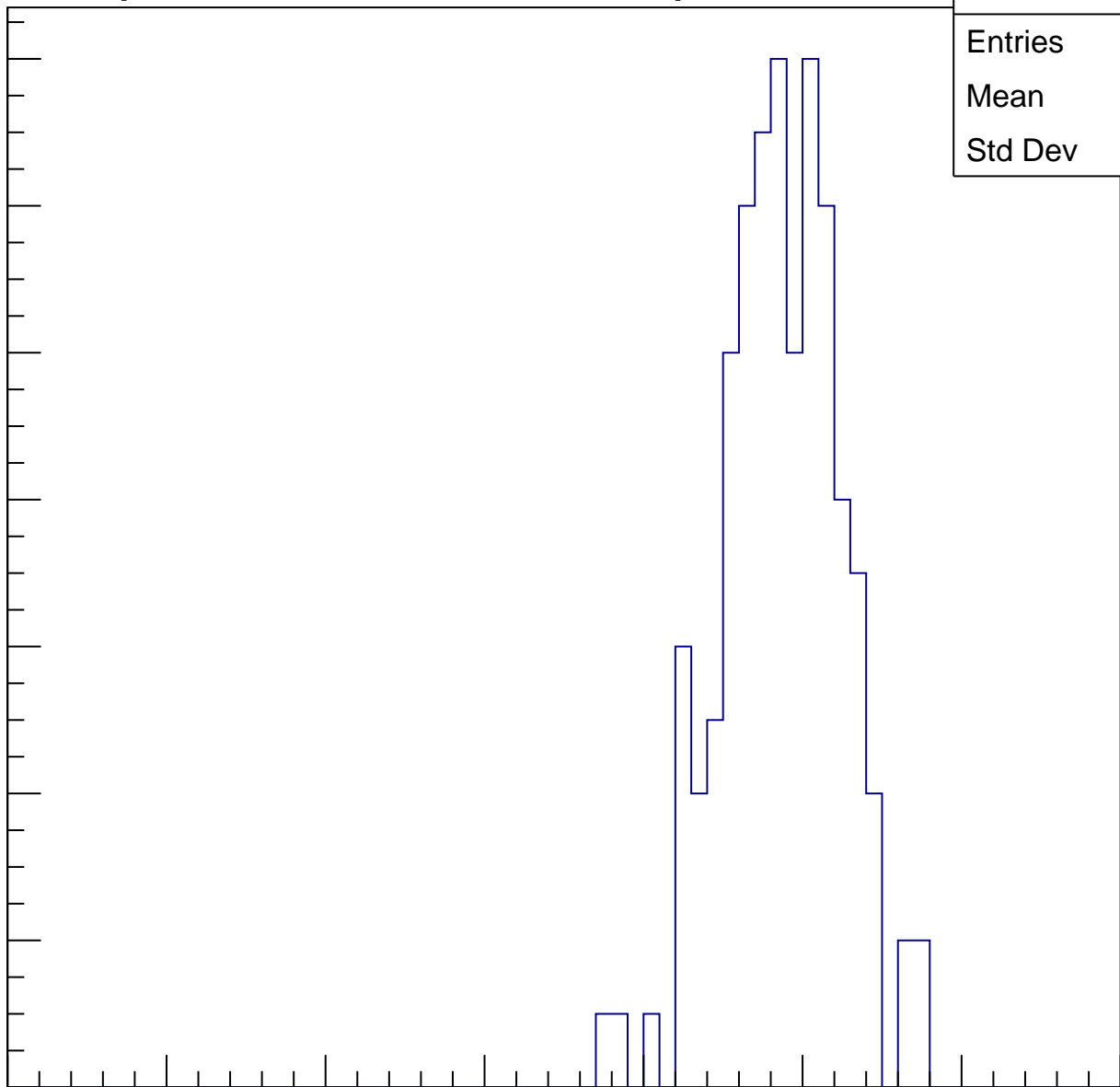
|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 48.21 |
| Std Dev | 3.713 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

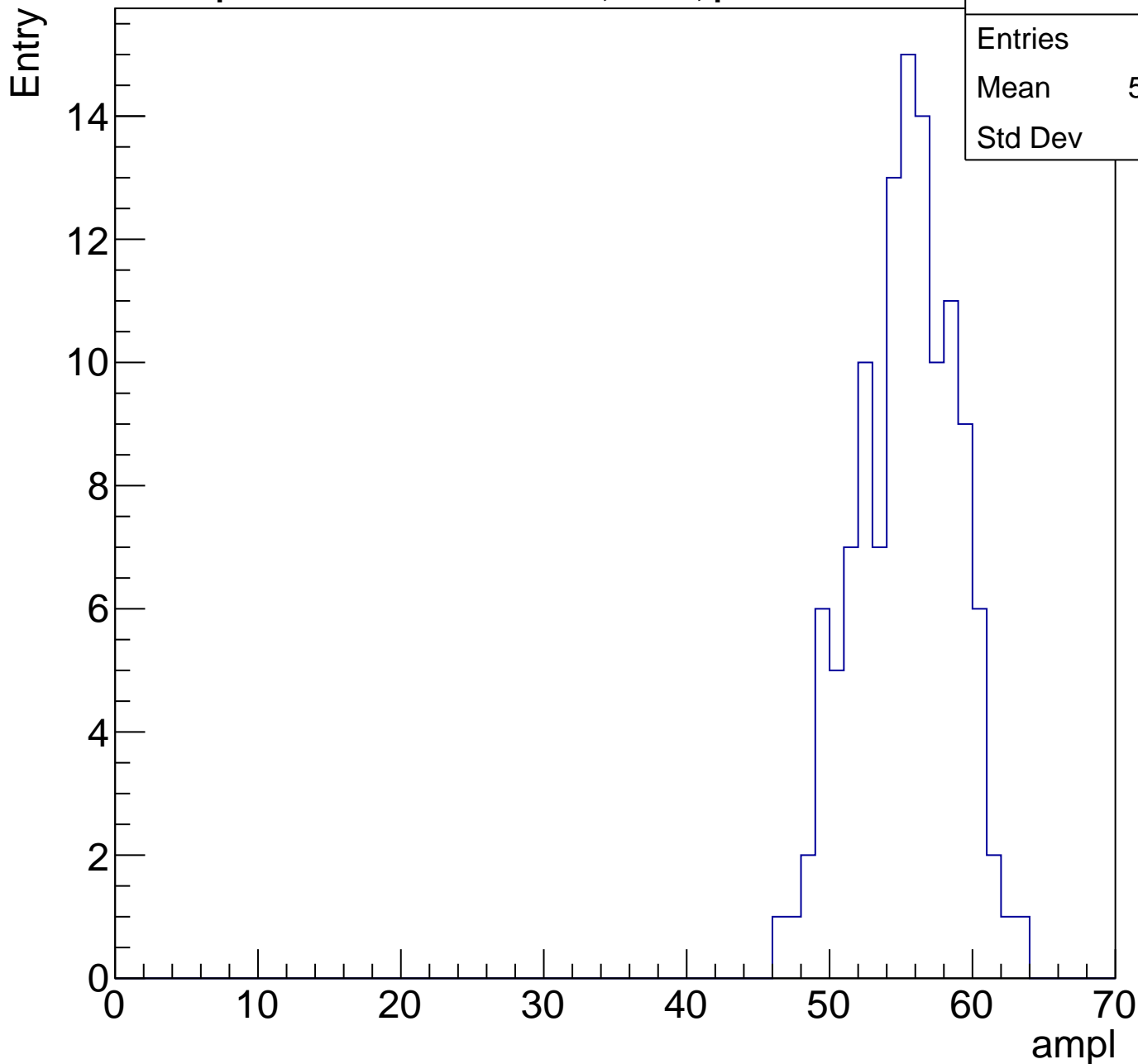
ampl



# B0L101S, U16-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 121   |
| Mean    | 54.86 |
| Std Dev | 3.47  |

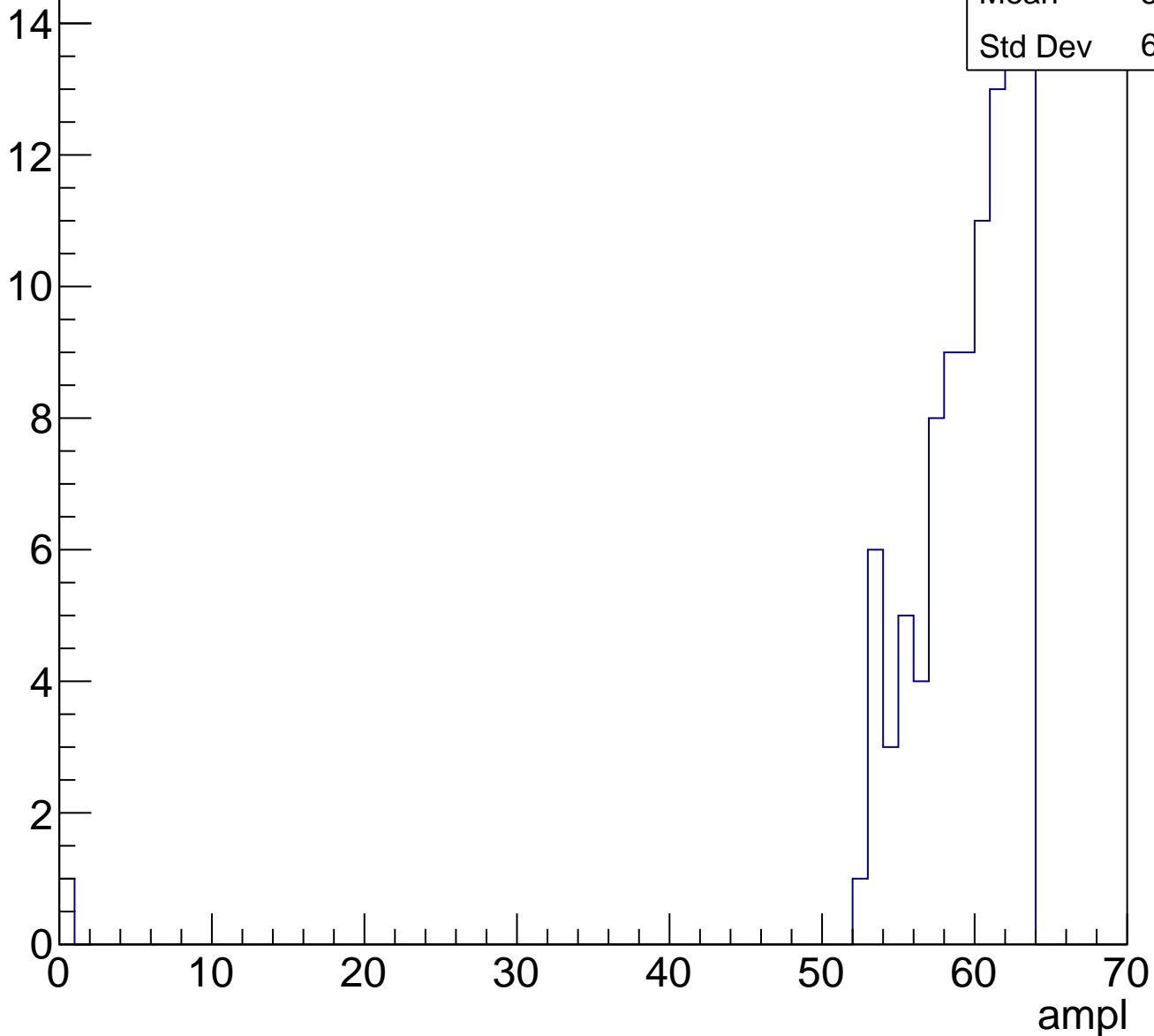


# B0L101S, U16-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 99    |
| Mean    | 58.64 |
| Std Dev | 6.657 |

Entry

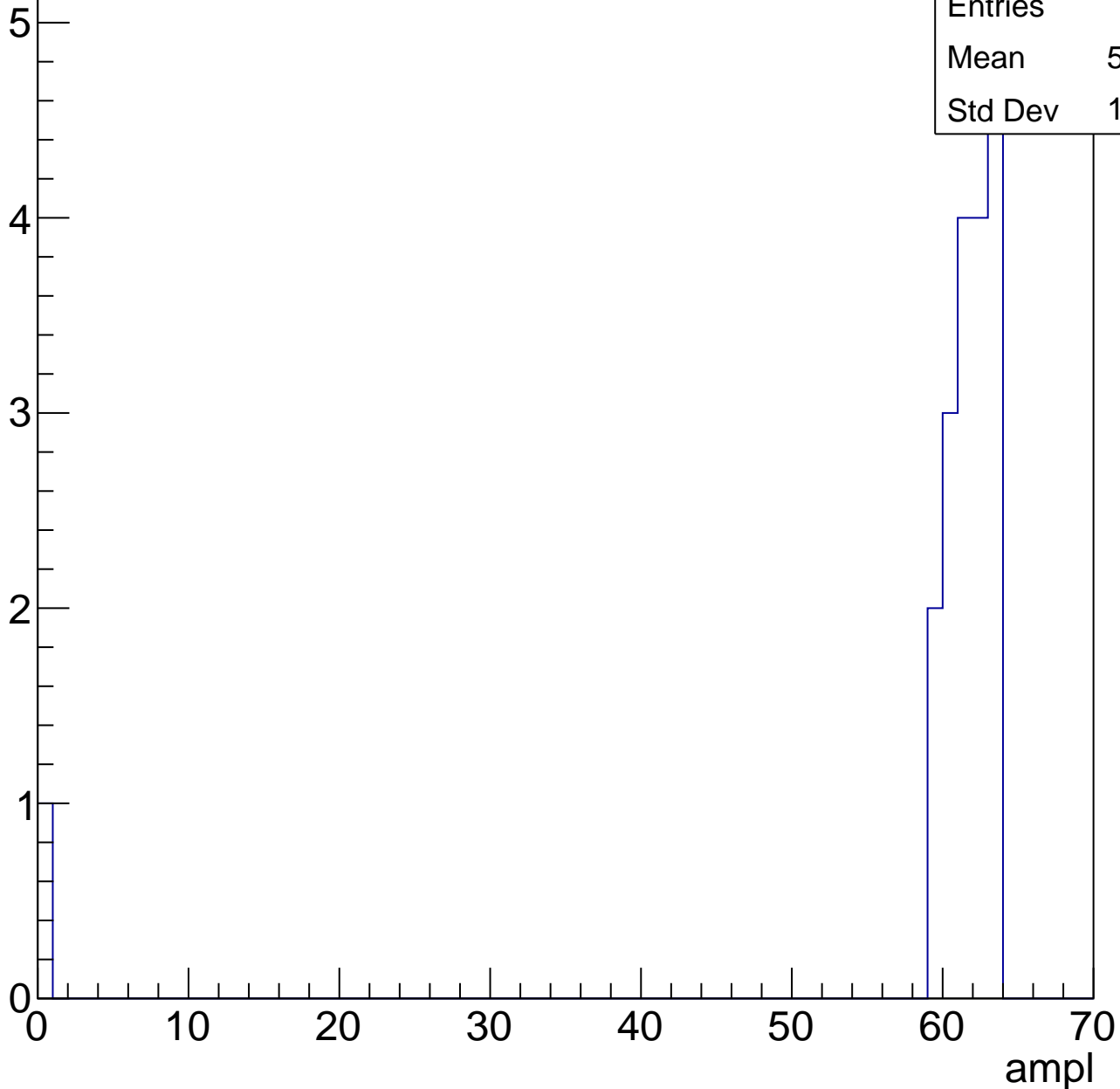


# B0L101S, U16-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 19    |
| Mean    | 58.16 |
| Std Dev | 13.77 |

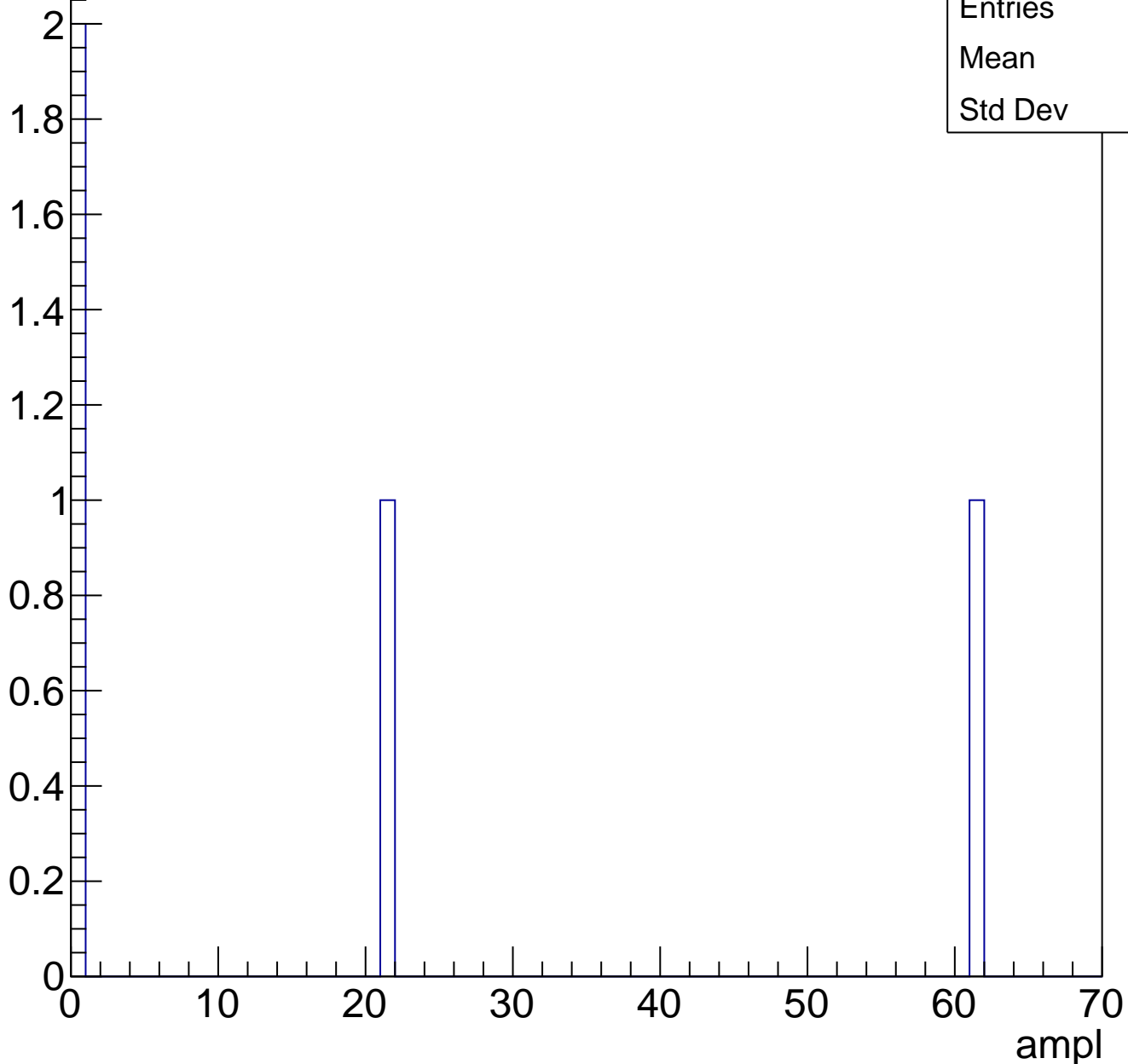




# B0L101S, U16-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |      |
|---------|------|
| Entries | 4    |
| Mean    | 20.5 |
| Std Dev | 24.9 |

# B0L101S, U16-ch116, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 126   |
| Mean    | 28.02 |
| Std Dev | 5.033 |

**Gaus mean : 28.4520**

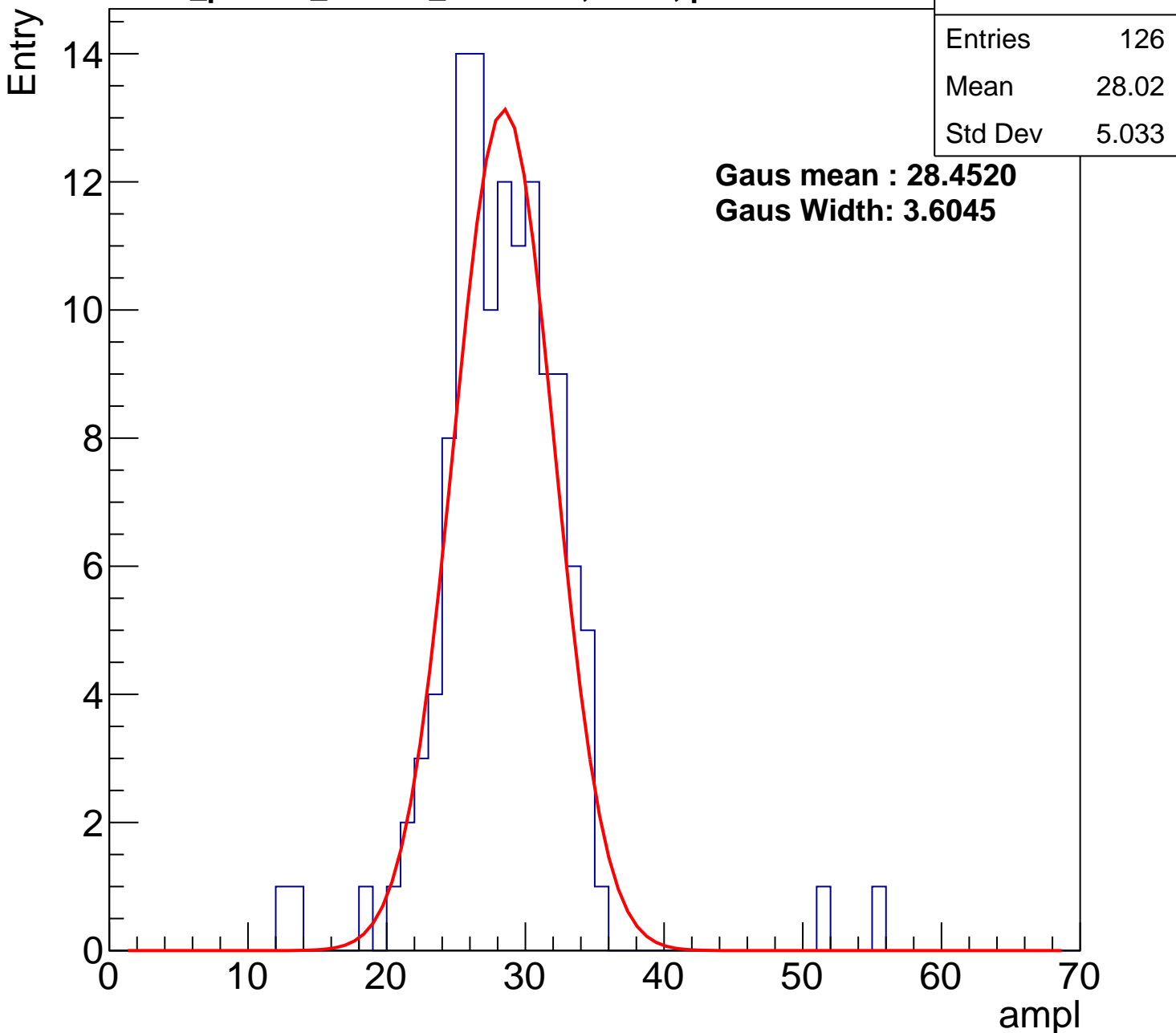
**Gaus Width: 3.6045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch116, adc1

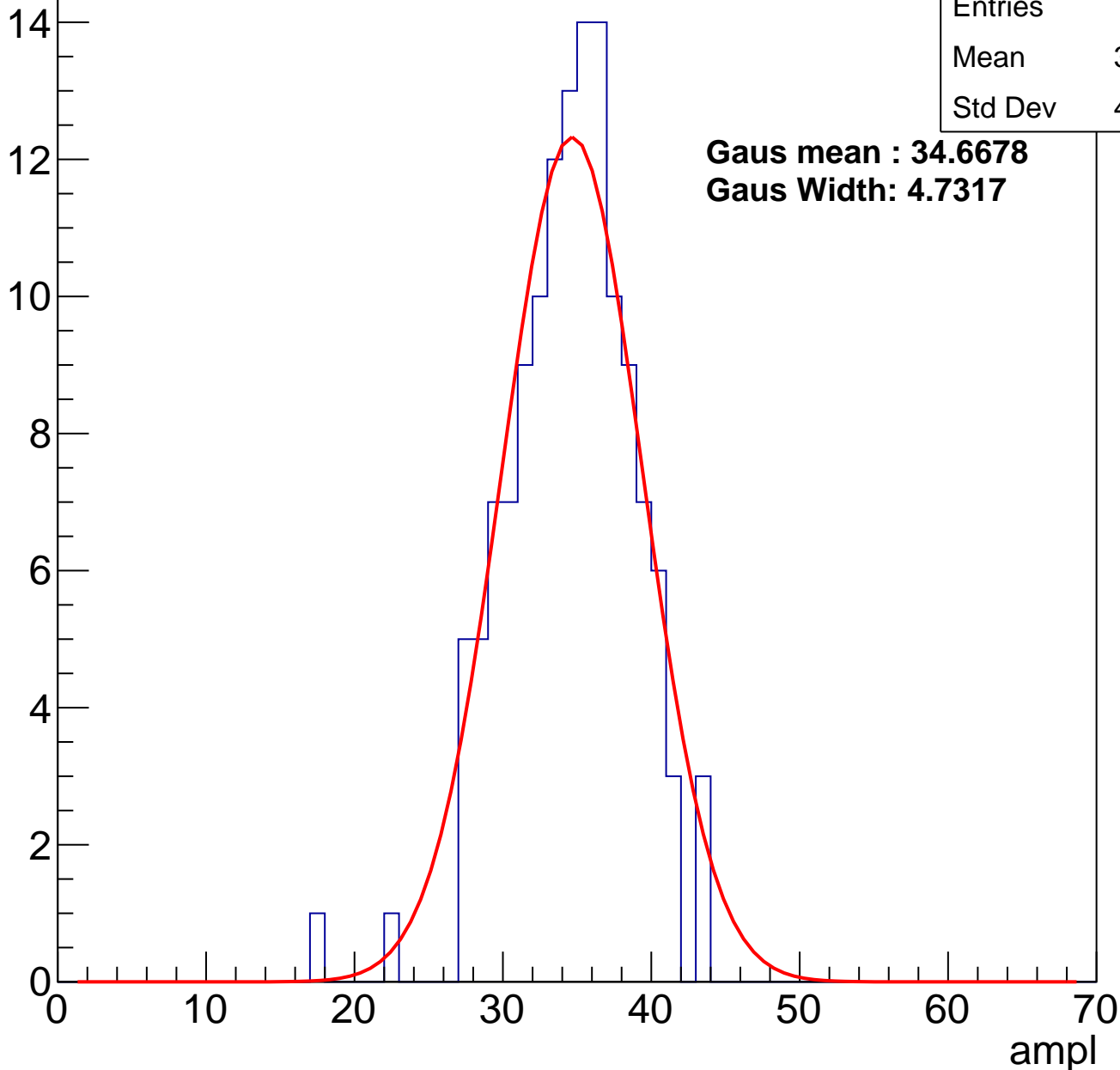
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 34.08 |
| Std Dev | 4.168 |

**Gaus mean : 34.6678**

**Gaus Width: 4.7317**

Entry



# B0L101S, U16-ch116, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

41.46

Std Dev

3.736

**Gaus mean : 41.8508**

**Gaus Width: 3.9403**

0

10

20

30

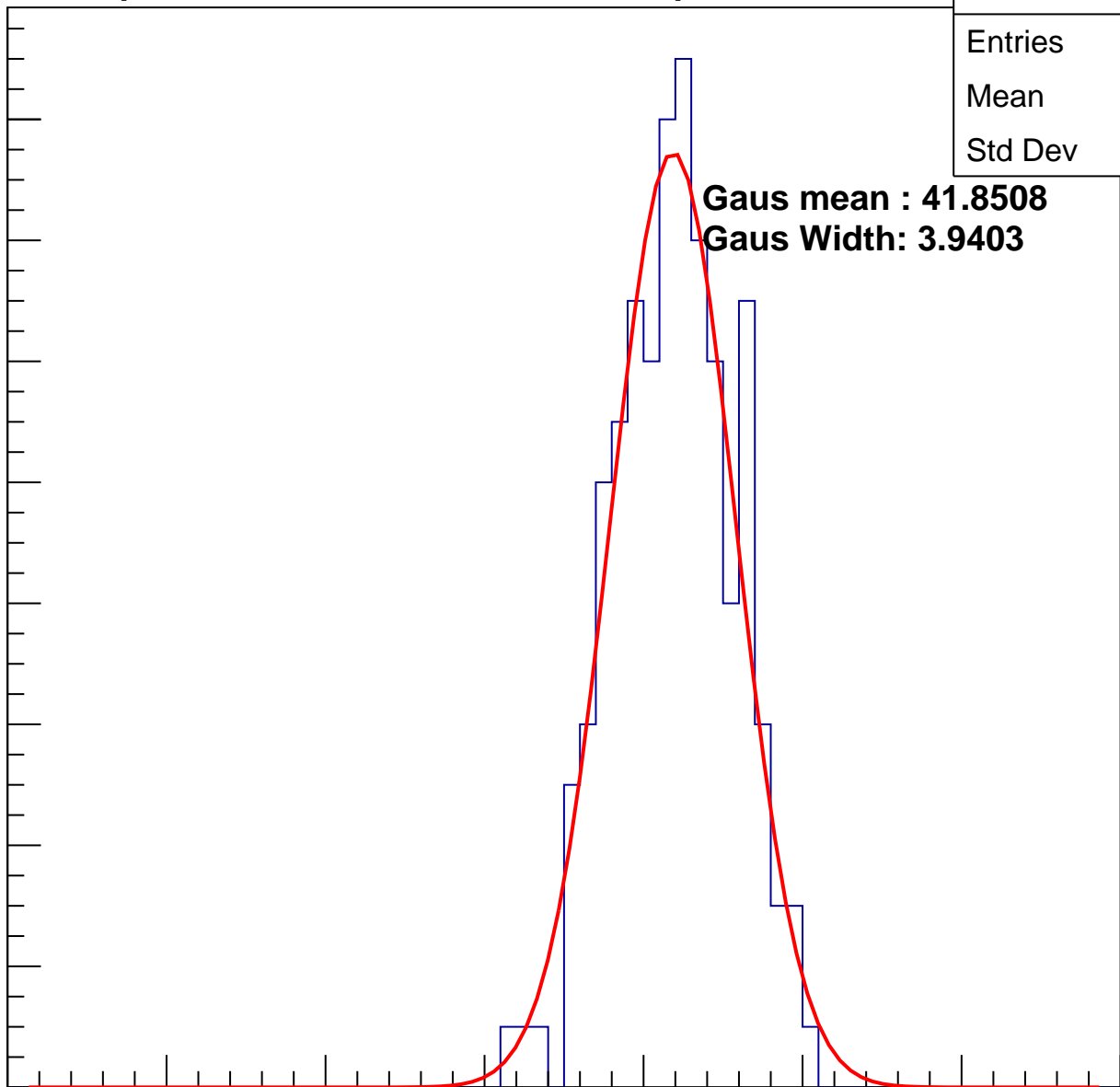
40

50

60

70

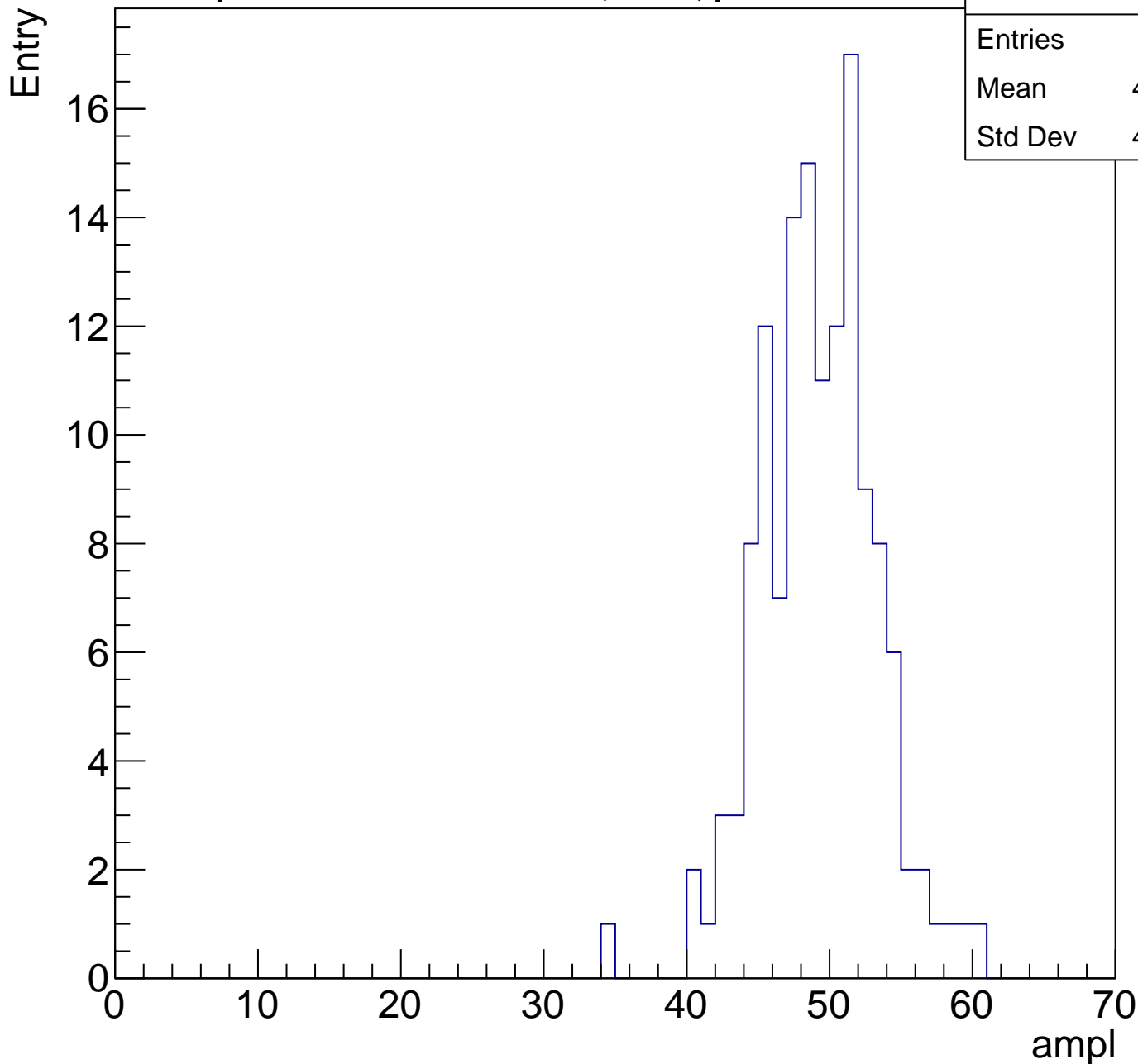
ampl



# B0L101S, U16-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 48.76 |
| Std Dev | 4.006 |



# B0L101S, U16-ch116, adc4

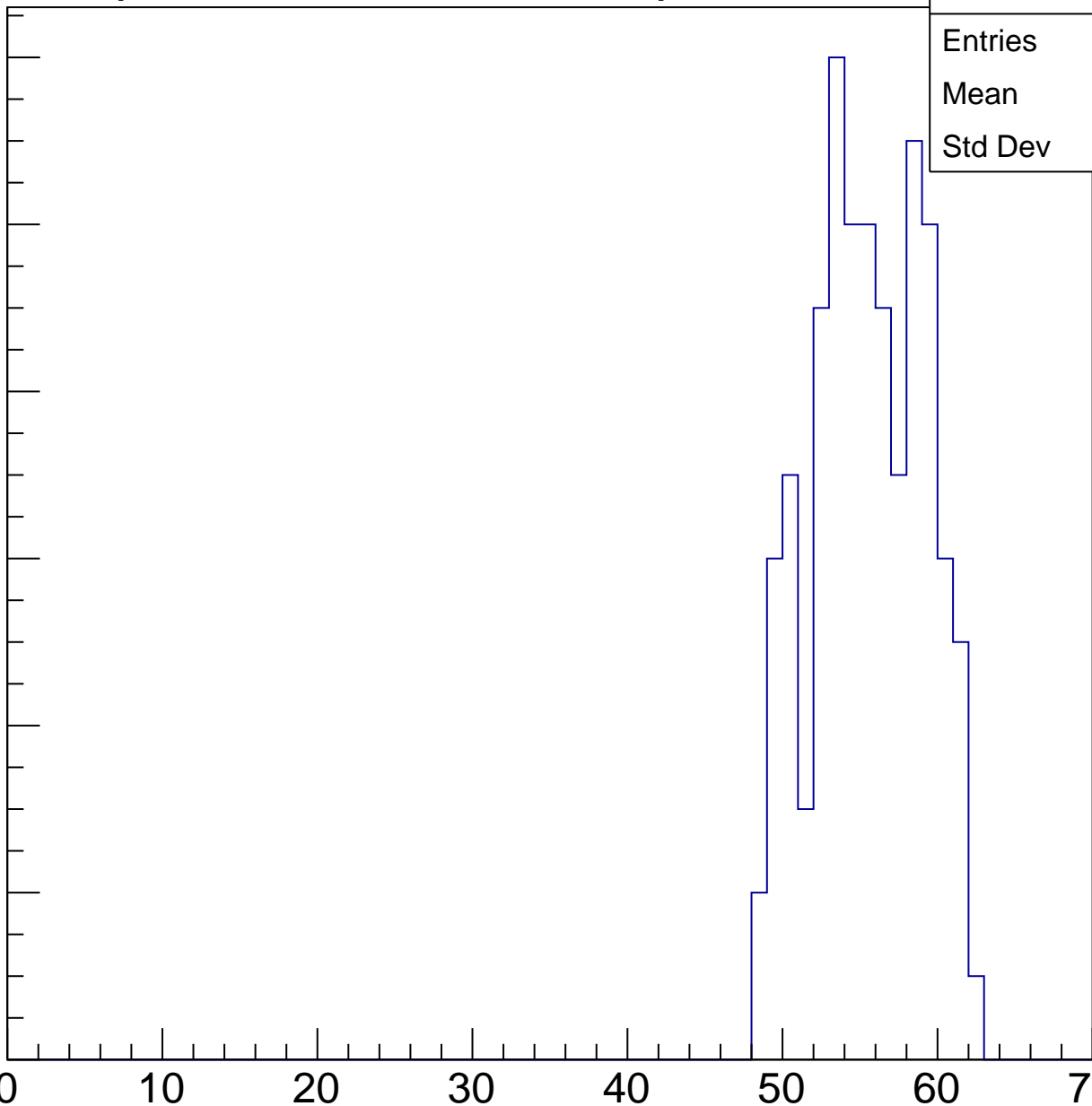
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 55.05 |
| Std Dev | 3.537 |

ampl



# B0L101S, U16-ch116, adc5

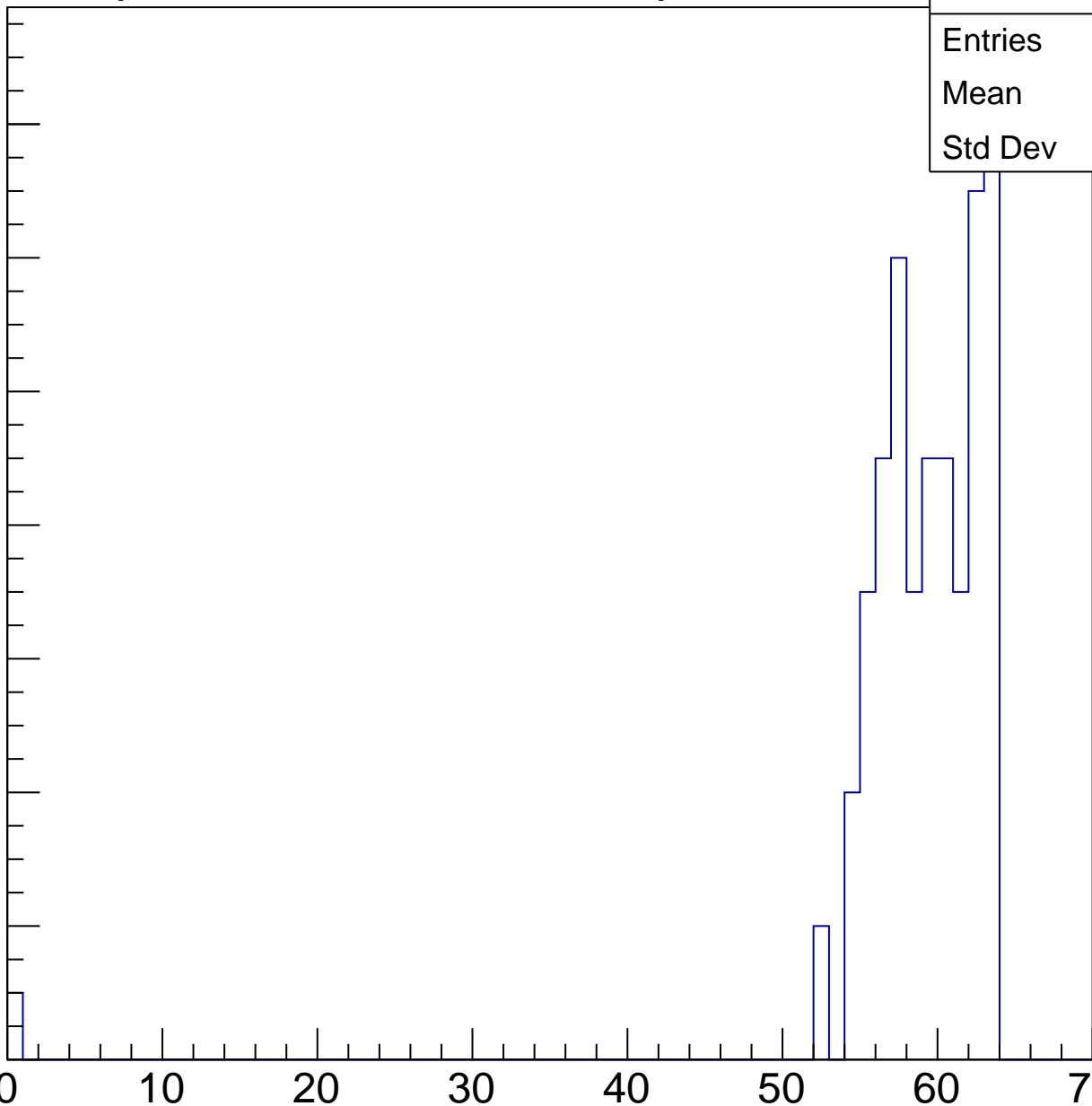
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 58.4  |
| Std Dev | 6.717 |

ampl

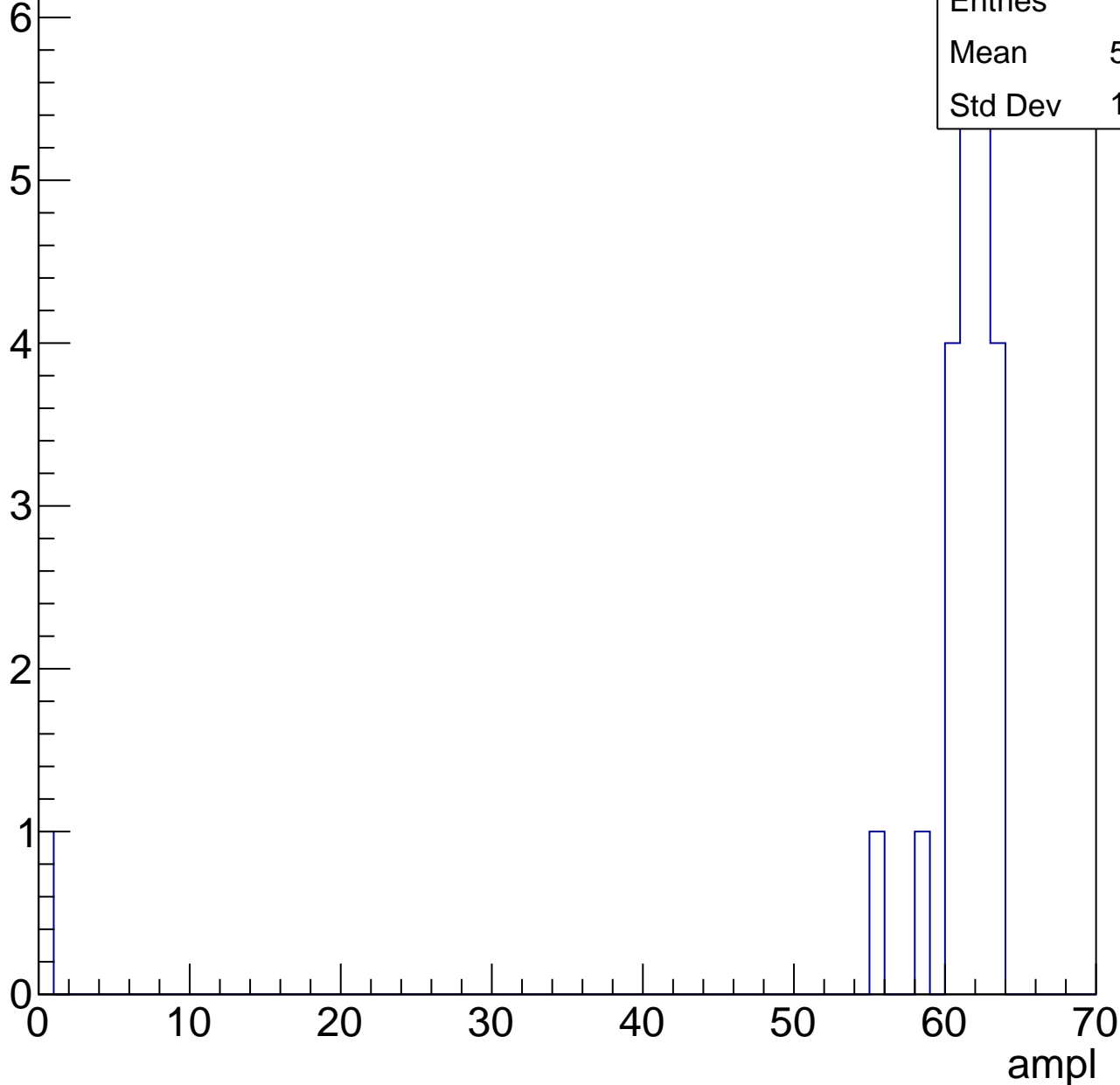


# B0L101S, U16-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 23    |
| Mean    | 58.39 |
| Std Dev | 12.57 |

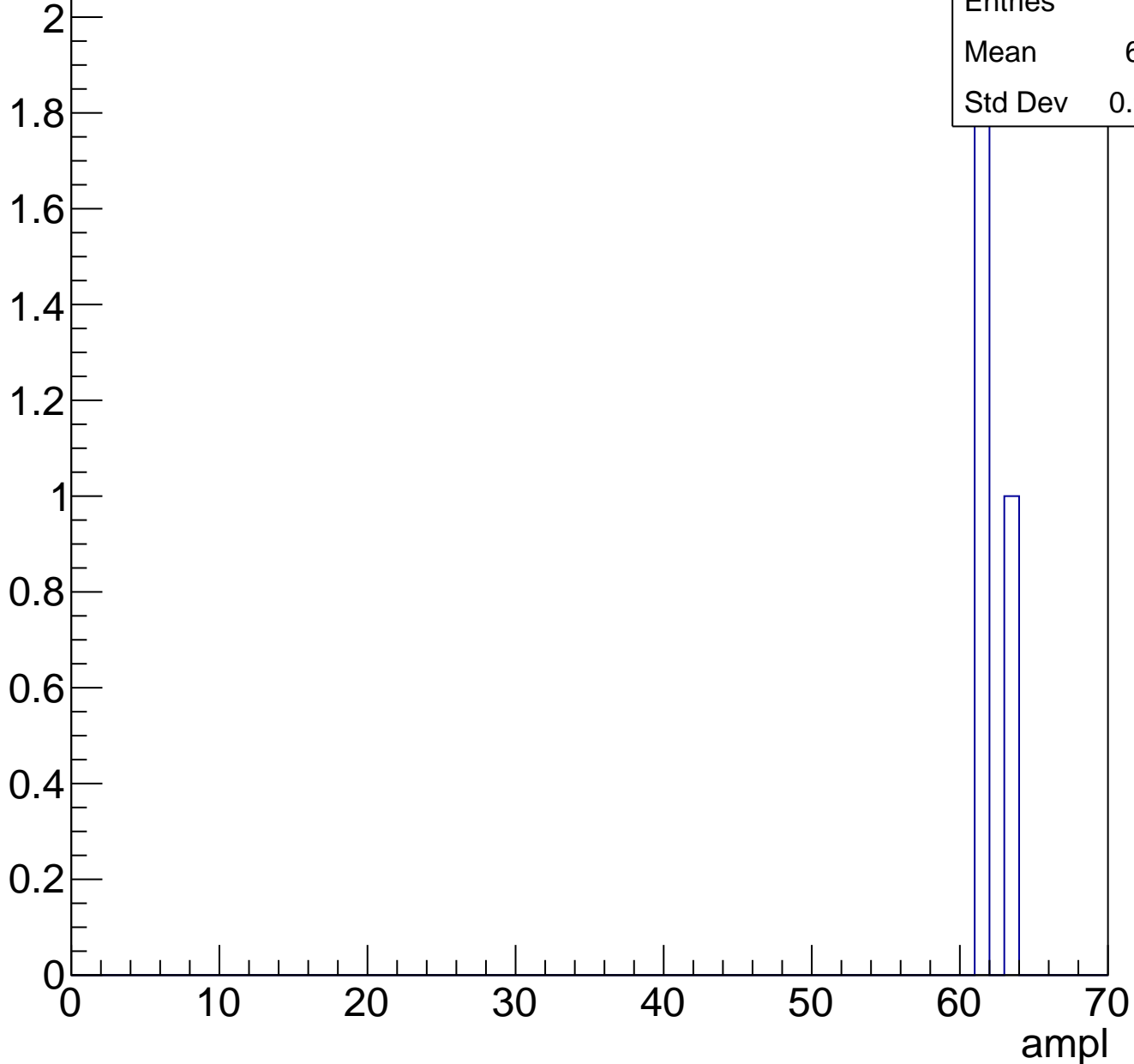




# B0L101S, U16-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch117, adc0

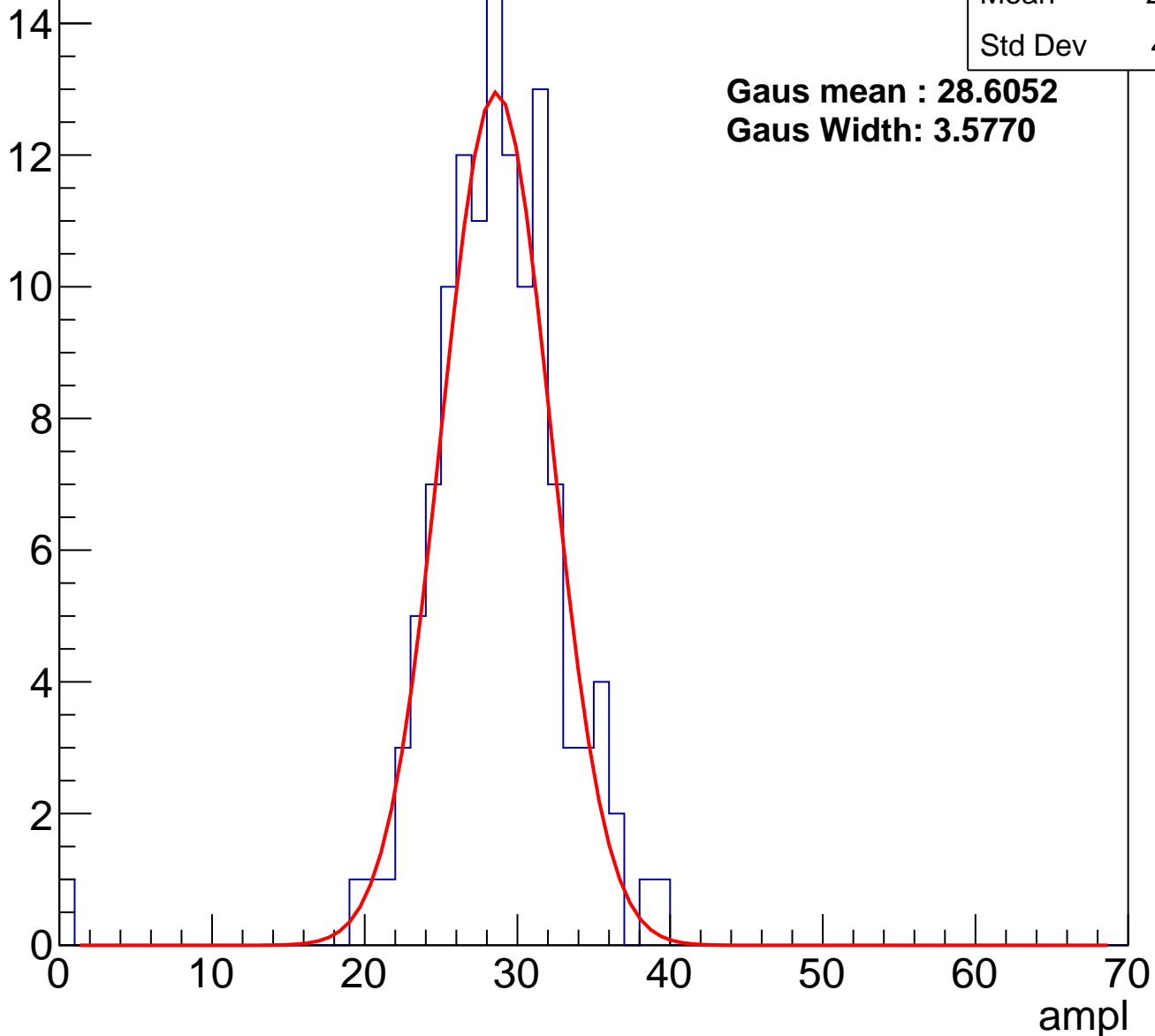
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 28.07 |
| Std Dev | 4.491 |

**Gaus mean : 28.6052**

**Gaus Width: 3.5770**

Entry



# B0L101S, U16-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 35.22 |
| Std Dev | 4.139 |

**Gaus mean : 35.6787**

**Gaus Width: 3.7015**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

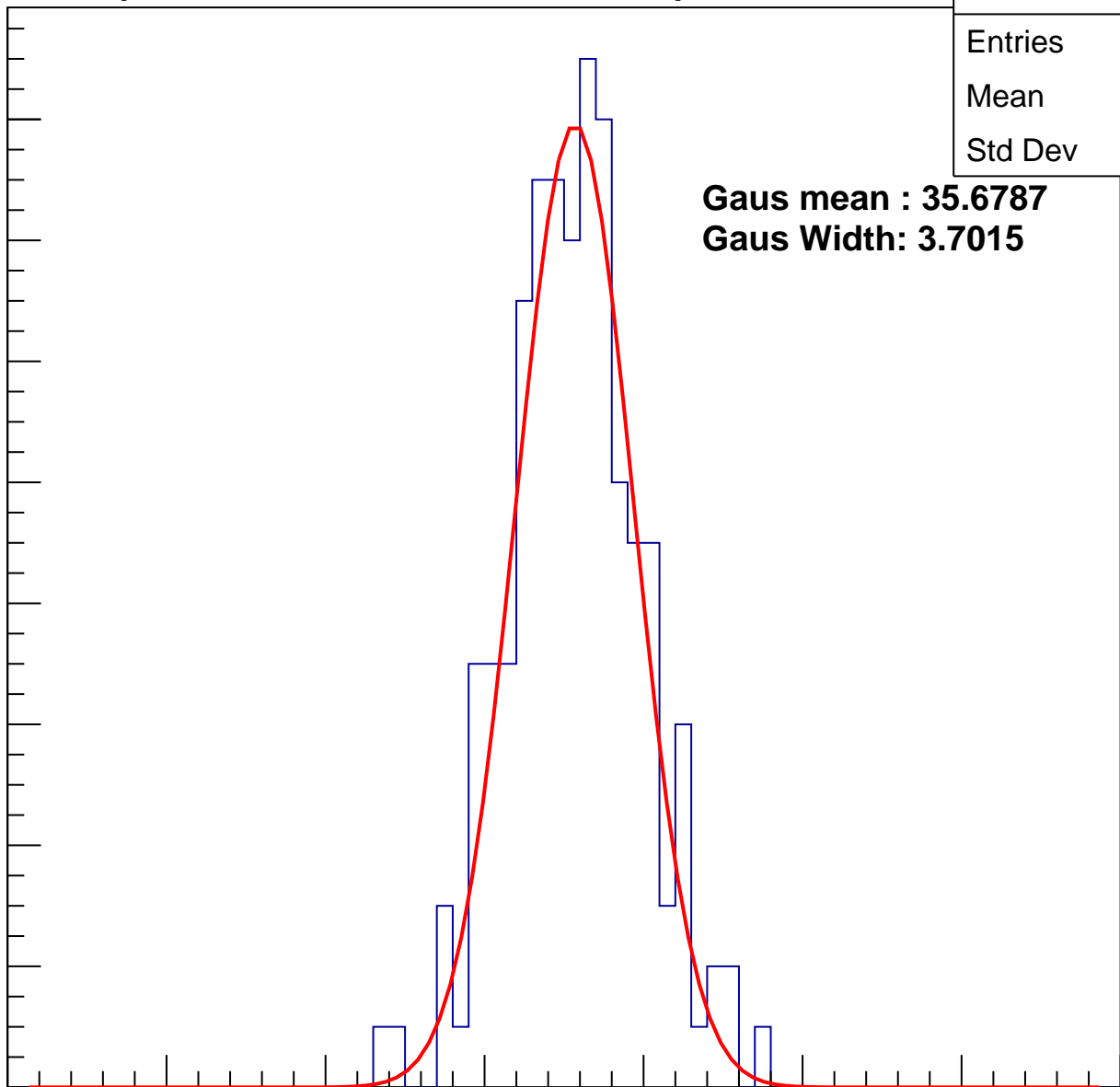
40

50

60

70

ampl



# B0L101S, U16-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 41.51 |
| Std Dev | 3.553 |

**Gaus mean : 42.1613**

**Gaus Width: 3.0926**

Entry

12

10

8

6

4

2

0

0

10

20

30

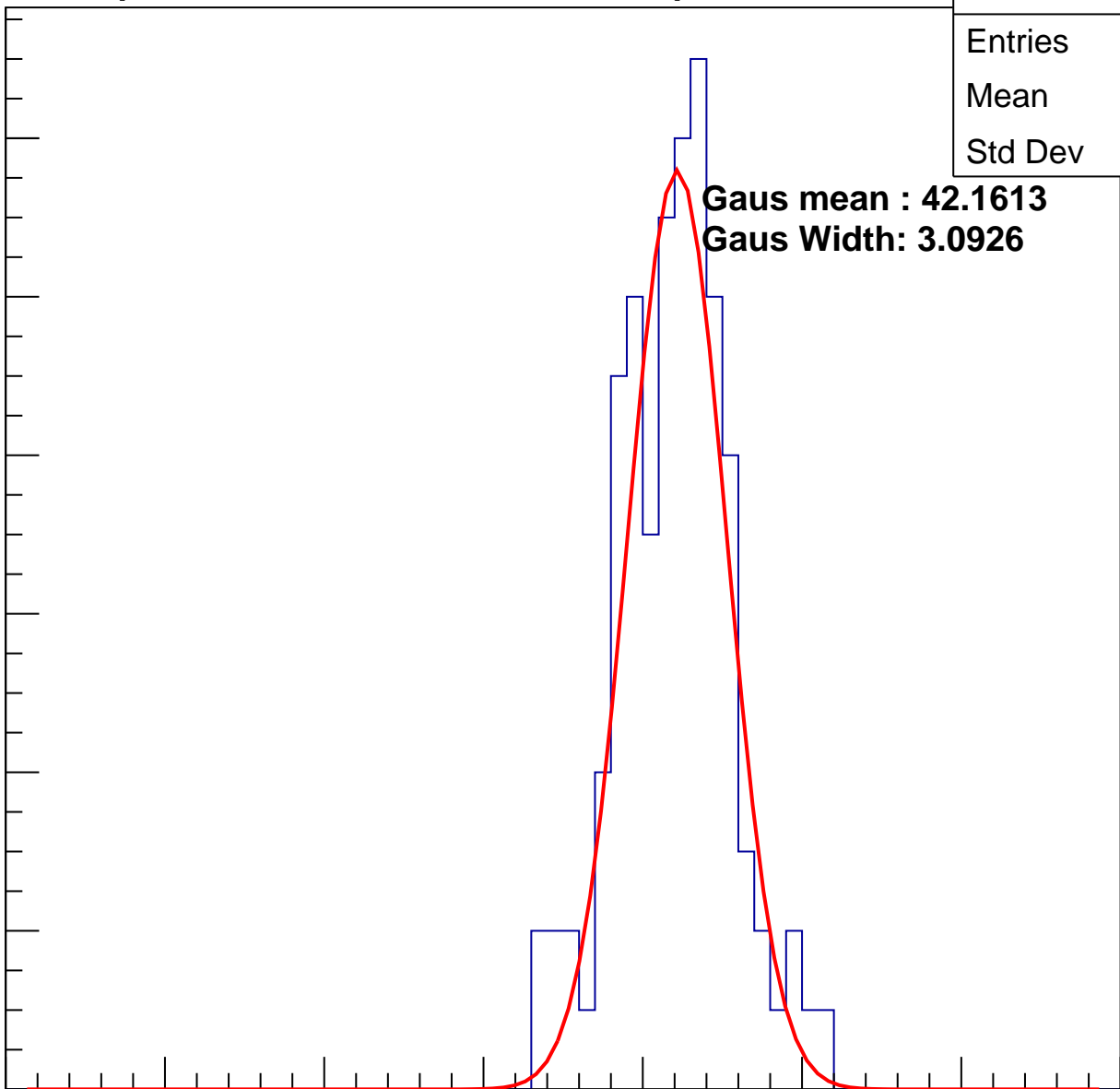
40

50

60

70

ampl

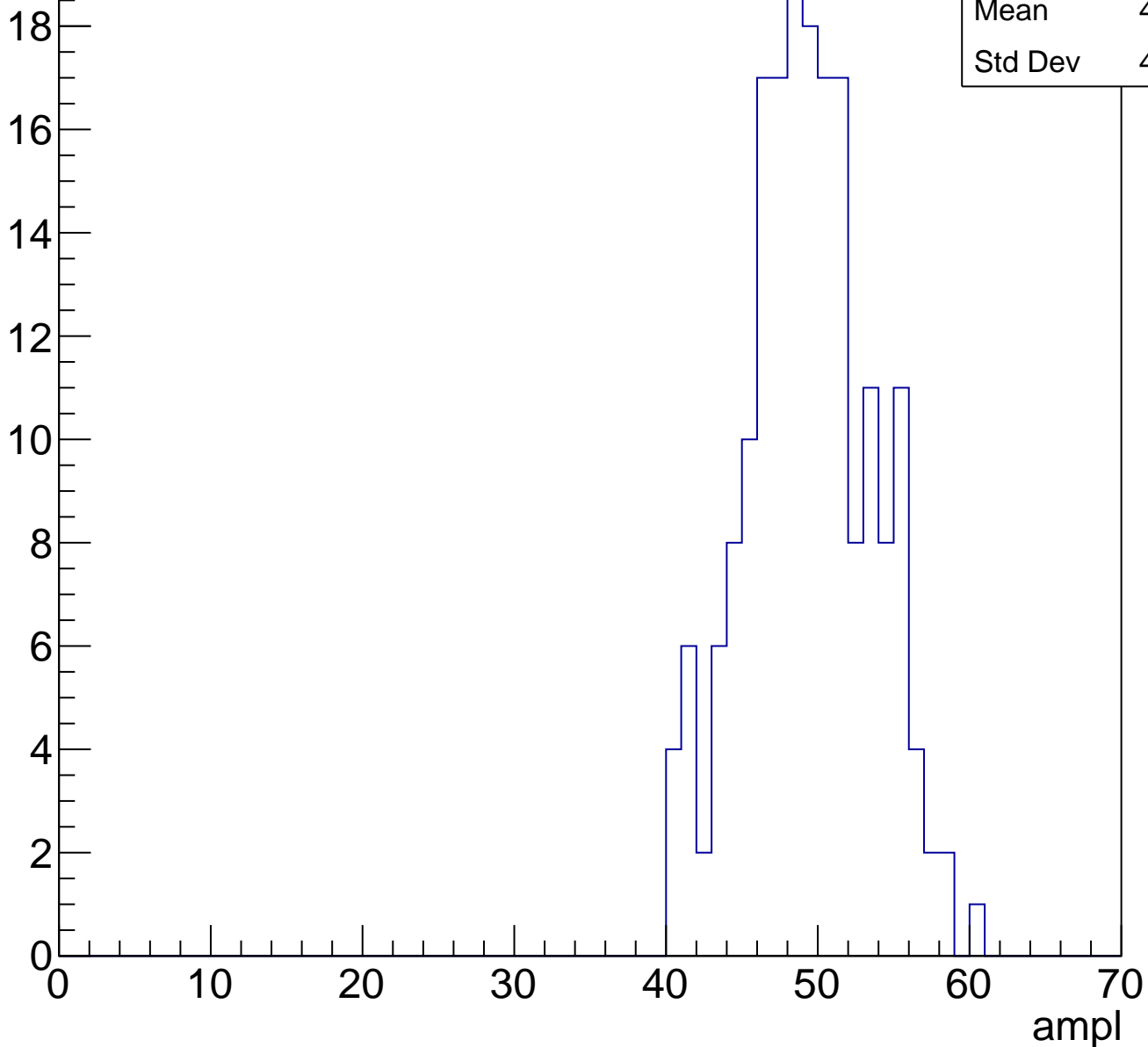


# B0L101S, U16-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 188   |
| Mean    | 48.89 |
| Std Dev | 4.097 |

Entry

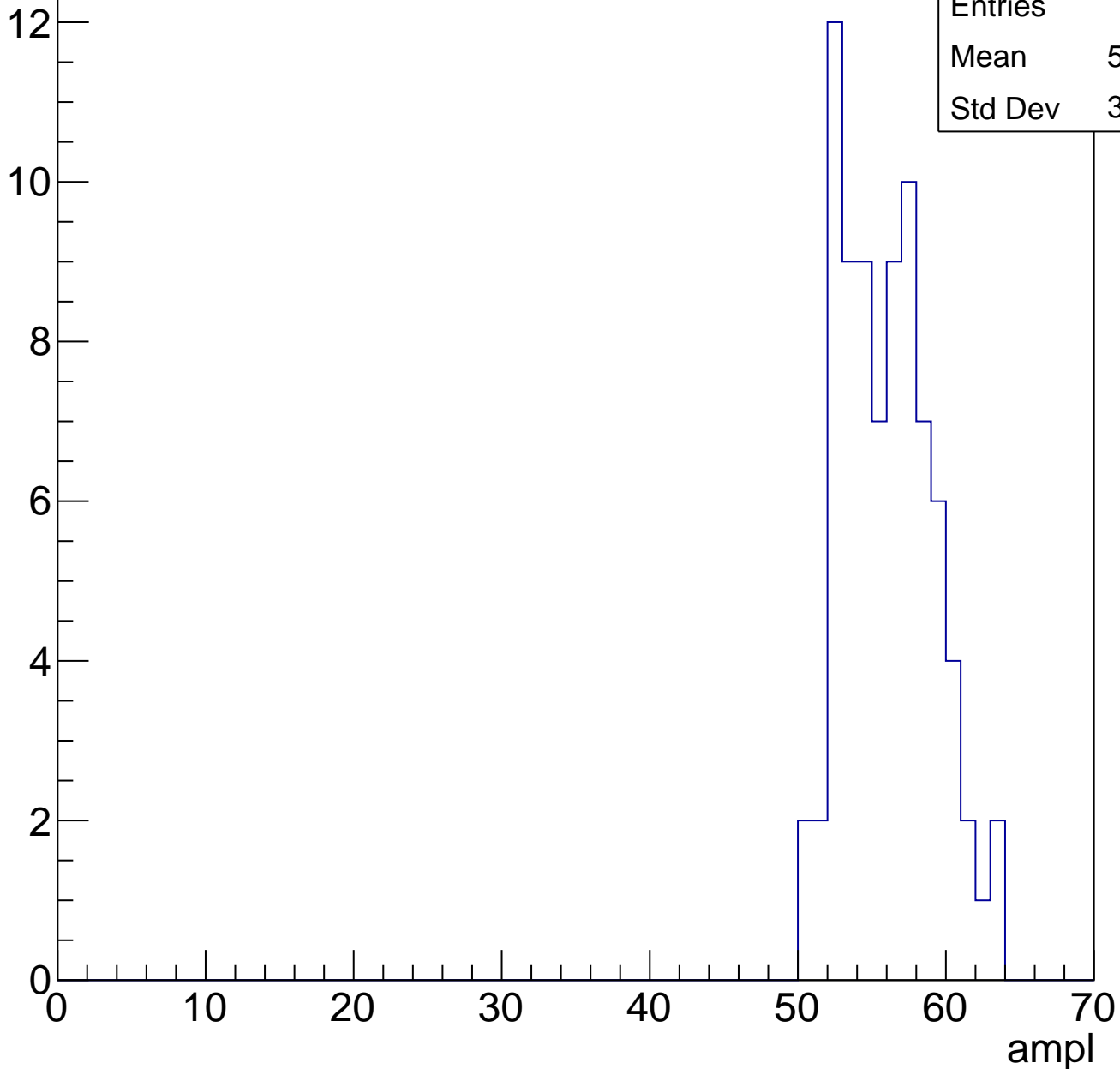


# B0L101S, U16-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 55.59 |
| Std Dev | 3.052 |

Entry

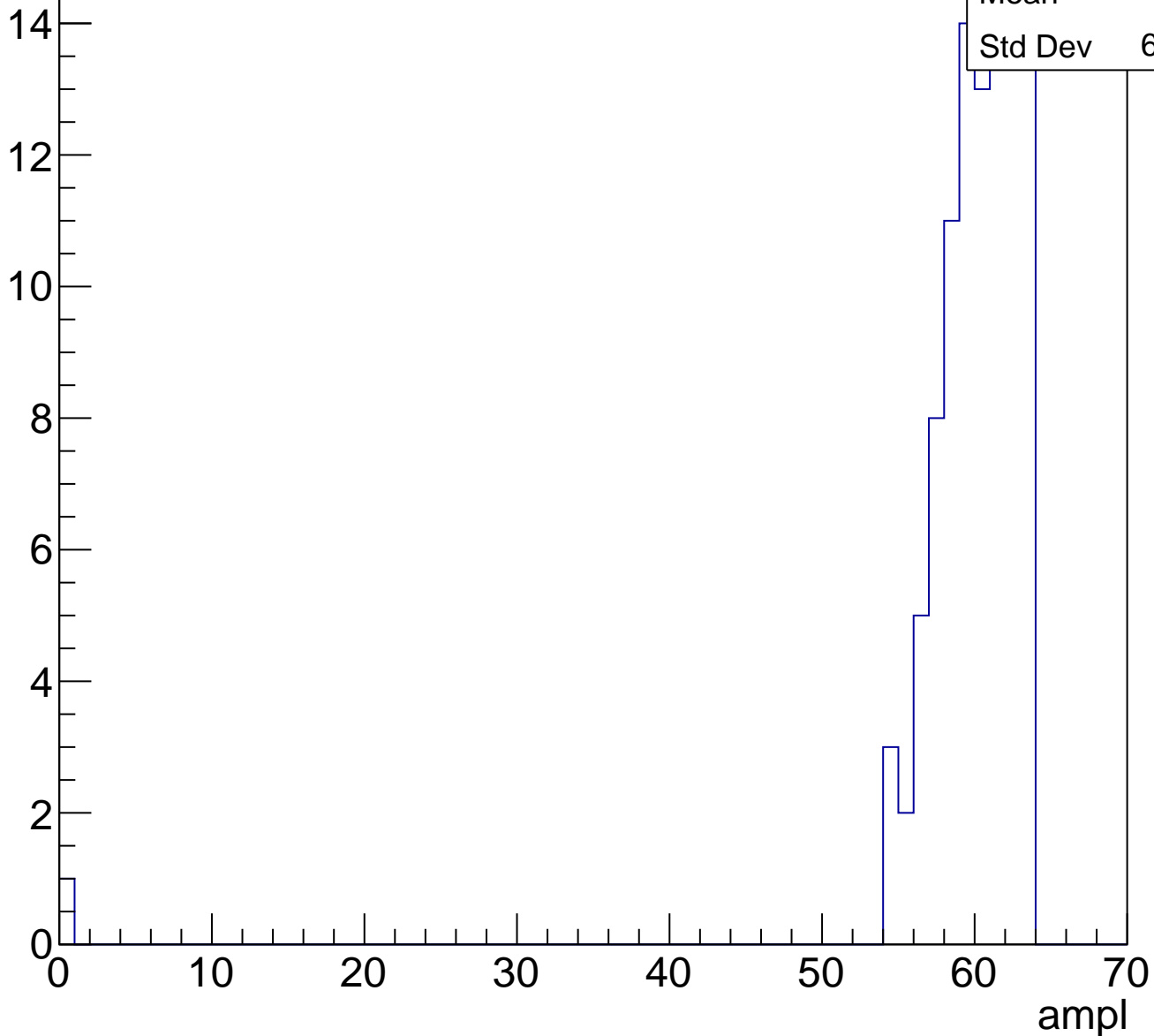


# B0L101S, U16-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 101   |
| Mean    | 59.2  |
| Std Dev | 6.374 |

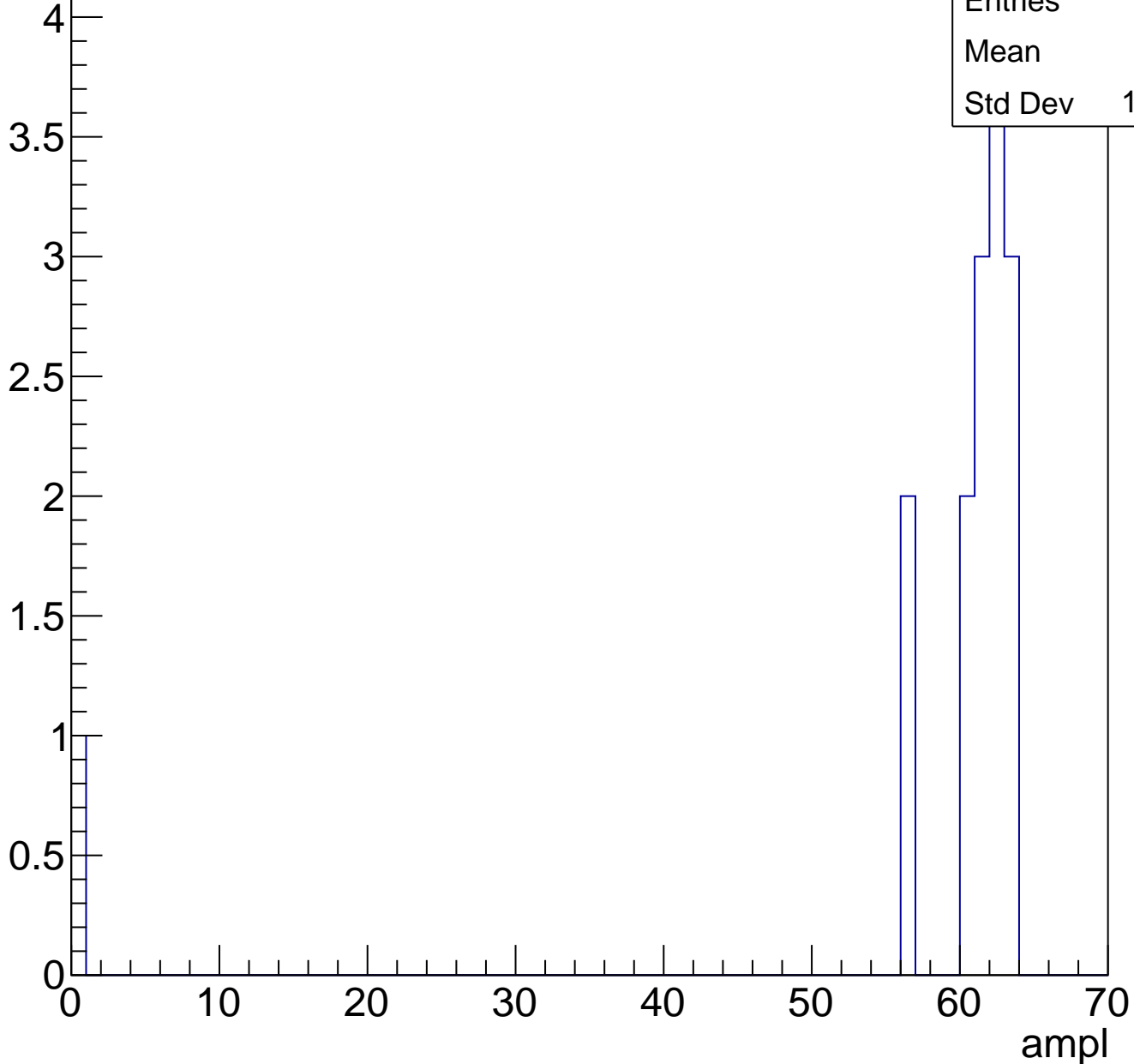
Entry



# B0L101S, U16-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch118, adc0

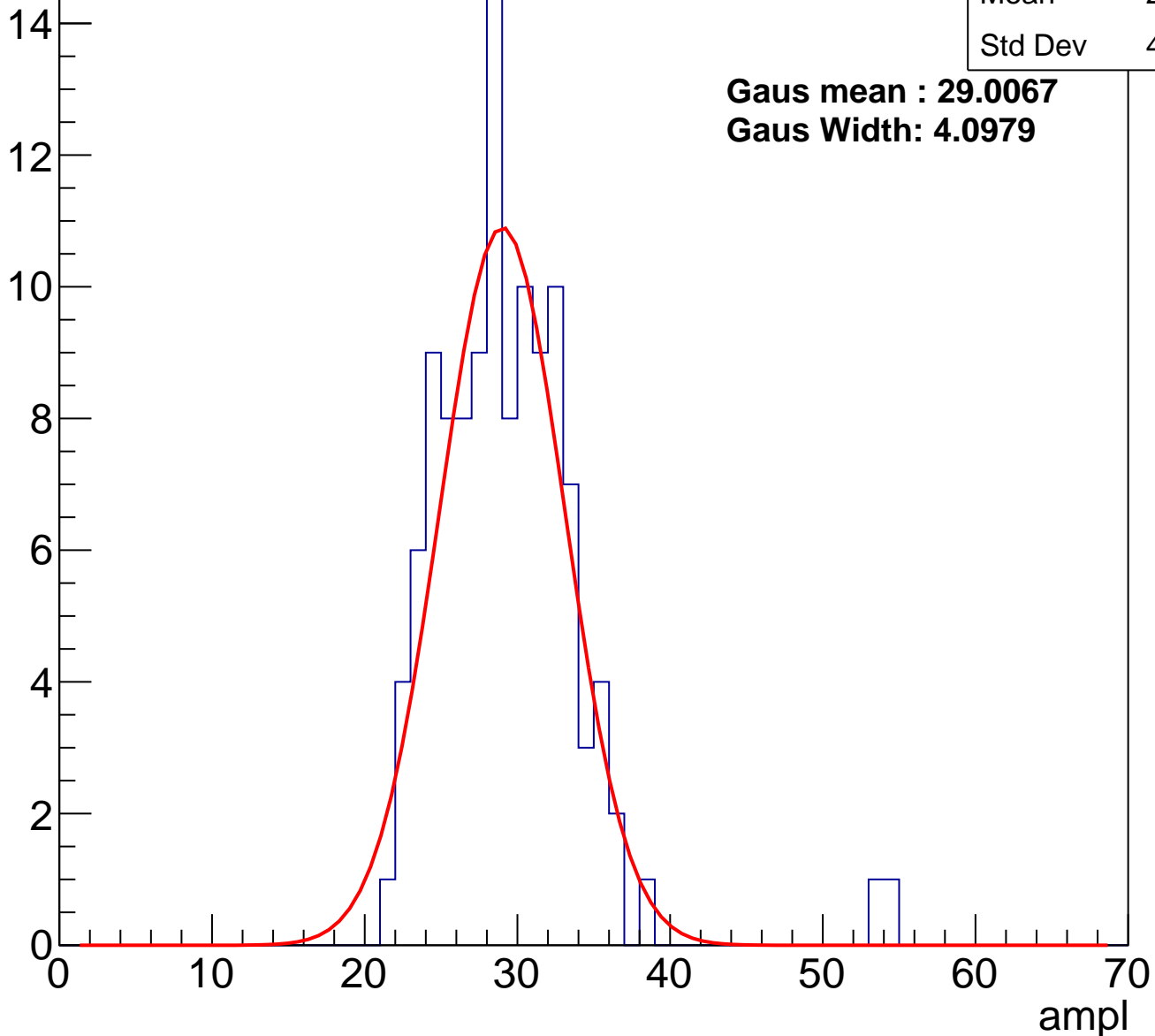
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 28.92 |
| Std Dev | 4.912 |

**Gaus mean : 29.0067**

**Gaus Width: 4.0979**

Entry



# B0L101S, U16-ch118, adc1

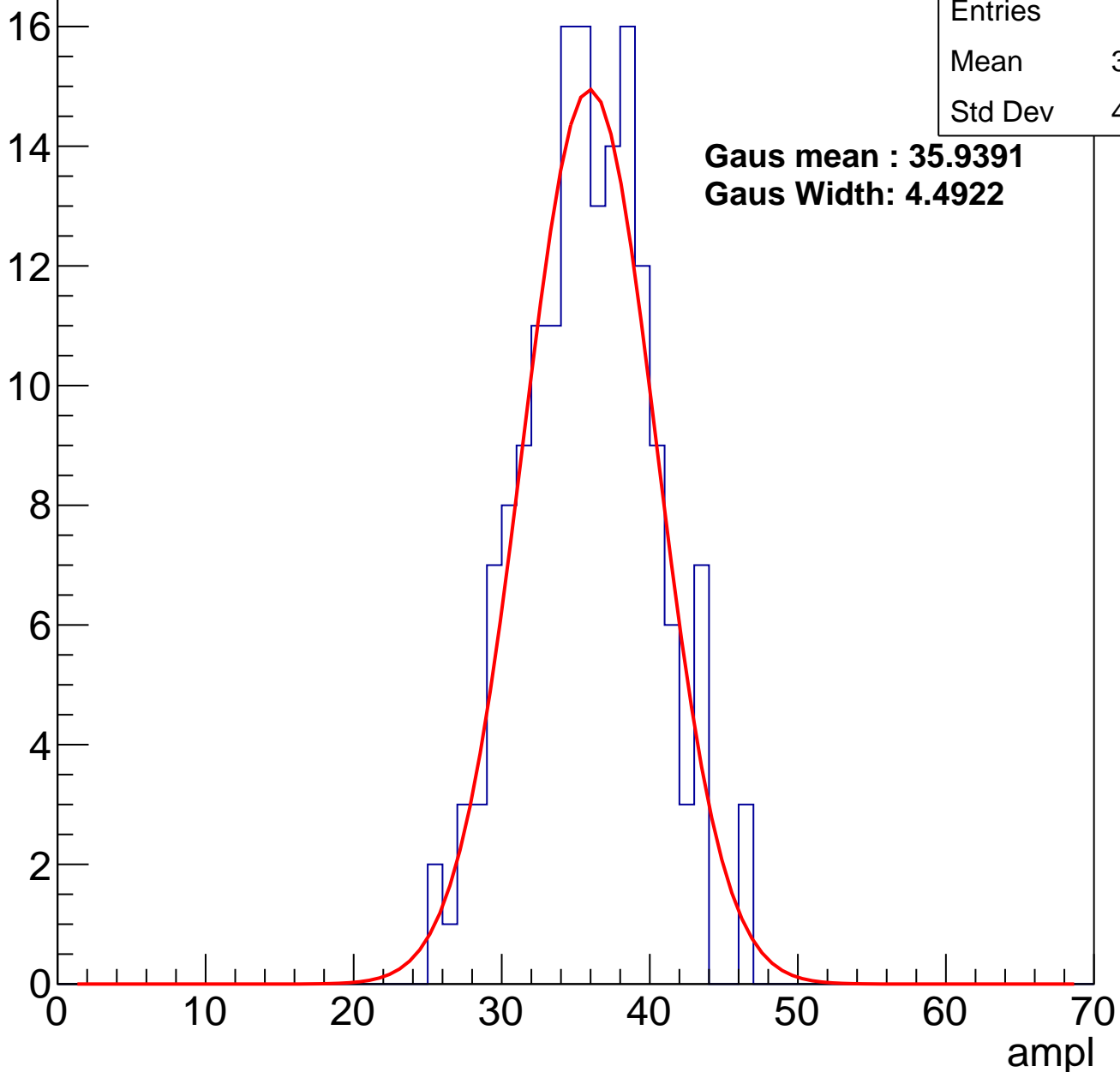
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 170   |
| Mean    | 35.38 |
| Std Dev | 4.307 |

**Gaus mean : 35.9391**

**Gaus Width: 4.4922**

Entry



# B0L101S, U16-ch118, adc2

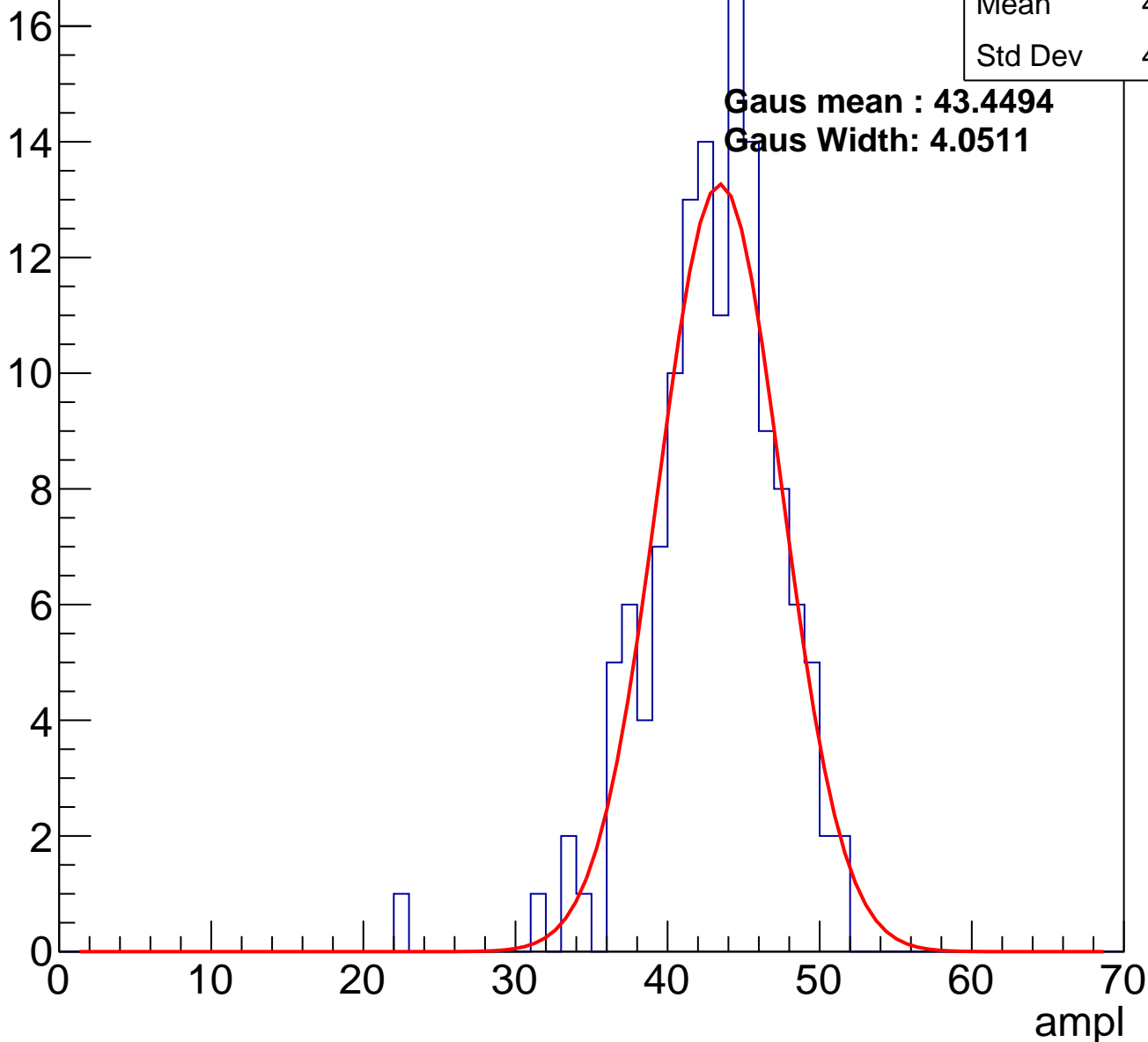
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 42.59 |
| Std Dev | 4.264 |

**Gaus mean : 43.4494**

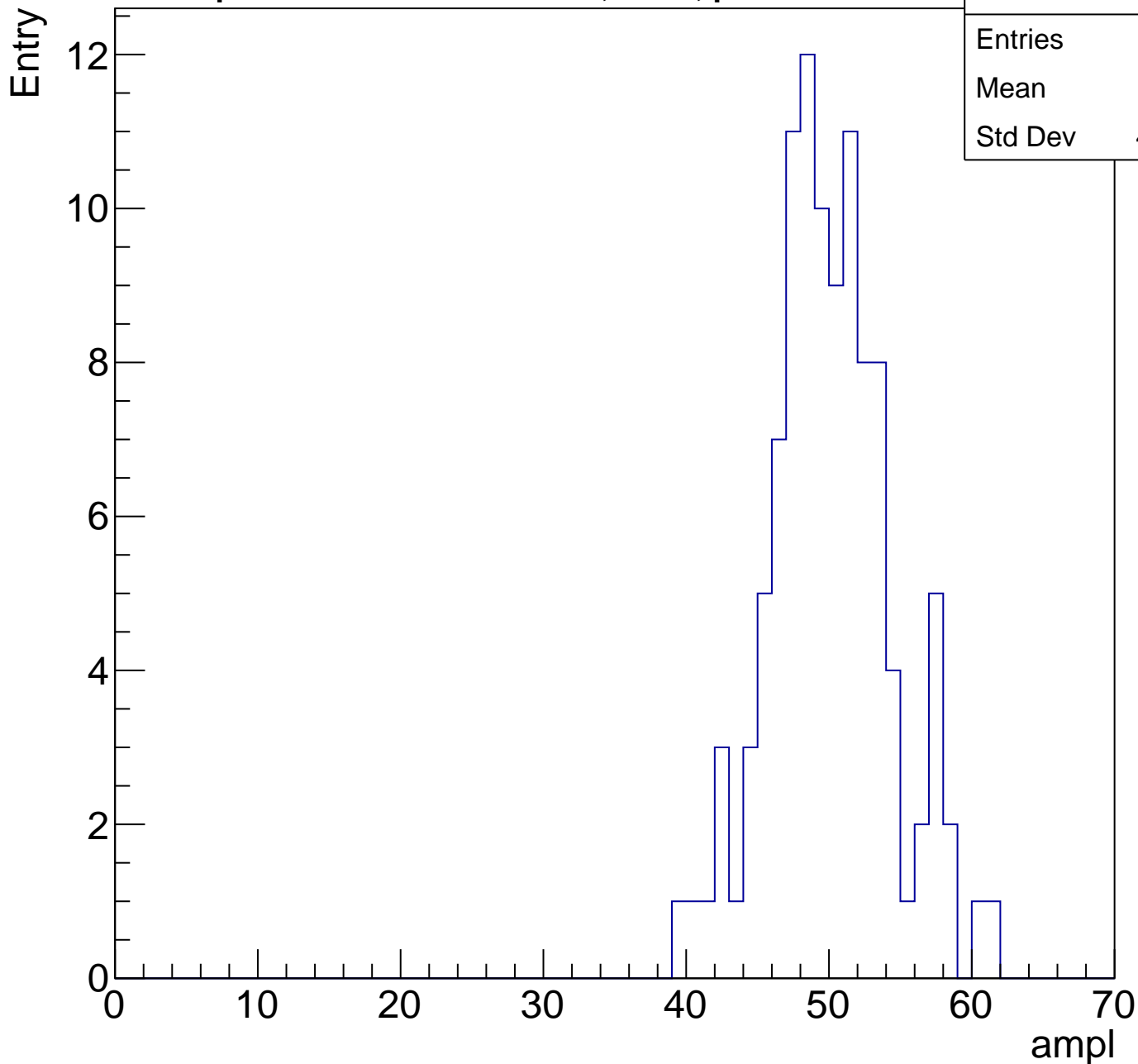
**Gaus Width: 4.0511**



# B0L101S, U16-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 49.6  |
| Std Dev | 4.211 |



# B0L101S, U16-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 124   |
| Mean    | 54.56 |
| Std Dev | 3.934 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

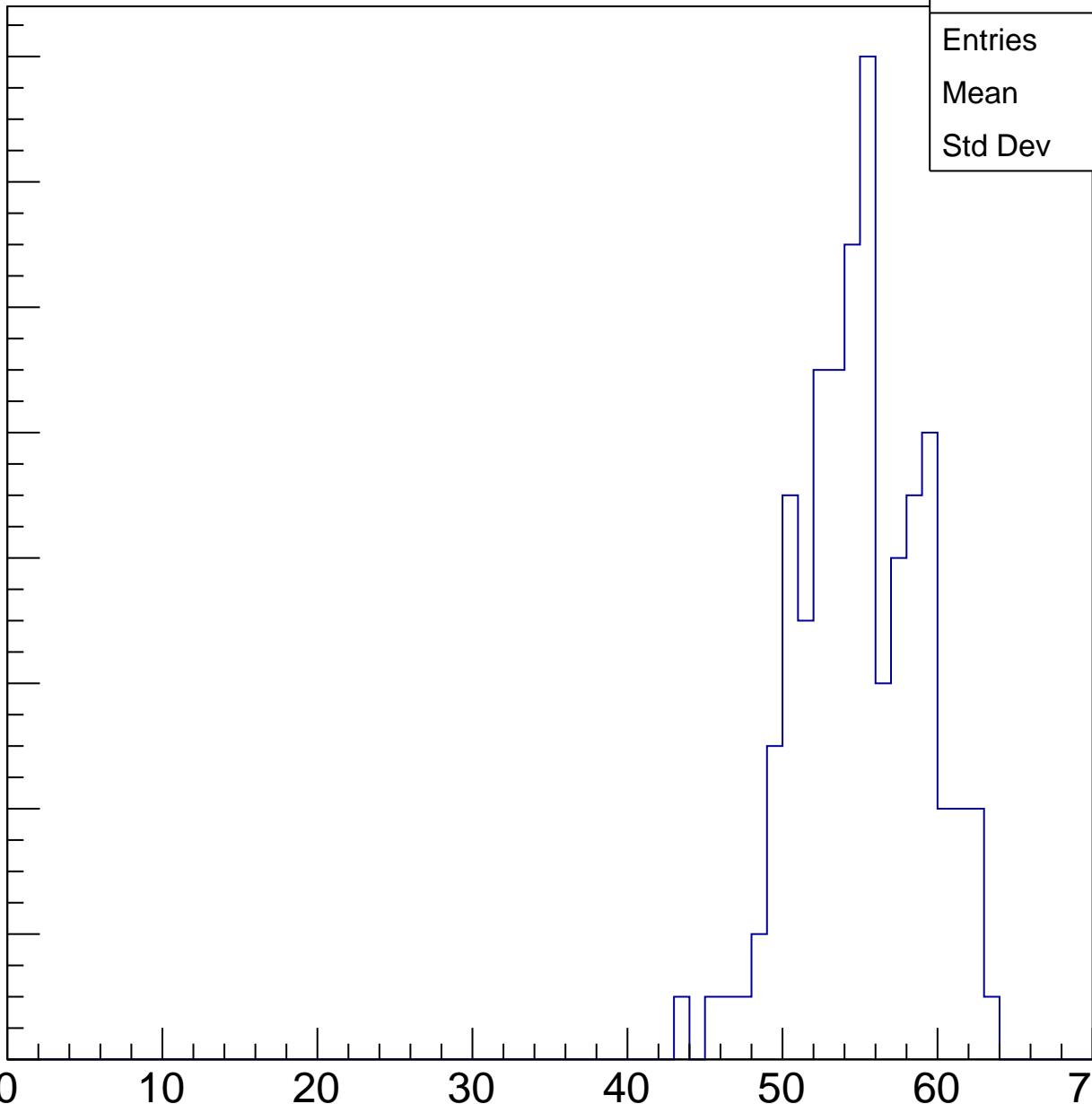
40

50

60

70

ampl



# B0L101S, U16-ch118, adc5

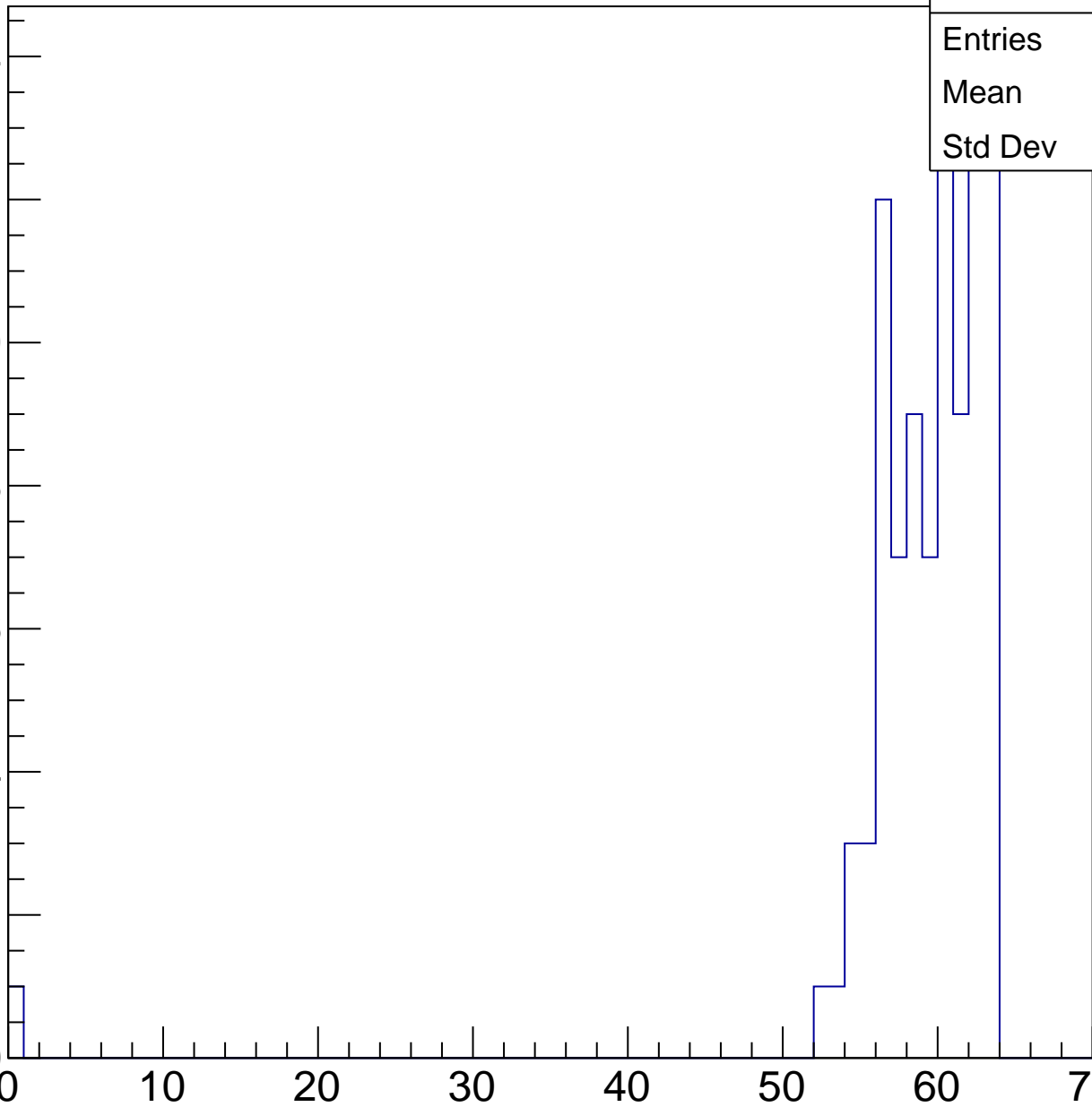
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 94    |
| Mean    | 58.66 |
| Std Dev | 6.69  |

ampl

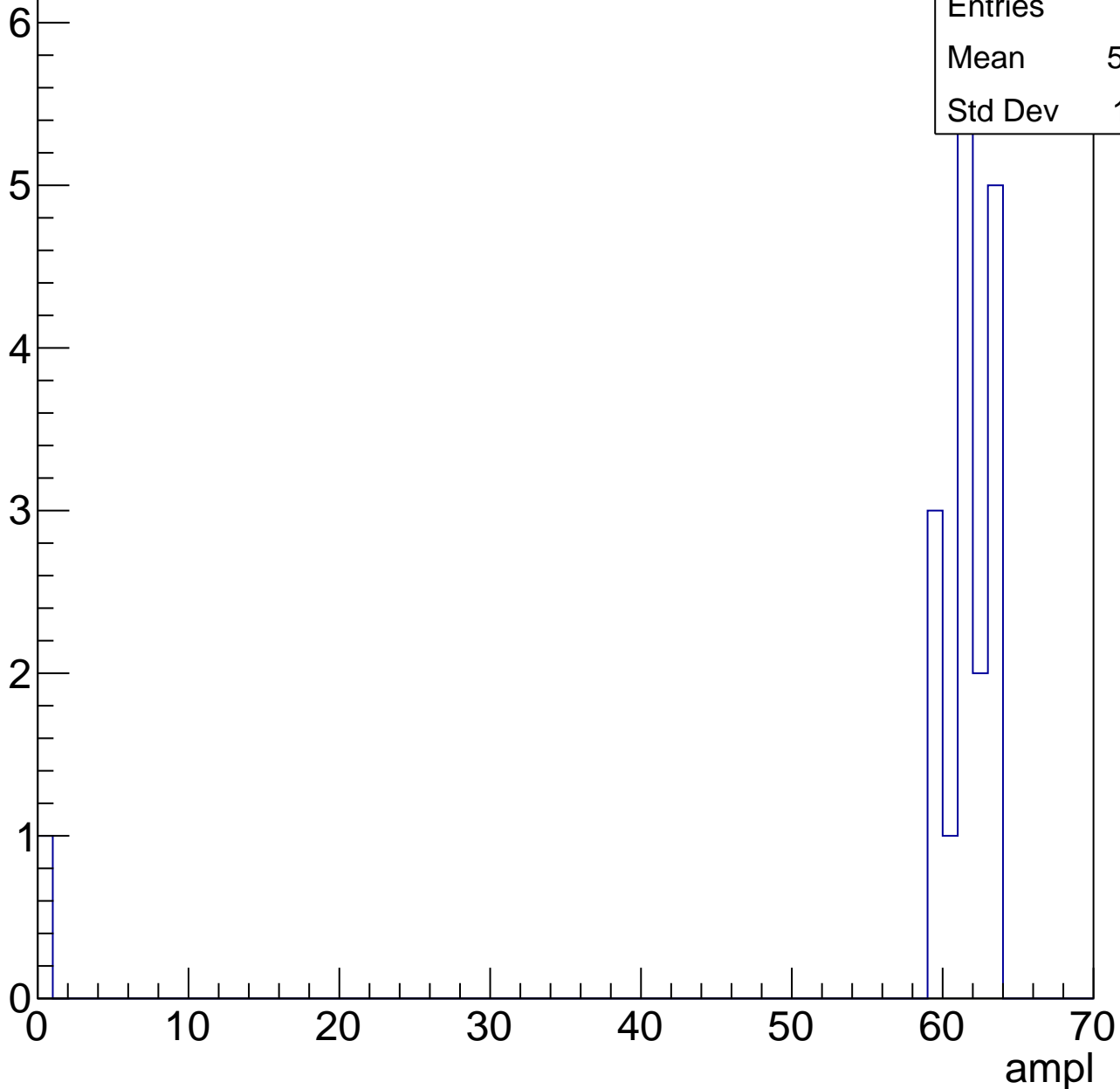


# B0L101S, U16-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 18    |
| Mean    | 57.89 |
| Std Dev | 14.11 |





# B0L101S, U16-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch119, adc0

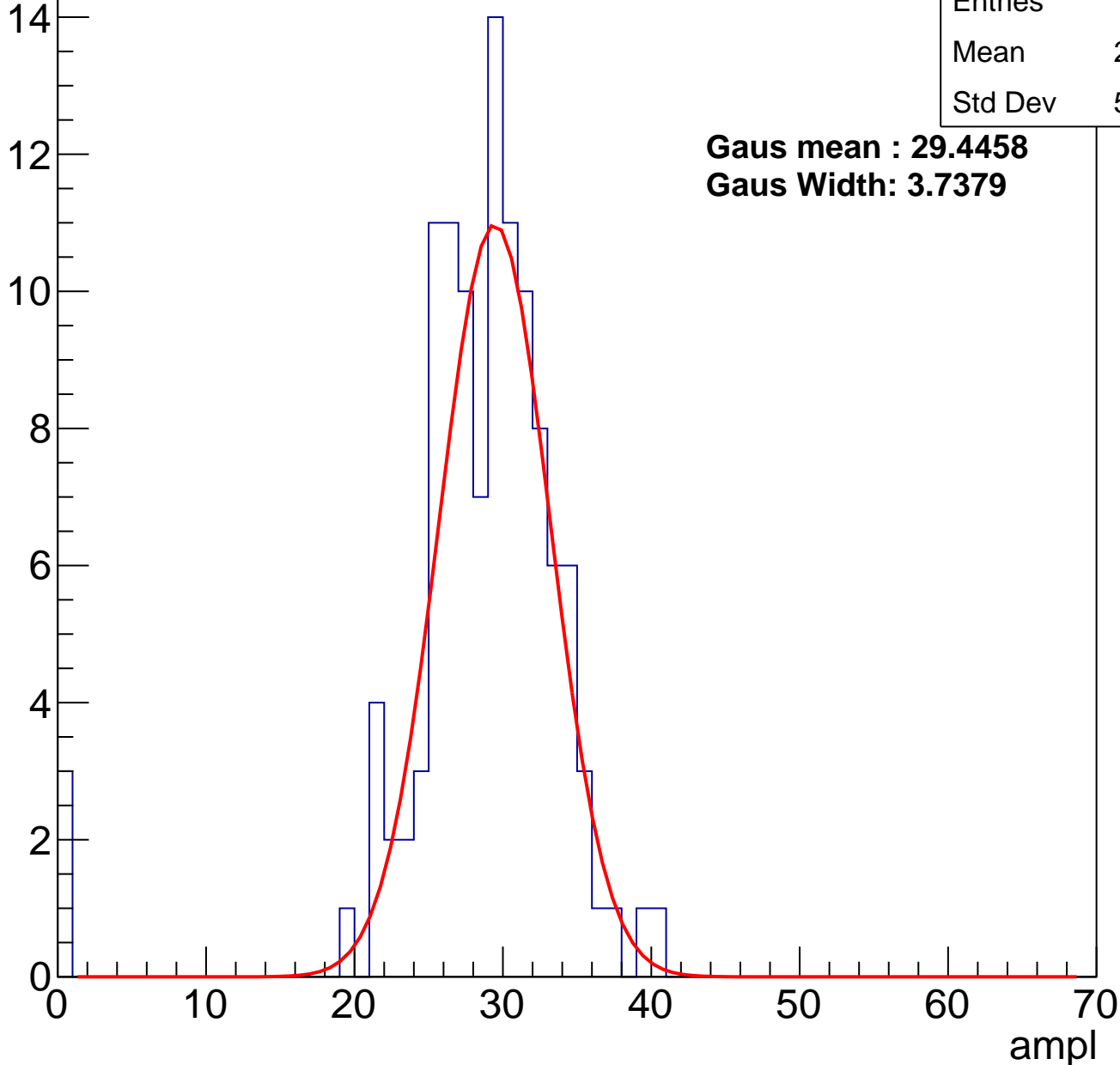
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 28.04 |
| Std Dev | 5.982 |

**Gaus mean : 29.4458**

**Gaus Width: 3.7379**

Entry



# B0L101S, U16-ch119, adc1

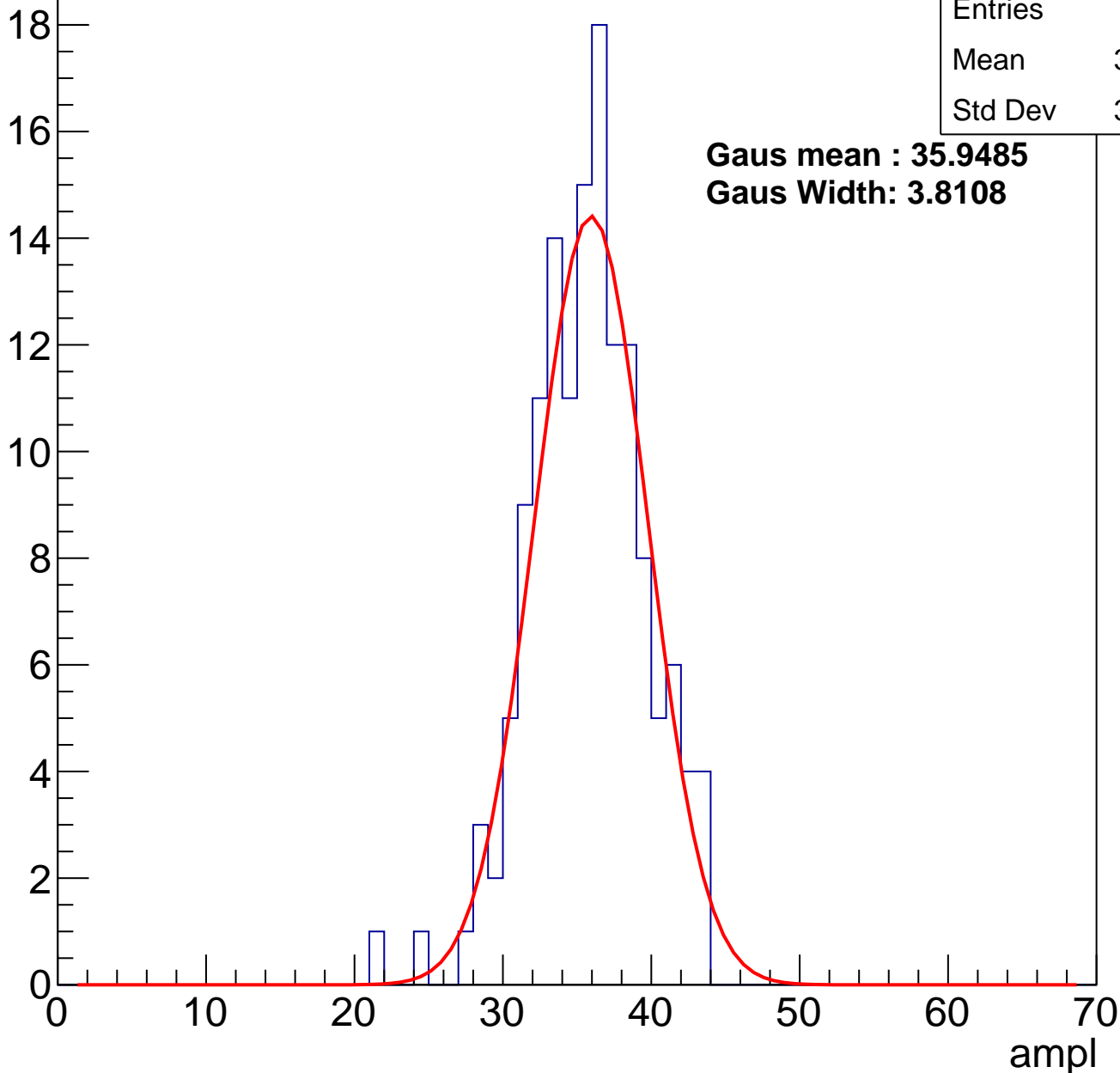
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 142   |
| Mean    | 35.23 |
| Std Dev | 3.866 |

**Gaus mean : 35.9485**

**Gaus Width: 3.8108**



# B0L101S, U16-ch119, adc2

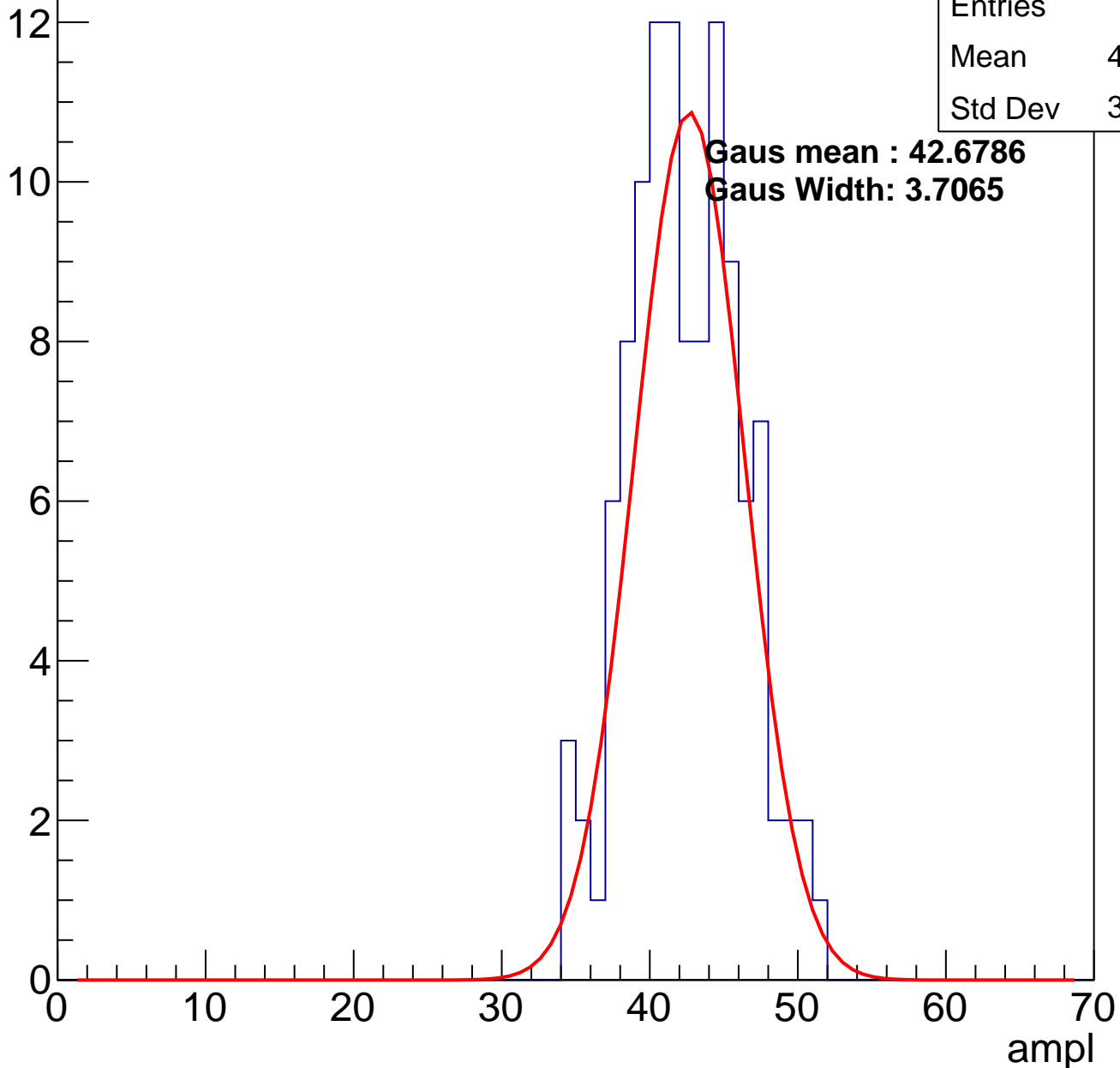
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 111   |
| Mean    | 41.97 |
| Std Dev | 3.726 |

**Gaus mean : 42.6786**

**Gaus Width: 3.7065**

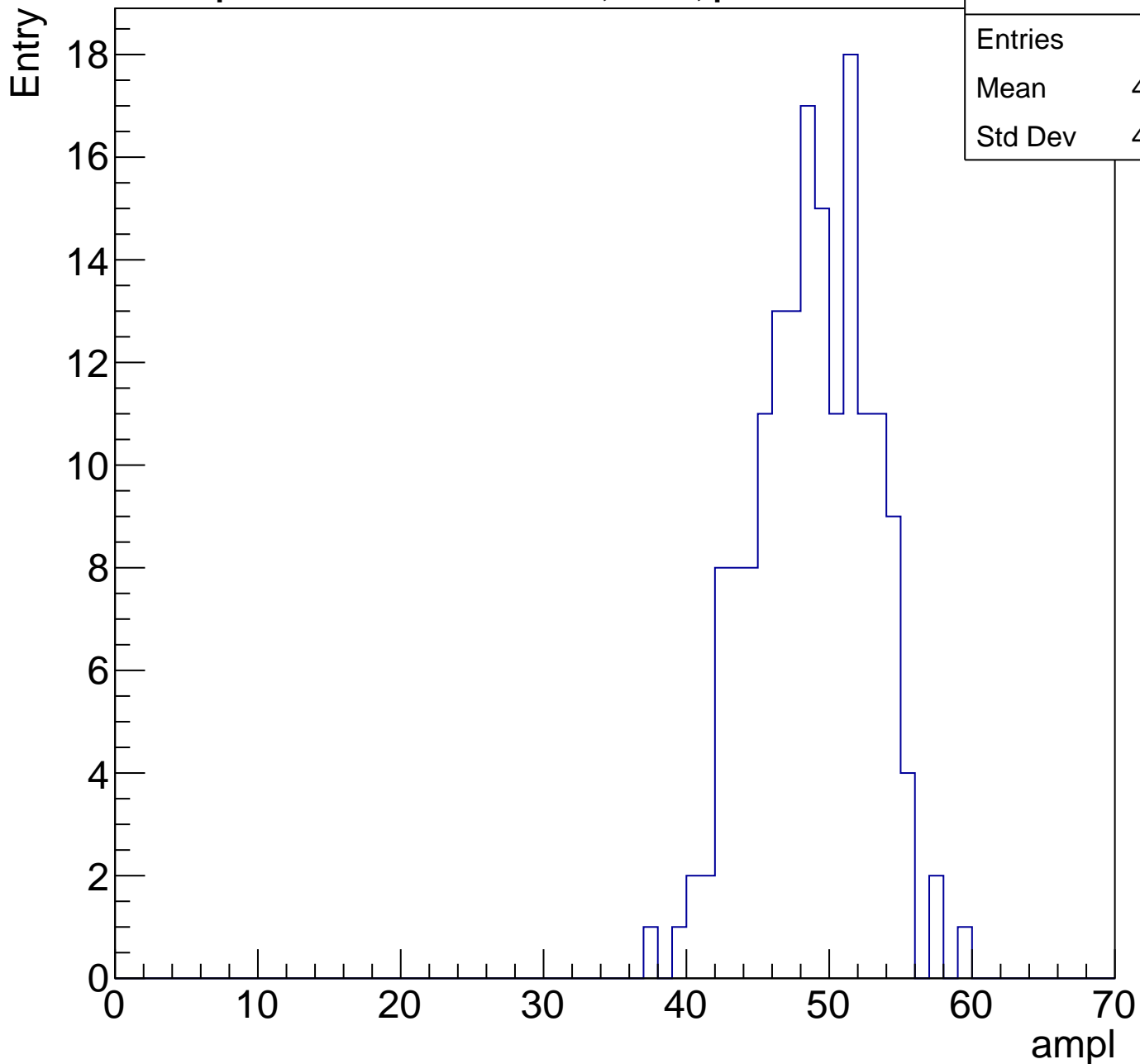
Entry



# B0L101S, U16-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

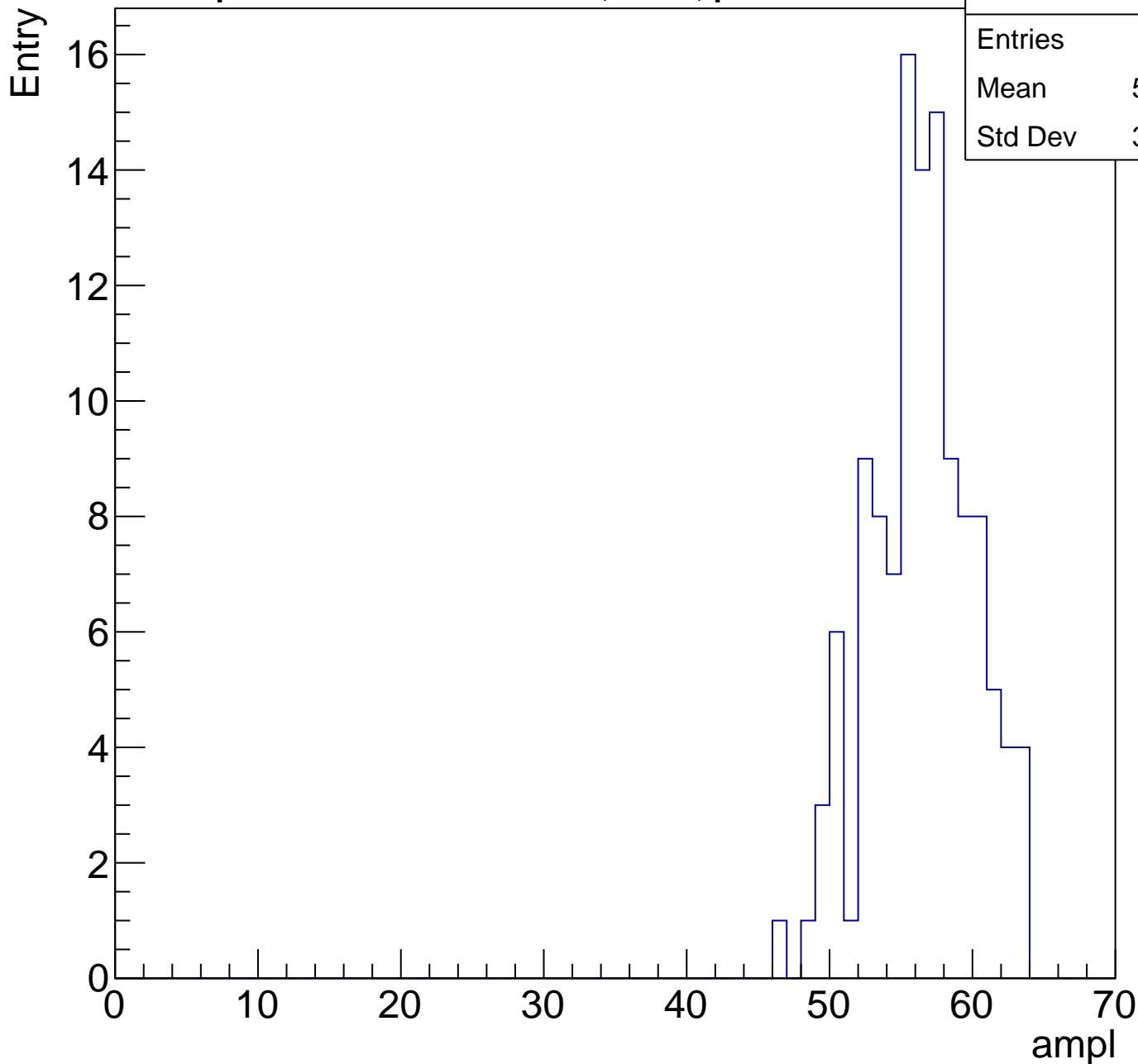
|         |       |
|---------|-------|
| Entries | 166   |
| Mean    | 48.36 |
| Std Dev | 4.005 |



# B0L101S, U16-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 55.97 |
| Std Dev | 3.583 |



# B0L101S, U16-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

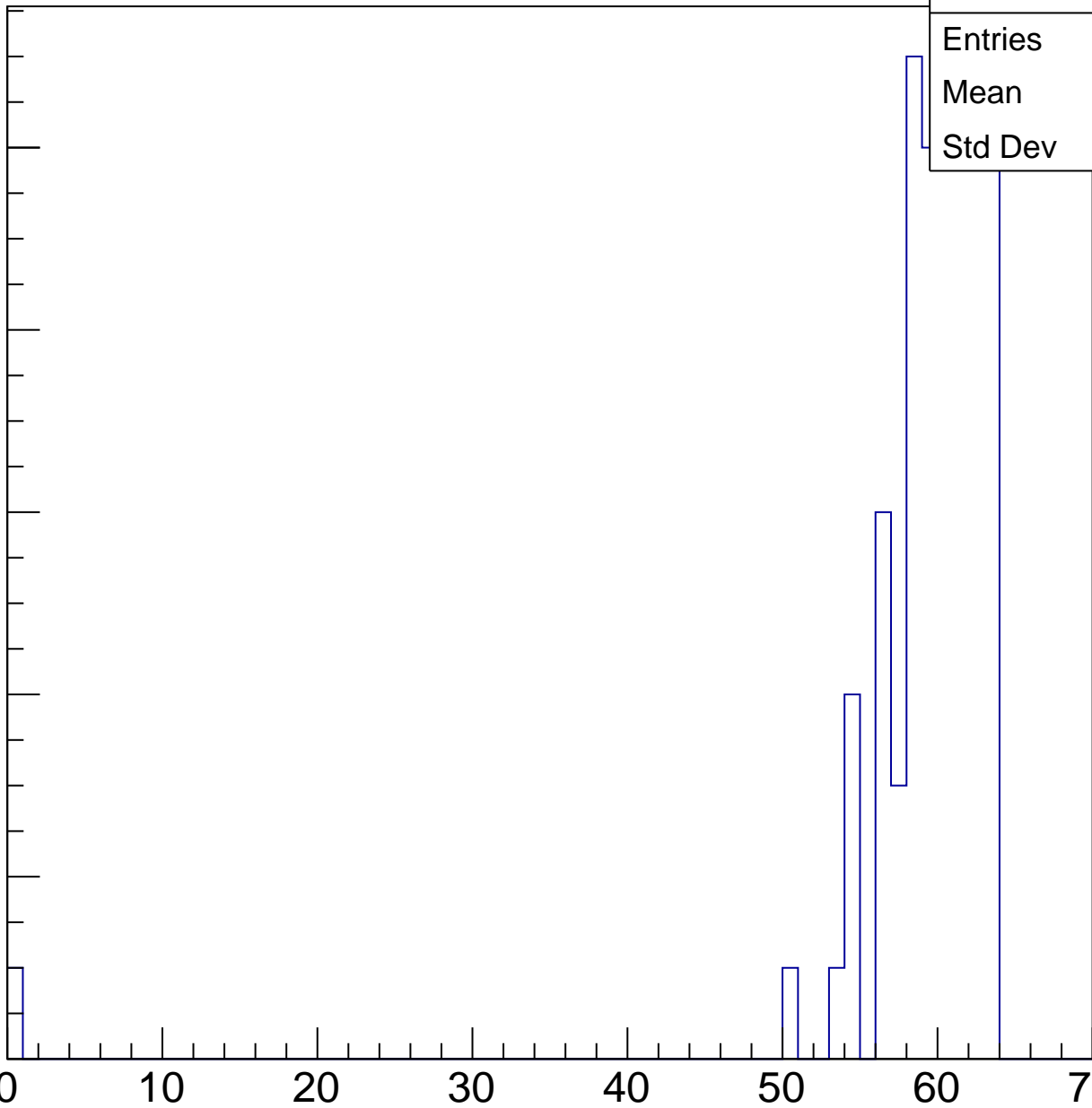
40

50

60

ampl

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 58.7  |
| Std Dev | 7.187 |

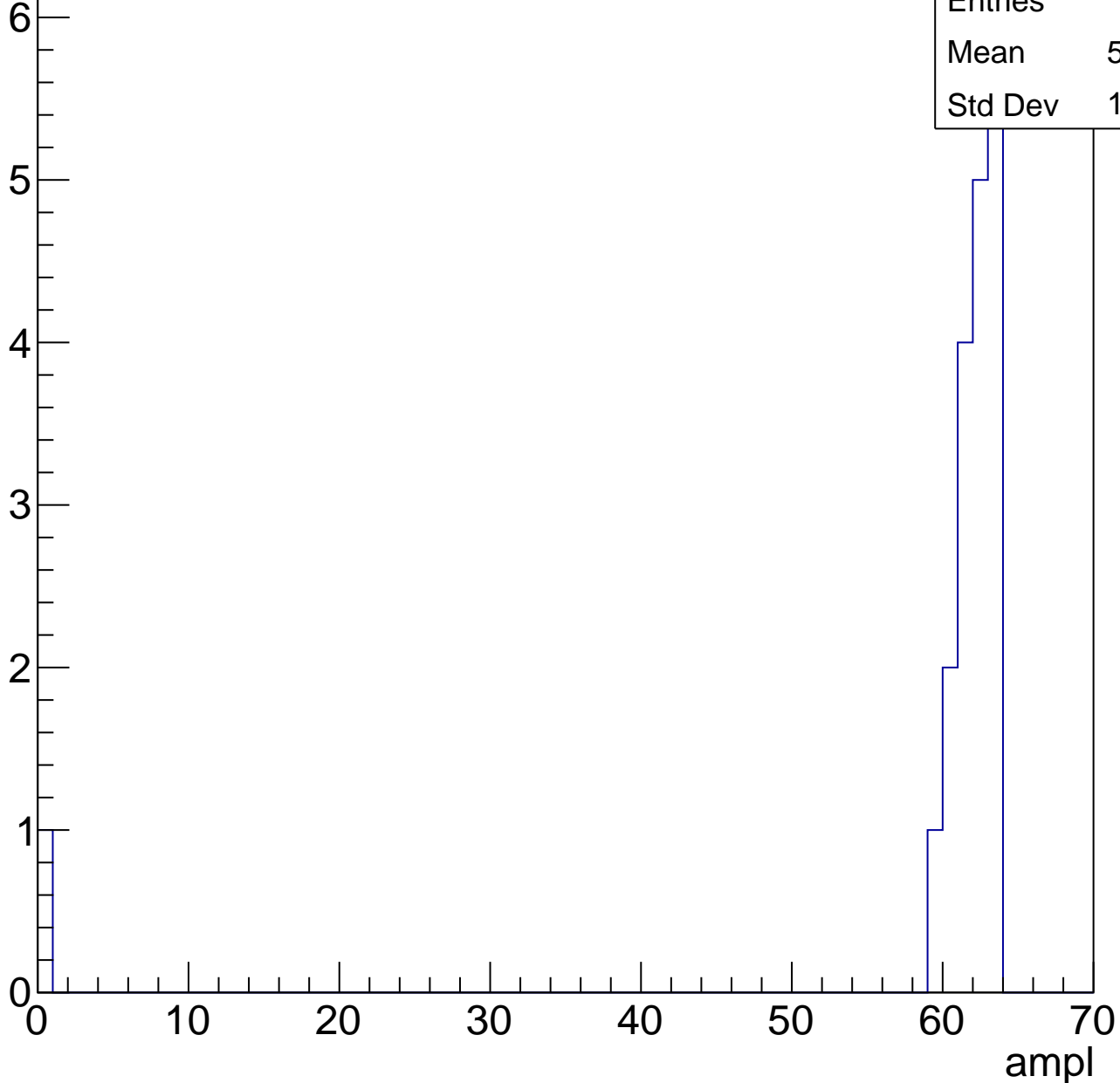


# B0L101S, U16-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 19    |
| Mean    | 58.47 |
| Std Dev | 13.83 |





# B0L101S, U16-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

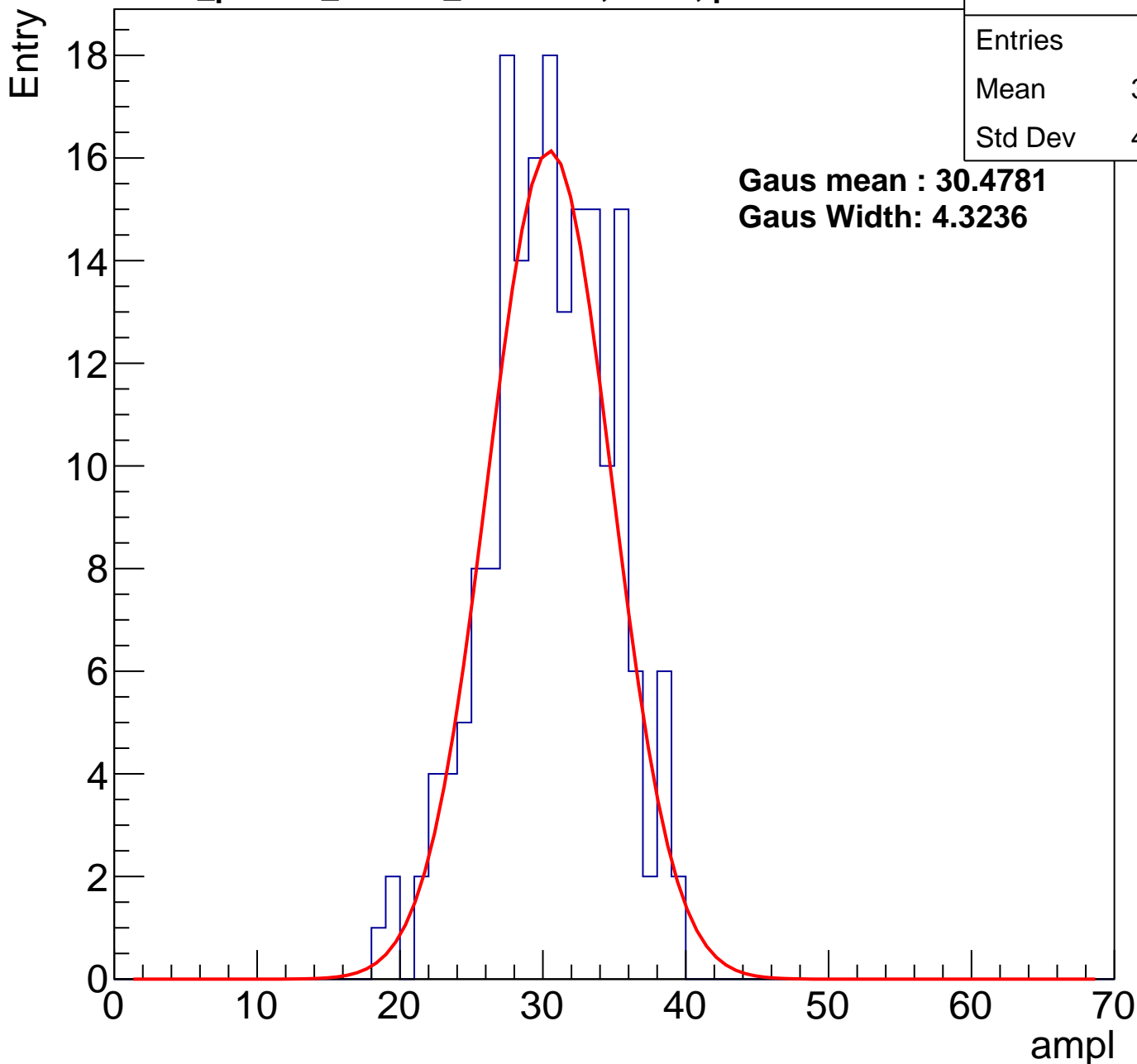
# B0L101S, U16-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 184   |
| Mean    | 30.04 |
| Std Dev | 4.264 |

**Gaus mean : 30.4781**

**Gaus Width: 4.3236**



# B0L101S, U16-ch120, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 91    |
| Mean    | 36.76 |
| Std Dev | 3.937 |

**Gaus mean : 37.4804**

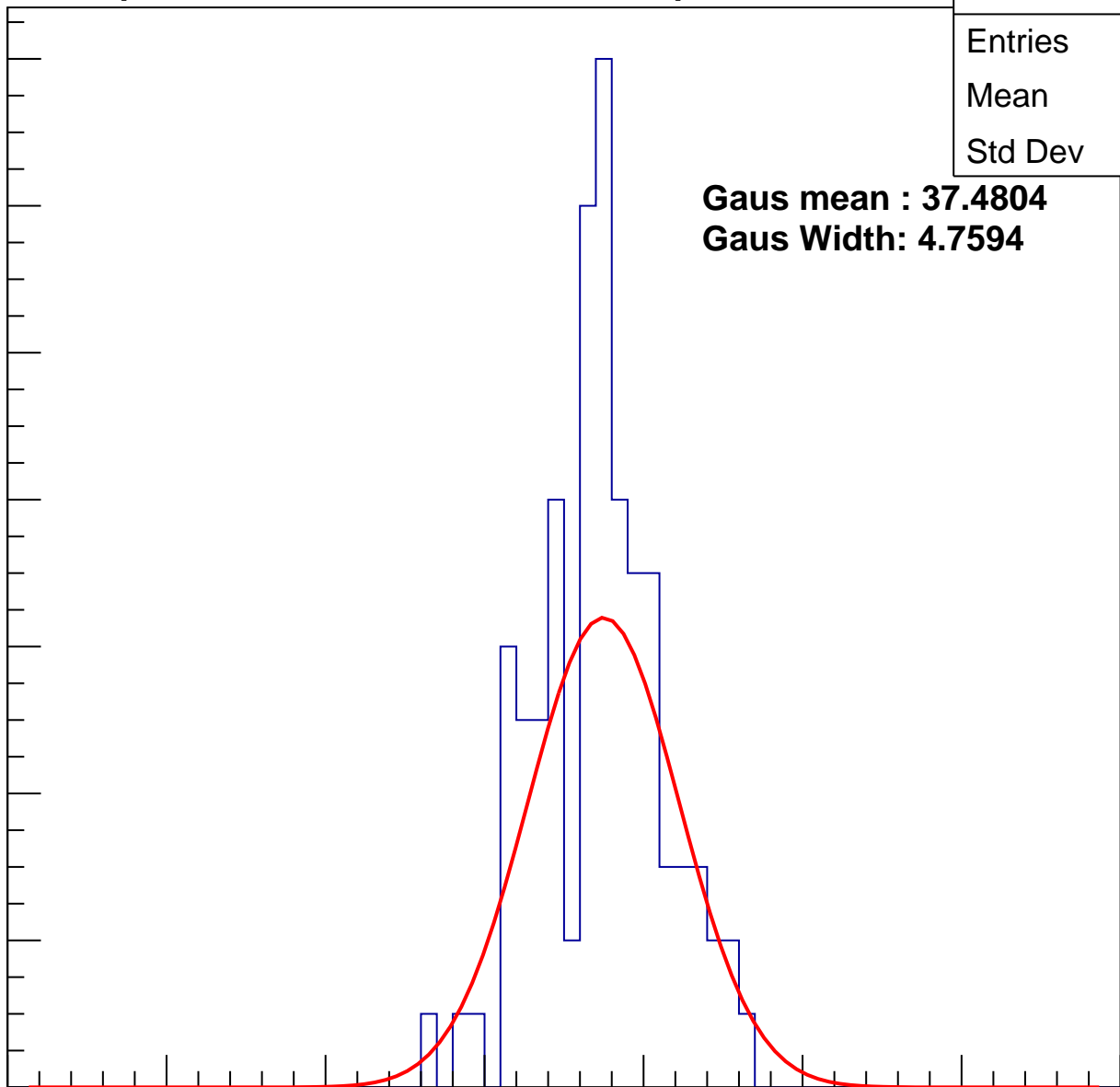
**Gaus Width: 4.7594**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 140   |
| Mean    | 42.59 |
| Std Dev | 3.693 |

**Gaus mean : 43.1832**

**Gaus Width: 3.4199**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch120, adc3

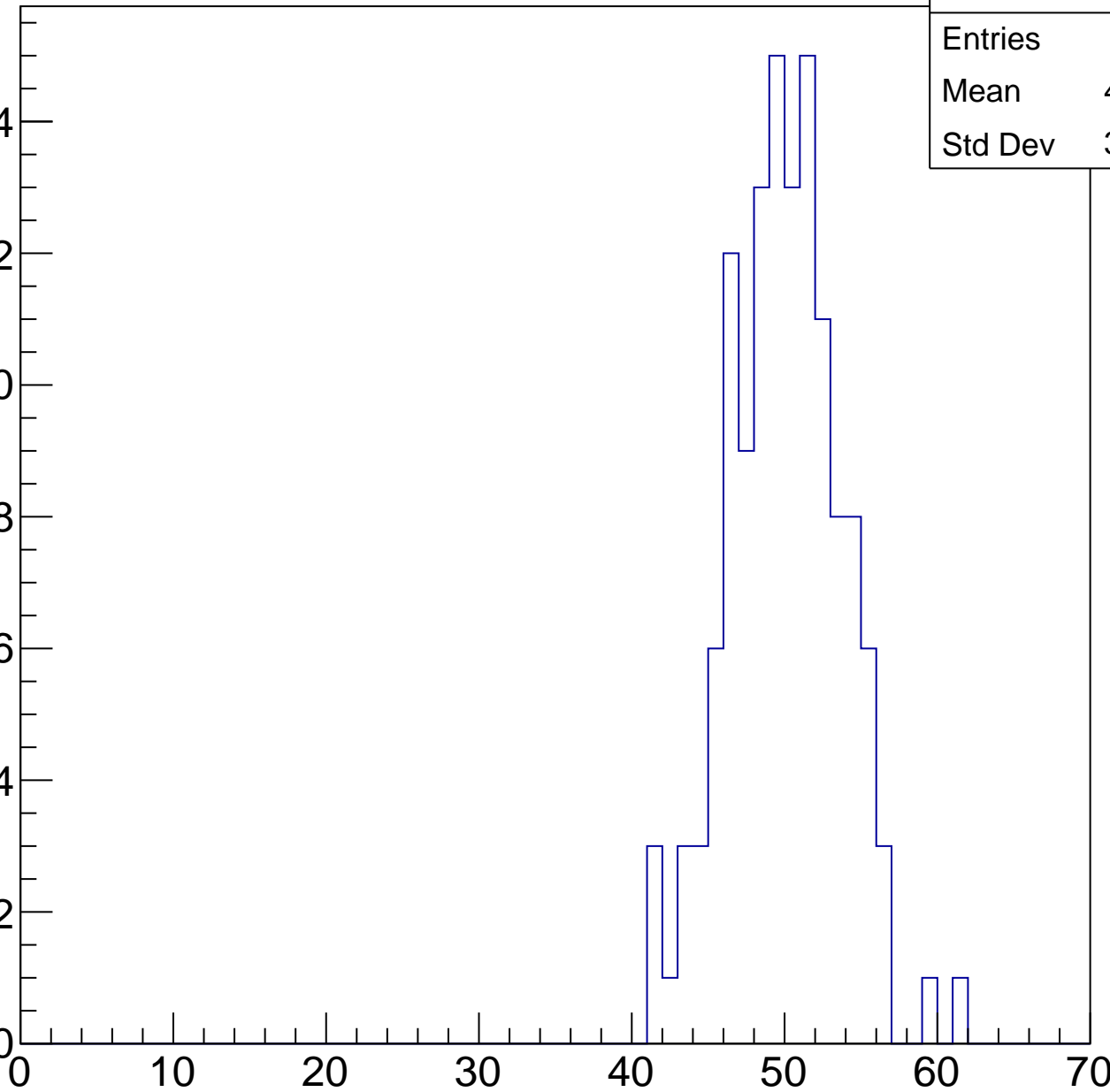
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 131   |
| Mean    | 49.55 |
| Std Dev | 3.649 |

ampl



# B0L101S, U16-ch120, adc4

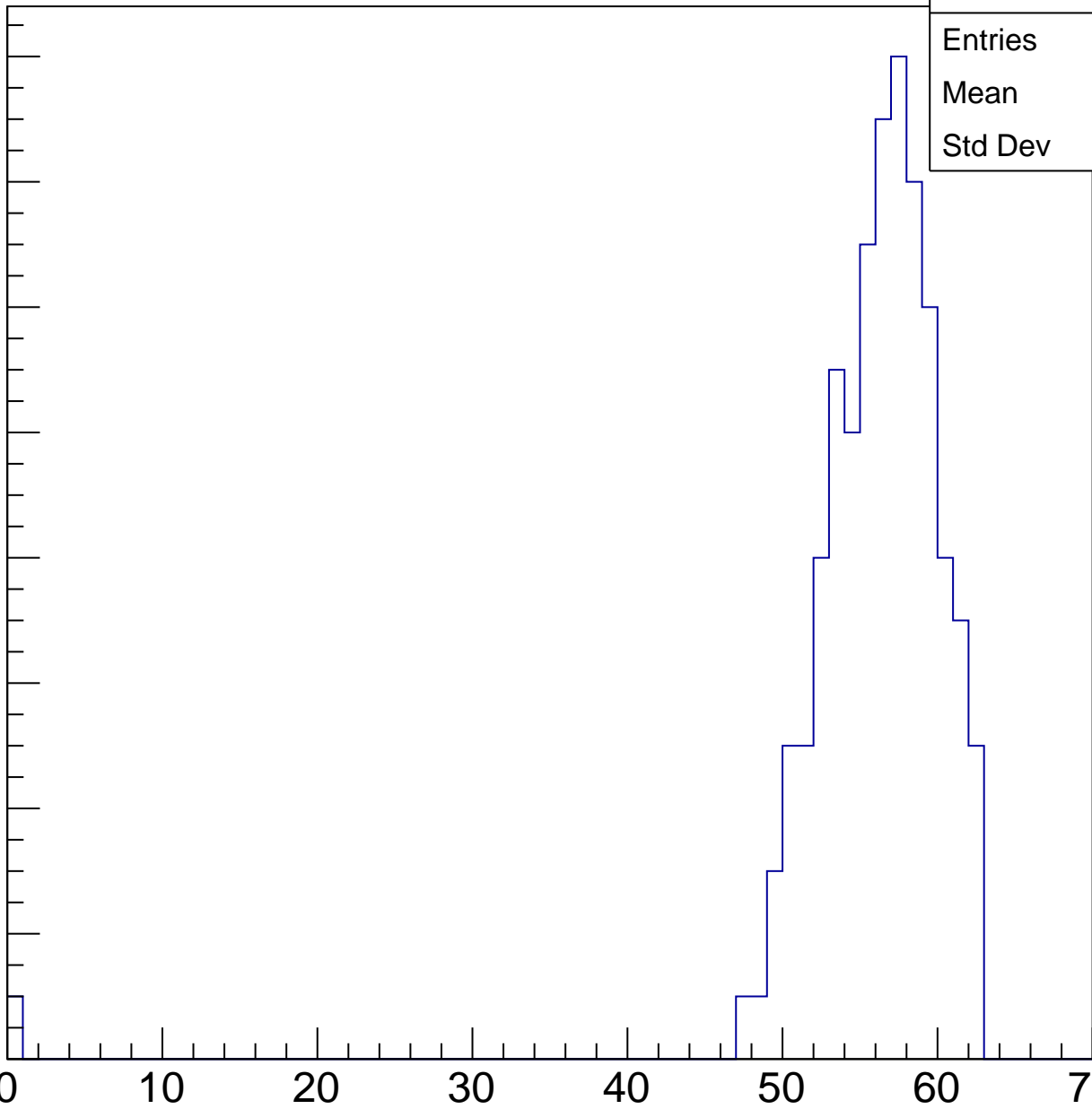
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 135   |
| Mean    | 55.48 |
| Std Dev | 5.862 |

ampl



# B0L101S, U16-ch120, adc5

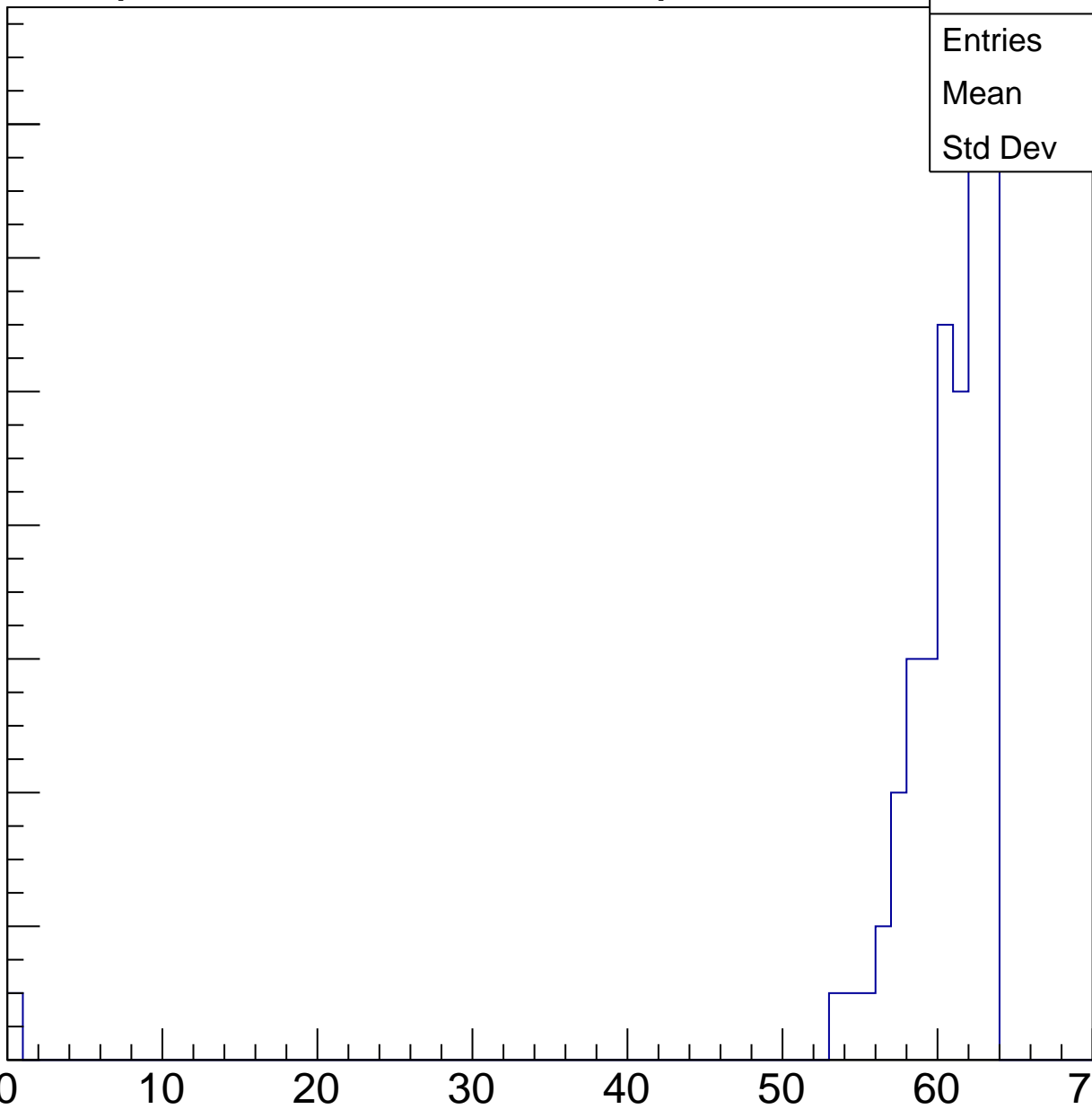
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 59.53 |
| Std Dev | 7.442 |

ampl

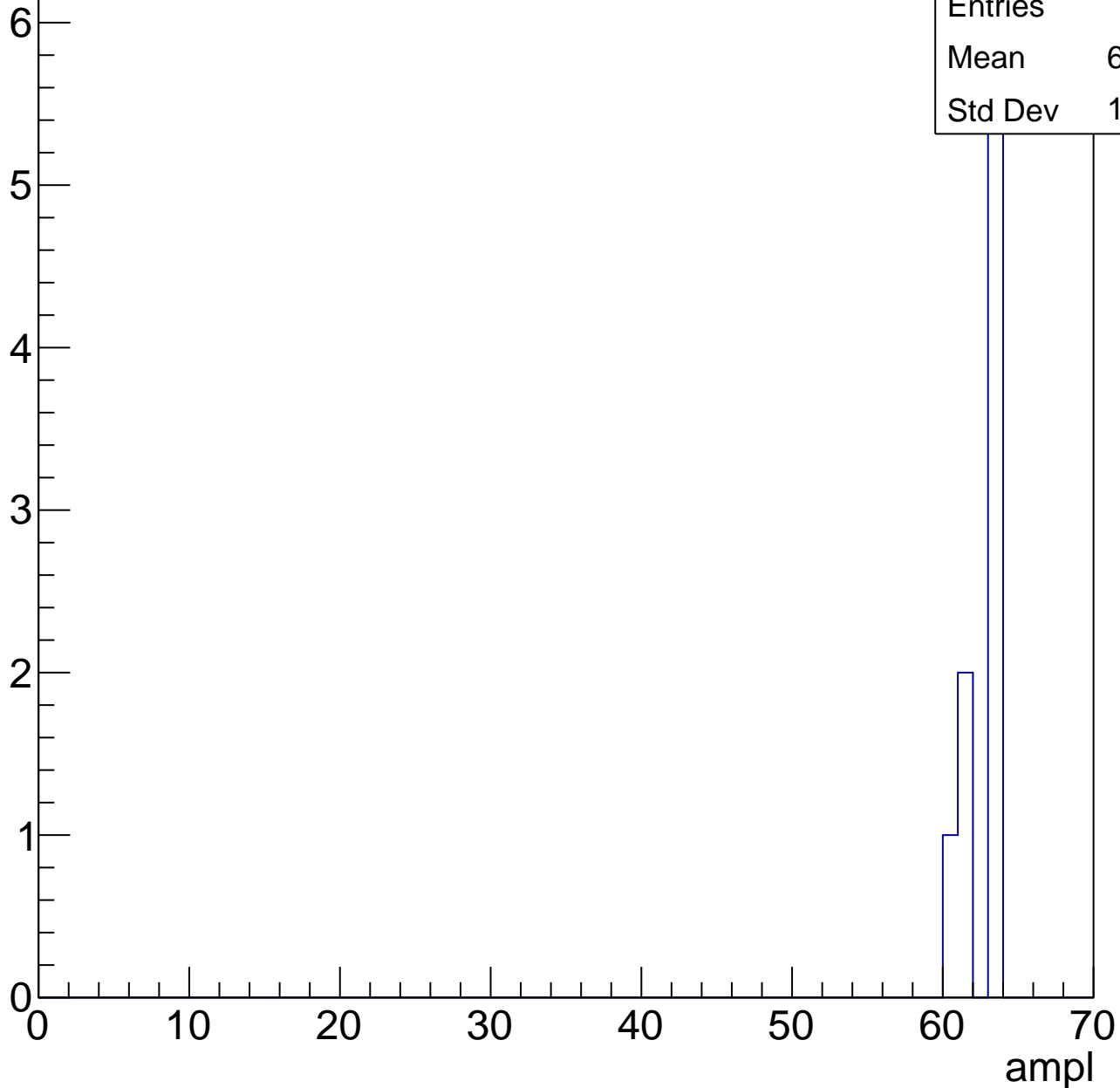


# B0L101S, U16-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 9     |
| Mean    | 62.22 |
| Std Dev | 1.133 |

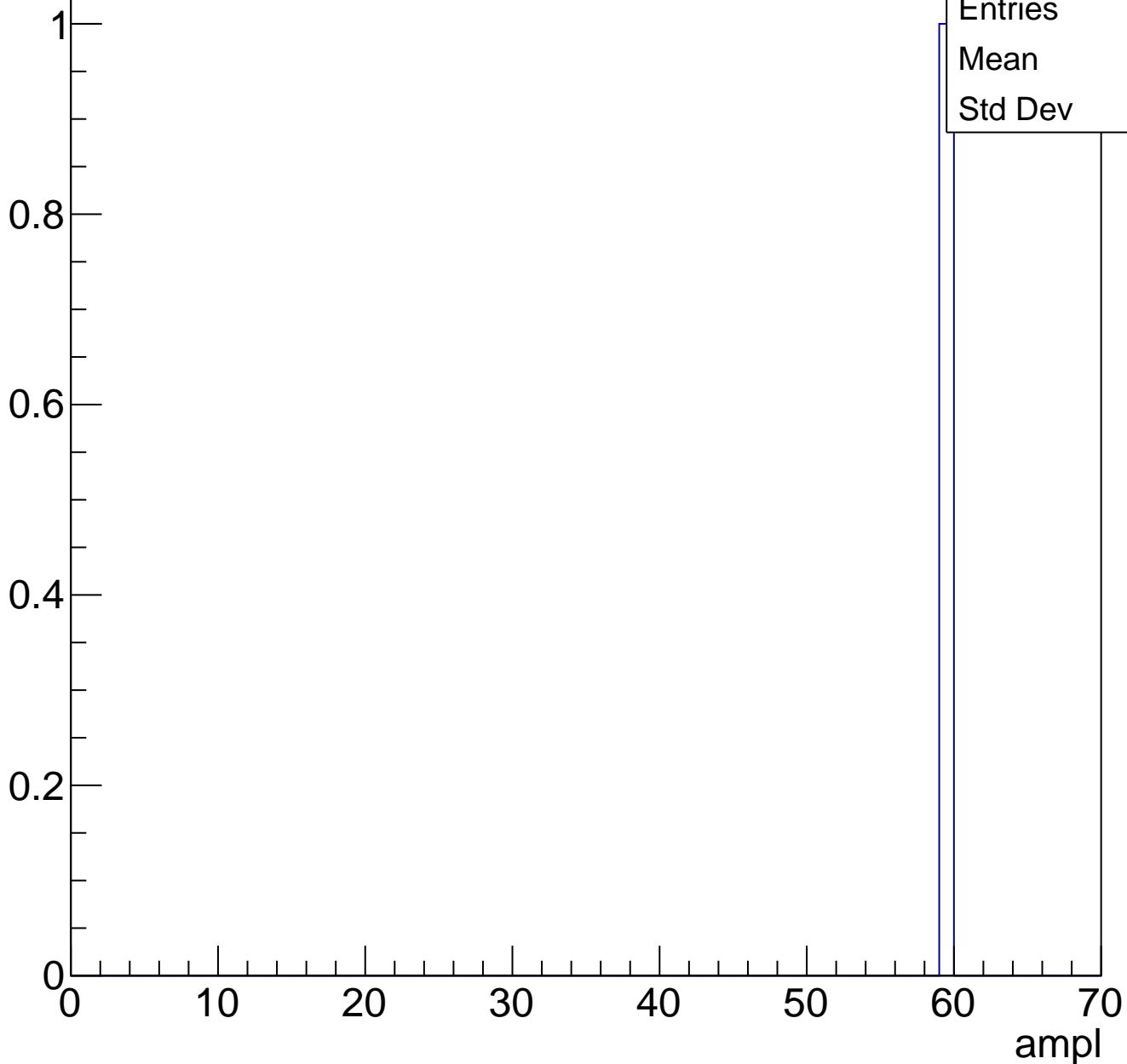




# B0L101S, U16-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 116   |
| Mean    | 30.51 |
| Std Dev | 3.701 |

**Gaus mean : 31.2897**

**Gaus Width: 3.6314**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U16-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 176   |
| Mean    | 37.81 |
| Std Dev | 4.062 |

**Gaus mean : 38.3584**

**Gaus Width: 4.1819**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

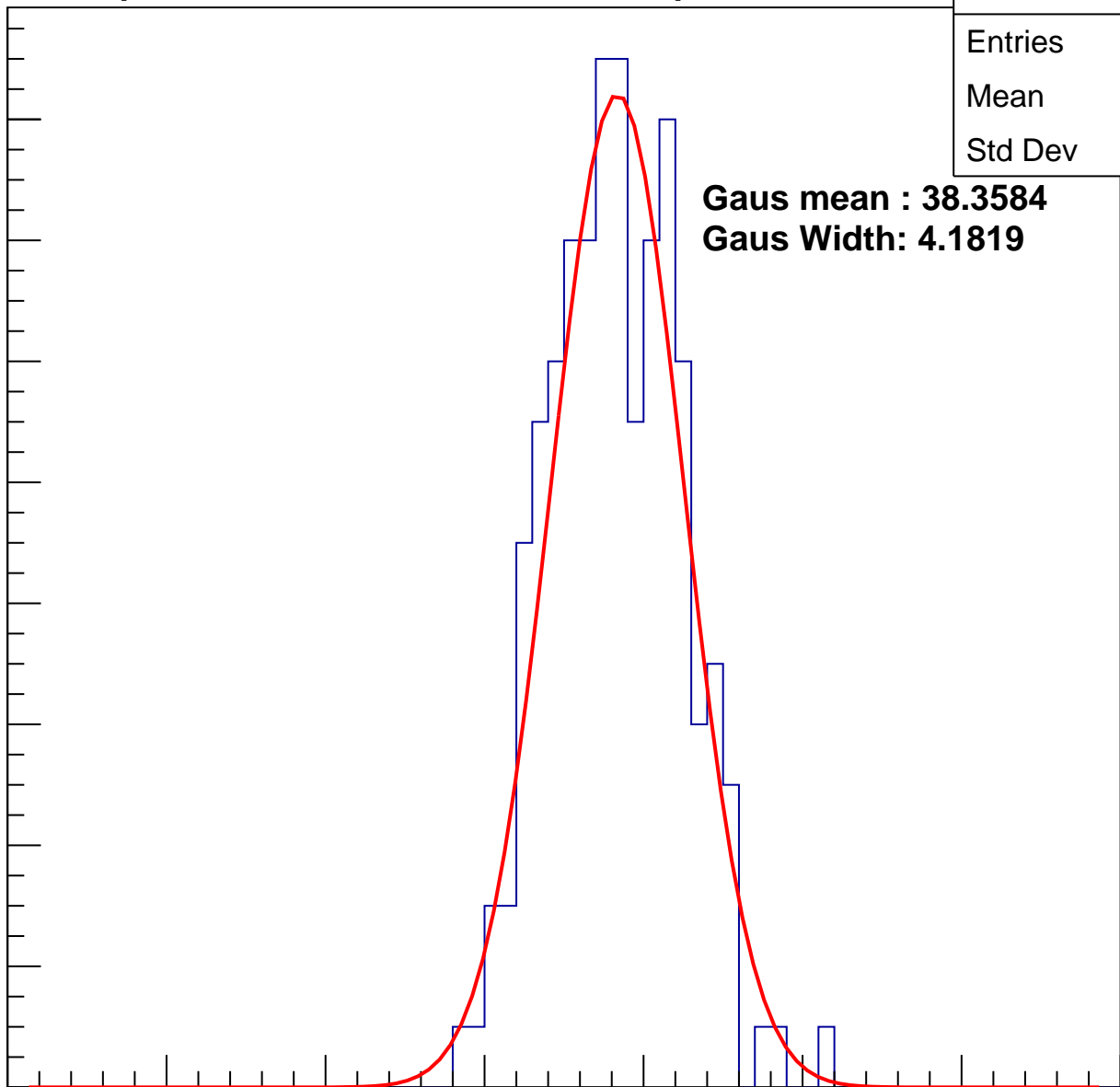
40

50

60

70

ampl



# B0L101S, U16-ch121, adc2

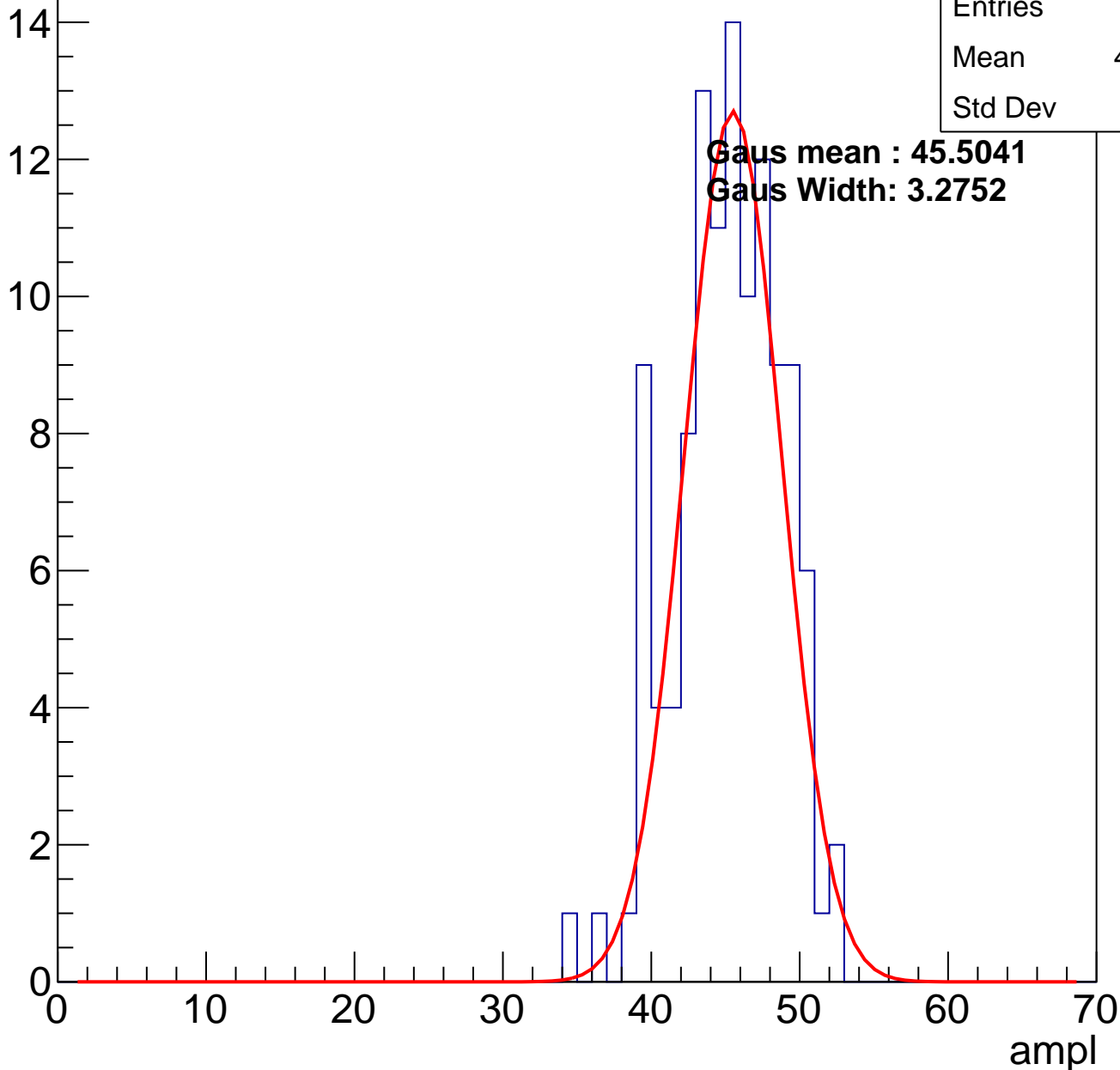
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 44.73 |
| Std Dev | 3.53  |

**Gaus mean : 45.5041**

**Gaus Width: 3.2752**

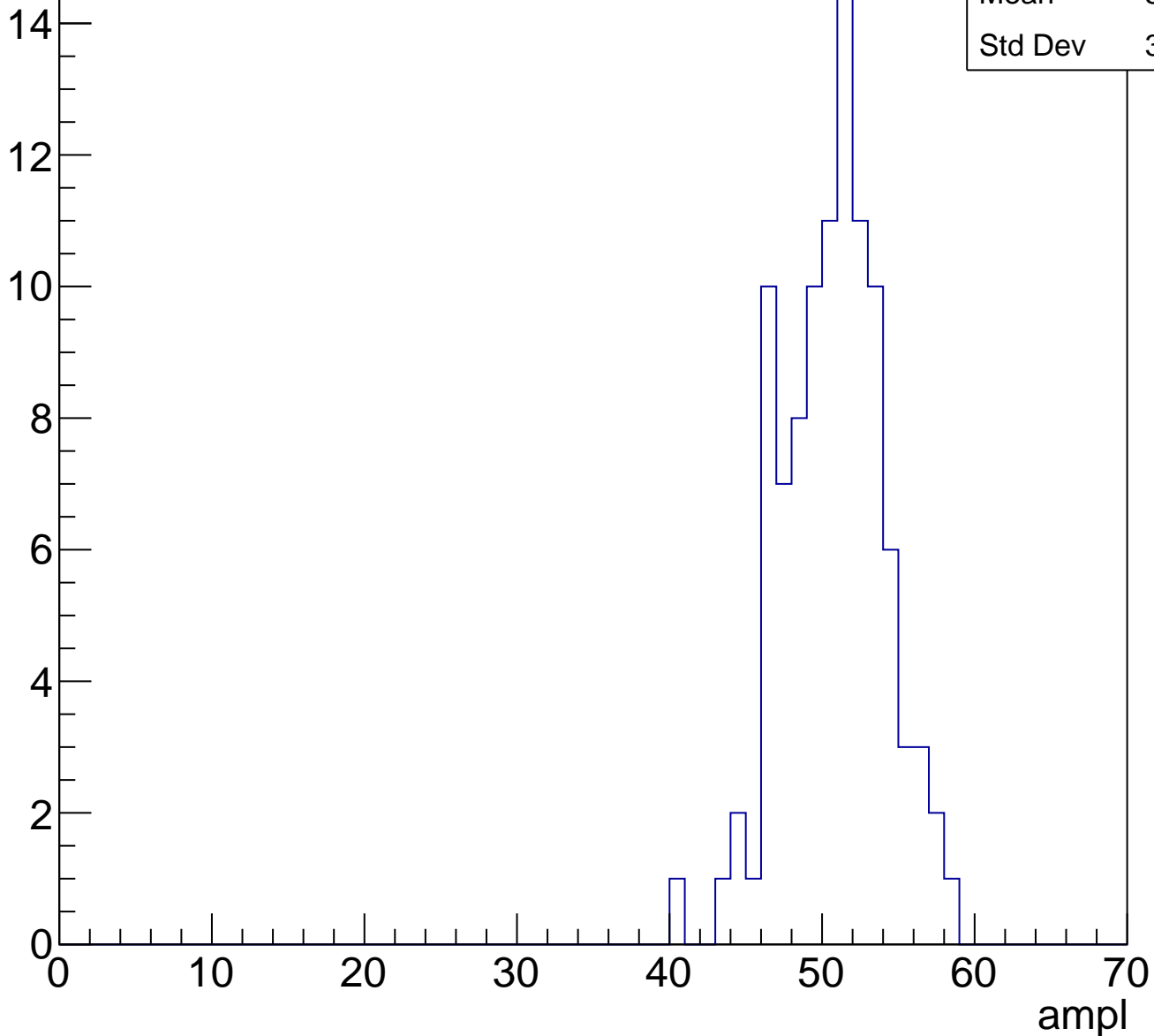


# B0L101S, U16-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 102   |
| Mean    | 50.25 |
| Std Dev | 3.276 |

Entry



# B0L101S, U16-ch121, adc4

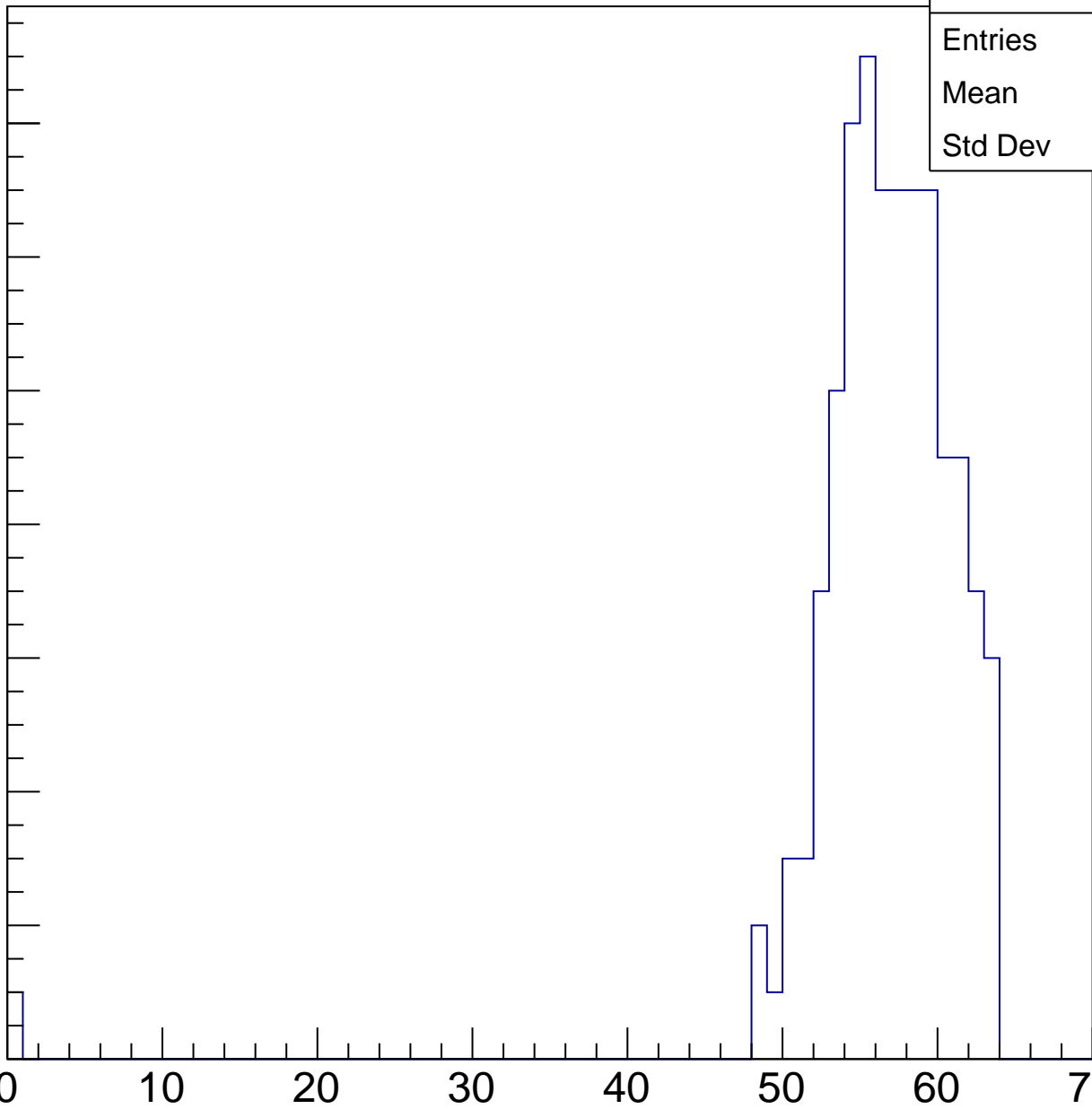
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 56.22 |
| Std Dev | 5.913 |

ampl



# B0L101S, U16-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

67

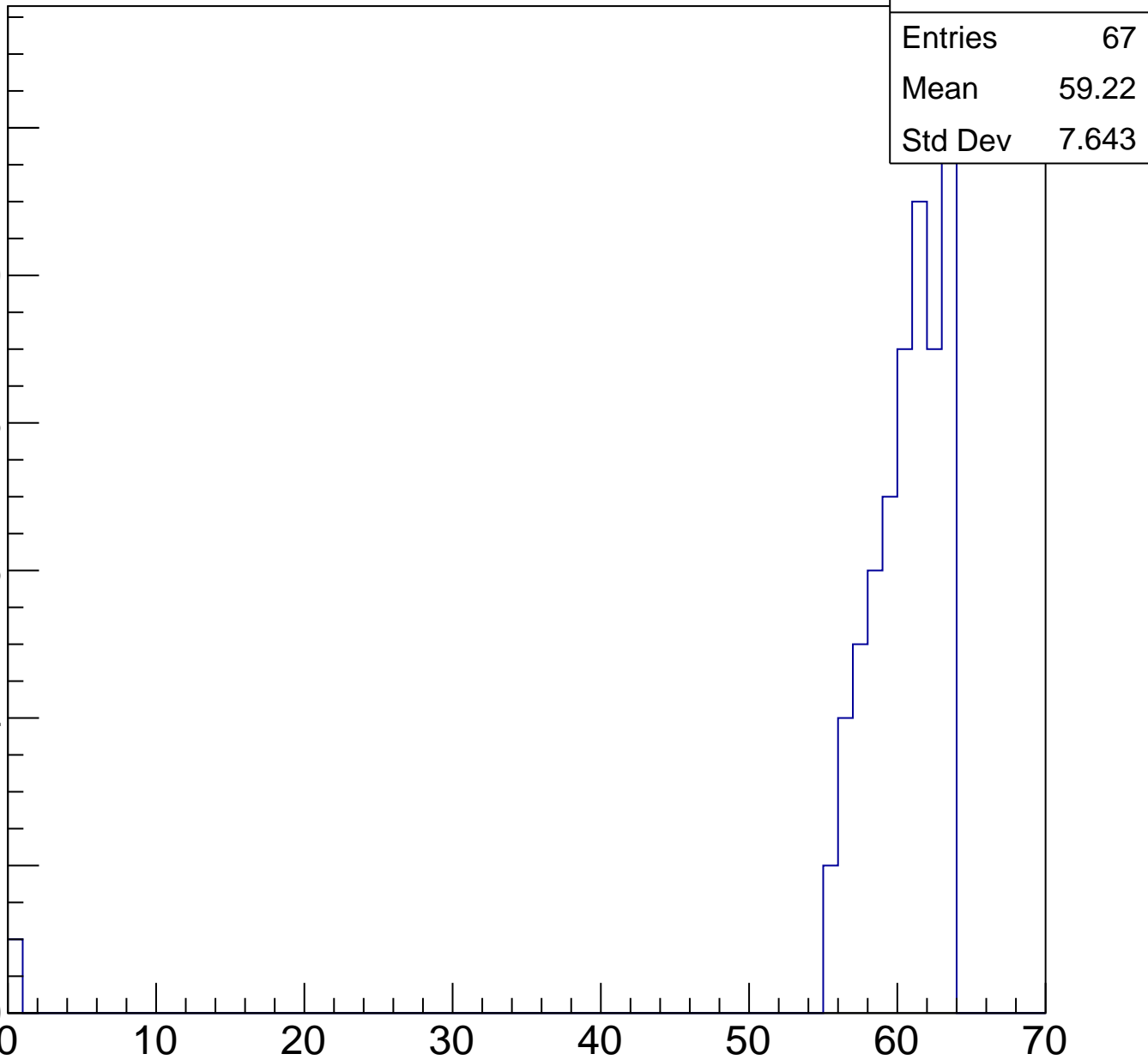
Mean

59.22

Std Dev

7.643

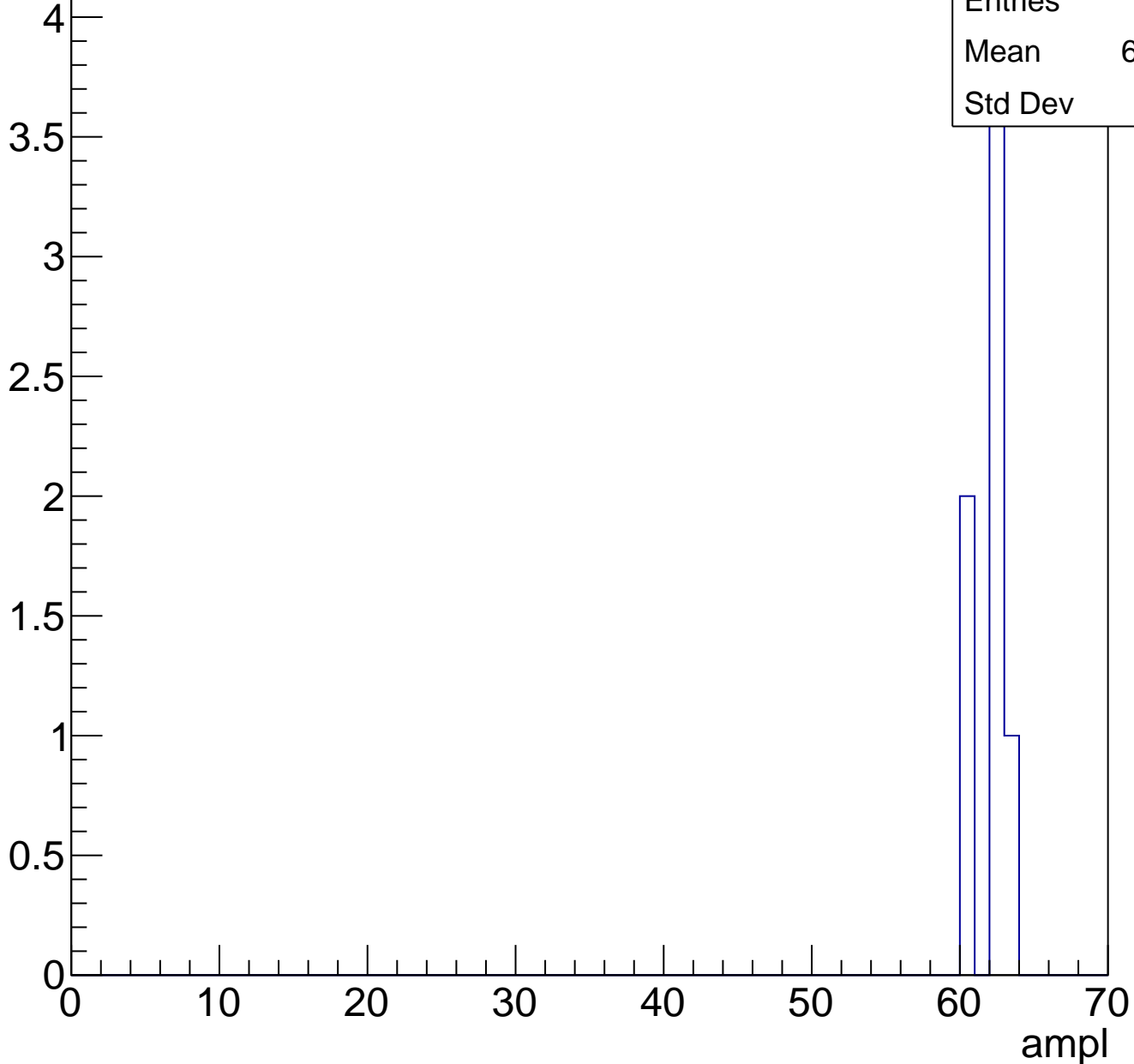
ampl



# B0L101S, U16-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U16-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 160   |
| Mean    | 28.54 |
| Std Dev | 5.393 |

**Gaus mean : 28.9189**

**Gaus Width: 4.4835**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

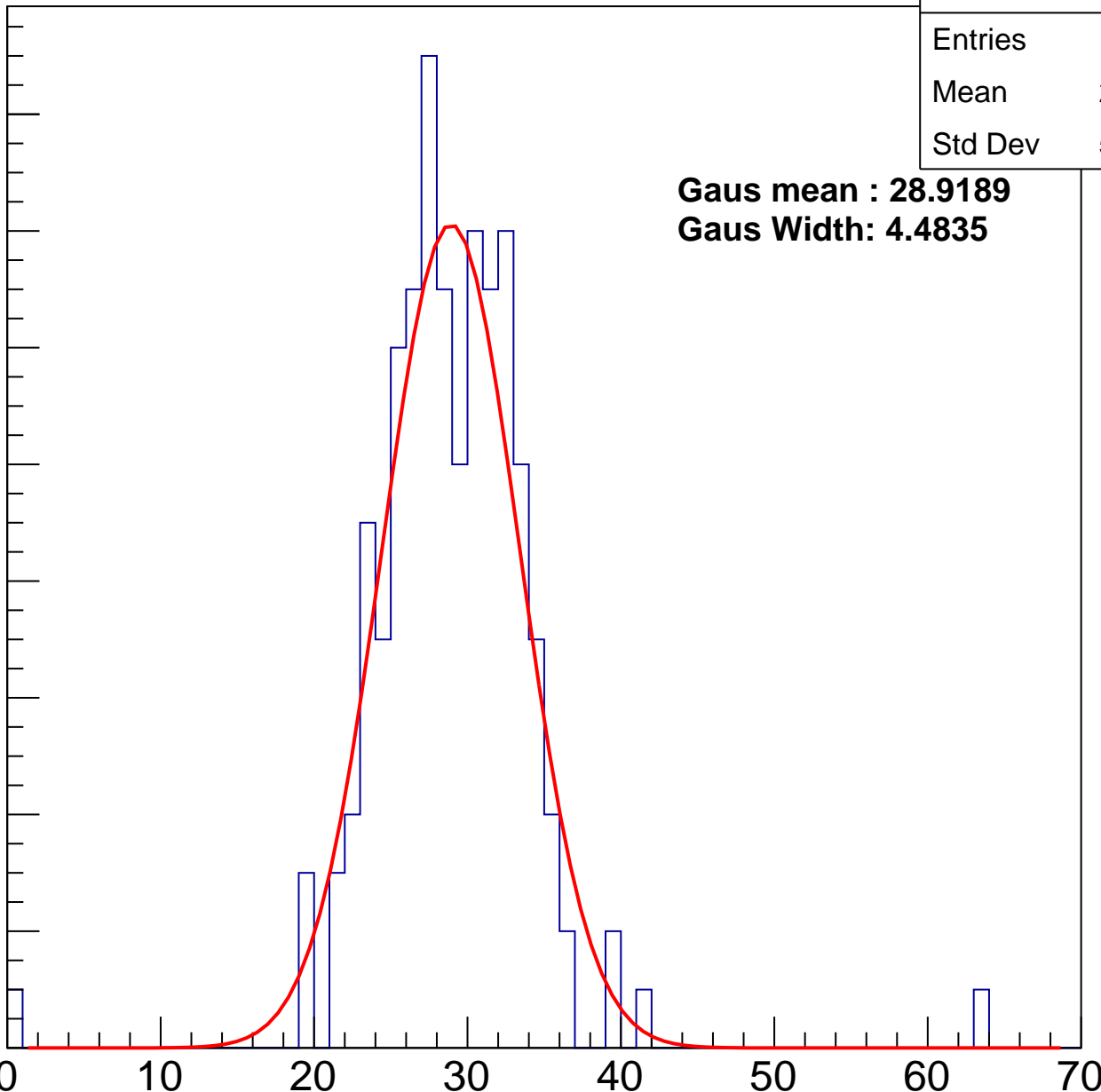
30

40

50

60

ampl



# B0L101S, U16-ch122, adc1

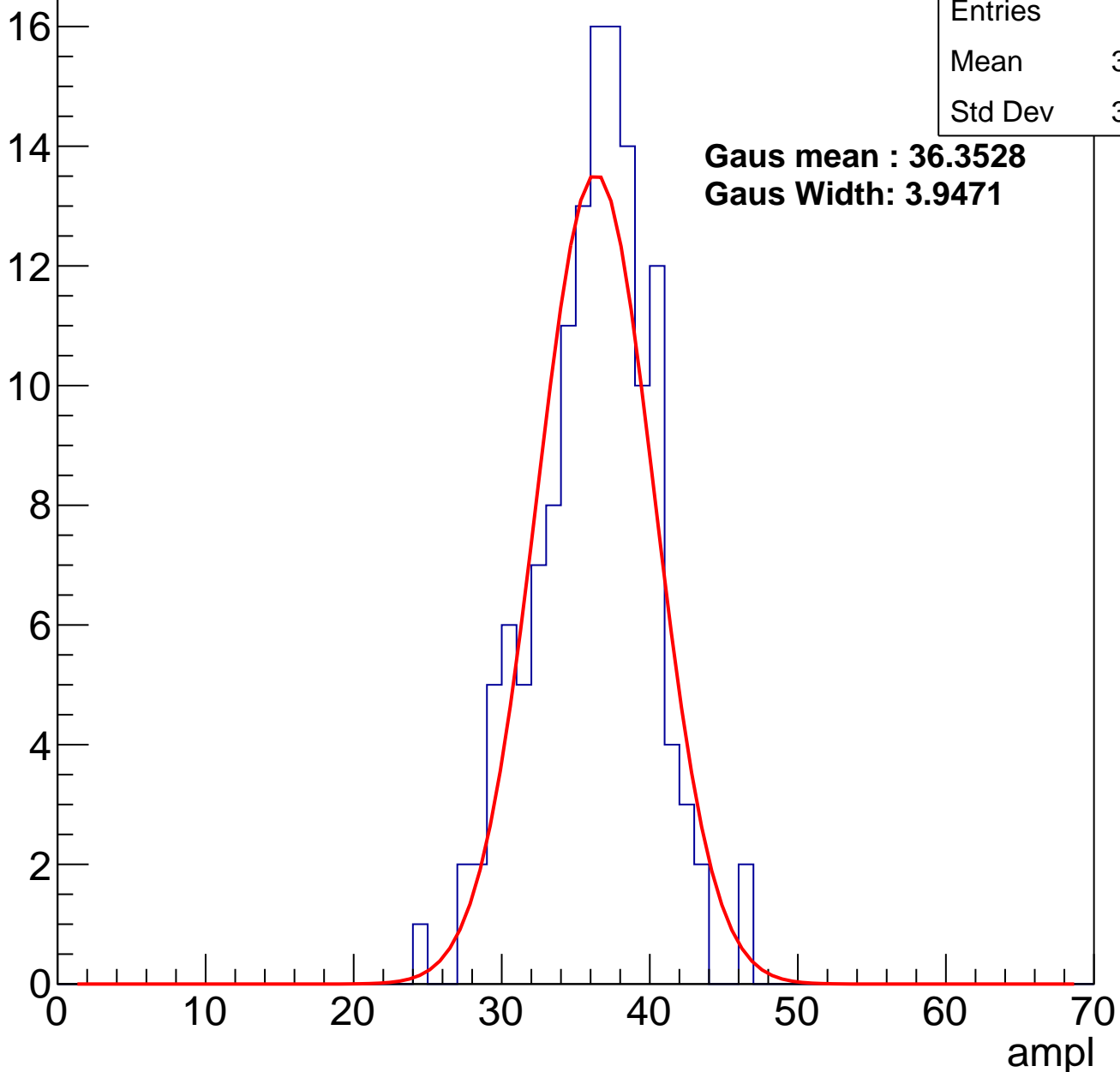
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 139   |
| Mean    | 35.75 |
| Std Dev | 3.898 |

**Gaus mean : 36.3528**

**Gaus Width: 3.9471**

Entry



# B0L101S, U16-ch122, adc2

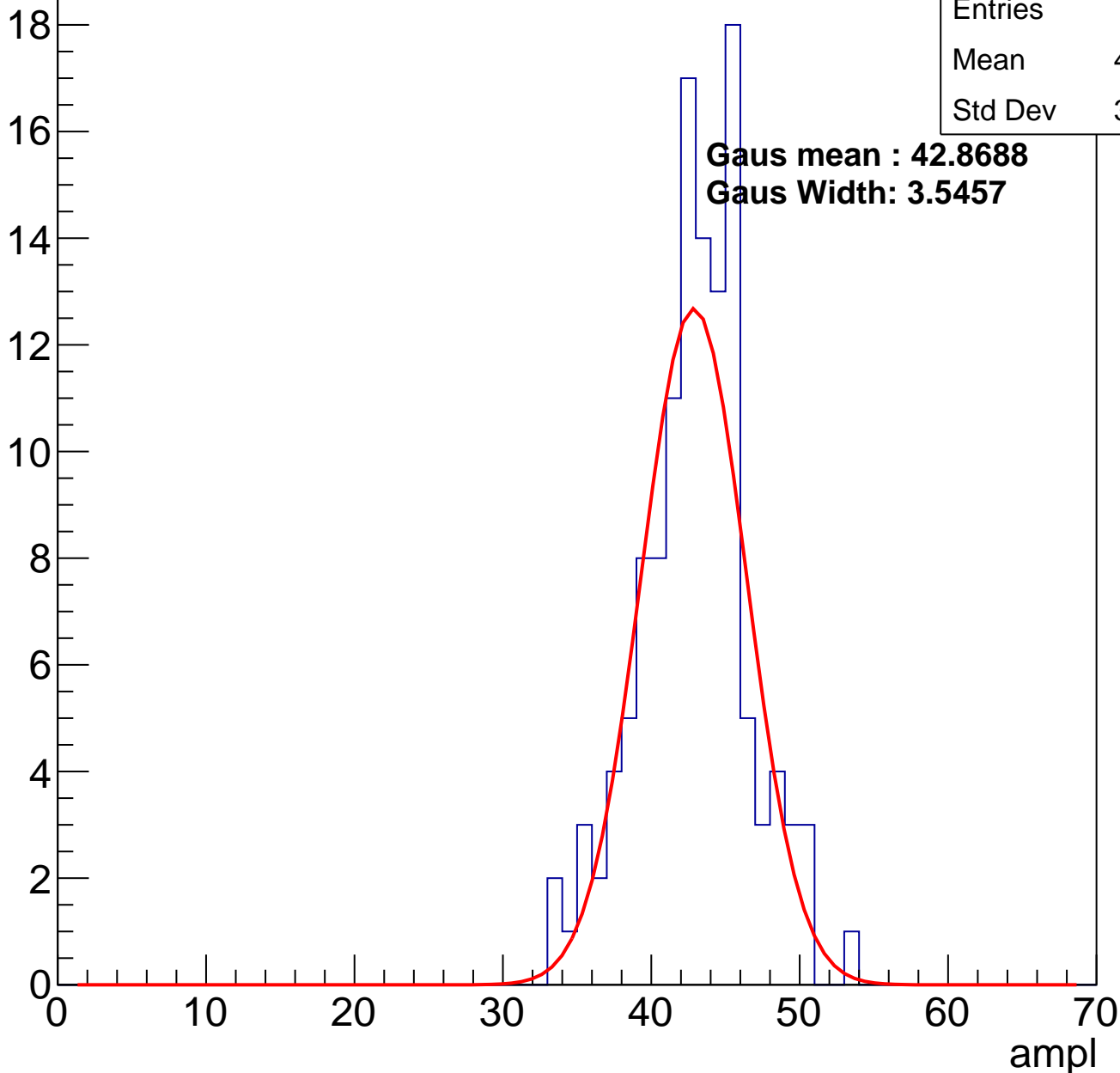
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 125   |
| Mean    | 42.47 |
| Std Dev | 3.687 |

**Gaus mean : 42.8688**

**Gaus Width: 3.5457**

Entry



# B0L101S, U16-ch122, adc3

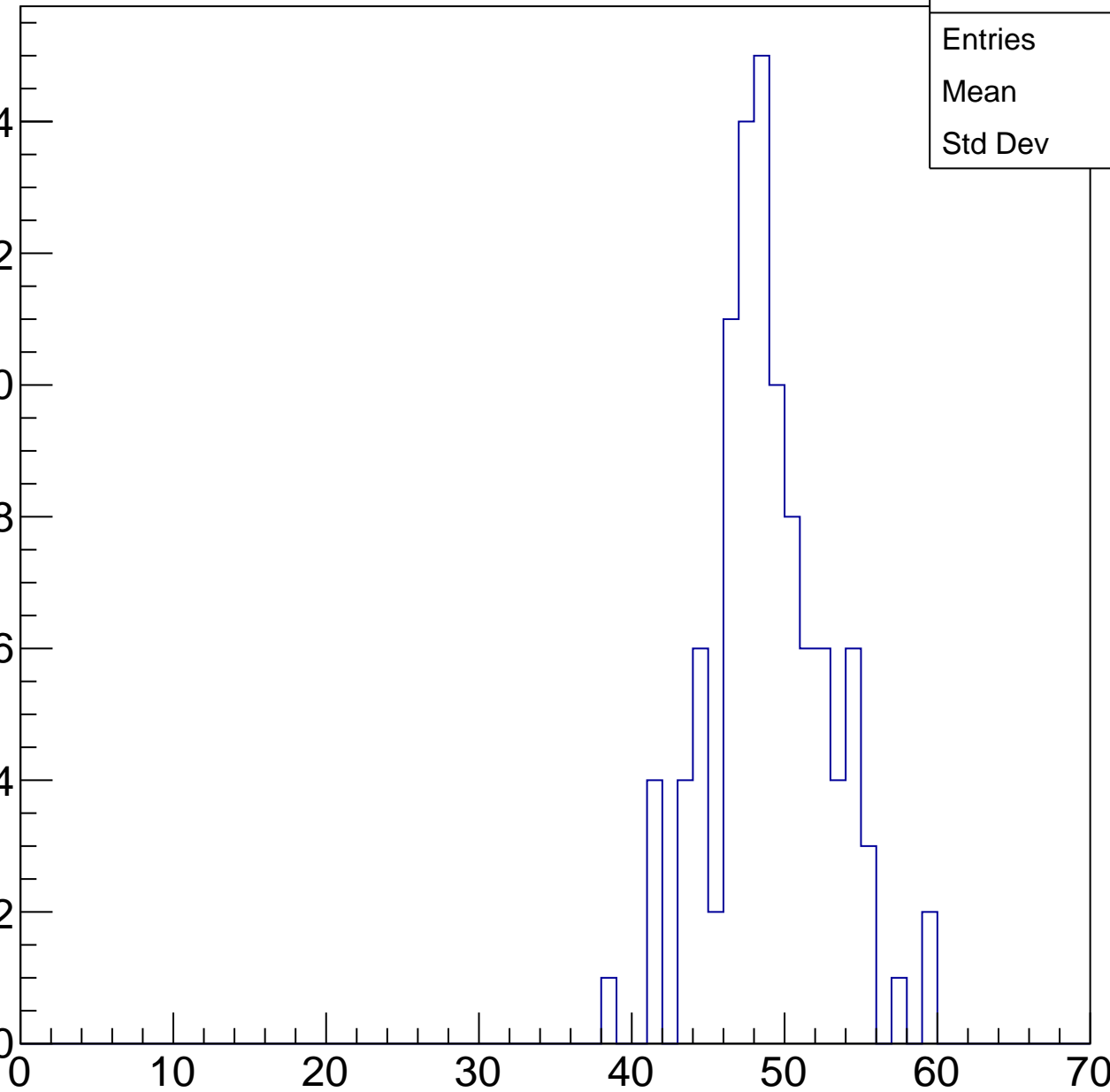
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 103   |
| Mean    | 48.5  |
| Std Dev | 3.841 |

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U16-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 146   |
| Mean    | 54.34 |
| Std Dev | 3.926 |

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

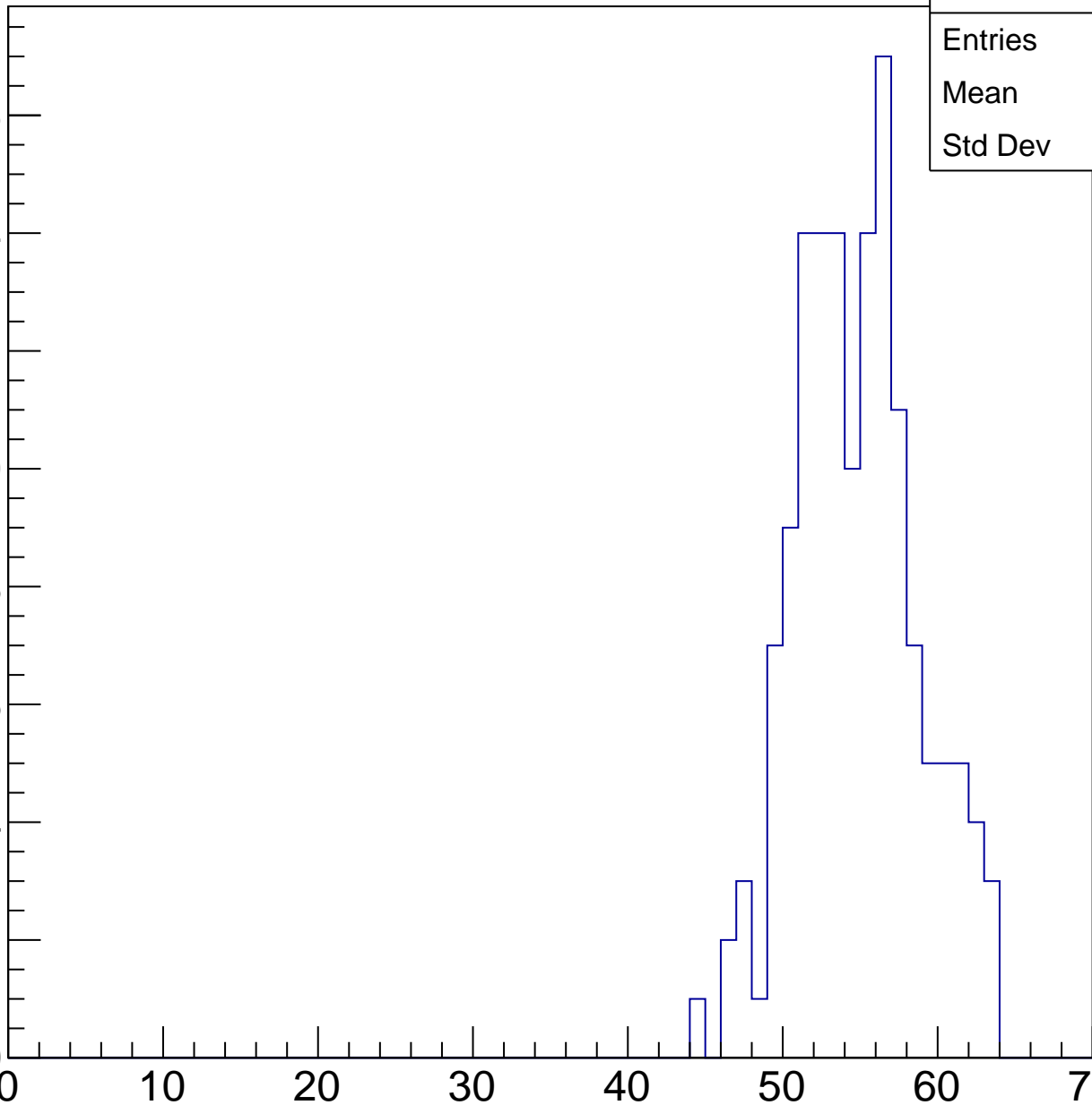
40

50

60

70

ampl



# B0L101S, U16-ch122, adc5

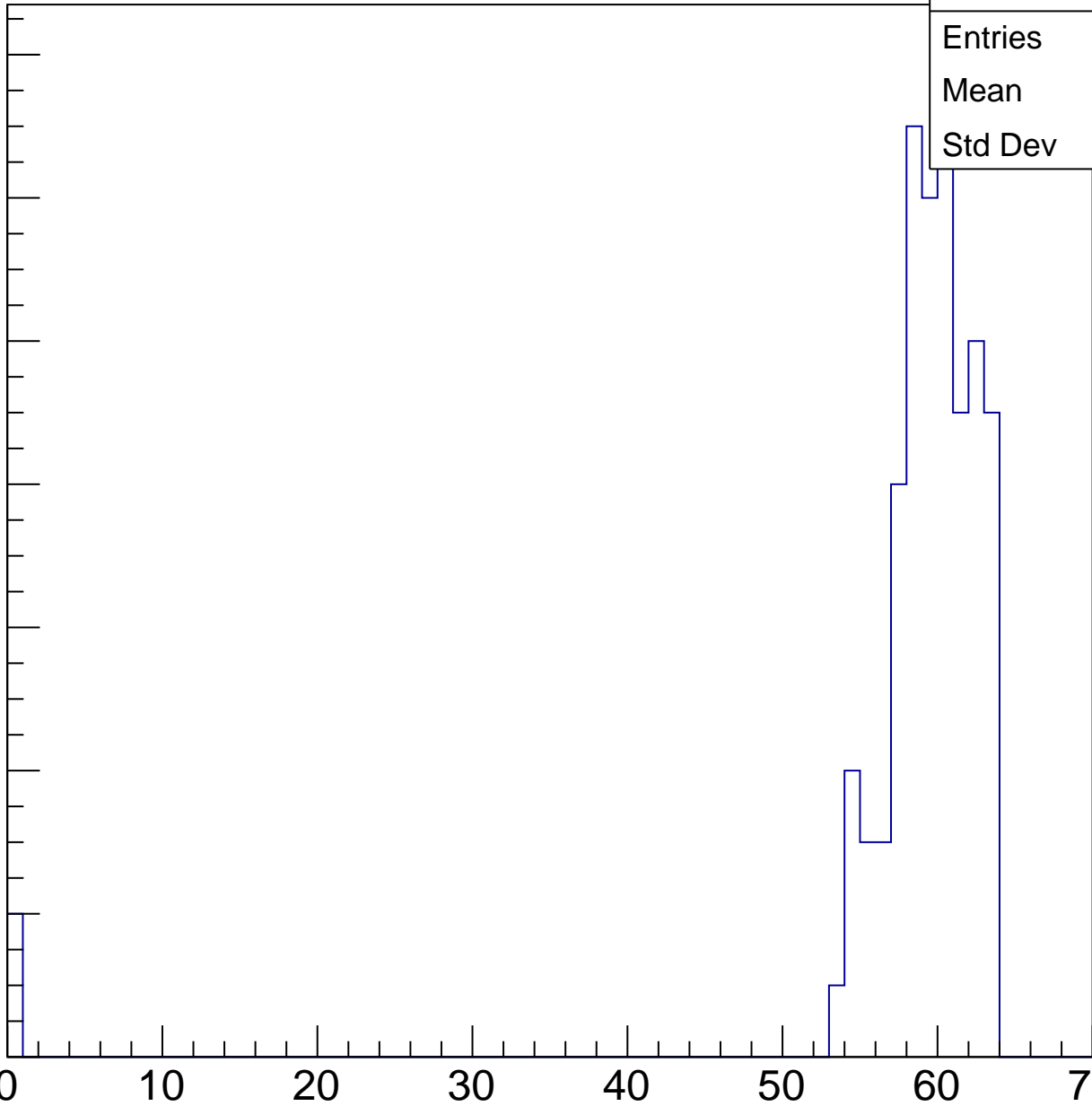
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 57.91 |
| Std Dev | 9.168 |

ampl

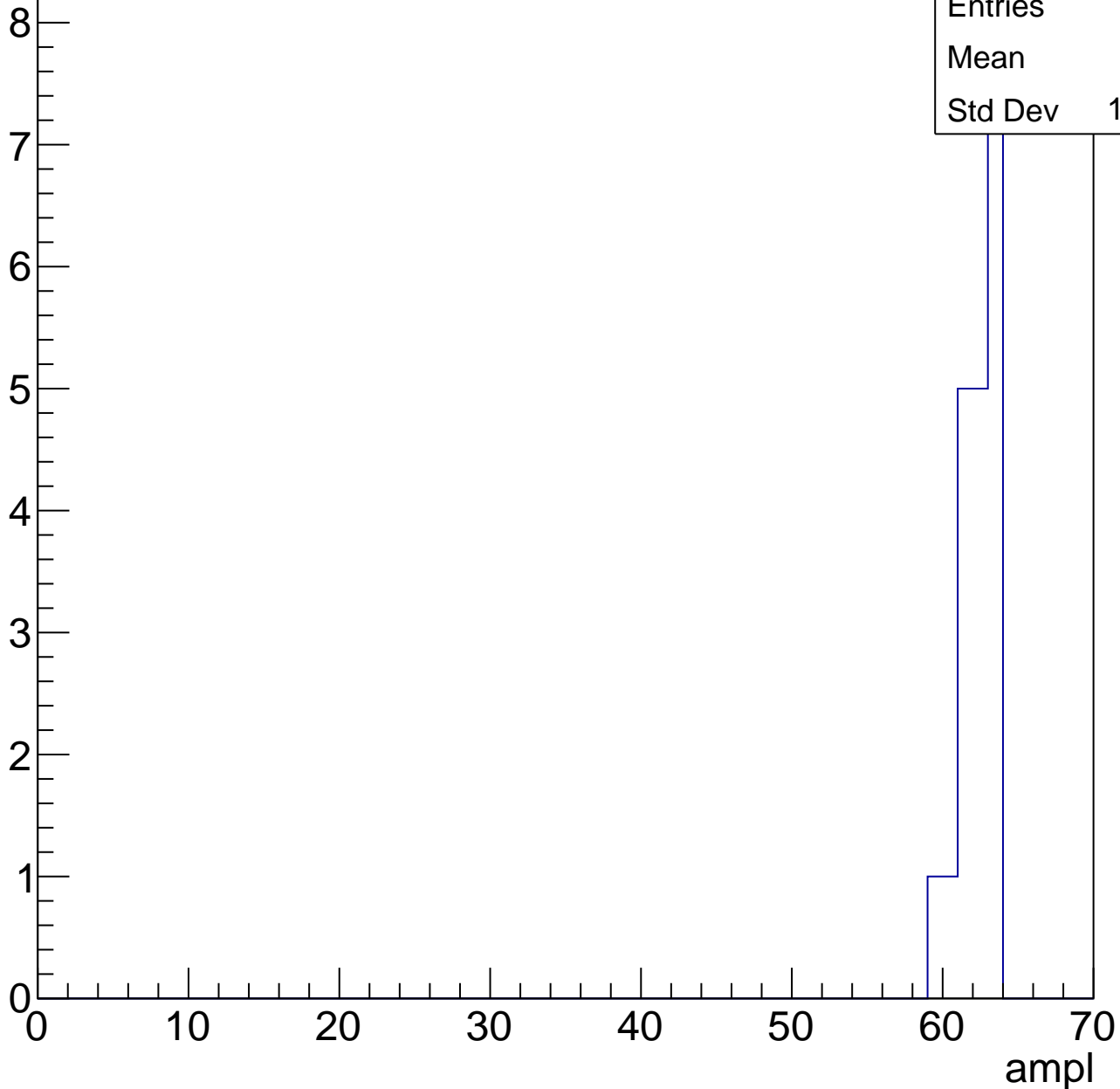


# B0L101S, U16-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 20    |
| Mean    | 61.9  |
| Std Dev | 1.136 |

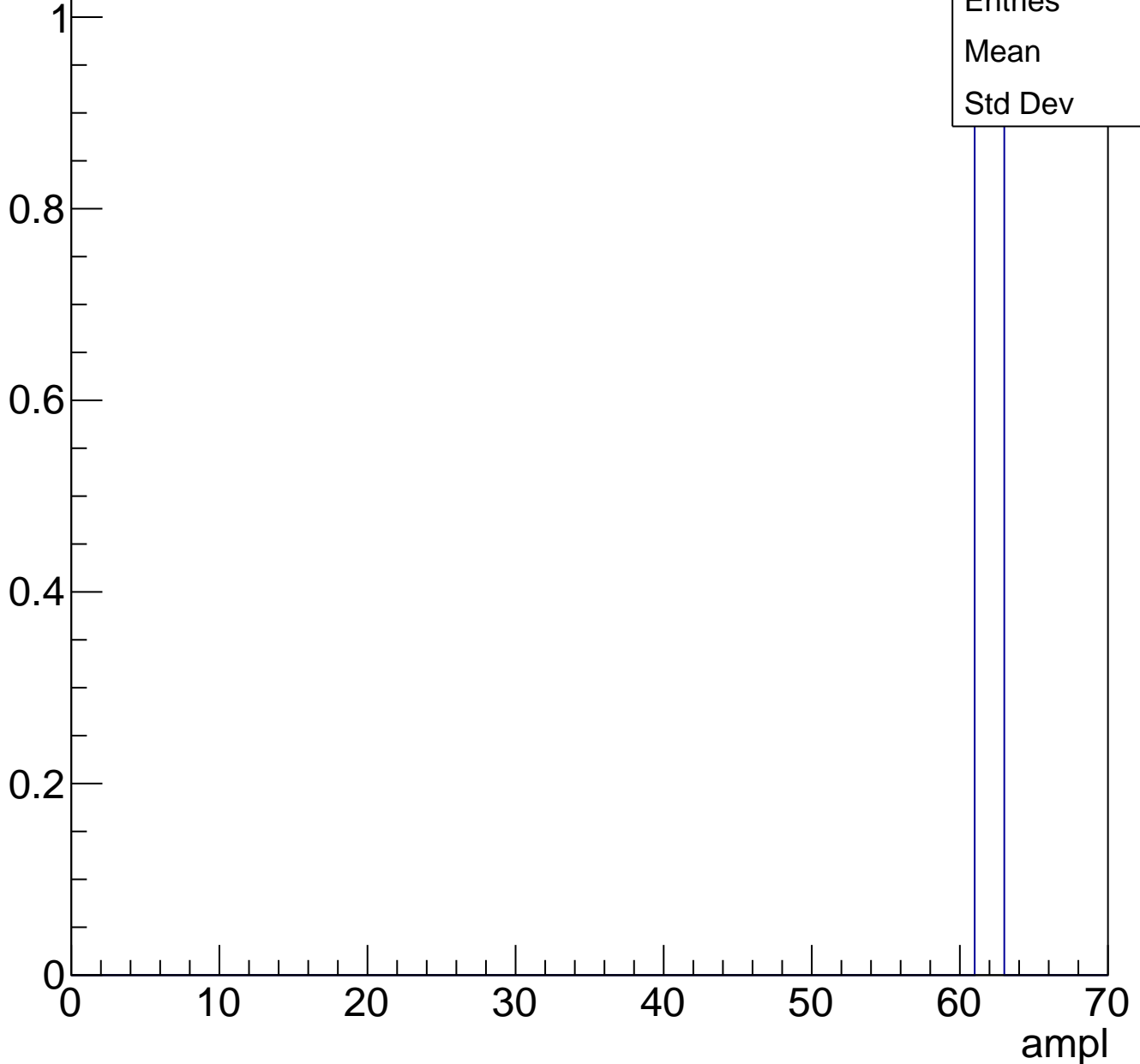




# B0L101S, U16-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch123, adc0

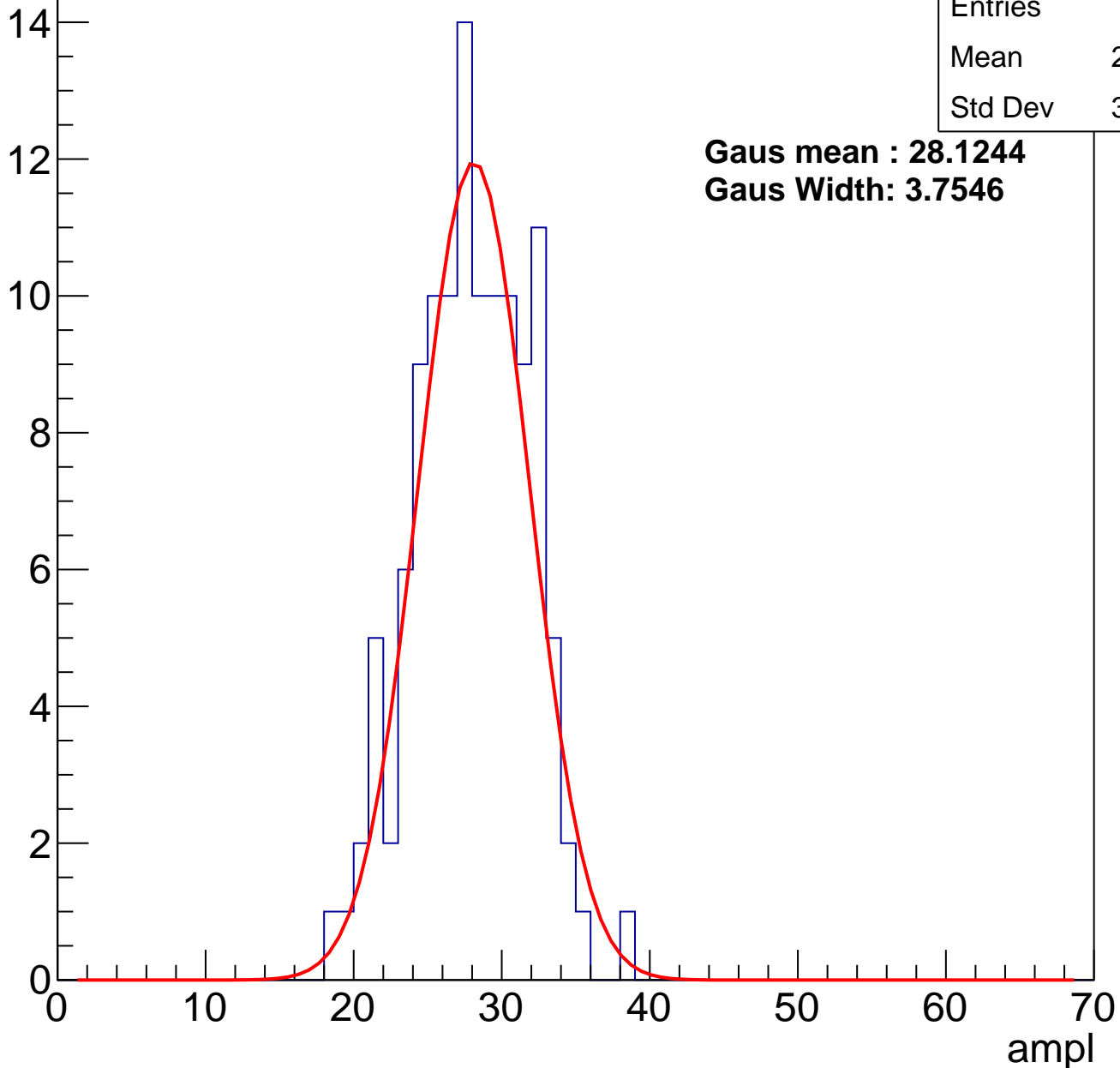
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 27.52 |
| Std Dev | 3.768 |

**Gaus mean : 28.1244**

**Gaus Width: 3.7546**

Entry



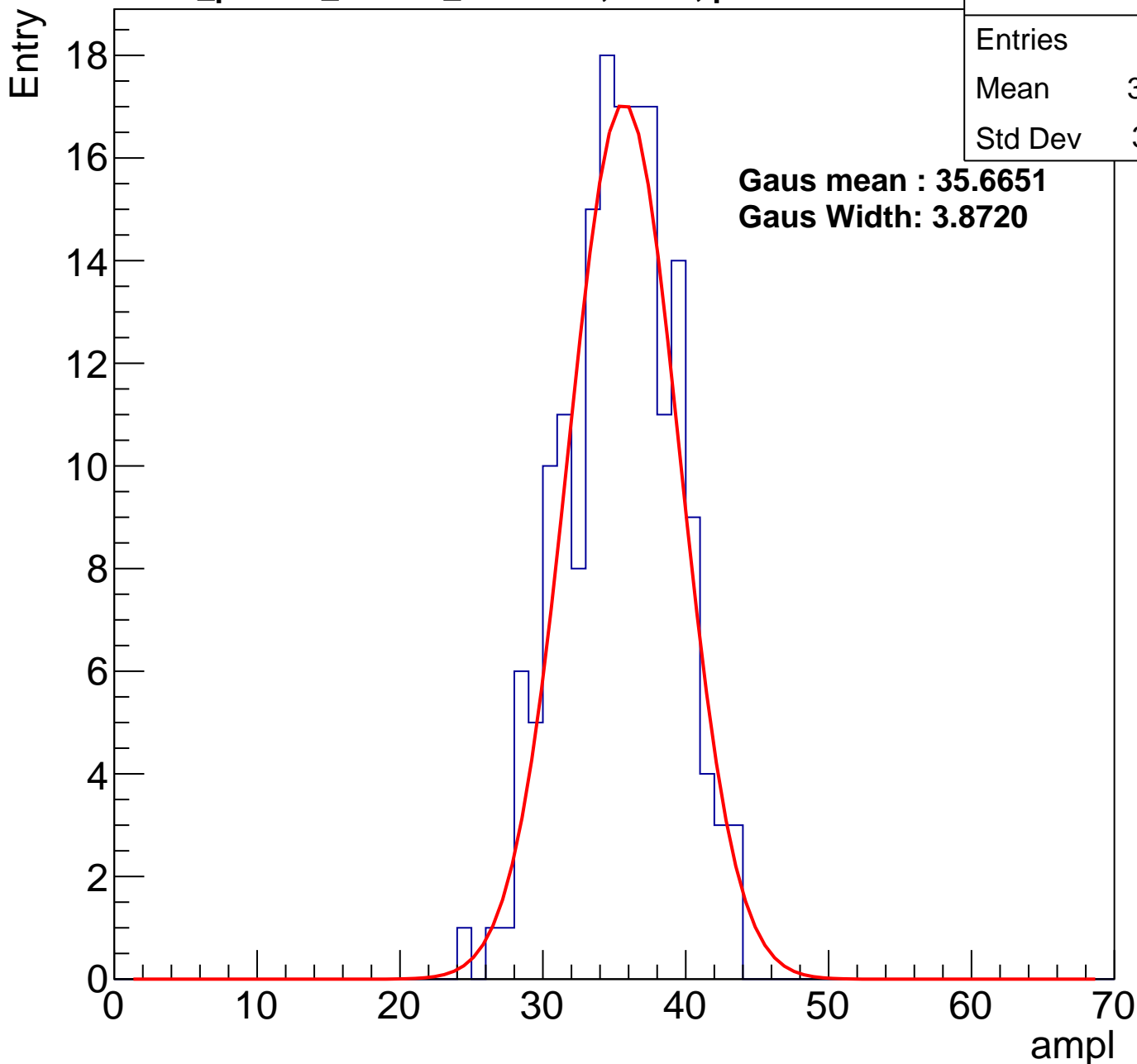
# B0L101S, U16-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 171   |
| Mean    | 34.93 |
| Std Dev | 3.771 |

**Gaus mean : 35.6651**

**Gaus Width: 3.8720**



# B0L101S, U16-ch123, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

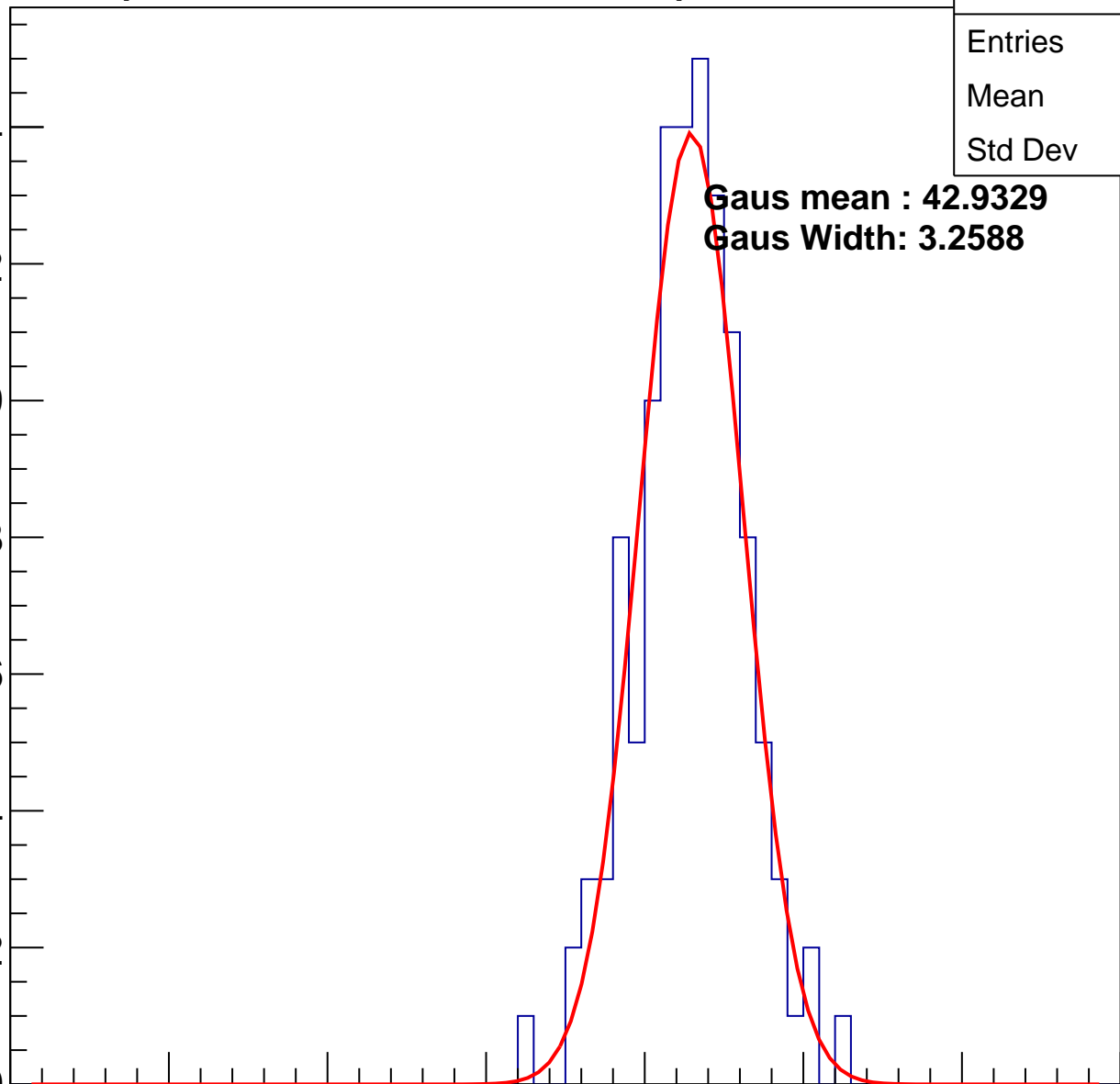
70

ampl

|         |       |
|---------|-------|
| Entries | 119   |
| Mean    | 42.37 |
| Std Dev | 3.425 |

**Gaus mean : 42.9329**

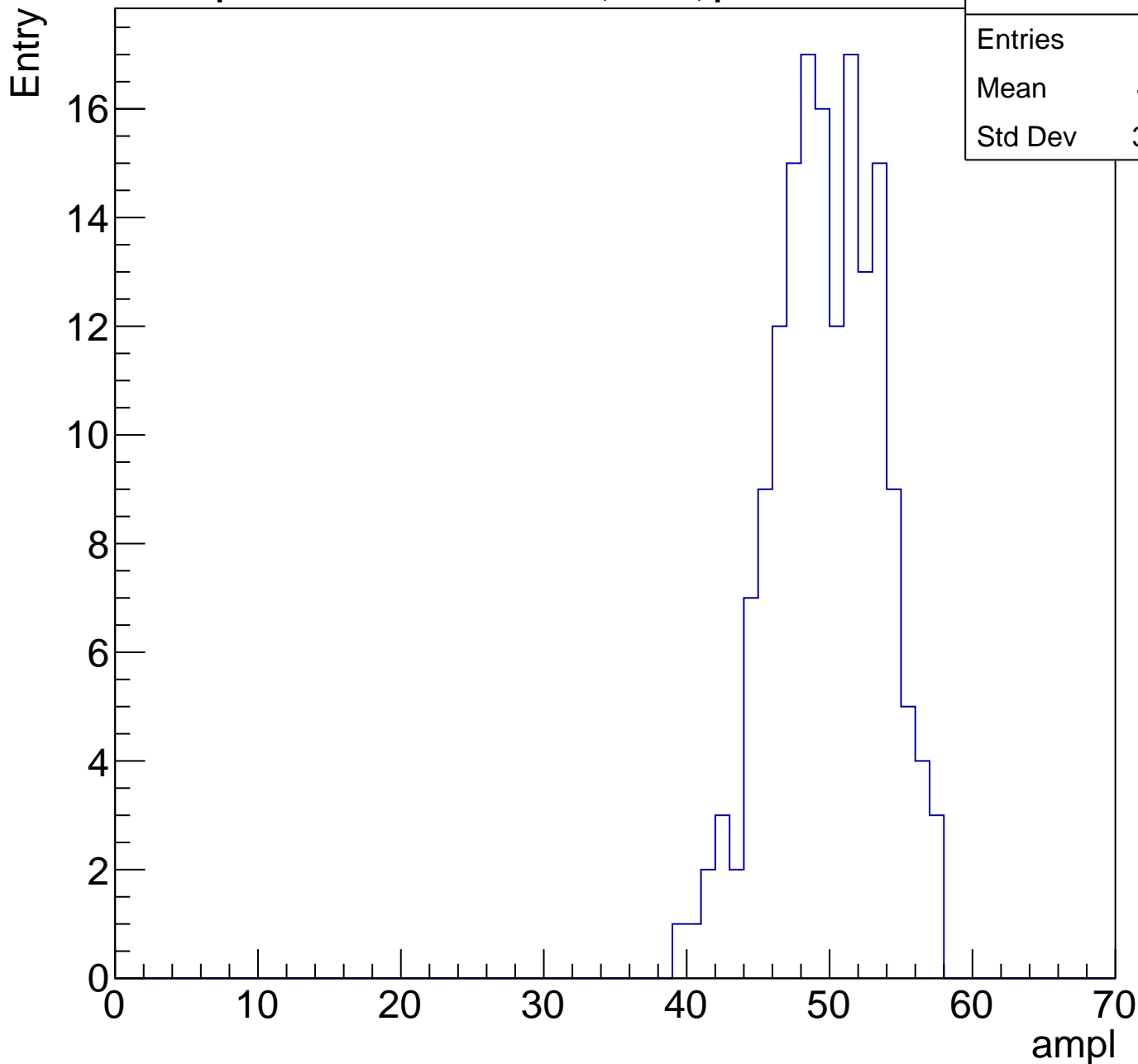
**Gaus Width: 3.2588**



# B0L101S, U16-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 163   |
| Mean    | 49.31 |
| Std Dev | 3.713 |



# B0L101S, U16-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

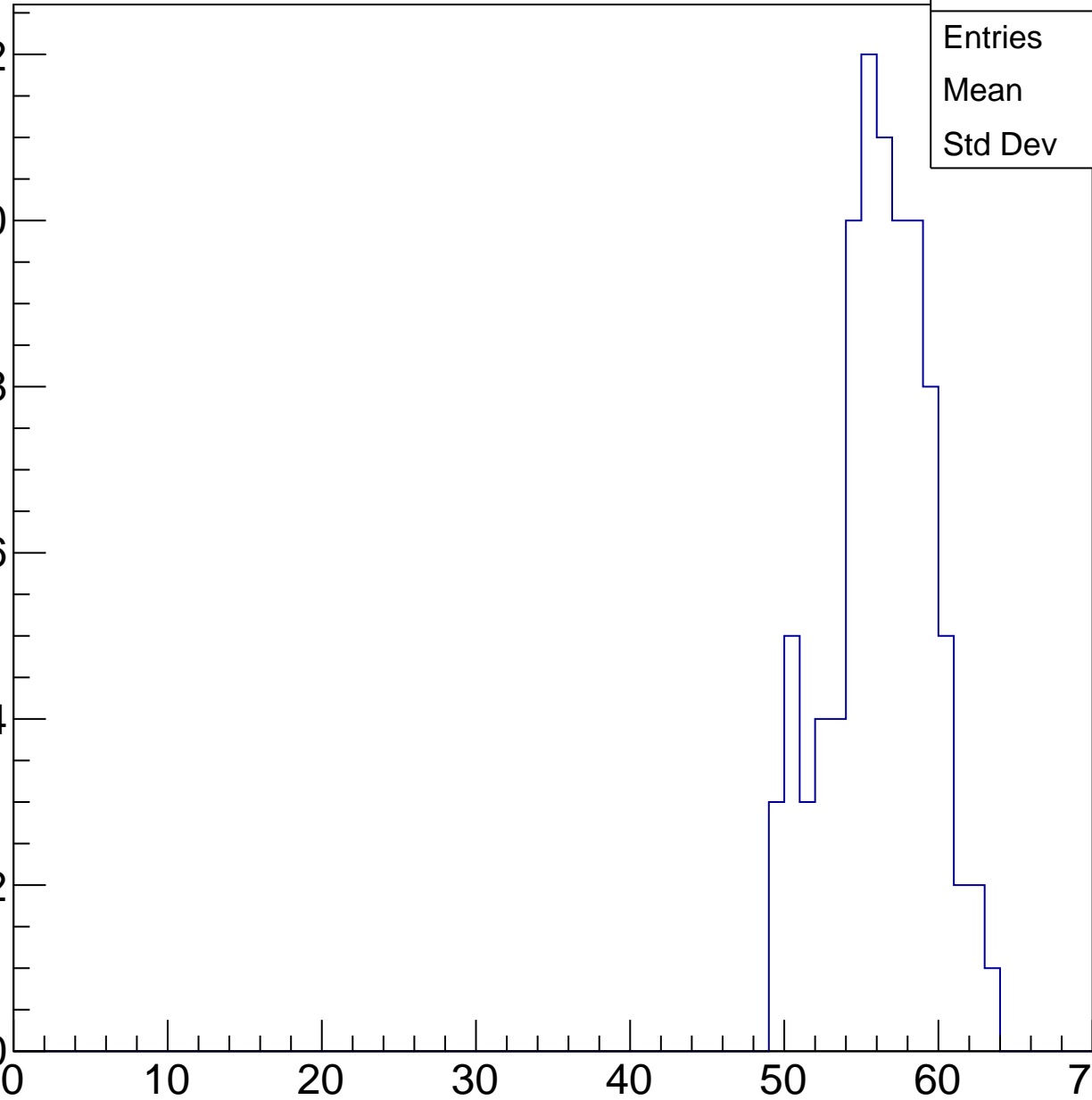
|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 55.74 |
| Std Dev | 3.199 |

Entry

12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U16-ch123, adc5

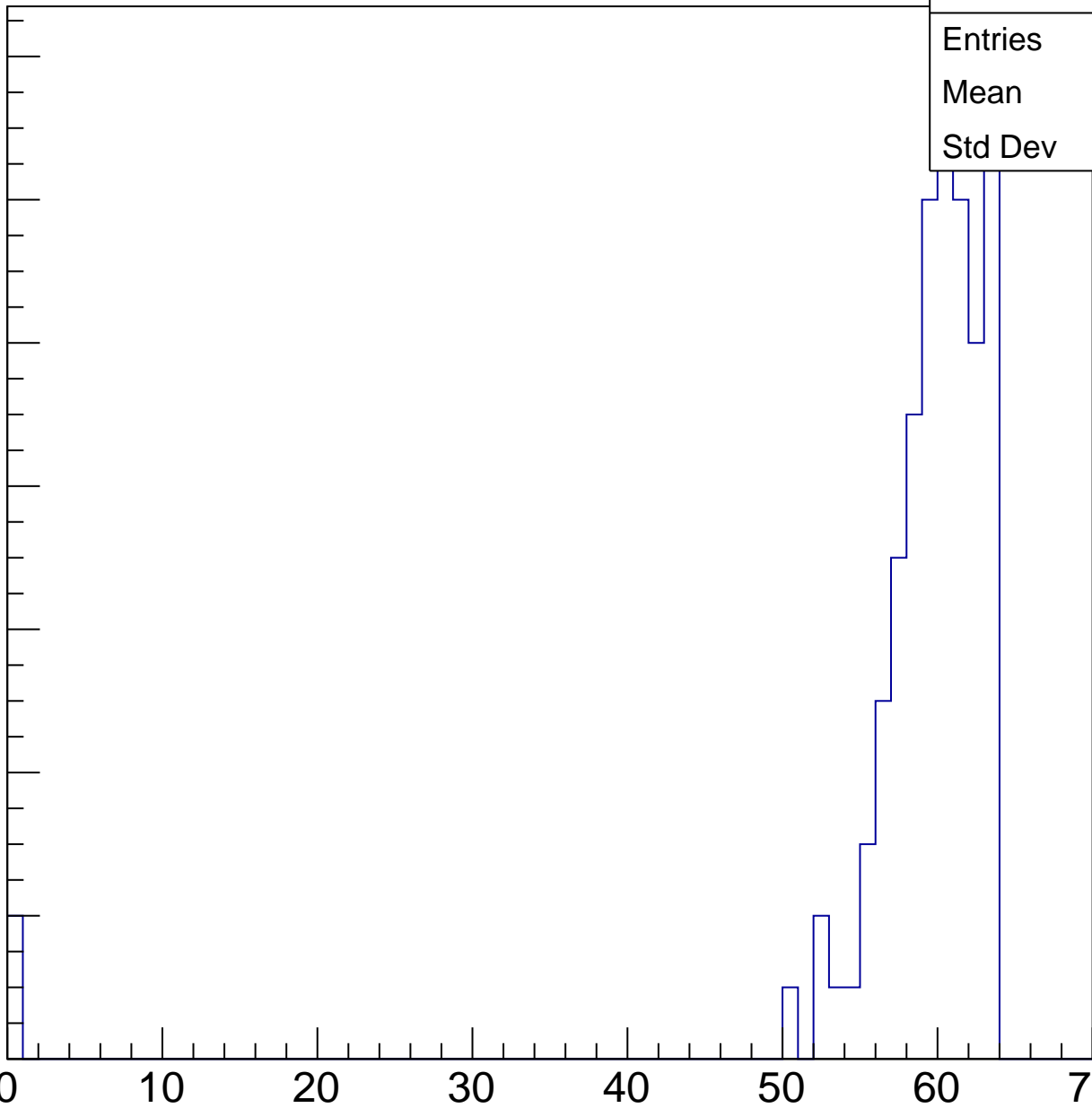
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 92    |
| Mean    | 58.14 |
| Std Dev | 9.112 |

ampl

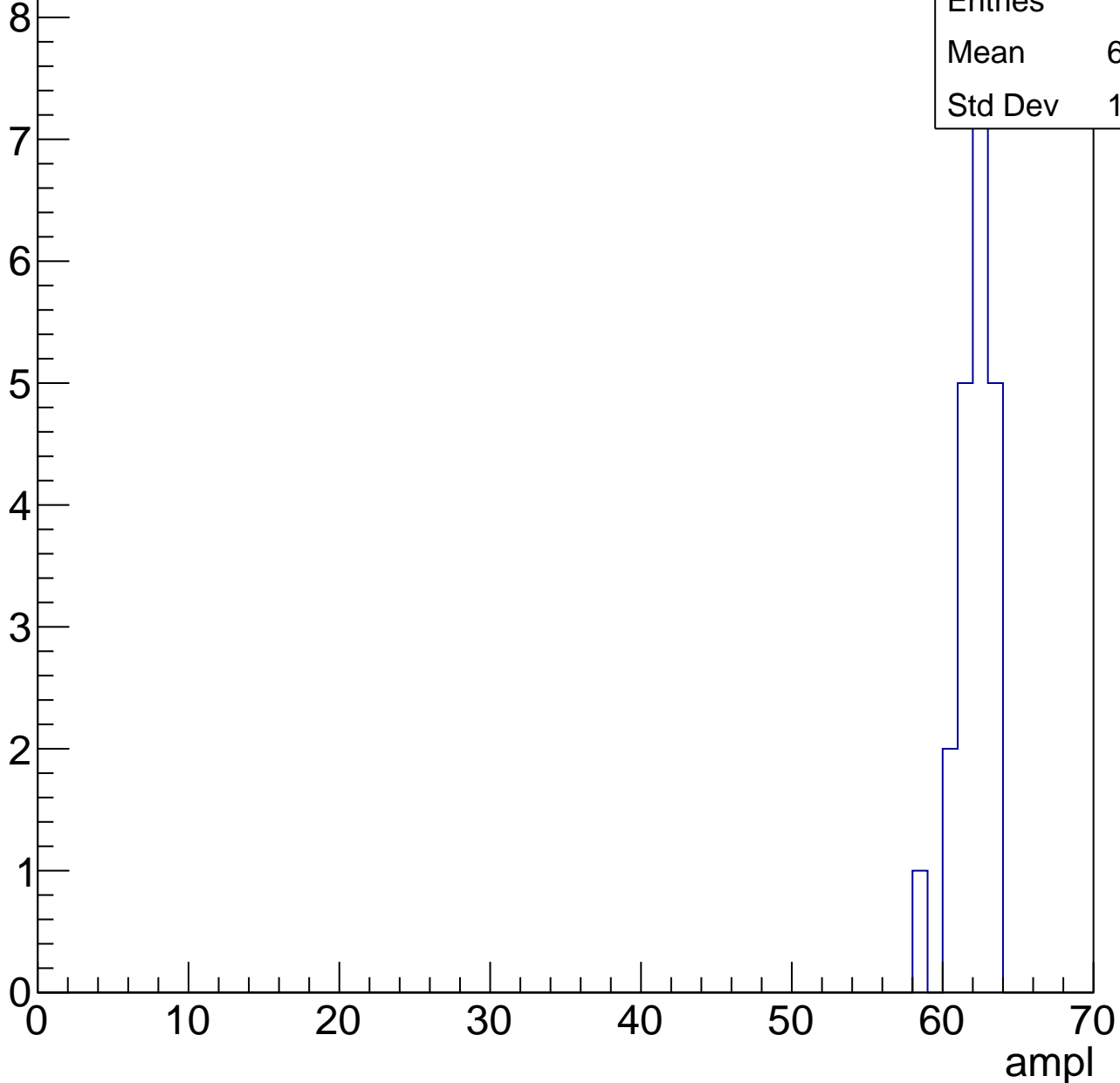


# B0L101S, U16-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 21    |
| Mean    | 61.62 |
| Std Dev | 1.214 |





# B0L101S, U16-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch124, adc0

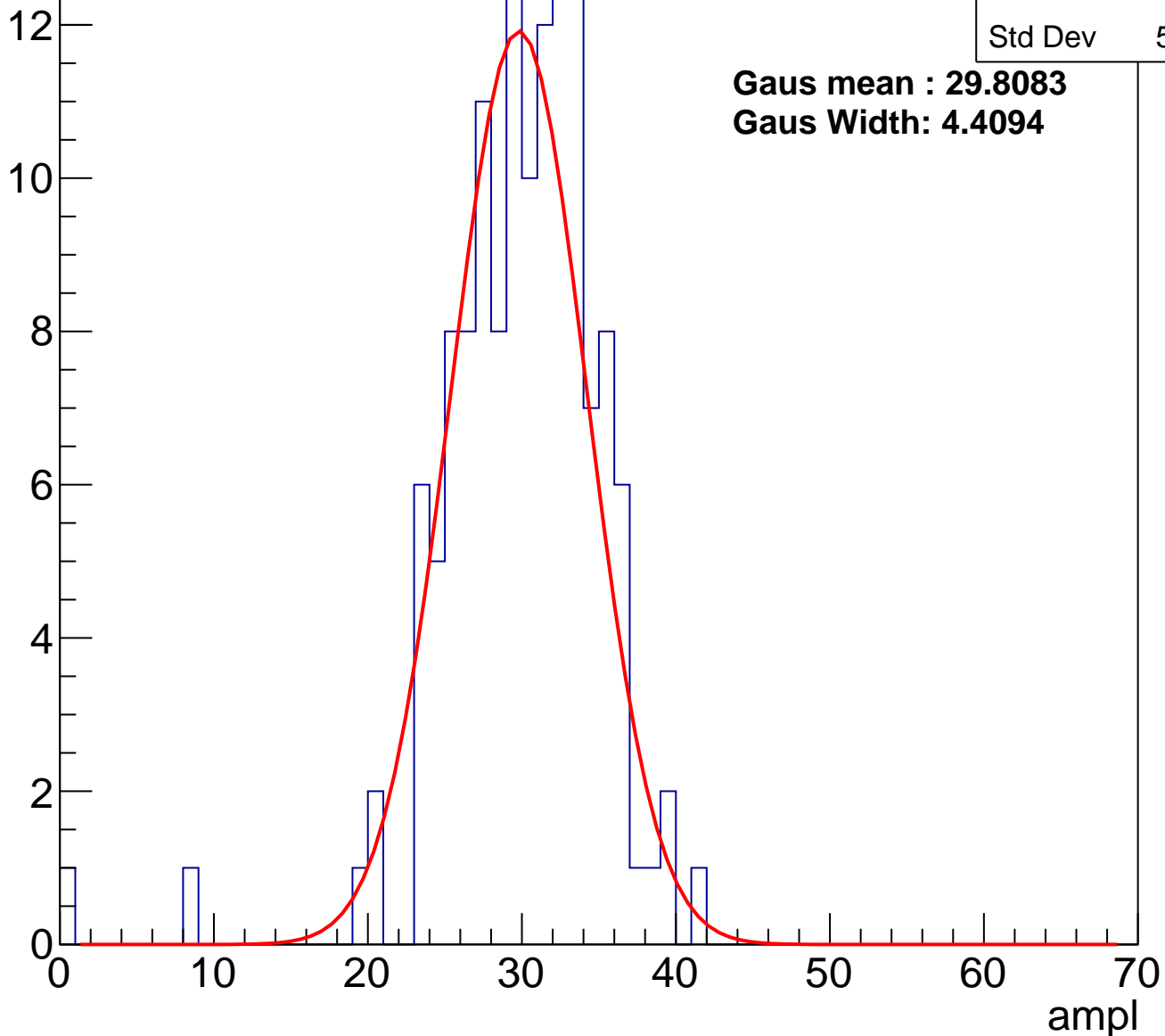
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 138   |
| Mean    | 29.54 |
| Std Dev | 5.196 |

**Gaus mean : 29.8083**

**Gaus Width: 4.4094**

Entry



# B0L101S, U16-ch124, adc1

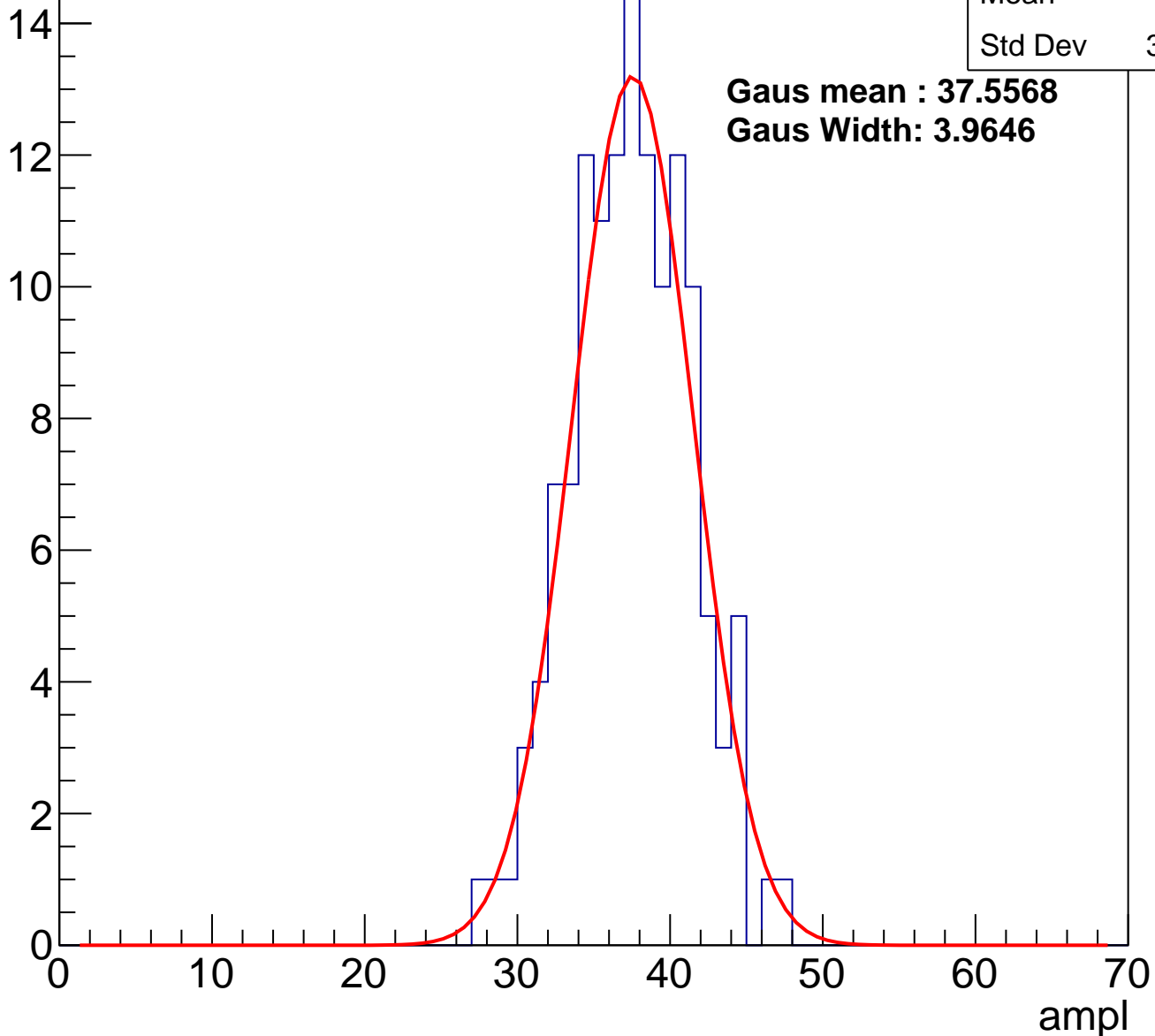
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 133   |
| Mean    | 37    |
| Std Dev | 3.845 |

**Gaus mean : 37.5568**

**Gaus Width: 3.9646**

Entry



# B0L101S, U16-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

43.1

Std Dev

3.837

**Gaus mean : 43.3109**

**Gaus Width: 3.9063**

0

10

20

30

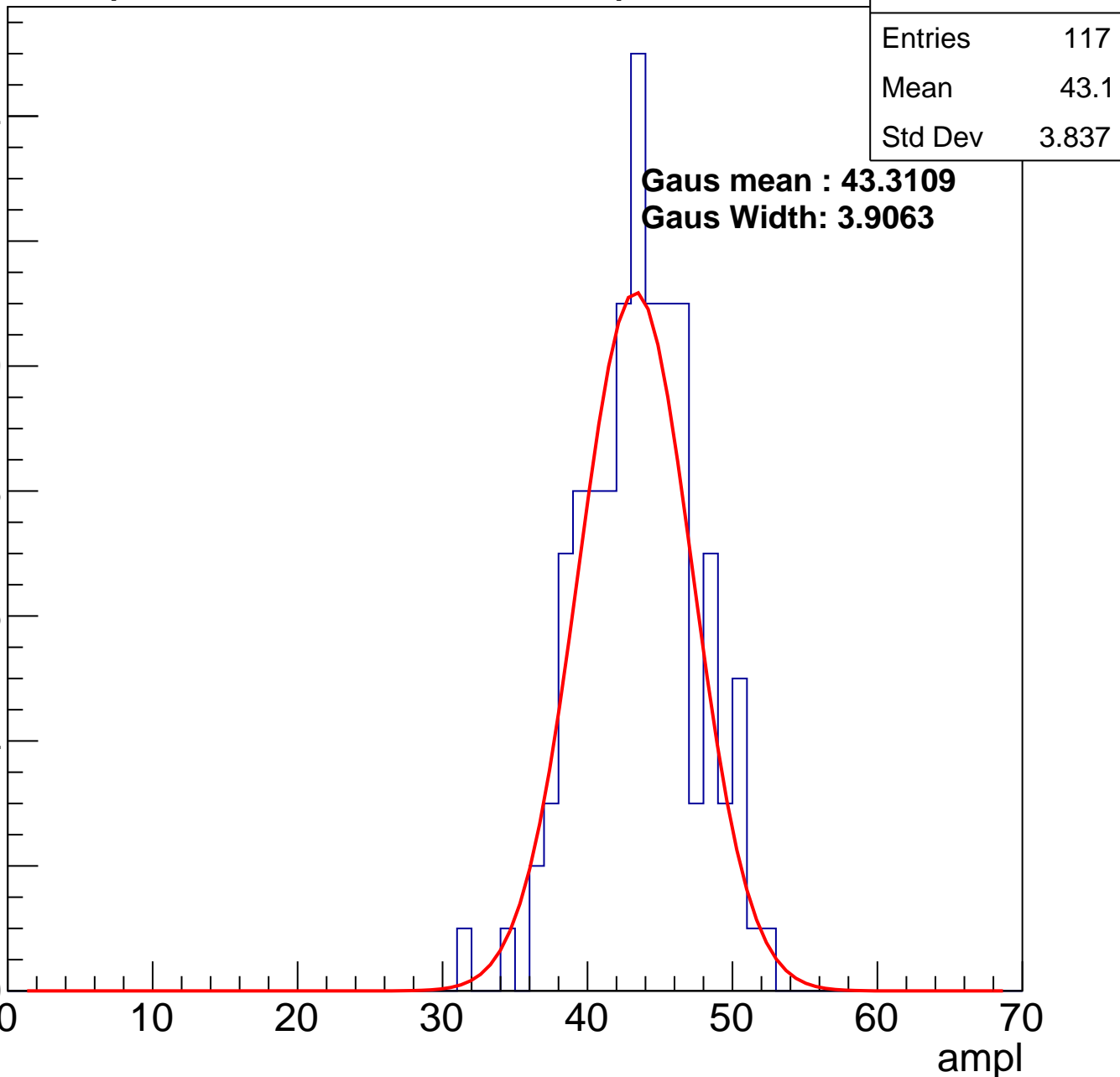
40

50

60

70

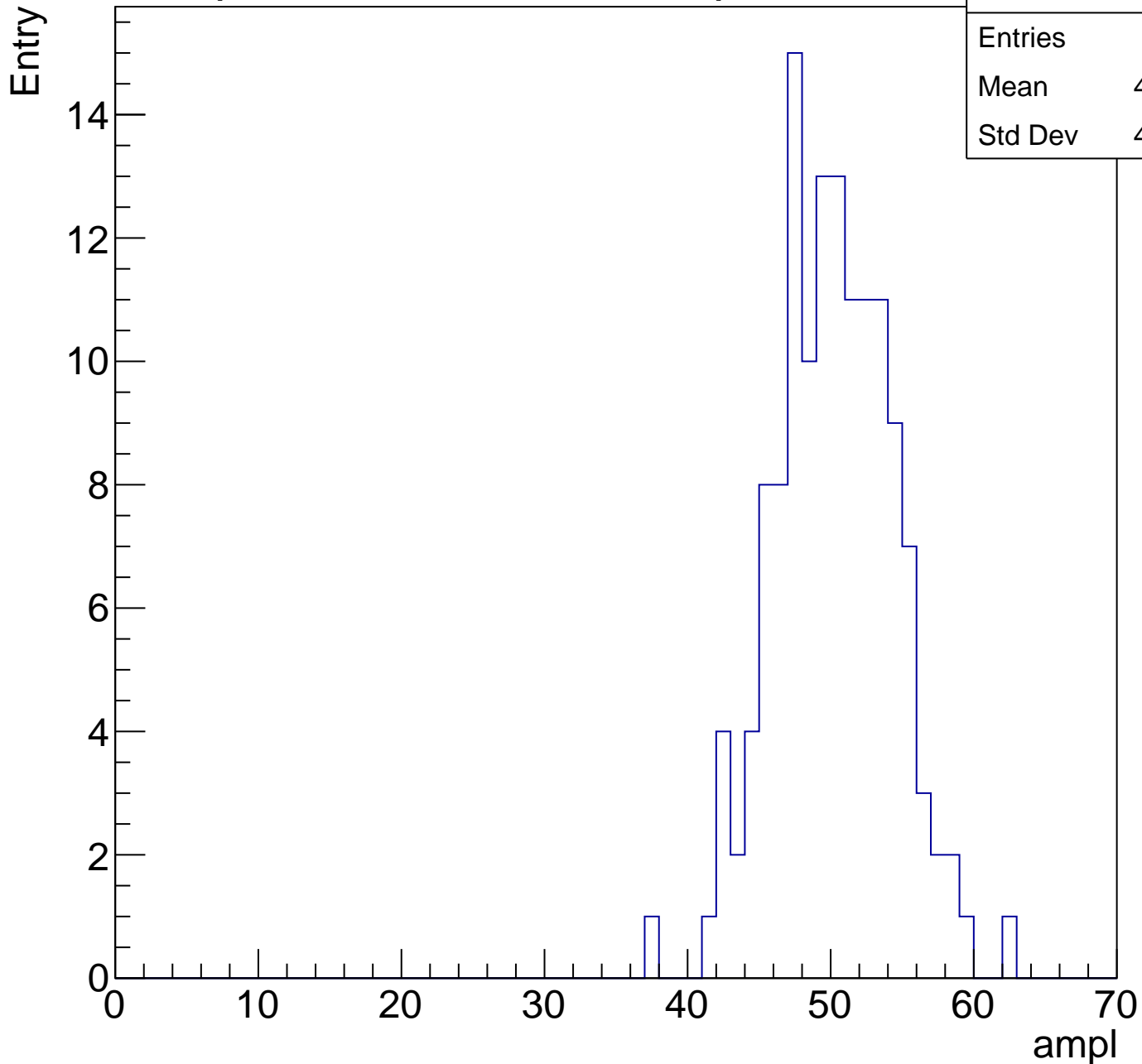
ampl



# B0L101S, U16-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

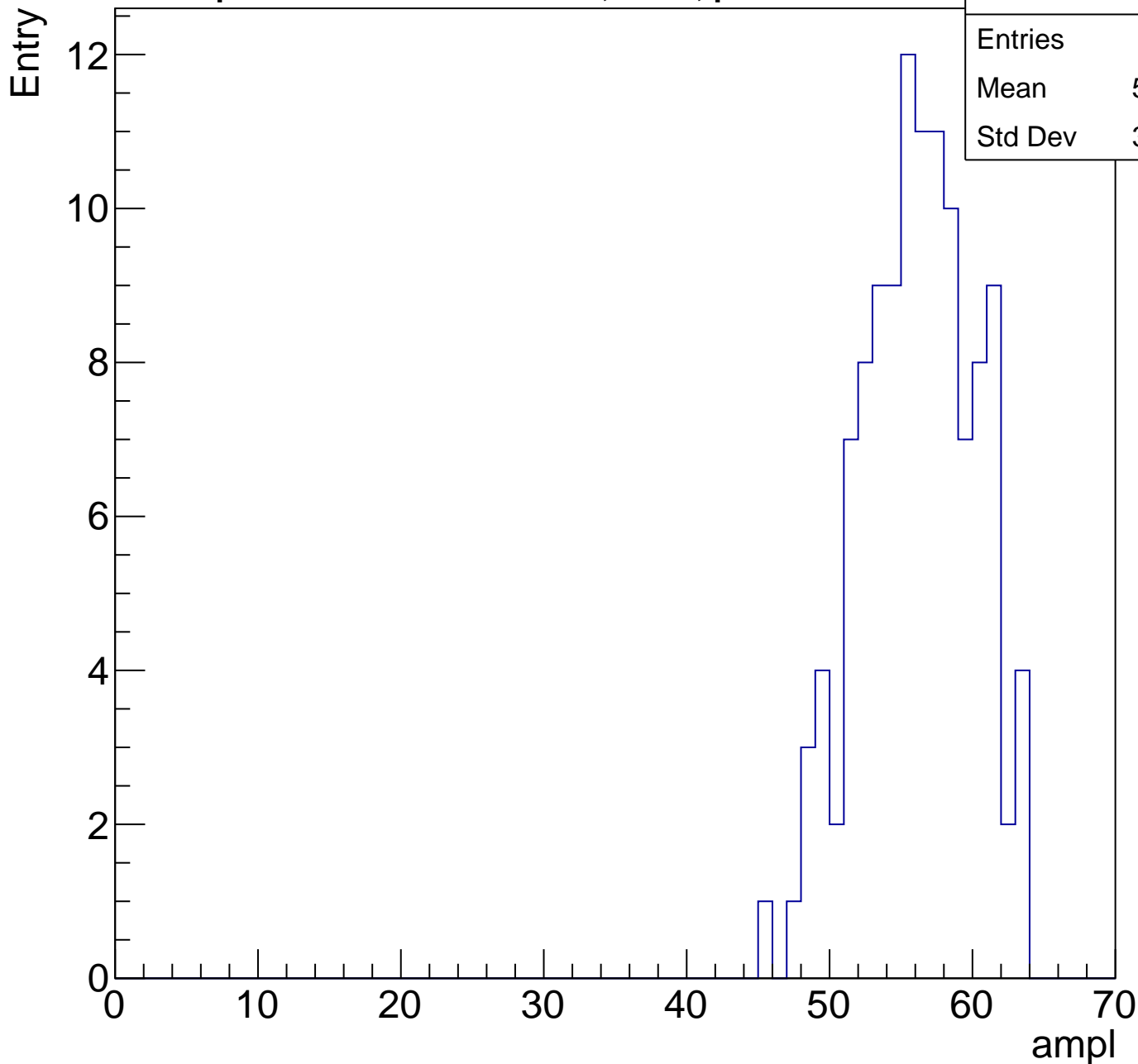
|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 49.74 |
| Std Dev | 4.085 |



# B0L101S, U16-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 118   |
| Mean    | 55.67 |
| Std Dev | 3.923 |



# B0L101S, U16-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

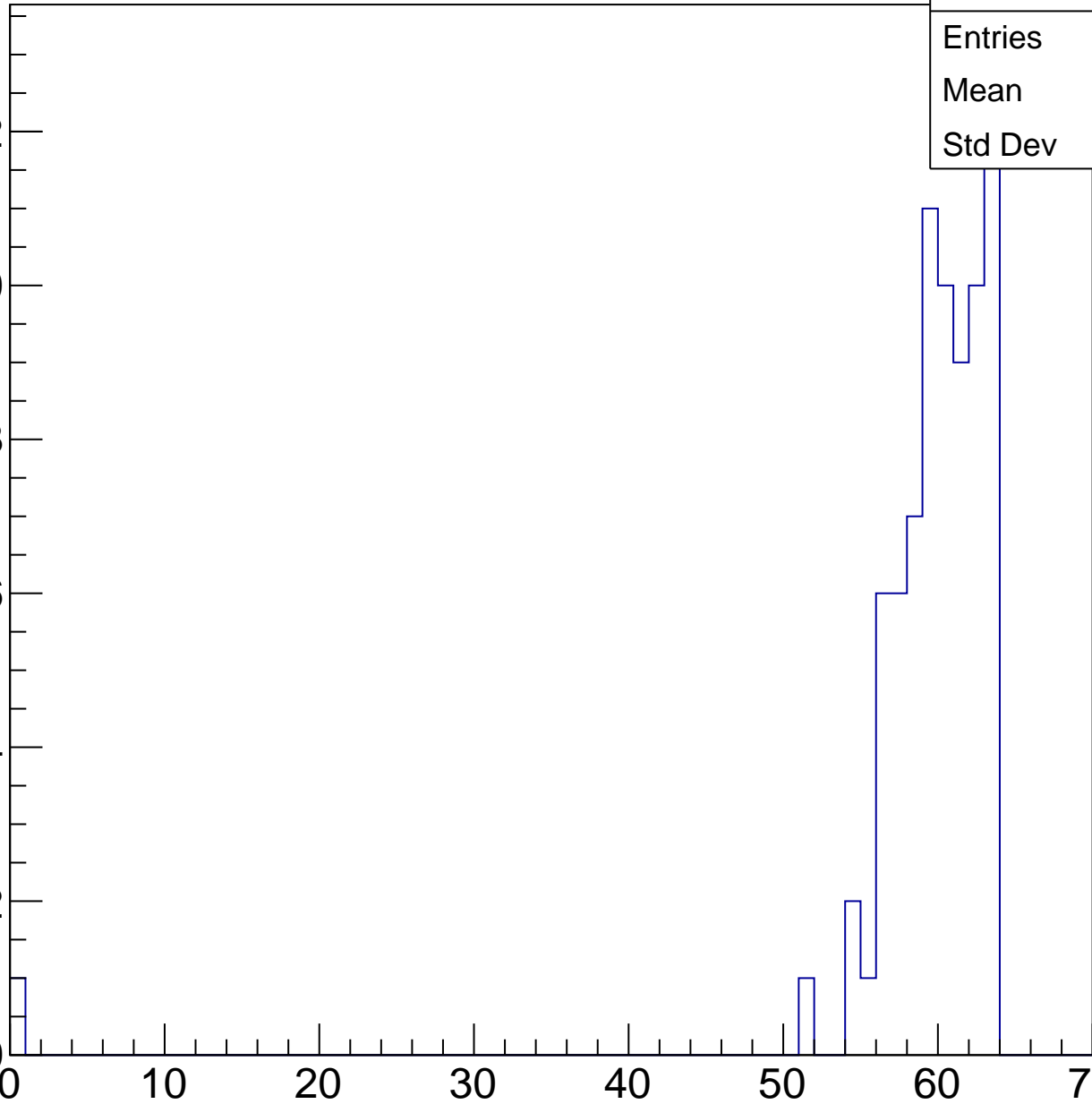
4

2

0

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 58.9  |
| Std Dev | 7.243 |

ampl

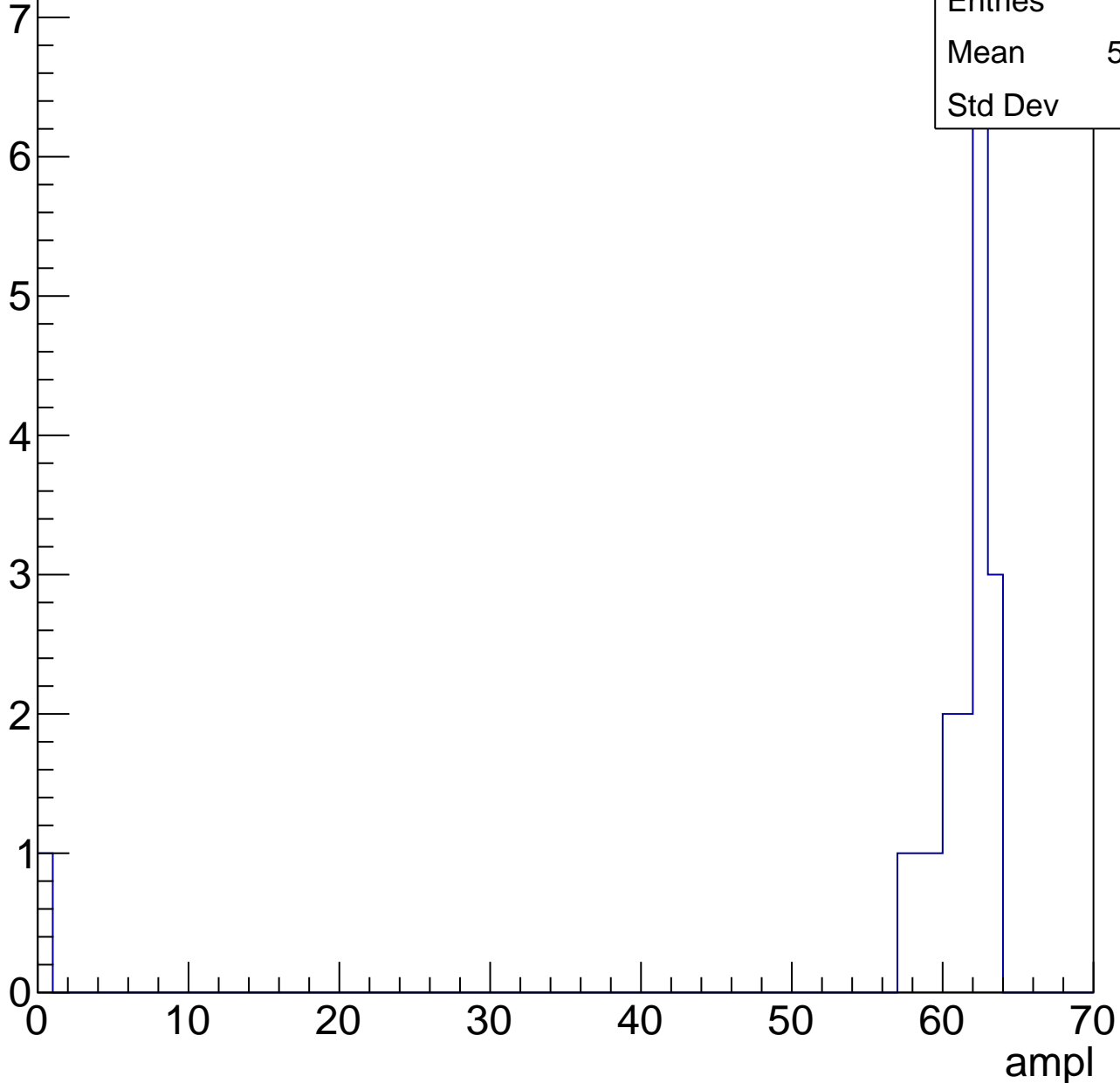


# B0L101S, U16-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 18    |
| Mean    | 57.72 |
| Std Dev | 14.1  |





# B0L101S, U16-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L101S, U16-ch125, adc0

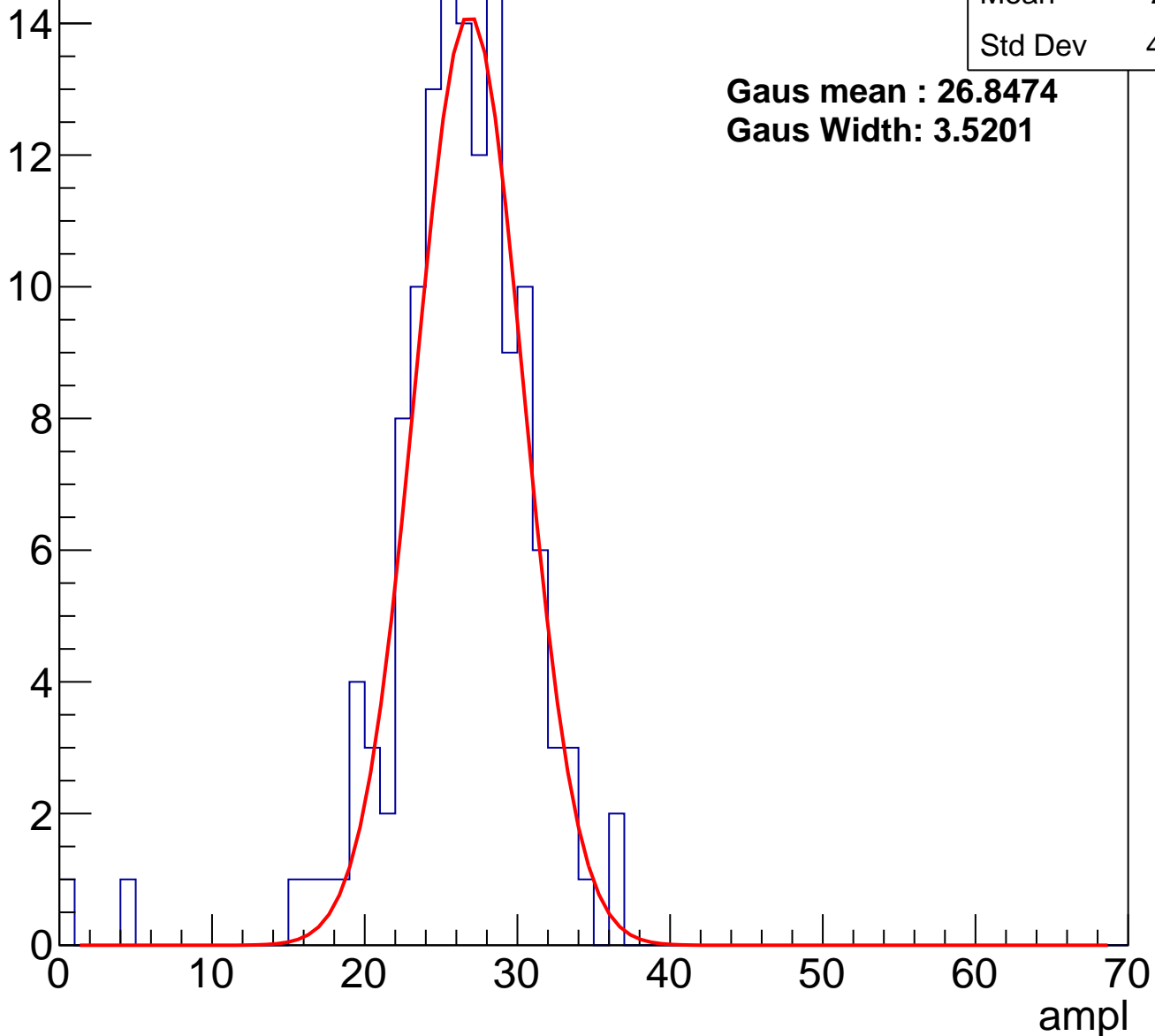
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 136   |
| Mean    | 25.71 |
| Std Dev | 4.802 |

**Gaus mean : 26.8474**

**Gaus Width: 3.5201**

Entry



# B0L101S, U16-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 33.3  |
| Std Dev | 3.503 |

**Gaus mean : 33.7508**

**Gaus Width: 3.2730**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

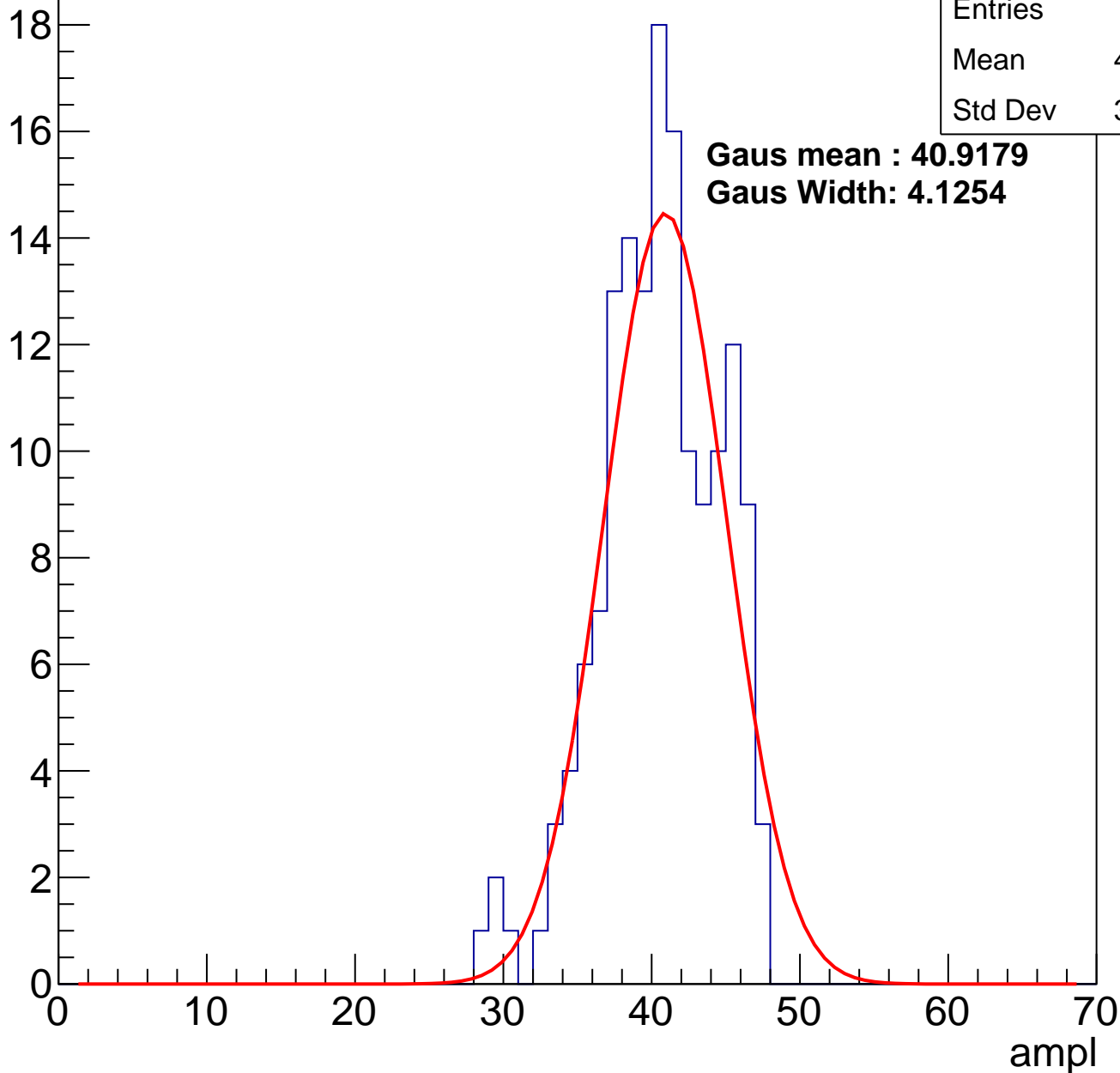
60

70

# B0L101S, U16-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch125, adc3

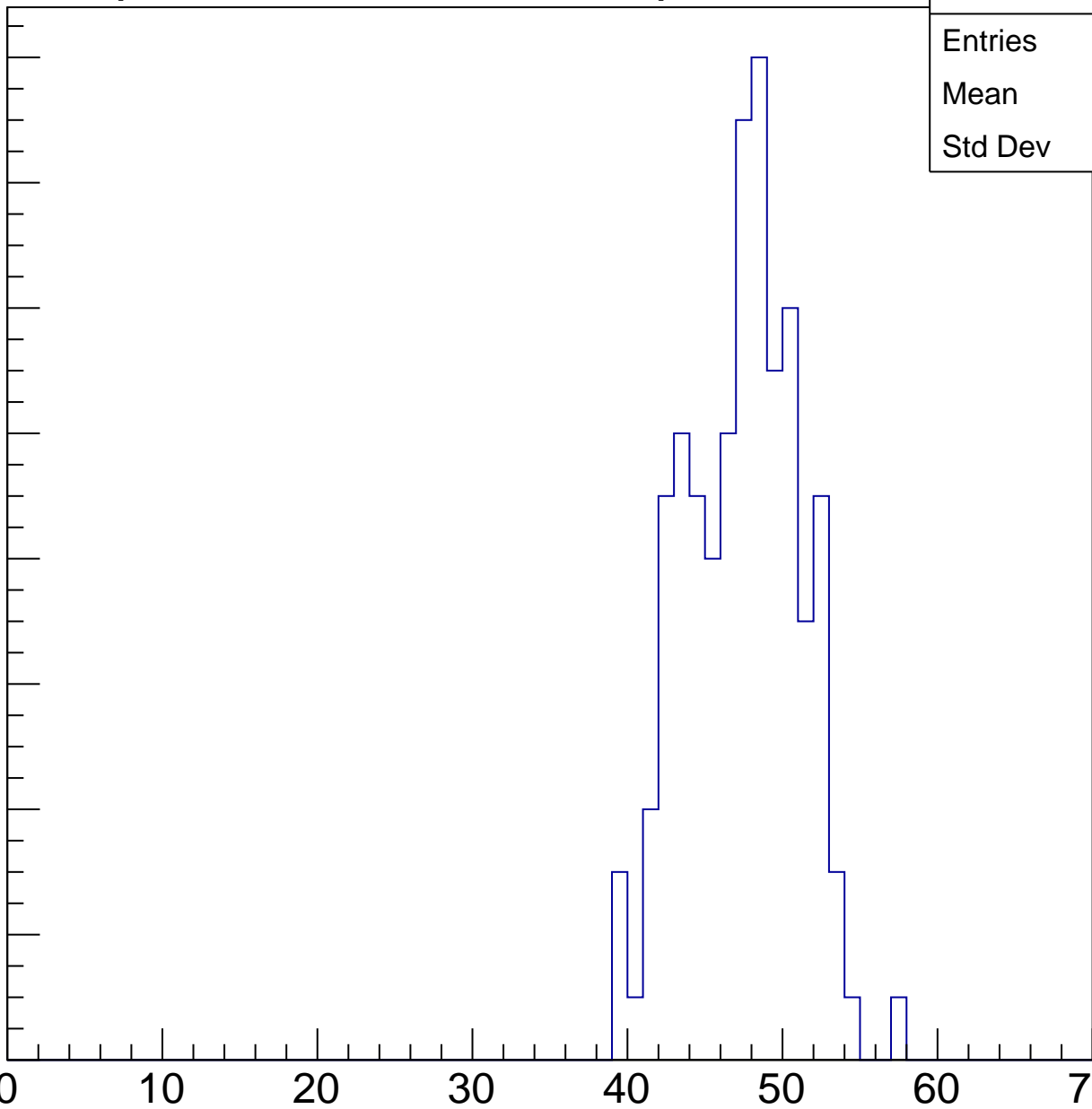
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 46.91 |
| Std Dev | 3.581 |

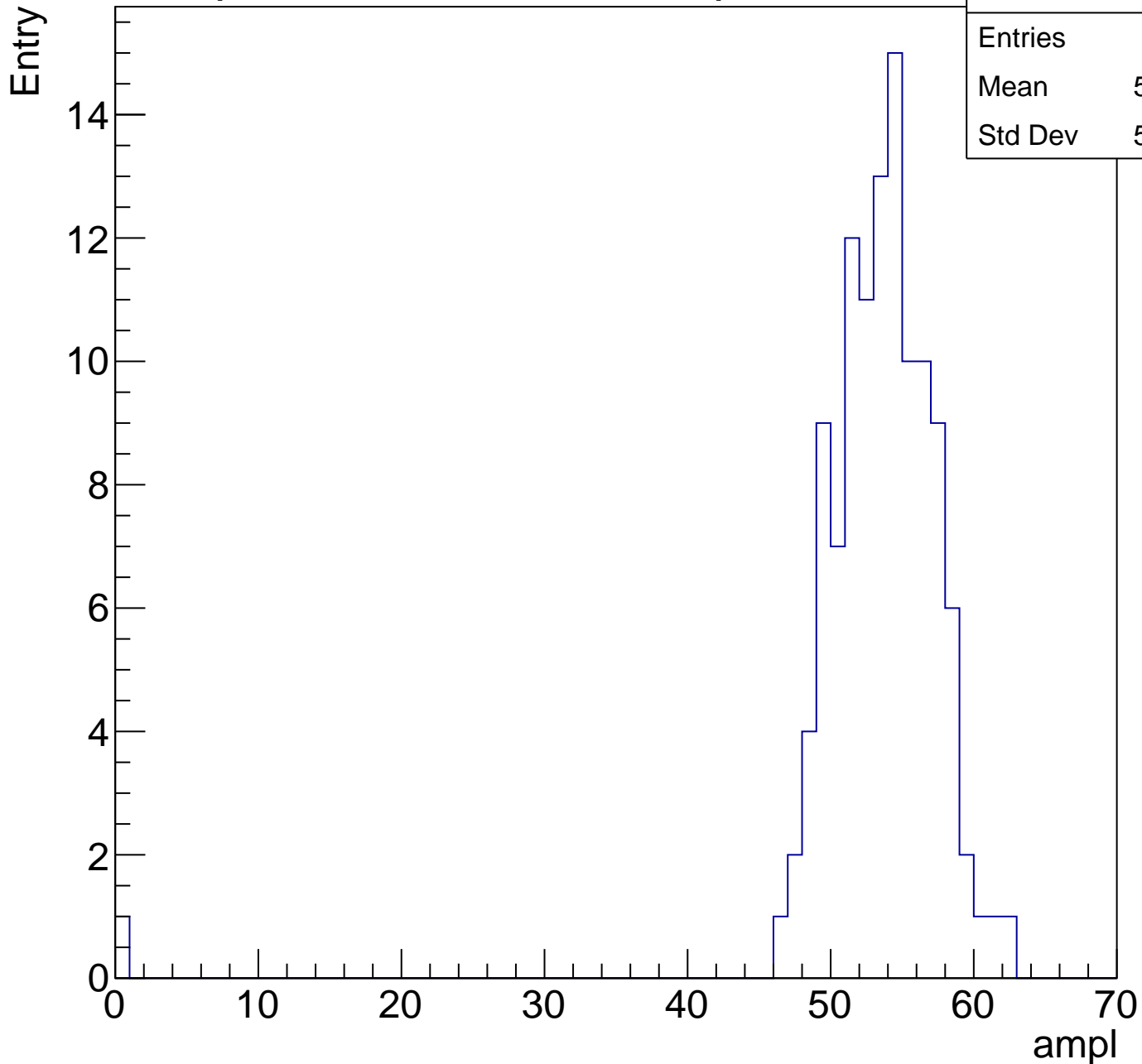
ampl



# B0L101S, U16-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

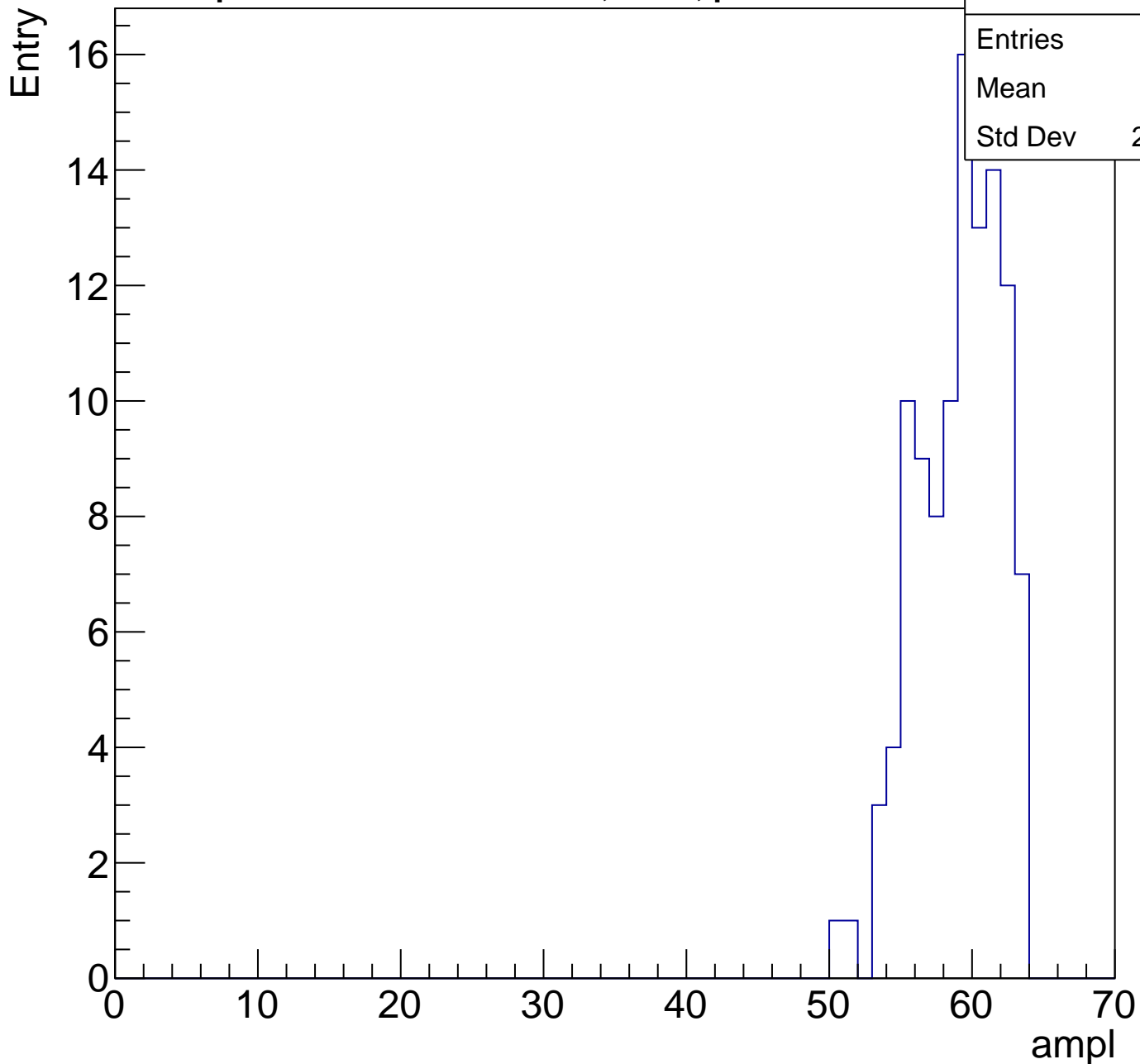
|         |       |
|---------|-------|
| Entries | 115   |
| Mean    | 52.85 |
| Std Dev | 5.897 |



# B0L101S, U16-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 108   |
| Mean    | 58.6  |
| Std Dev | 2.899 |



# B0L101S, U16-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

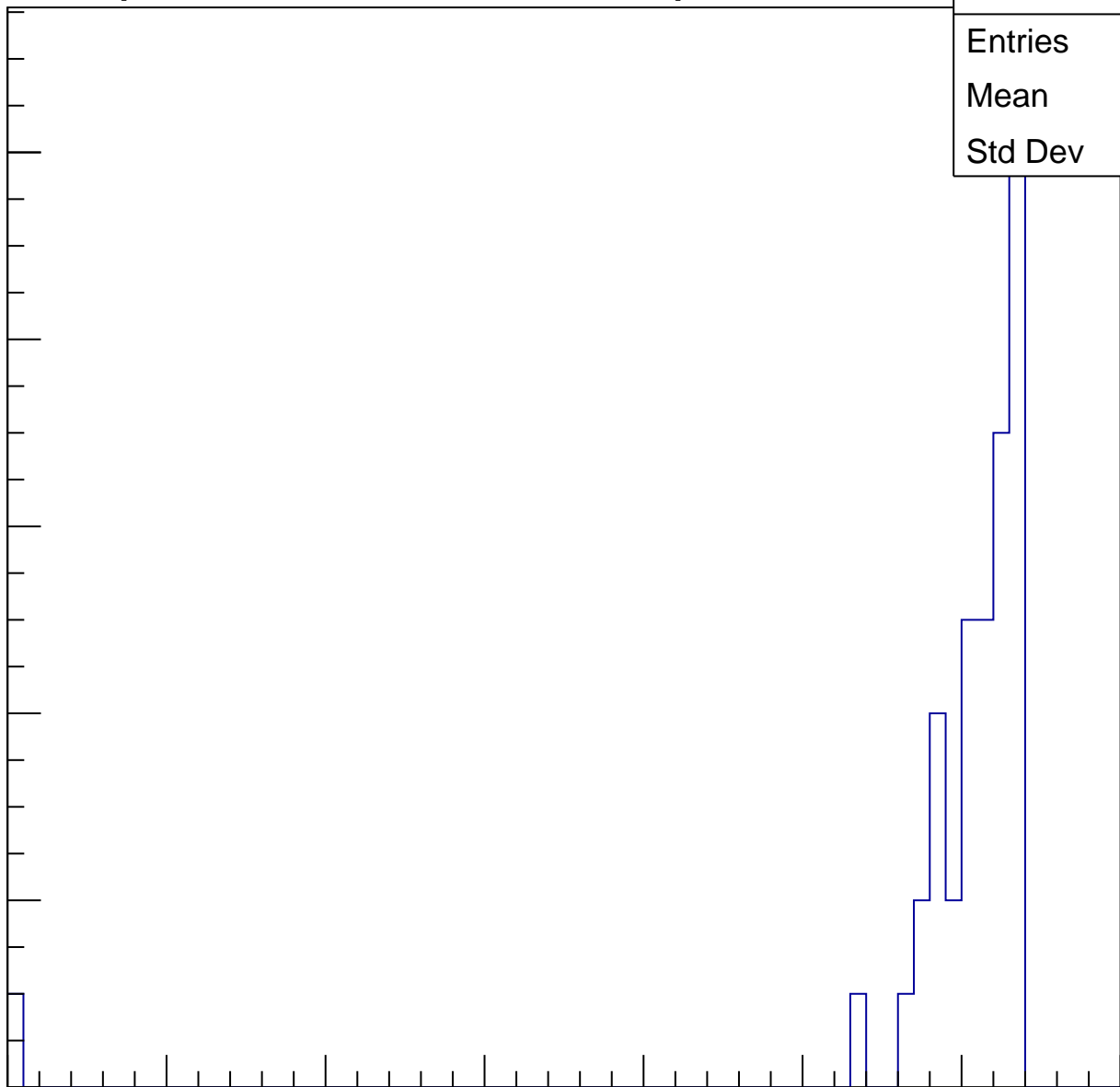
2

0

|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 59.1  |
| Std Dev | 9.873 |

ampl

0 10 20 30 40 50 60 70

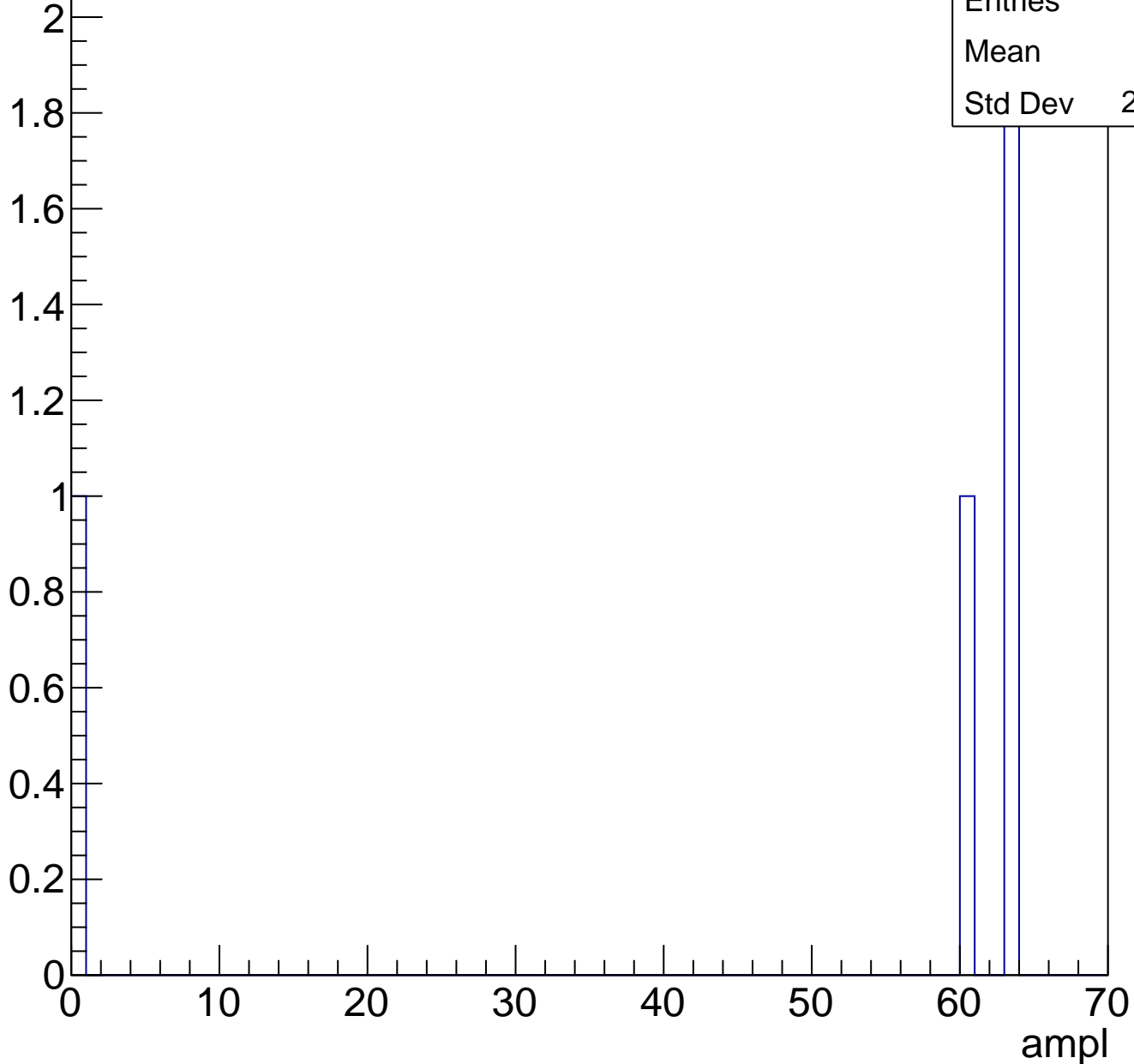




# B0L101S, U16-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch126, adc0

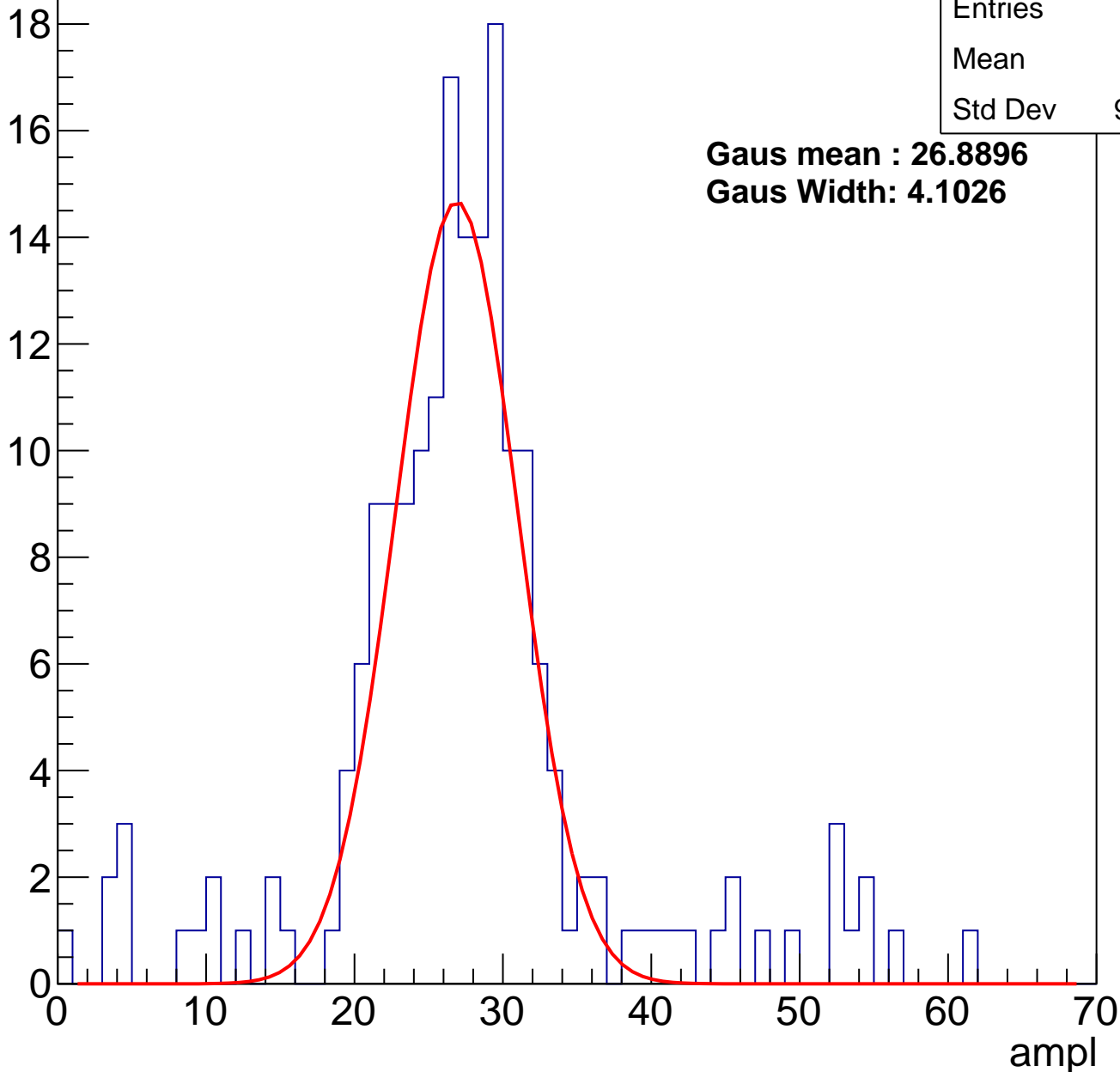
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 189   |
| Mean    | 27.2  |
| Std Dev | 9.359 |

**Gaus mean : 26.8896**

**Gaus Width: 4.1026**

Entry



# B0L101S, U16-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

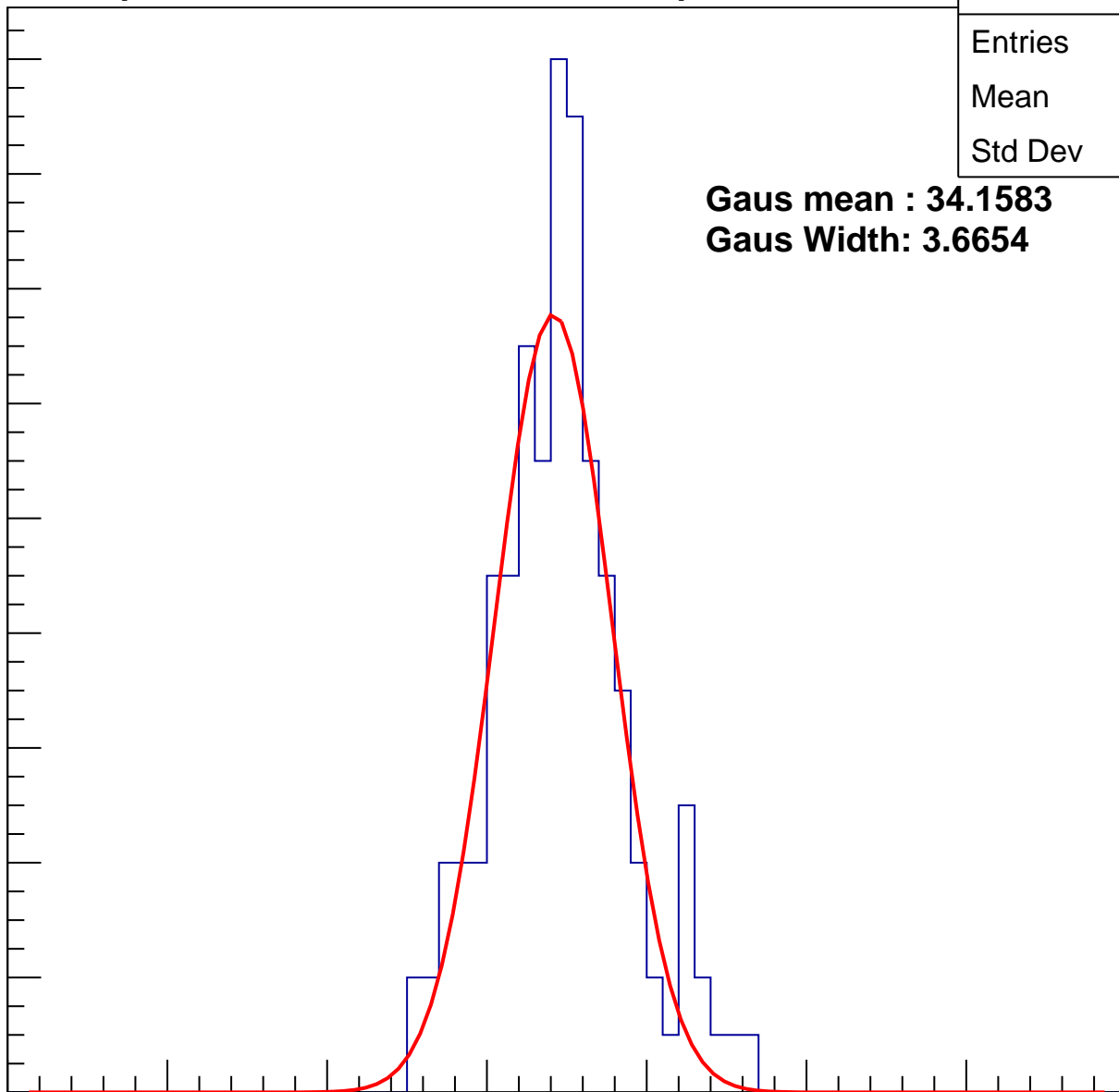
|         |       |
|---------|-------|
| Entries | 137   |
| Mean    | 34.13 |
| Std Dev | 4.131 |

**Gaus mean : 34.1583**

**Gaus Width: 3.6654**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch126, adc2

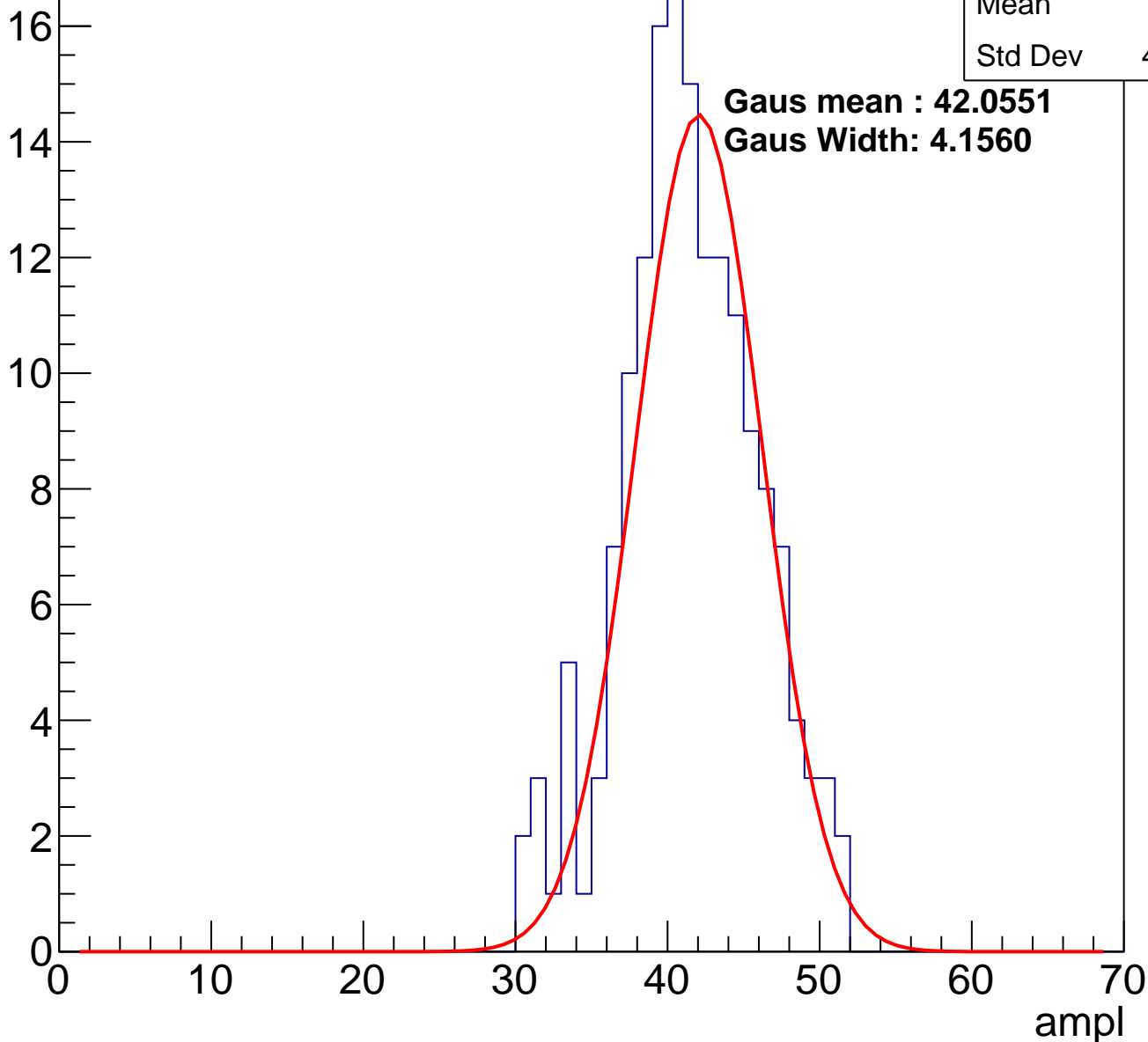
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

|         |       |
|---------|-------|
| Entries | 163   |
| Mean    | 41    |
| Std Dev | 4.438 |

**Gaus mean : 42.0551**

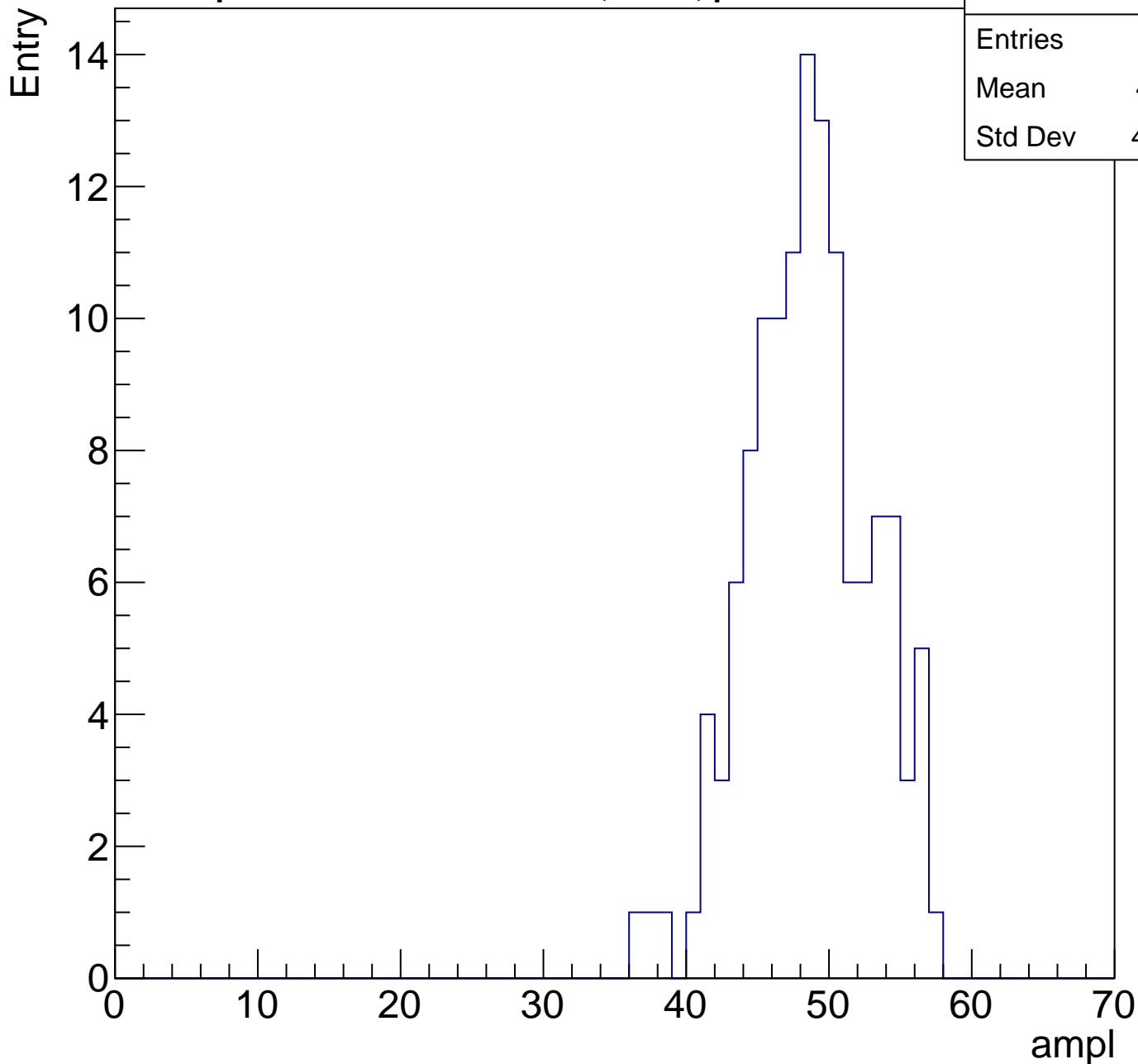
**Gaus Width: 4.1560**



# B0L101S, U16-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

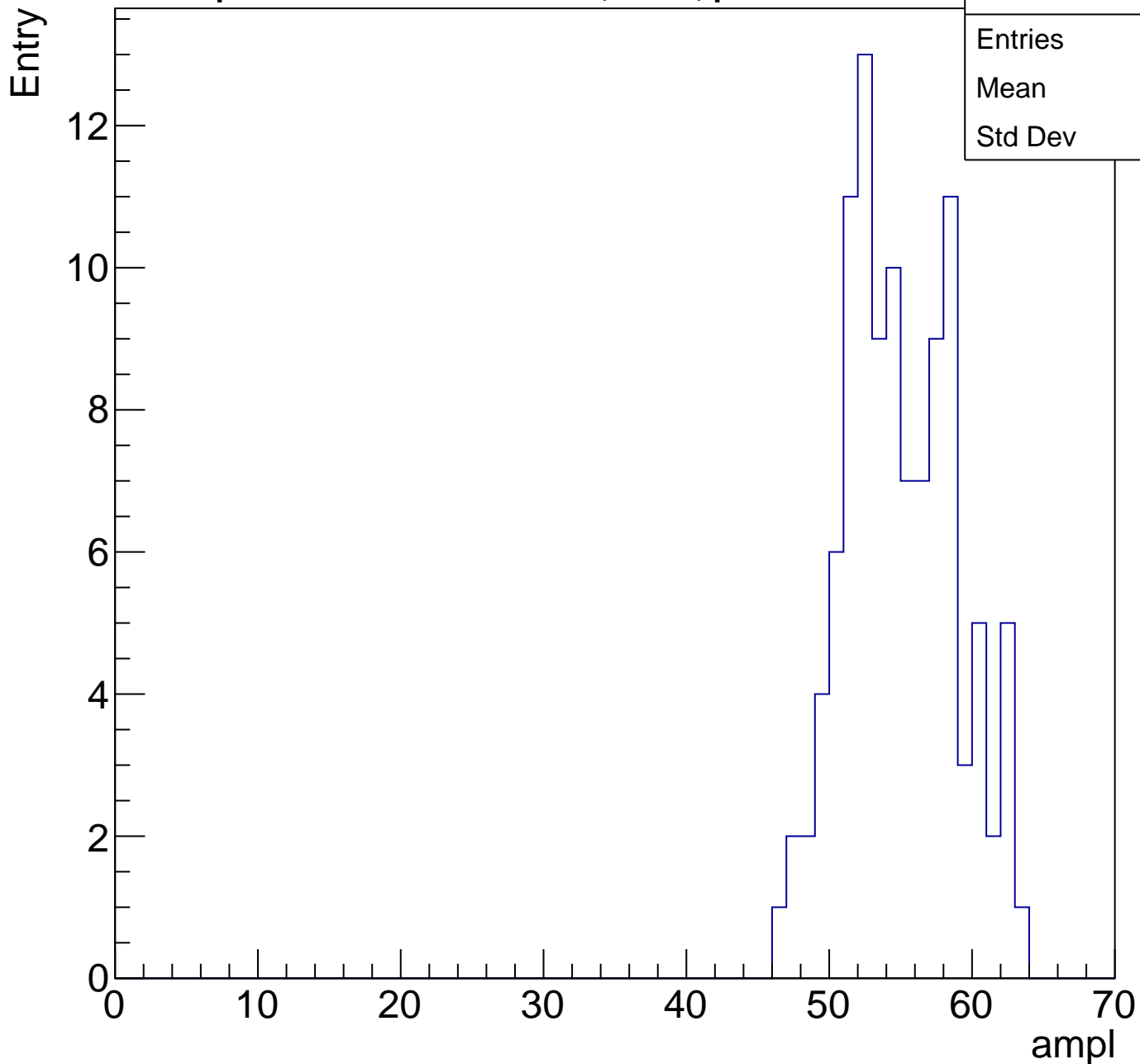
|         |       |
|---------|-------|
| Entries | 129   |
| Mean    | 48.11 |
| Std Dev | 4.252 |



# B0L101S, U16-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |      |
|---------|------|
| Entries | 108  |
| Mean    | 54.5 |
| Std Dev | 3.86 |



# B0L101S, U16-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 57.04 |
| Std Dev | 9.082 |

Entry

12

10

8

6

4

2

0

0

10

20

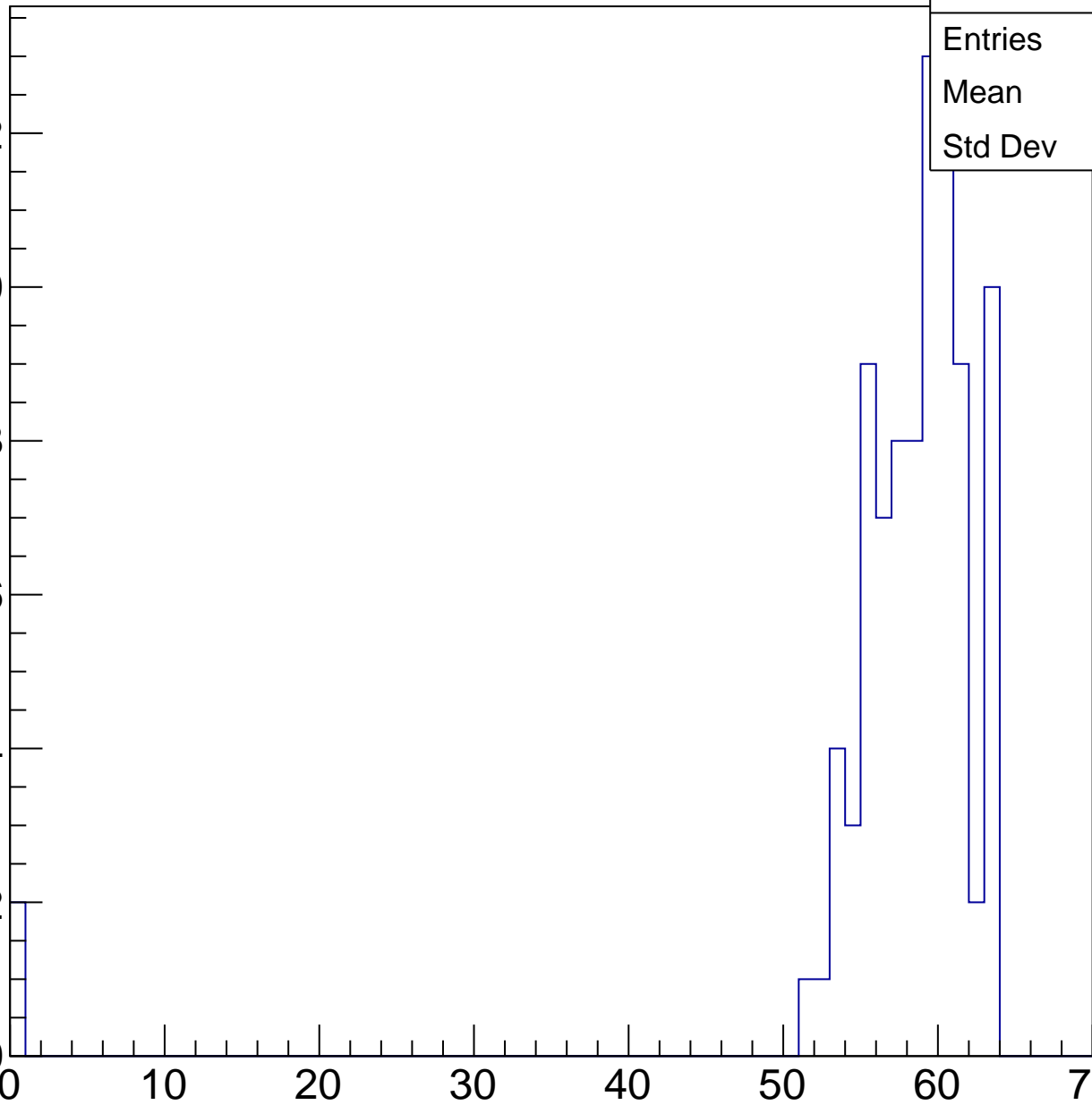
30

40

50

60

ampl



# B0L101S, U16-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

37

Mean

60.73

Std Dev

2.009

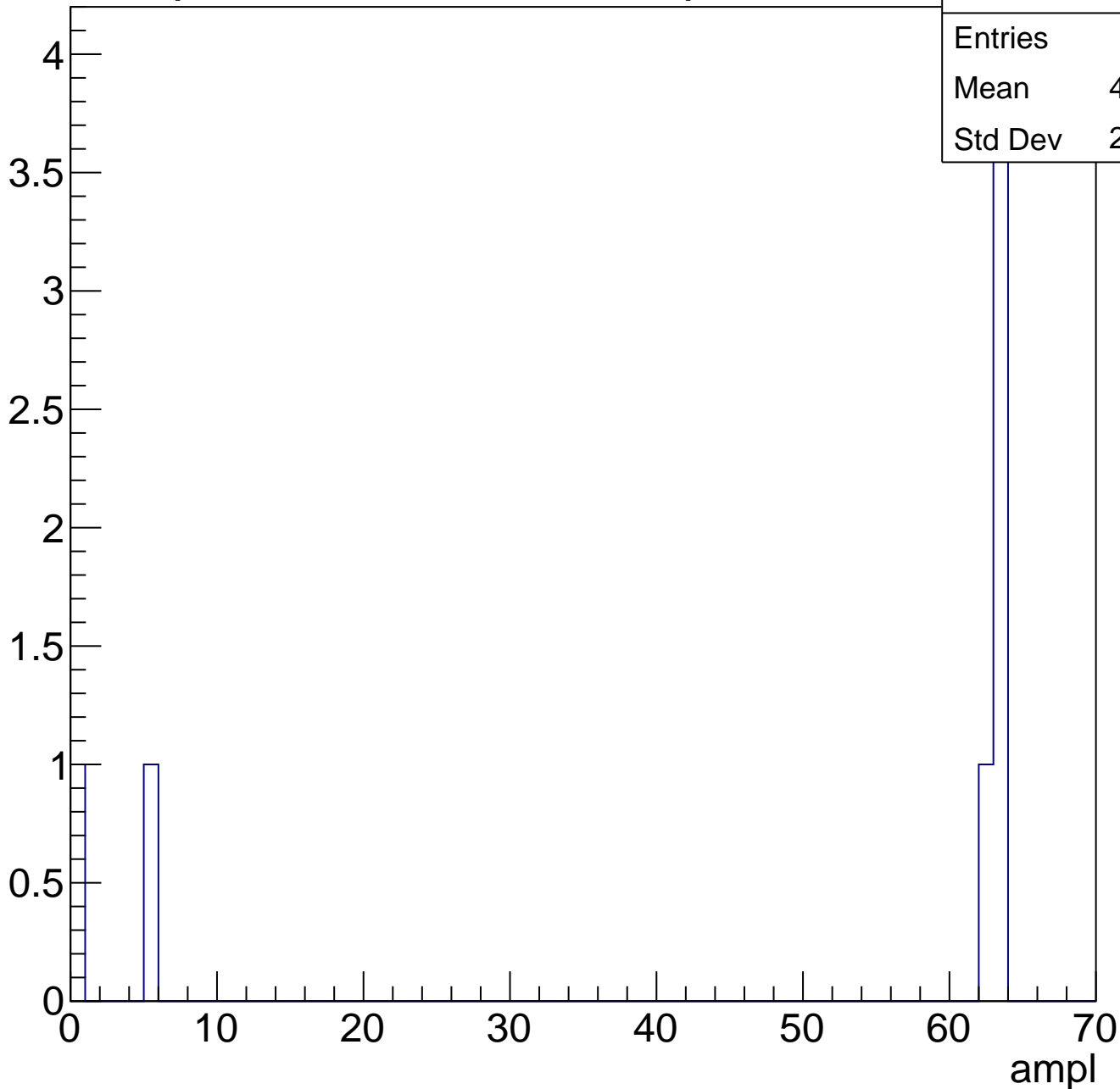
|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 60.73 |
| Std Dev | 2.009 |



# B0L101S, U16-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U16-ch127, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 148   |
| Mean    | 29.29 |
| Std Dev | 7.503 |

**Gaus mean : 28.9393**

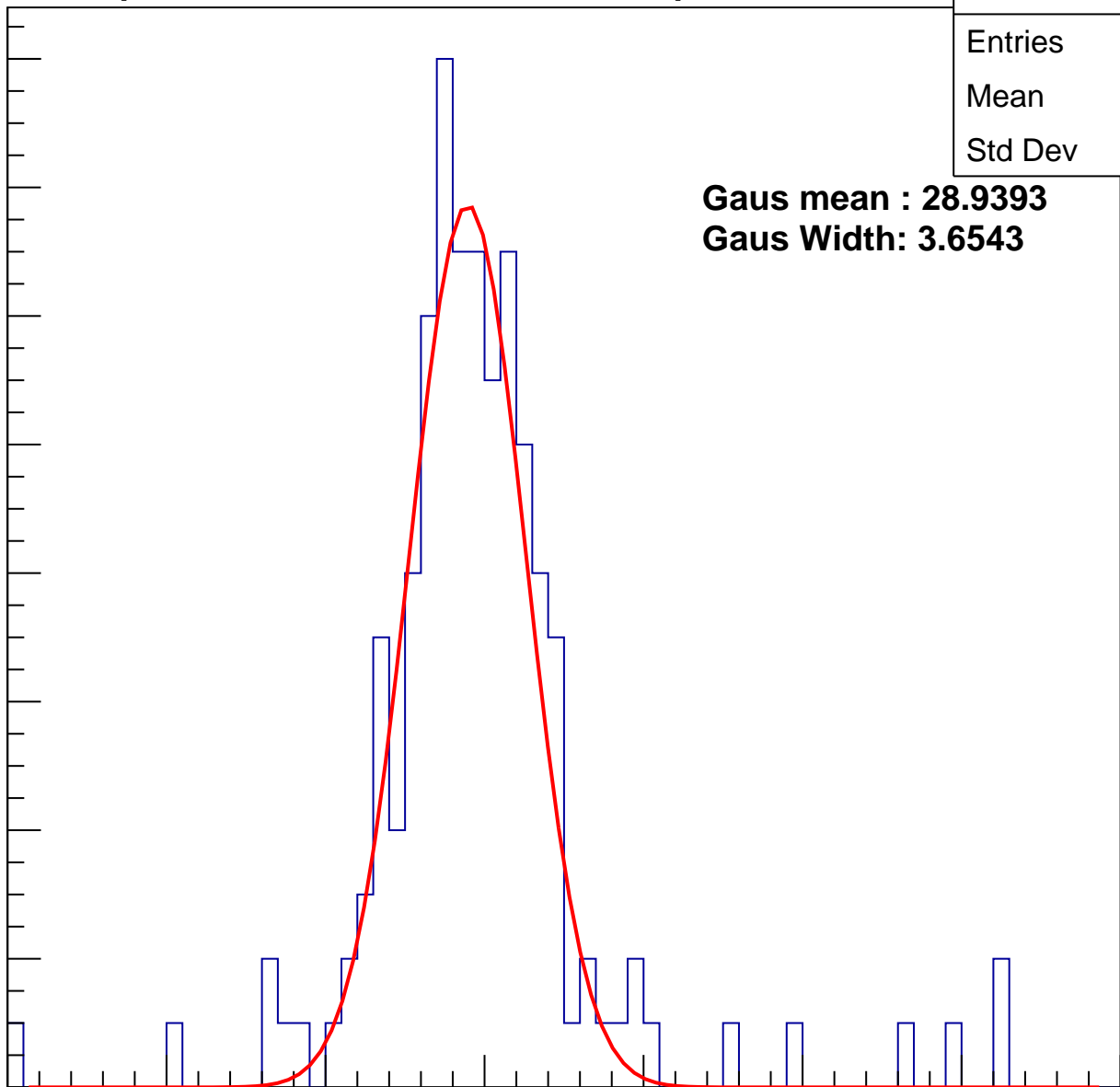
**Gaus Width: 3.6543**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U16-ch127, adc1

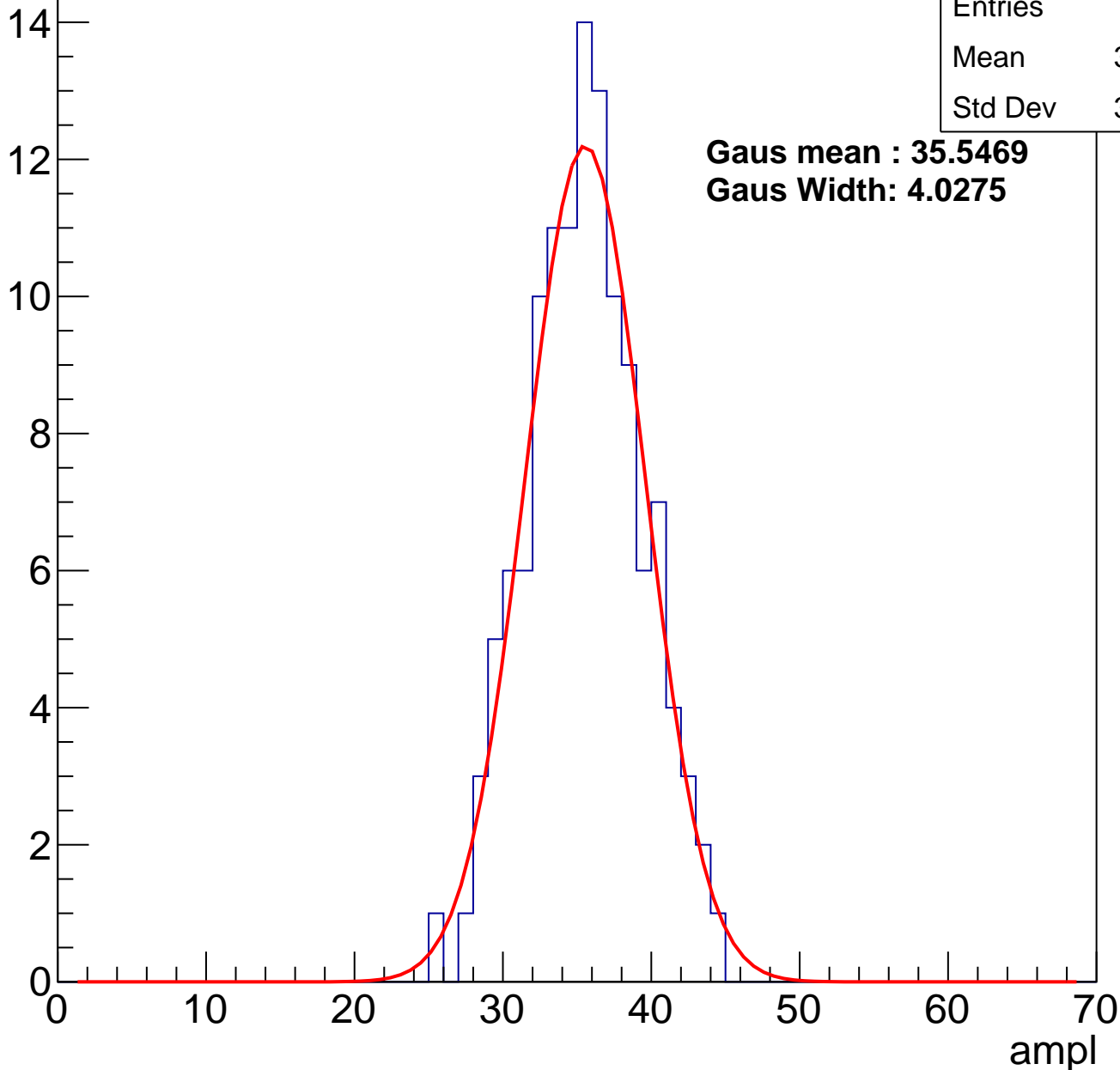
calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 123   |
| Mean    | 35.02 |
| Std Dev | 3.812 |

**Gaus mean : 35.5469**

**Gaus Width: 4.0275**

Entry



# B0L101S, U16-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

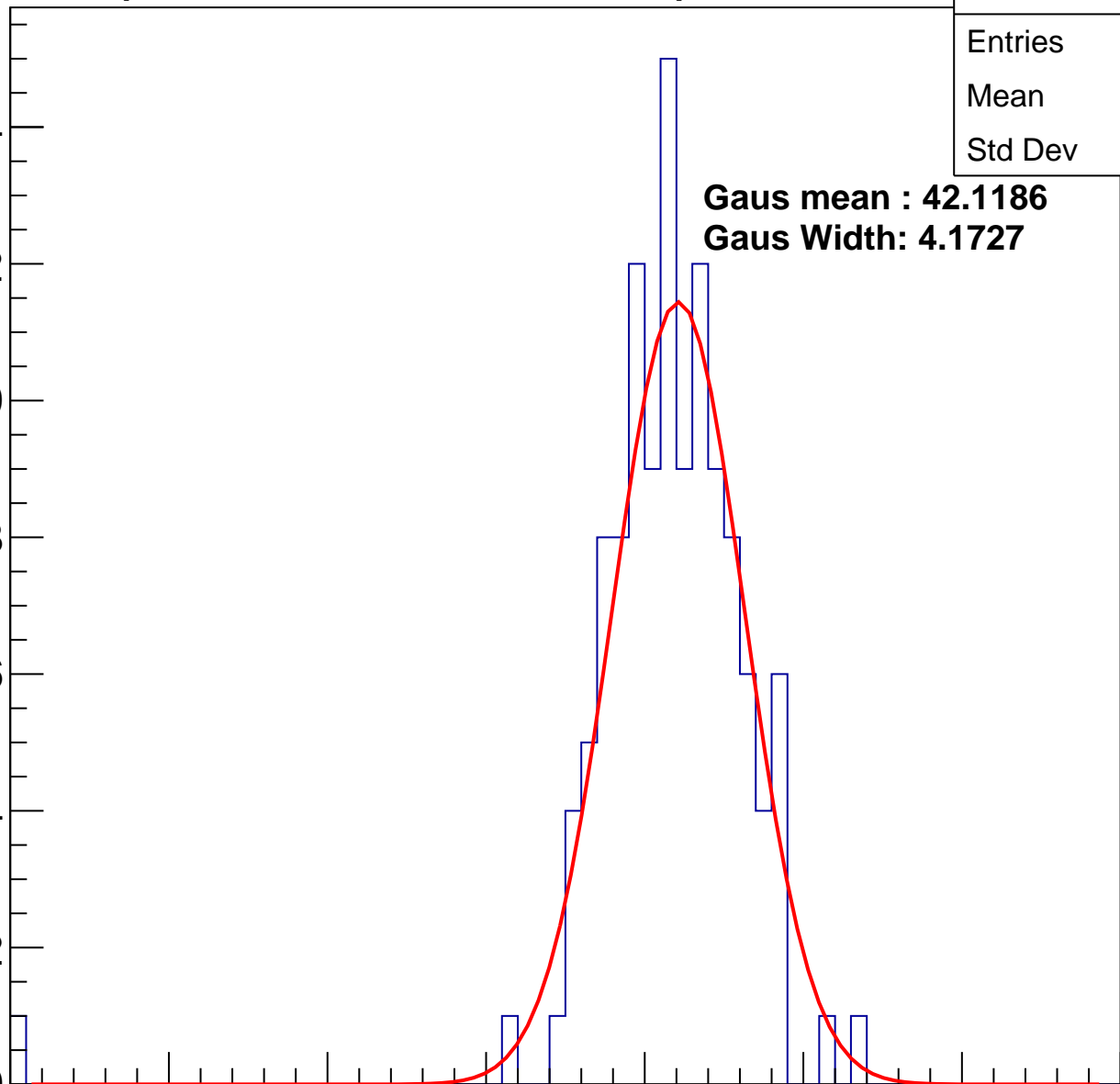
70

ampl

|         |       |
|---------|-------|
| Entries | 120   |
| Mean    | 41.12 |
| Std Dev | 5.364 |

**Gaus mean : 42.1186**

**Gaus Width: 4.1727**



# B0L101S, U16-ch127, adc3

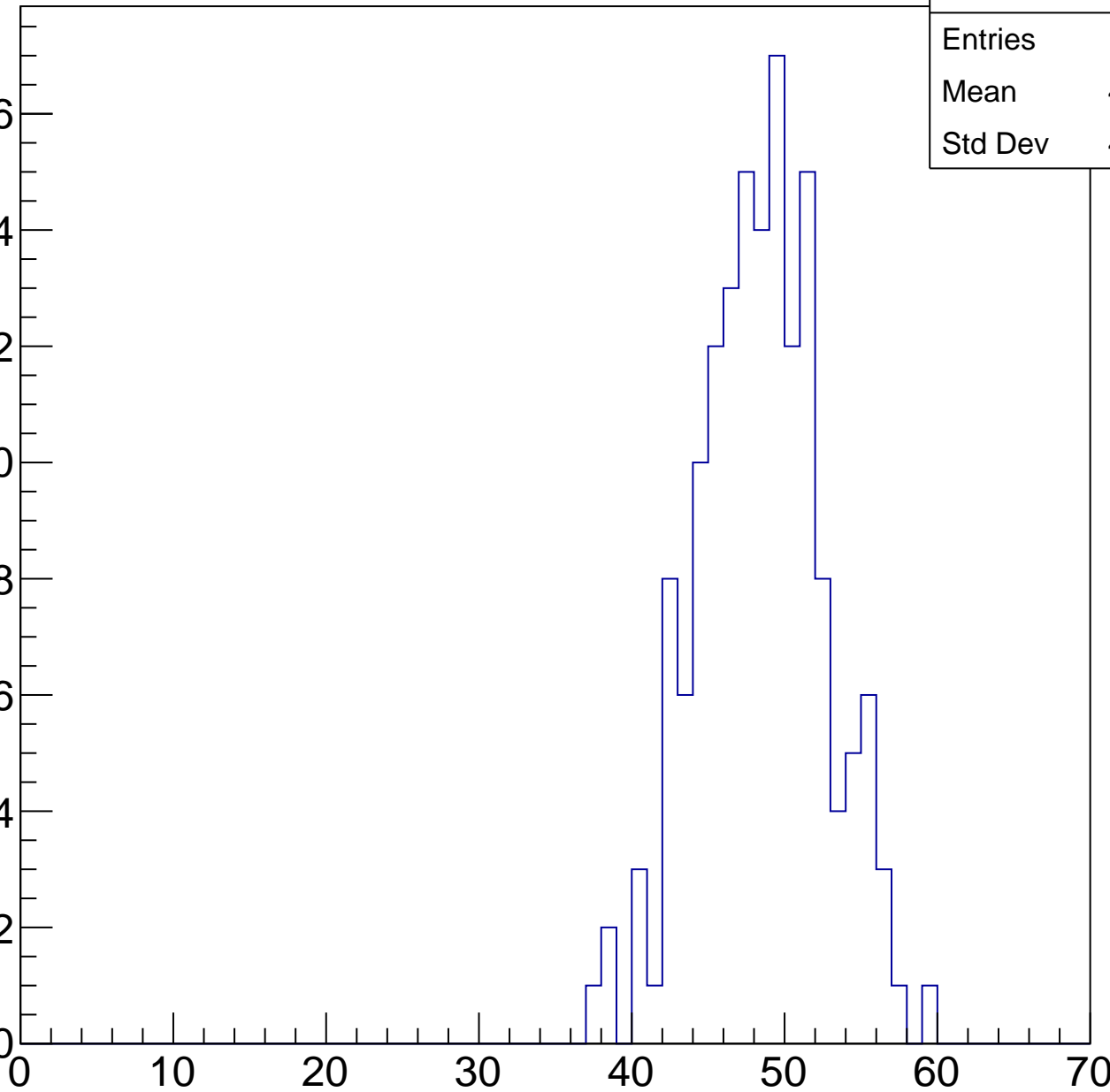
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 157   |
| Mean    | 47.98 |
| Std Dev | 4.094 |

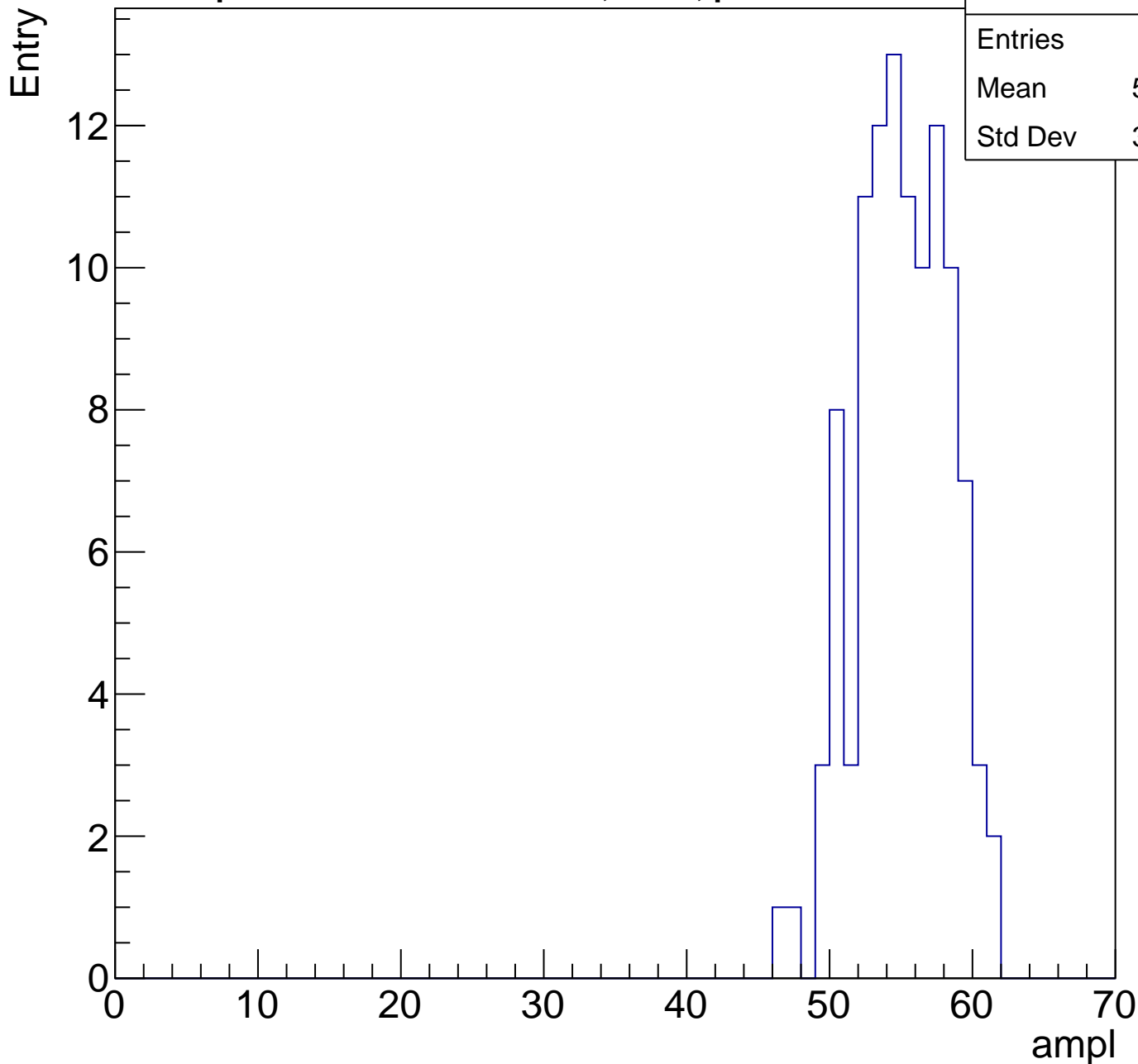
ampl



# B0L101S, U16-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 54.64 |
| Std Dev | 3.139 |



# B0L101S, U16-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

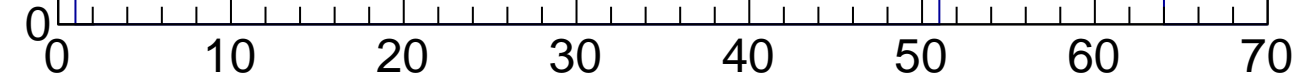
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 107   |
| Mean    | 58.47 |
| Std Dev | 8.527 |

ampl

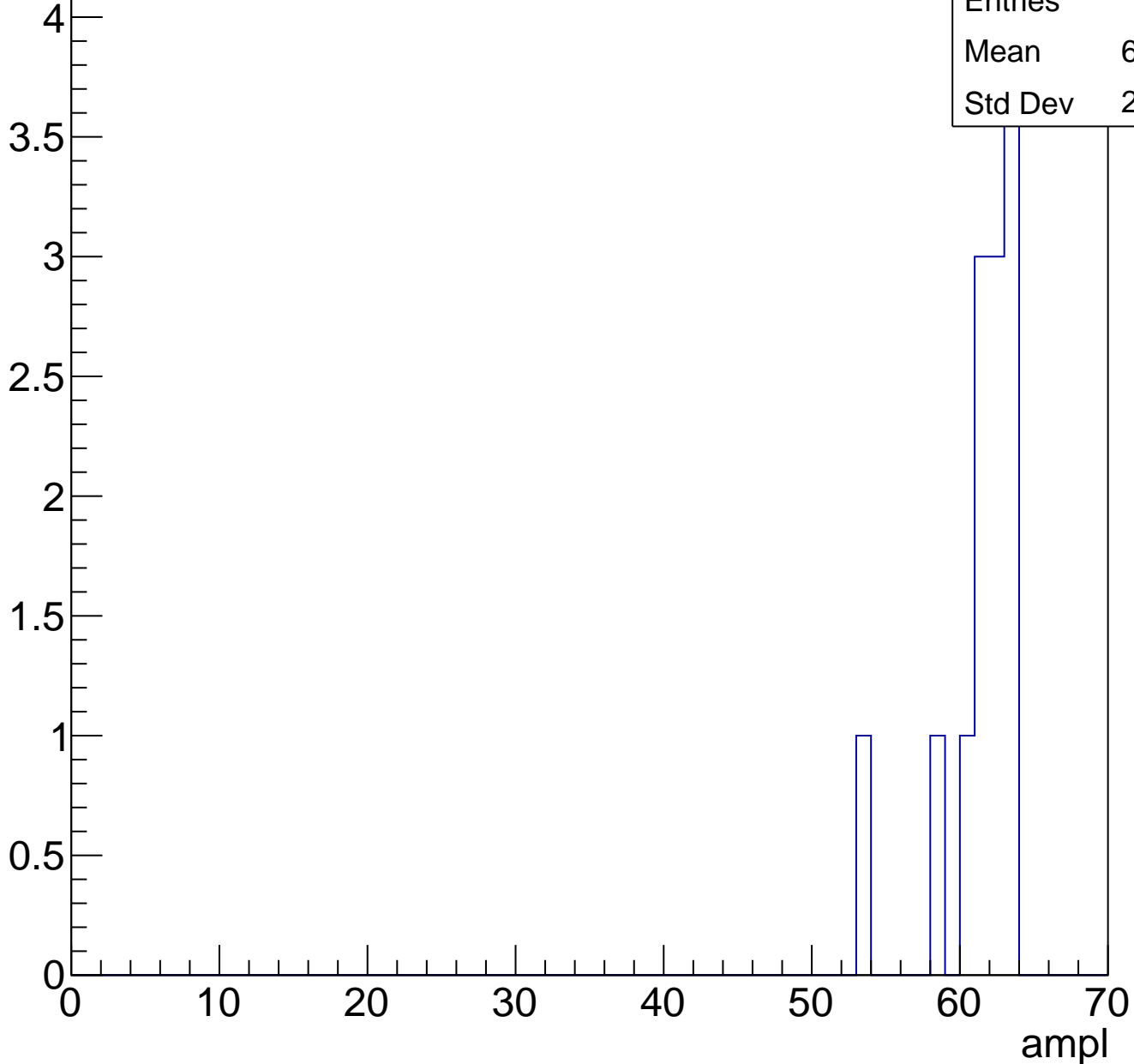
0 10 20 30 40 50 60 70



# B0L101S, U16-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

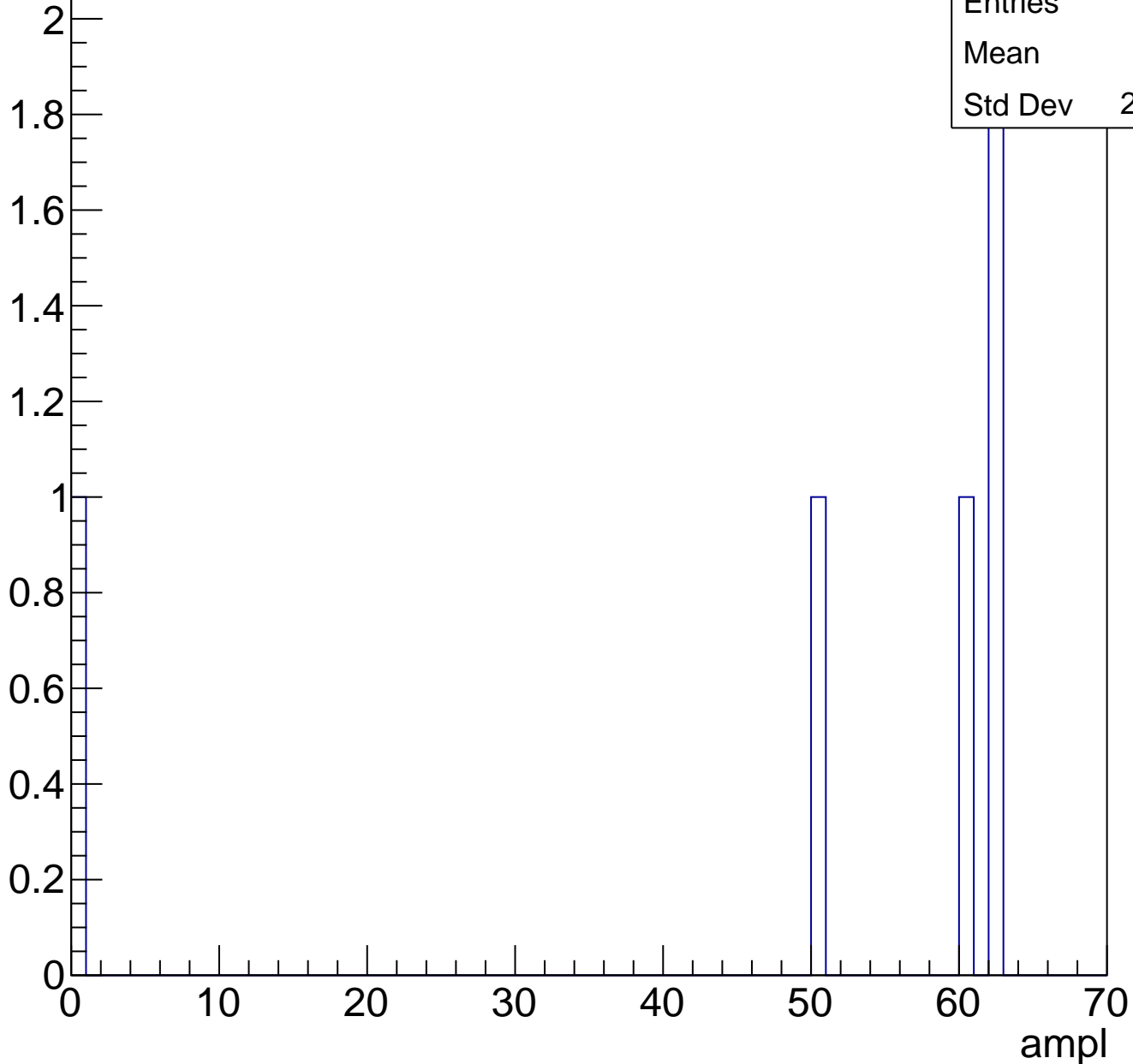




# B0L101S, U16-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

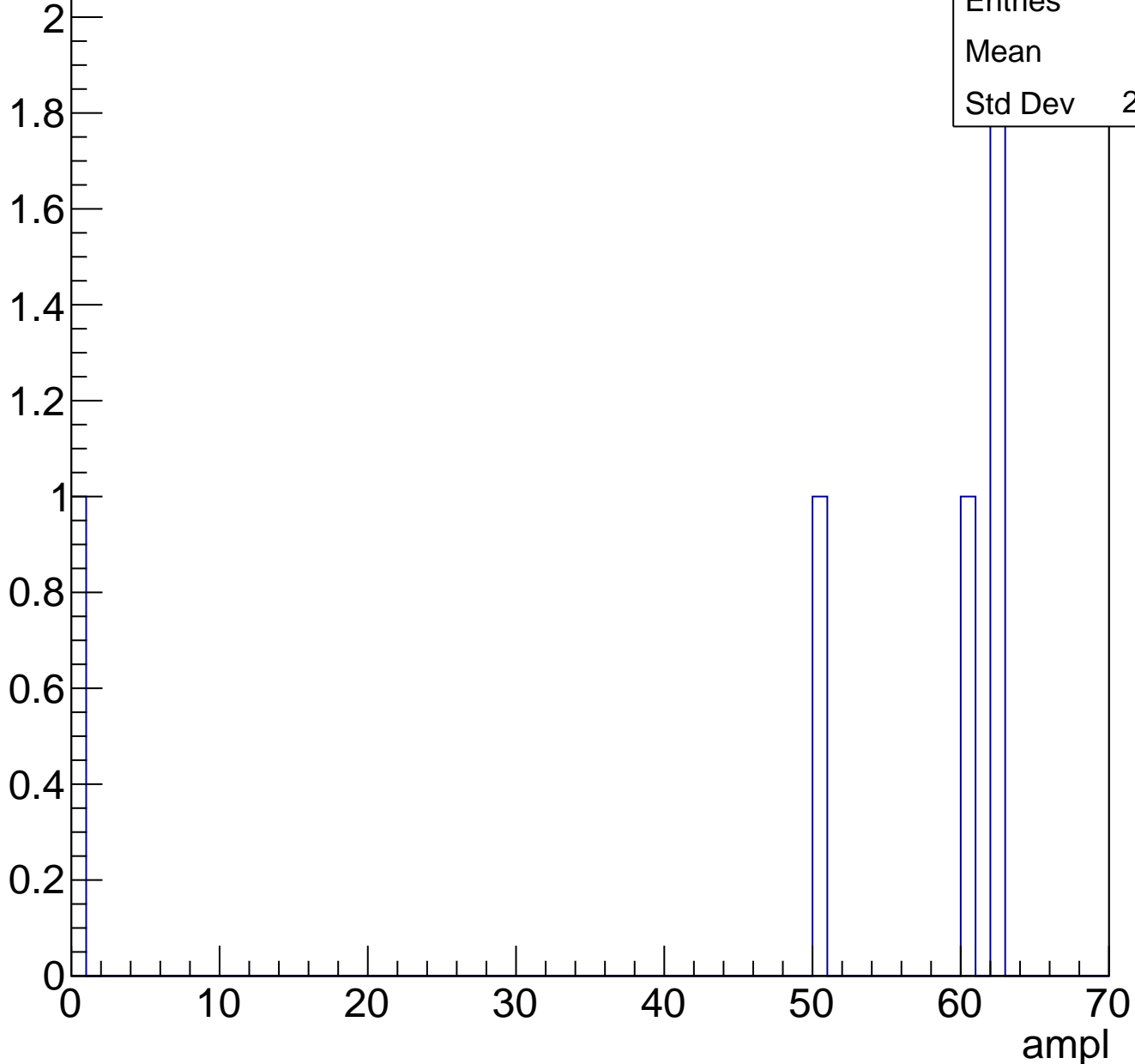


|         |       |
|---------|-------|
| Entries | 5     |
| Mean    | 46.8  |
| Std Dev | 23.82 |

# B0L101S, U16-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



|         |       |
|---------|-------|
| Entries | 5     |
| Mean    | 46.8  |
| Std Dev | 23.82 |