

B0L102S, U13-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.59
Std Dev	12.19

Turn on : 25.8272

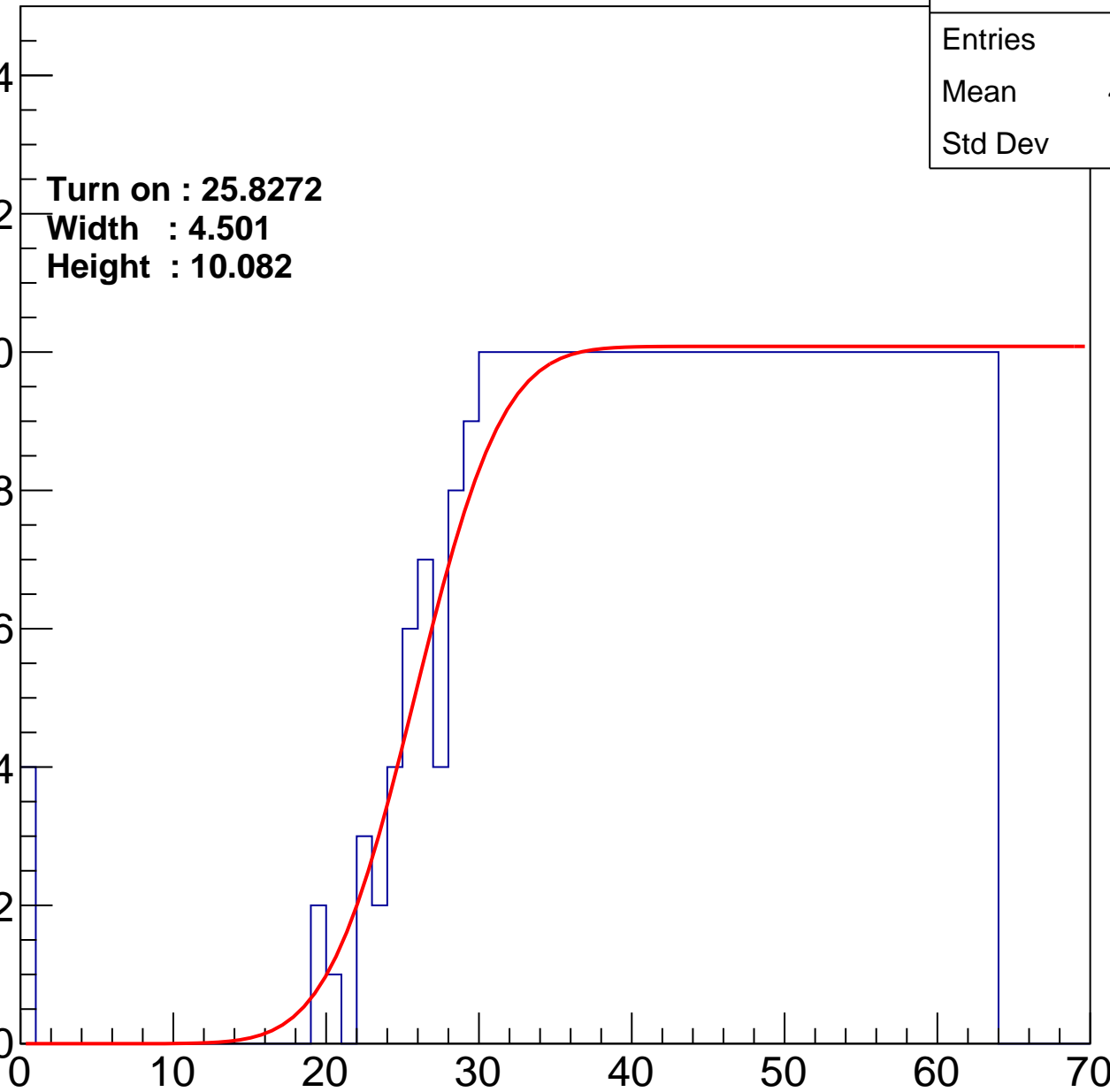
Width : 4.501

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.3
Std Dev	11.69

Turn on : 27.1447

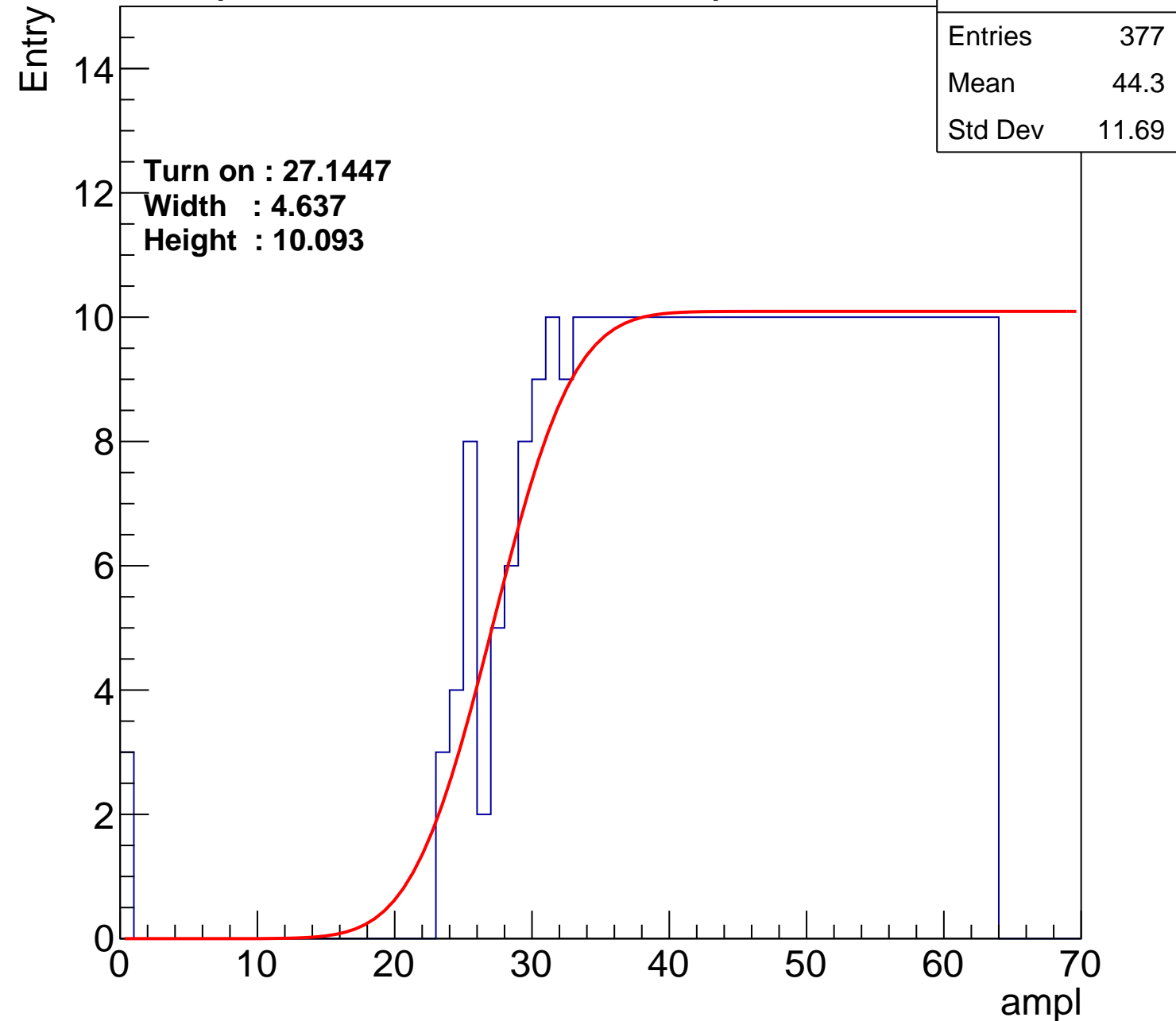
Width : 4.637

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.27
Std Dev	12.23

Turn on : 25.3266

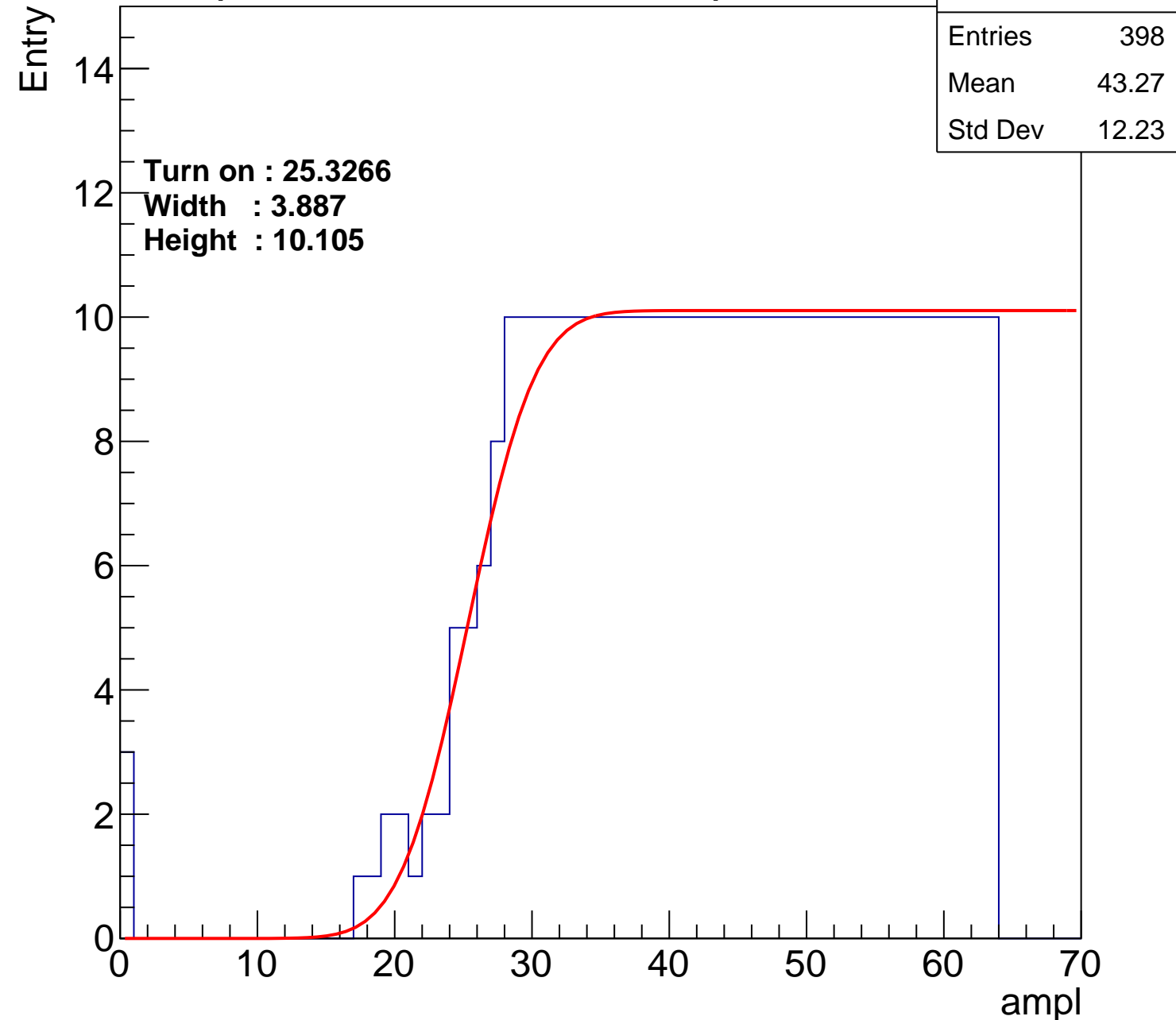
Width : 3.887

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.1472

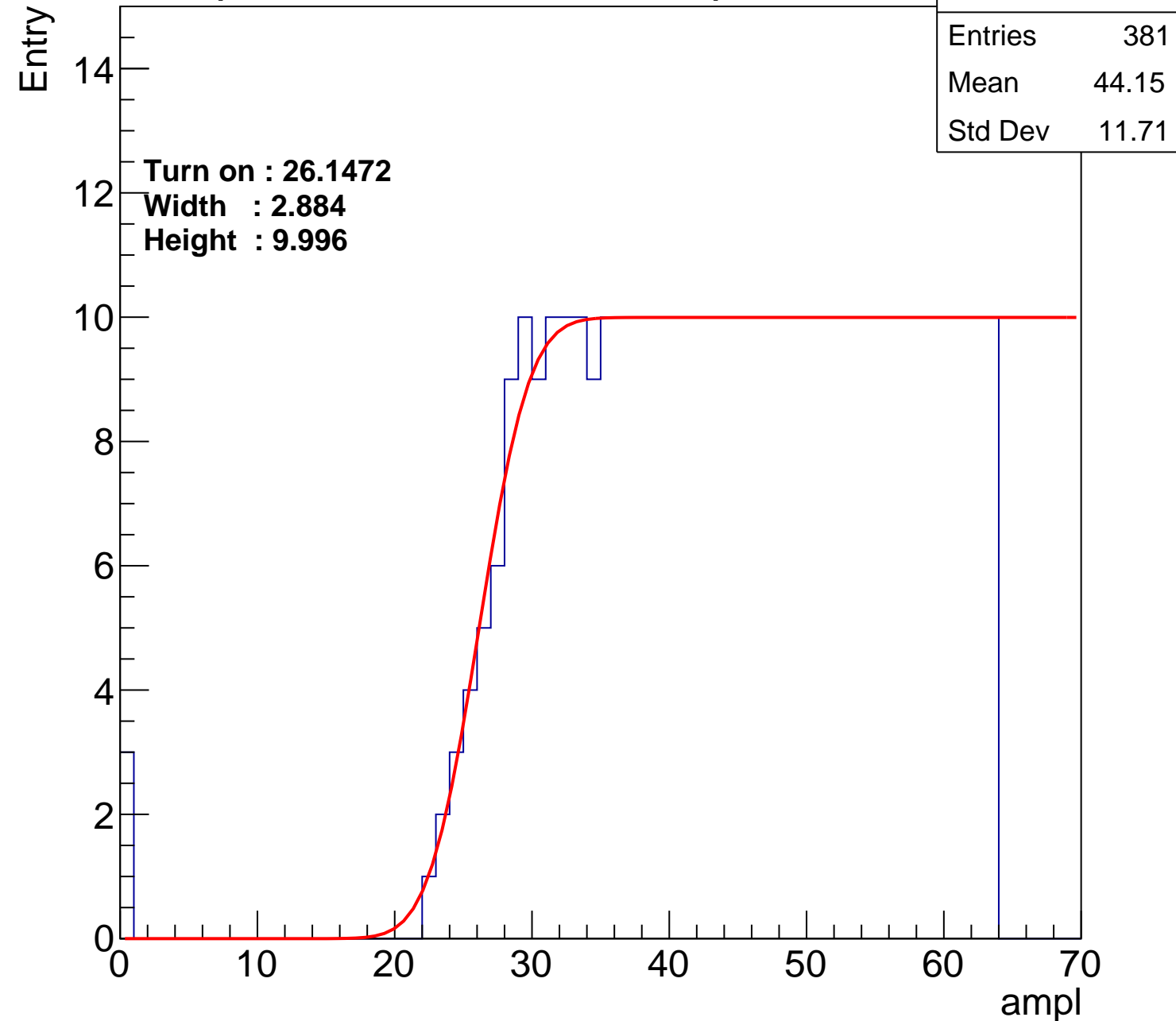
Width : 2.884

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.43
Std Dev	11.32

Turn on : 26.1454

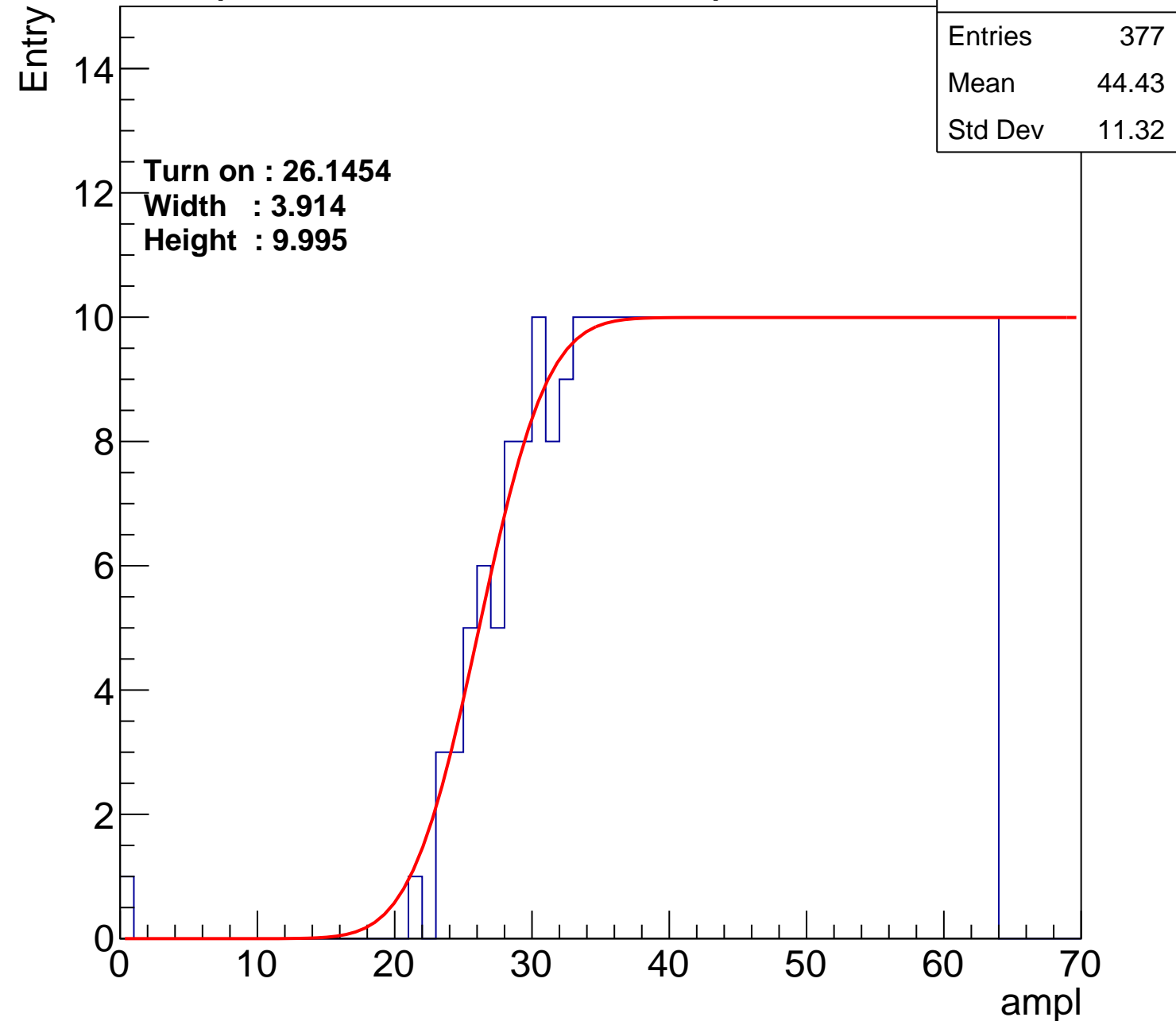
Width : 3.914

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.7257

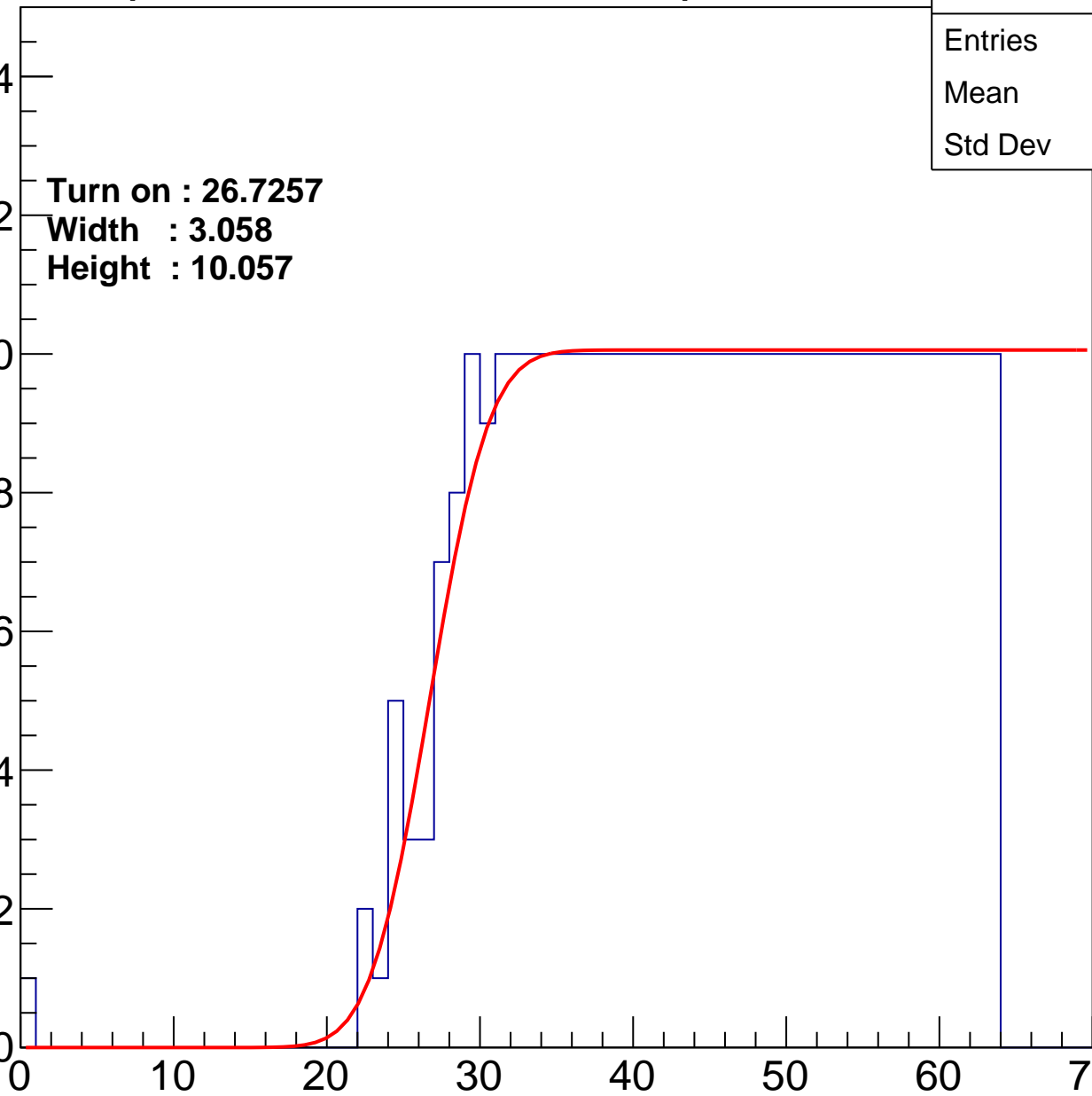
Width : 3.058

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch6

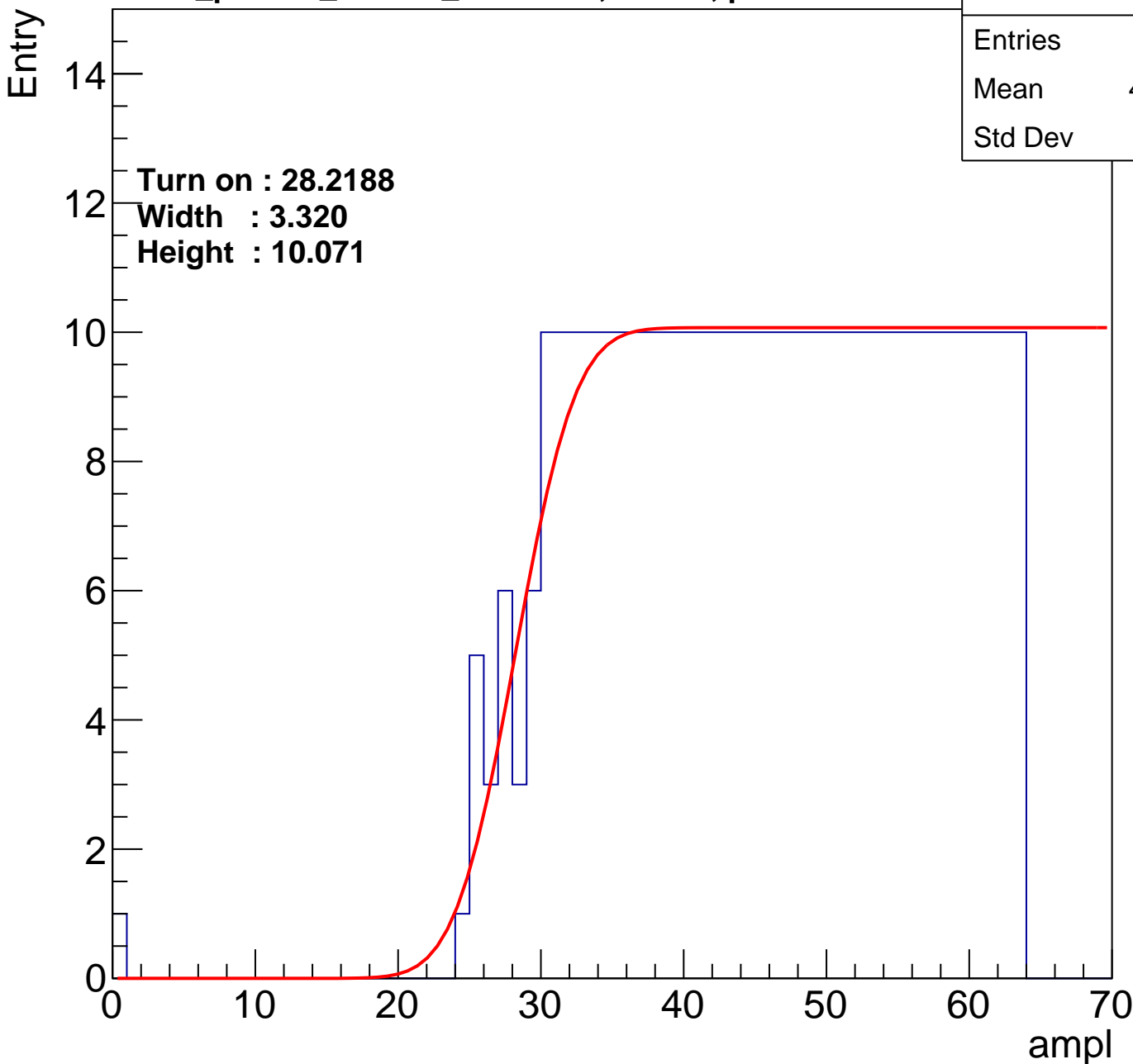
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.2188

Width : 3.320

Height : 10.071

Entries	365
Mean	45.09
Std Dev	10.9



B0L102S, U13-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.43
Std Dev	10.71

Turn on : 28.5846

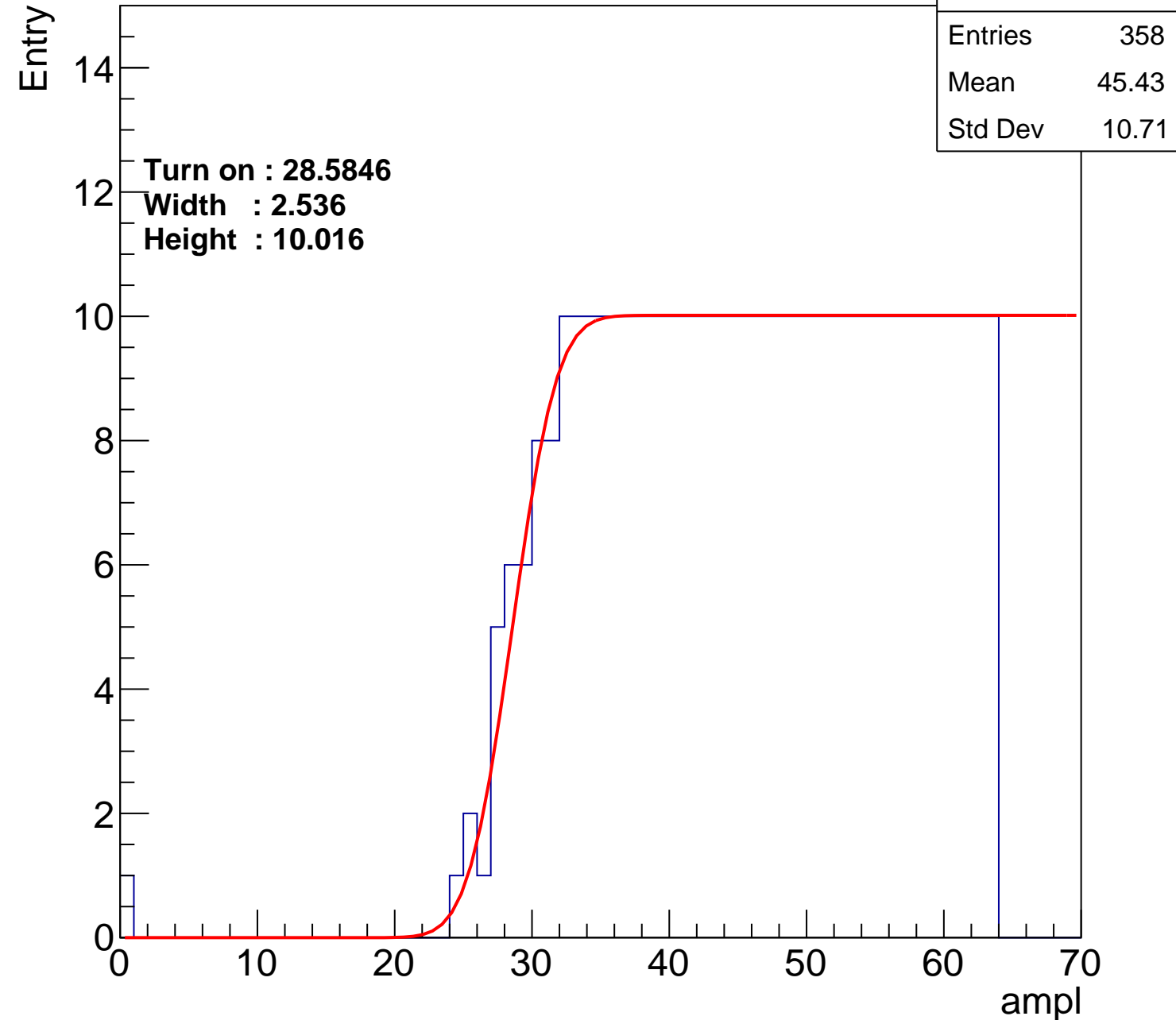
Width : 2.536

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.33
Std Dev	11.59

Turn on : 26.8160

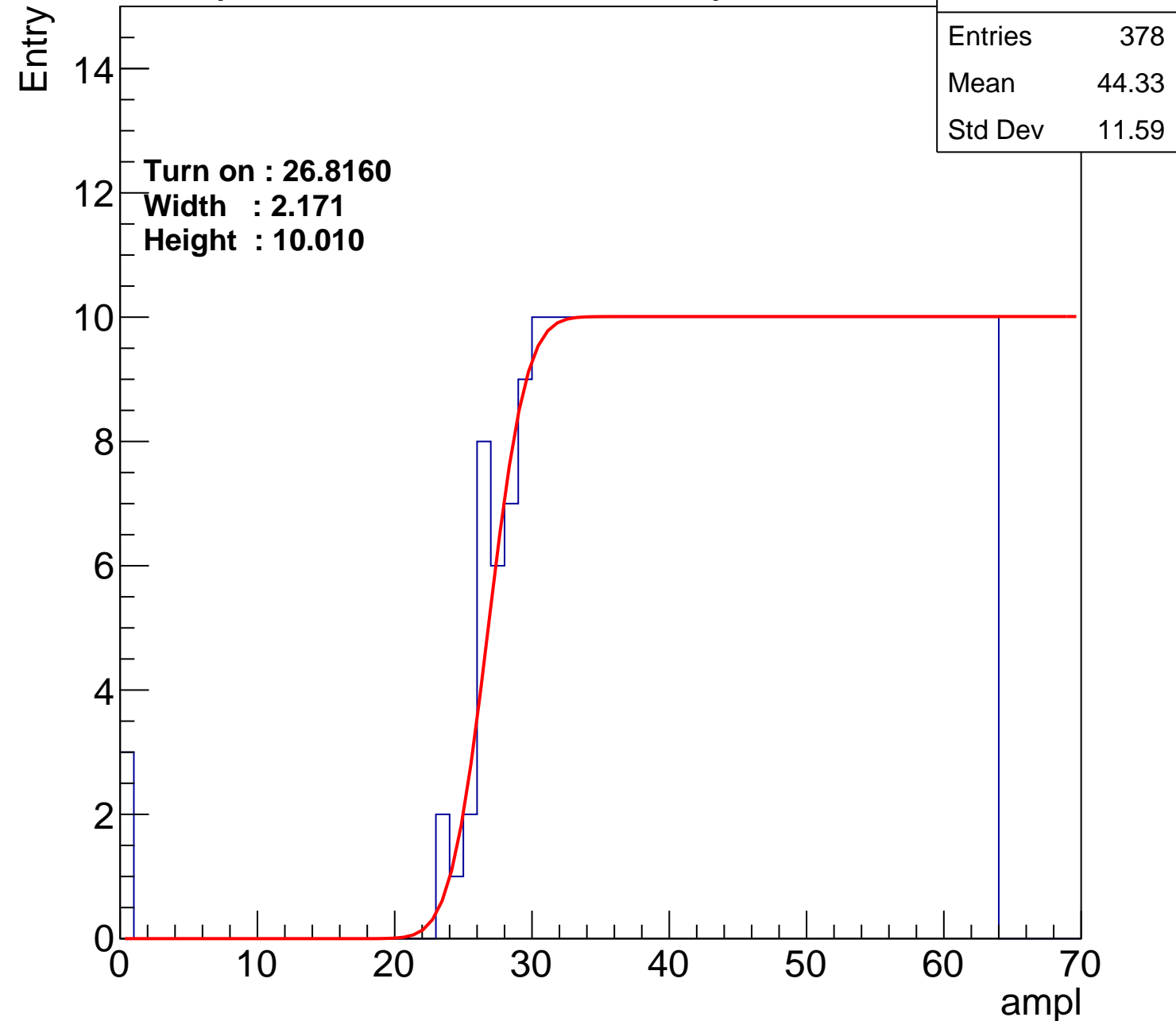
Width : 2.171

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.9595

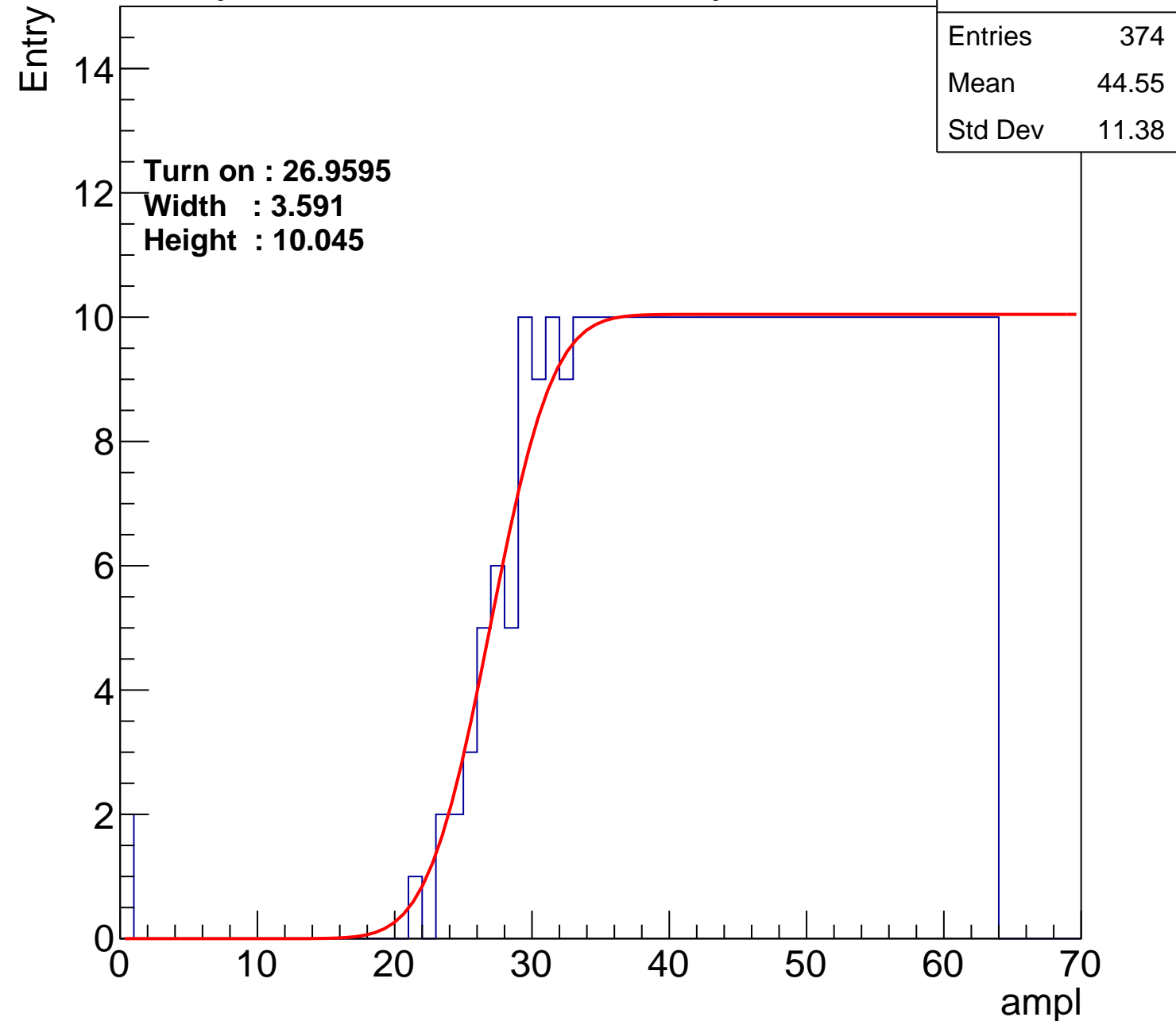
Width : 3.591

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch10

calib_packv5_042523_0143.root, FC#12, port B1

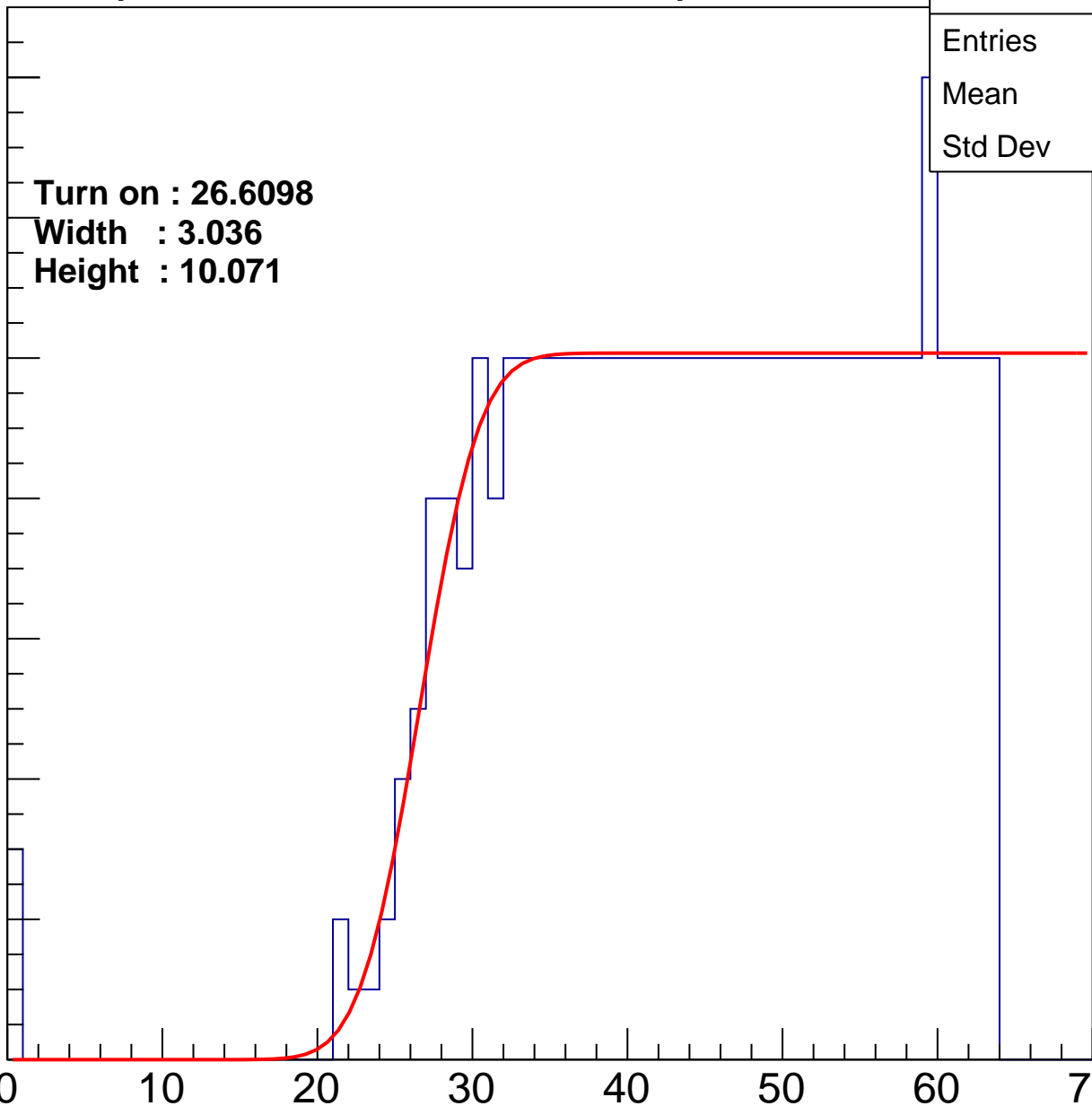
Entry

14
12
10
8
6
4
2
0

Turn on : 26.6098
Width : 3.036
Height : 10.071

Entries	383
Mean	44.37
Std Dev	11.76

ampl



B0L102S, U13-ch11

calib_packv5_042523_0143.root, FC#12, port B1

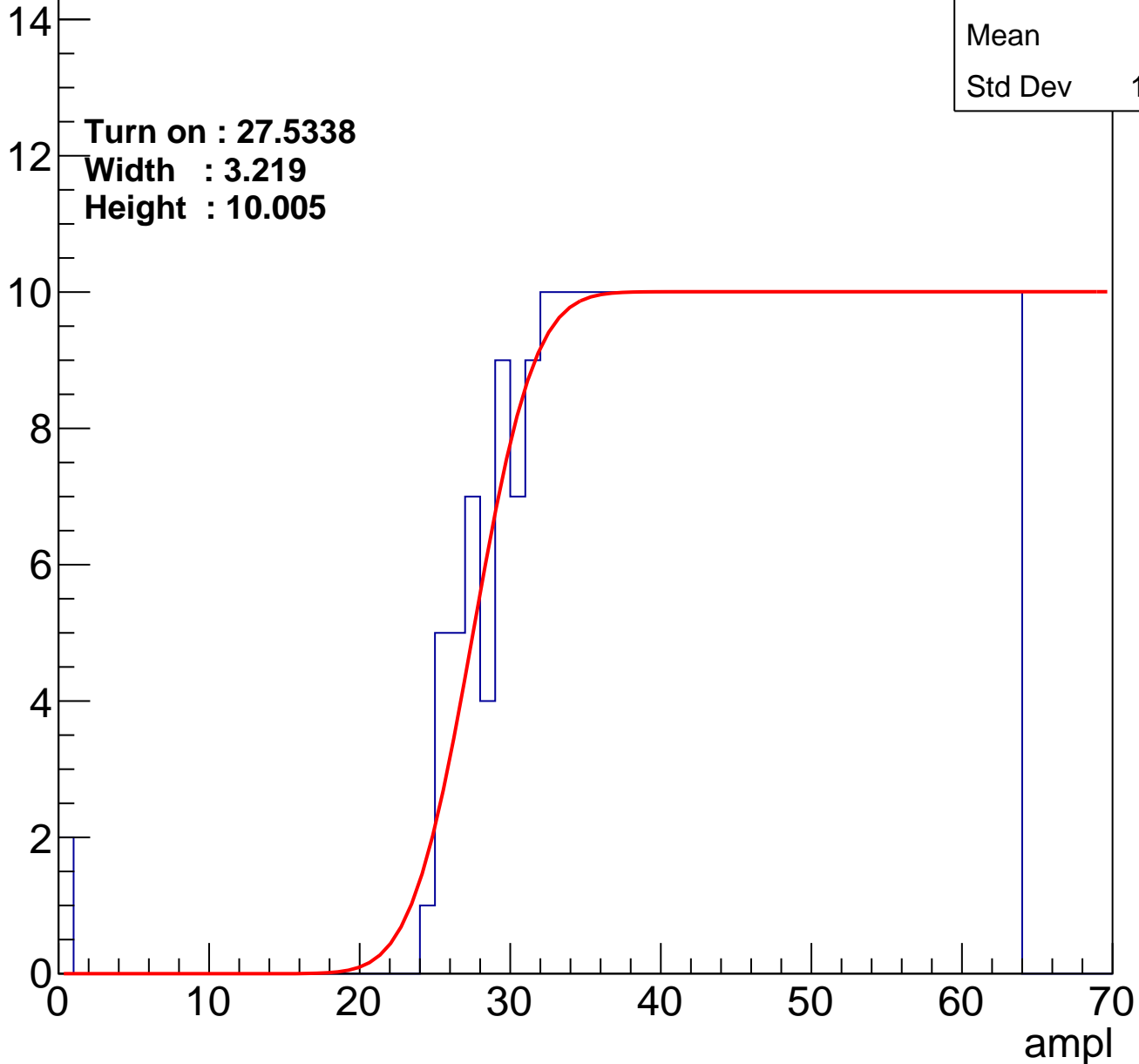
Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.5338

Width : 3.219

Height : 10.005

Entry



B0L102S, U13-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.68
Std Dev	12.16

Turn on : 25.4744

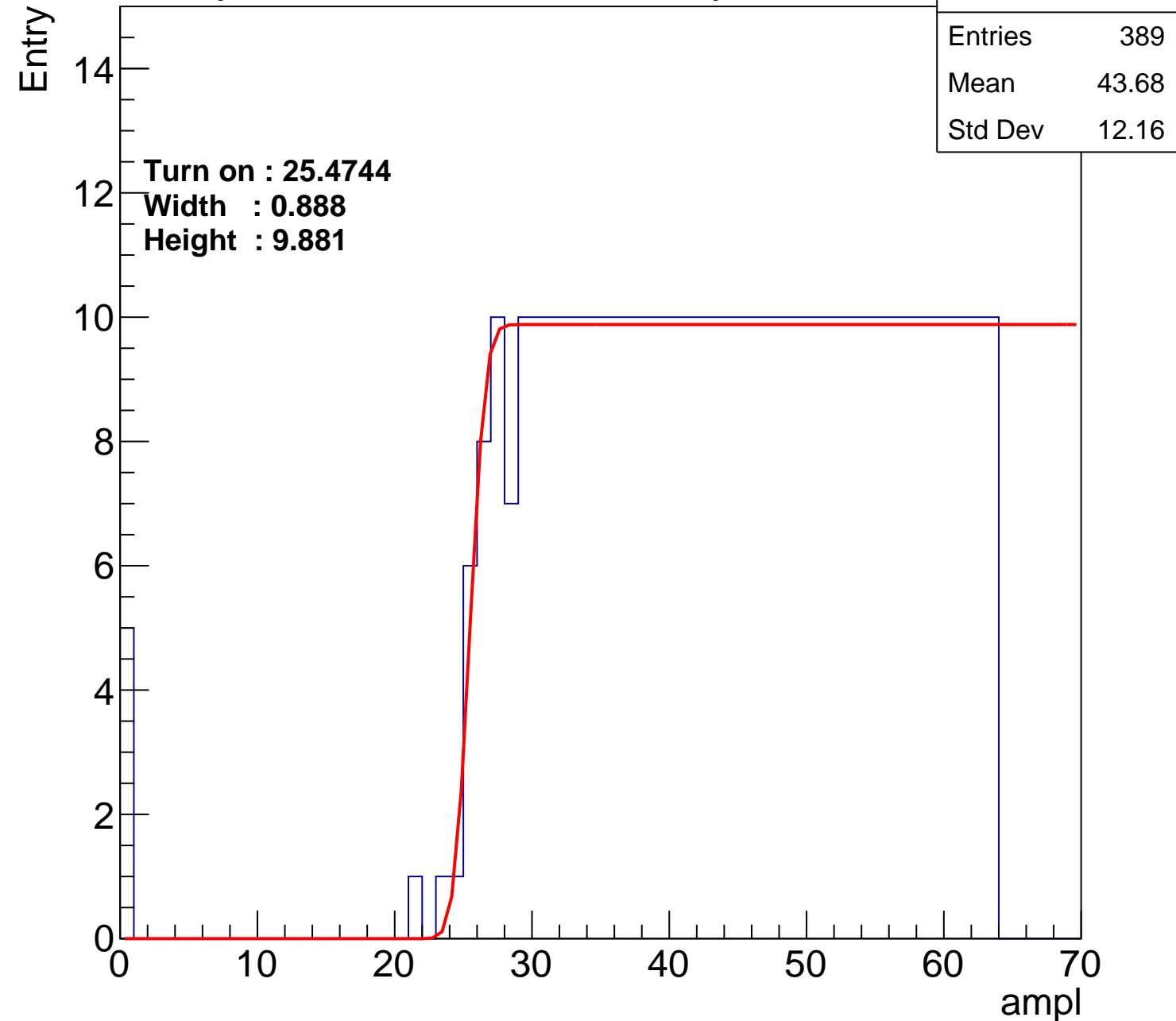
Width : 0.888

Height : 9.881

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 27.1205

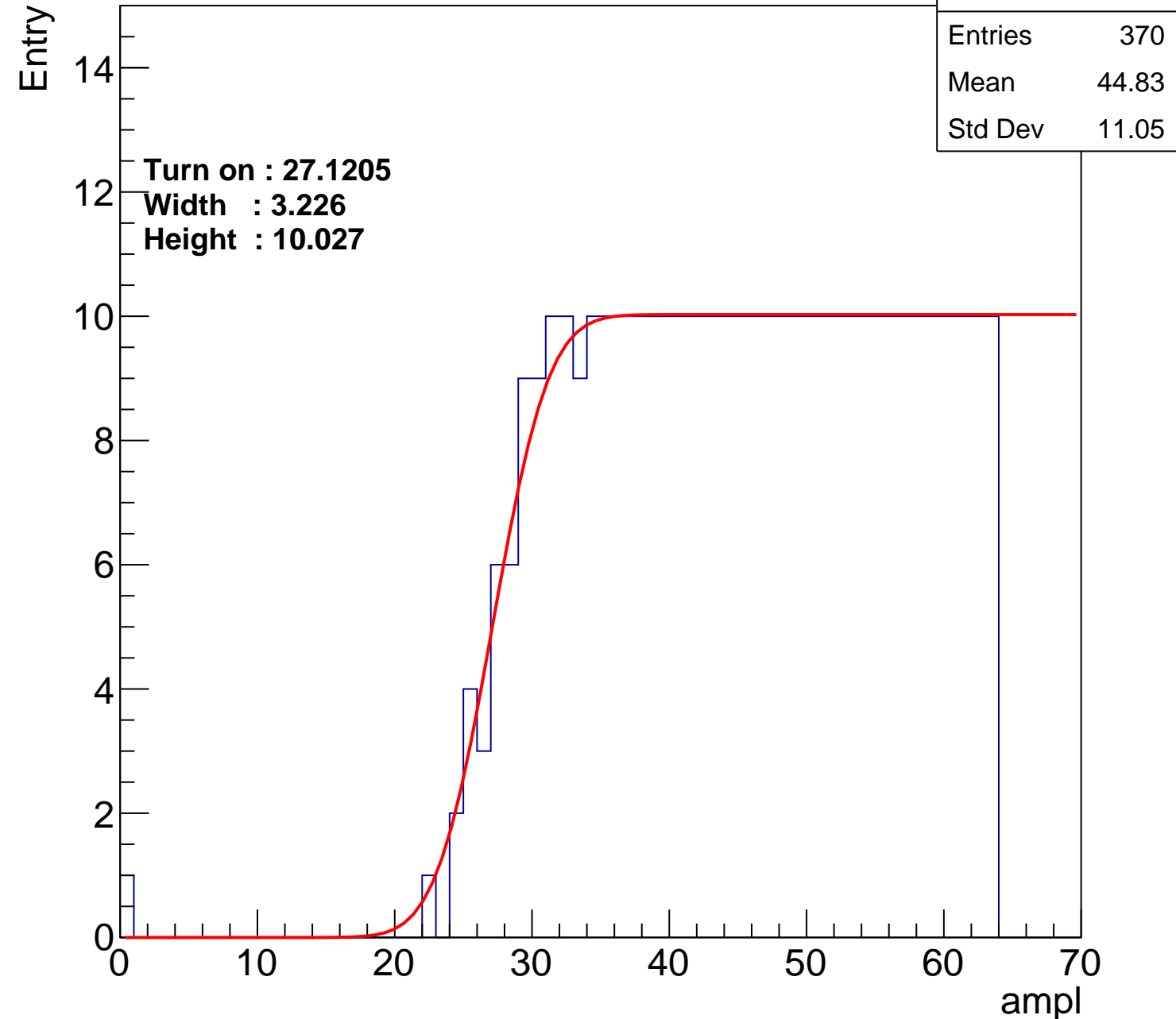
Width : 3.226

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 26.4168

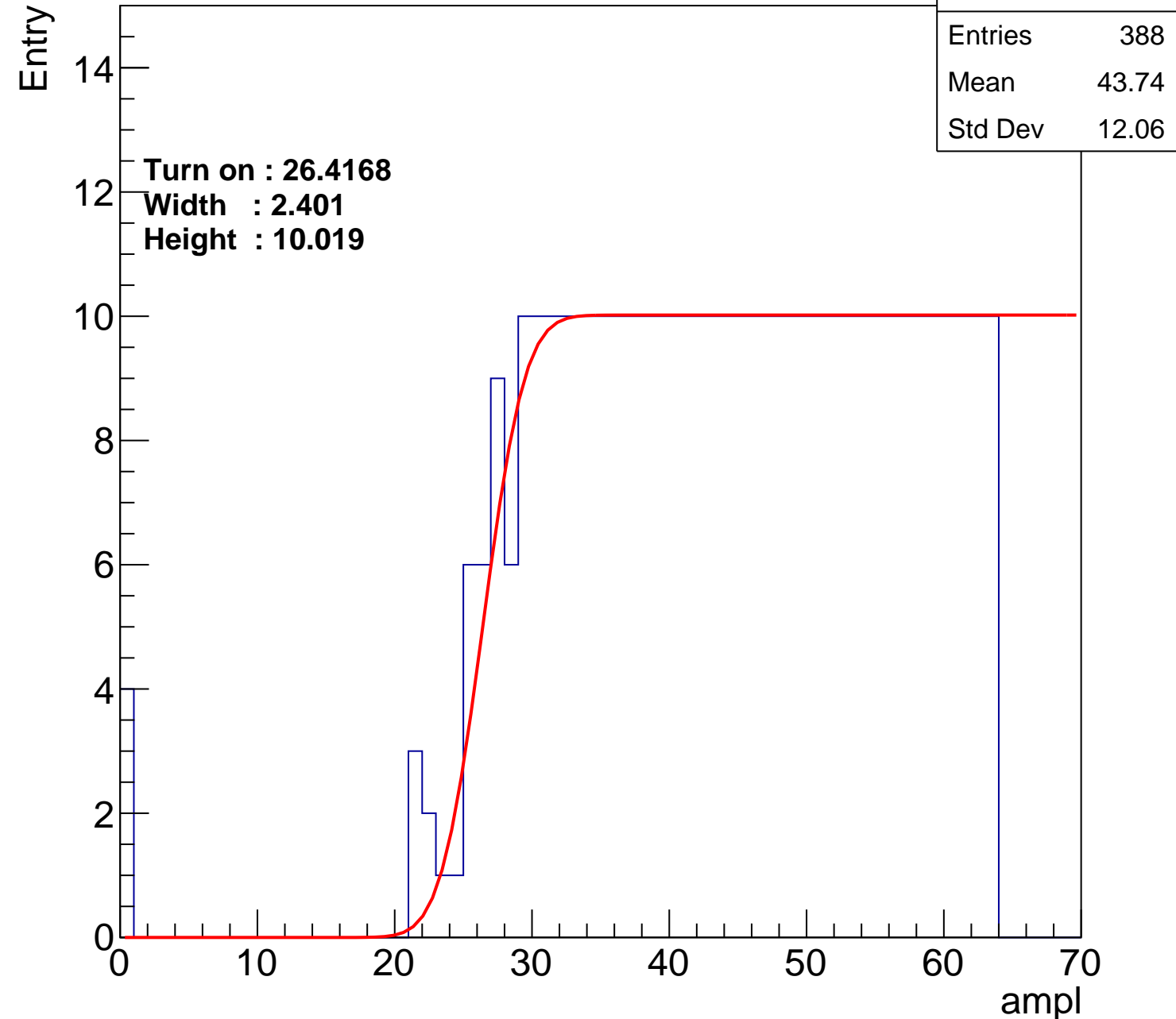
Width : 2.401

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch15

calib_packv5_042523_0143.root, FC#12, port B1

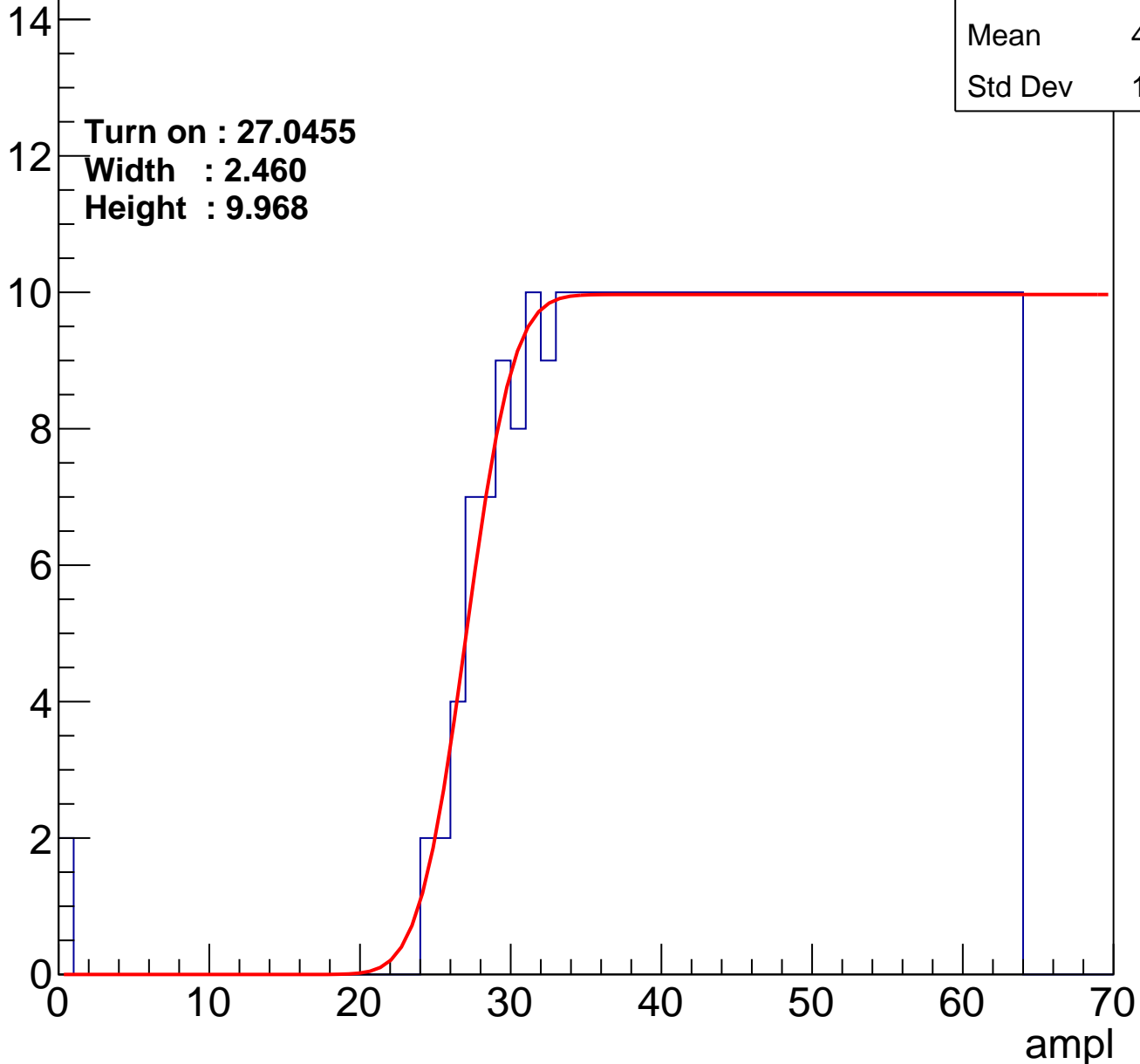
Entries	370
Mean	44.77
Std Dev	11.23

Turn on : 27.0455

Width : 2.460

Height : 9.968

Entry



B0L102S, U13-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 26.9651

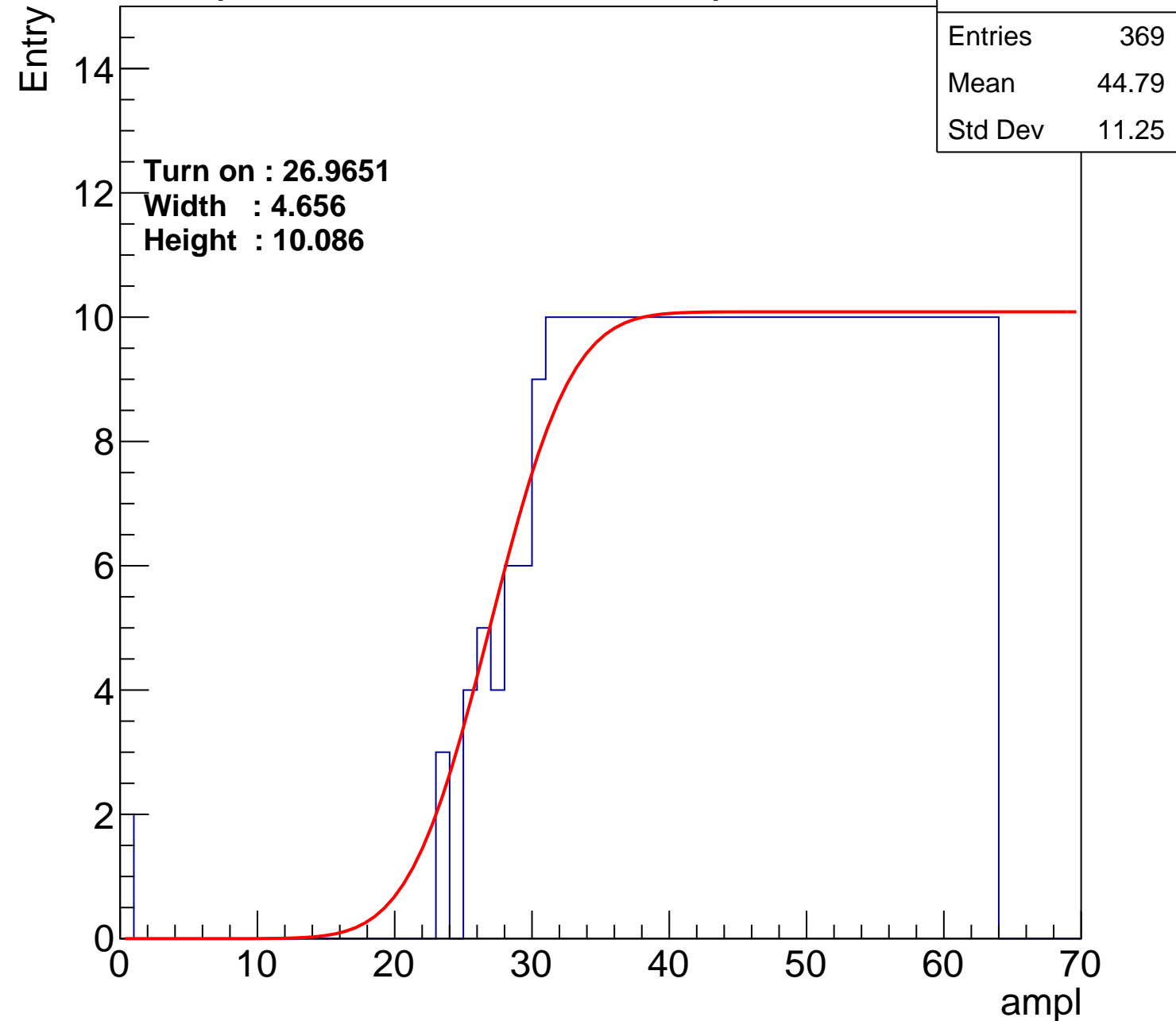
Width : 4.656

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch17

calib_packv5_042523_0143.root, FC#12, port B1

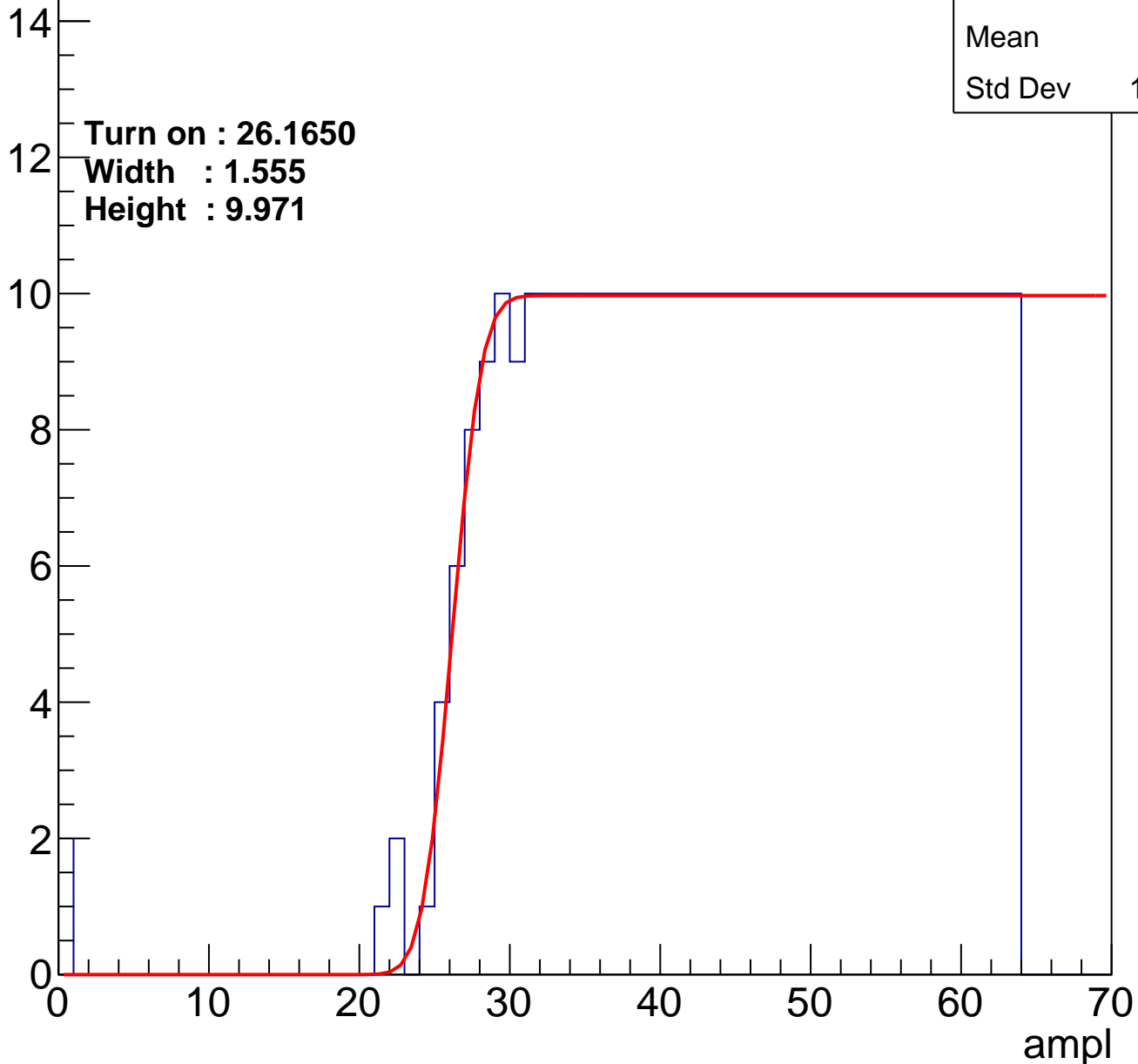
Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.1650

Width : 1.555

Height : 9.971

Entry



B0L102S, U13-ch18

calib_packv5_042523_0143.root, FC#12, port B1

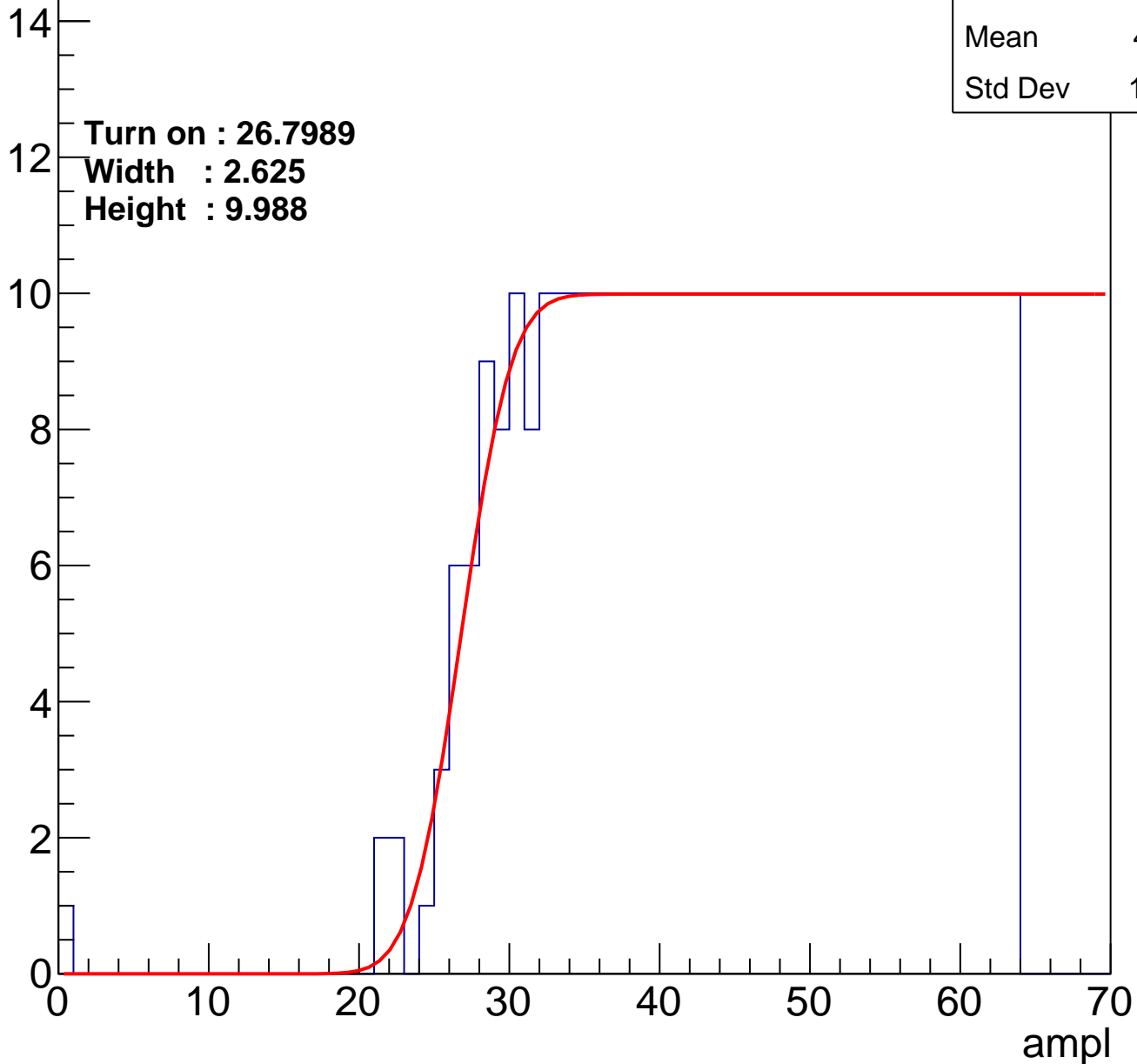
Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.7989

Width : 2.625

Height : 9.988

Entry



B0L102S, U13-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.75
Std Dev	12.05

Turn on : 25.8902

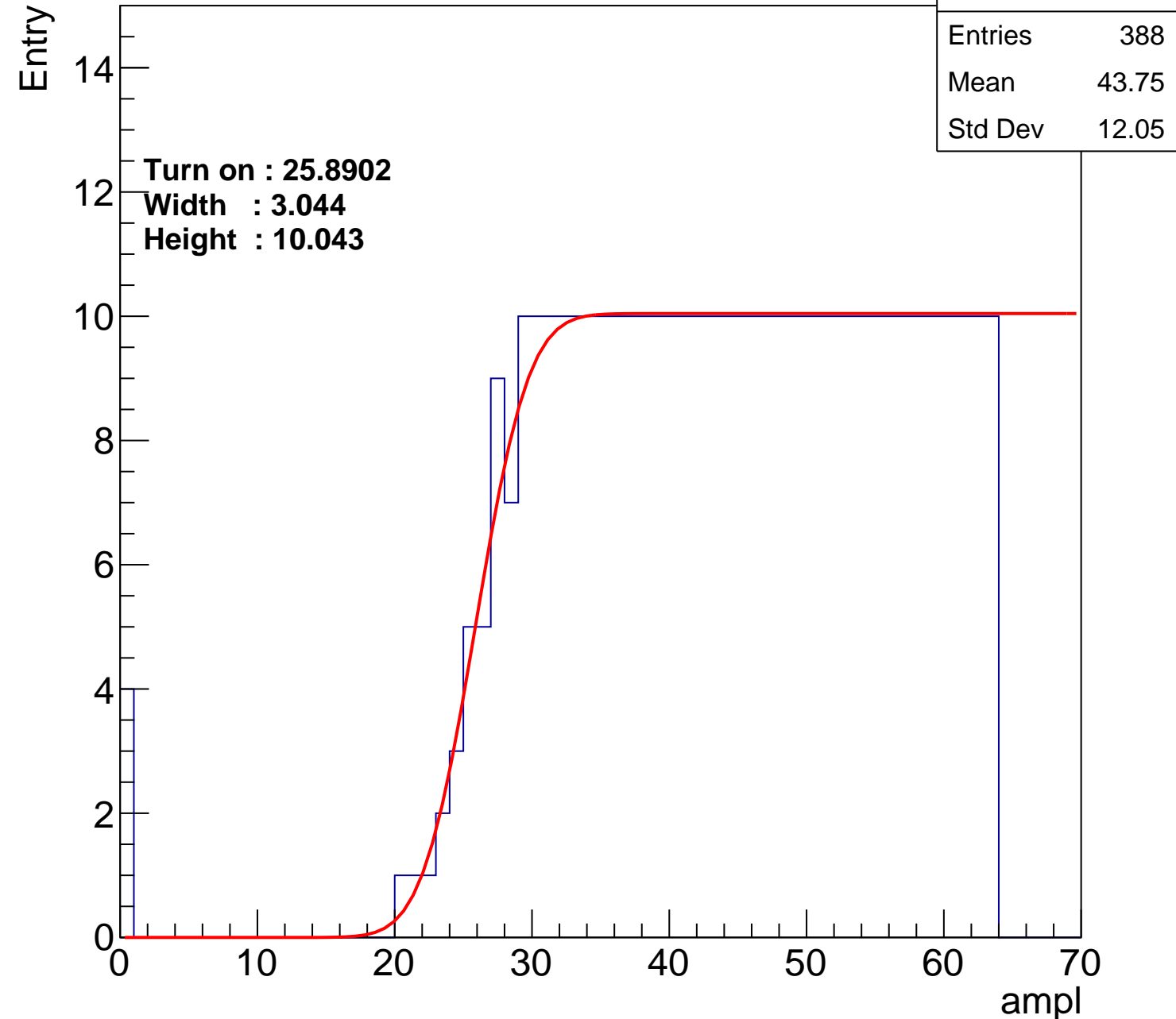
Width : 3.044

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch20

calib_packv5_042523_0143.root, FC#12, port B1

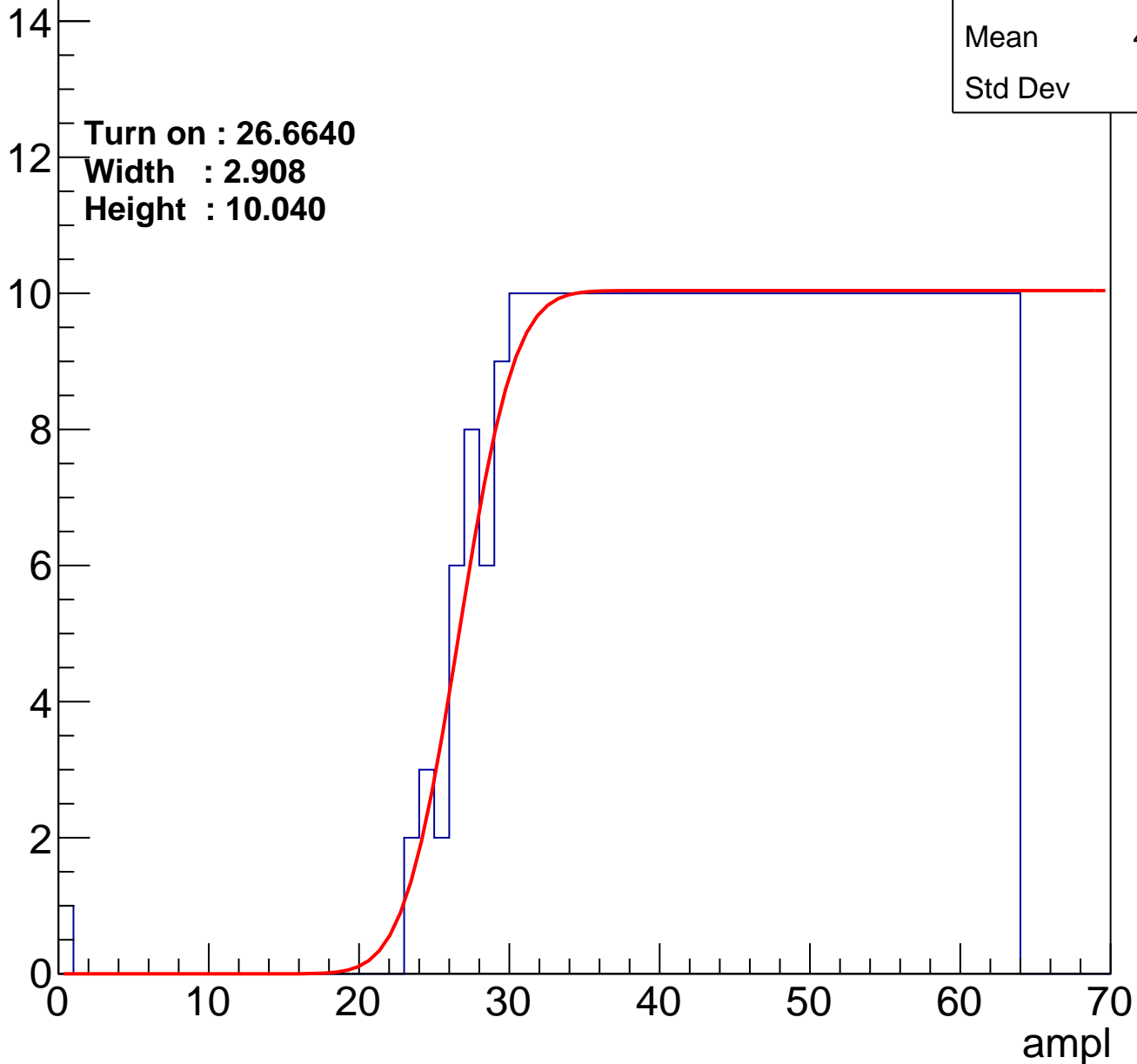
Entries	377
Mean	44.51
Std Dev	11.2

Turn on : 26.6640

Width : 2.908

Height : 10.040

Entry



B0L102S, U13-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.24
Std Dev	11.82

Turn on : 27.1434

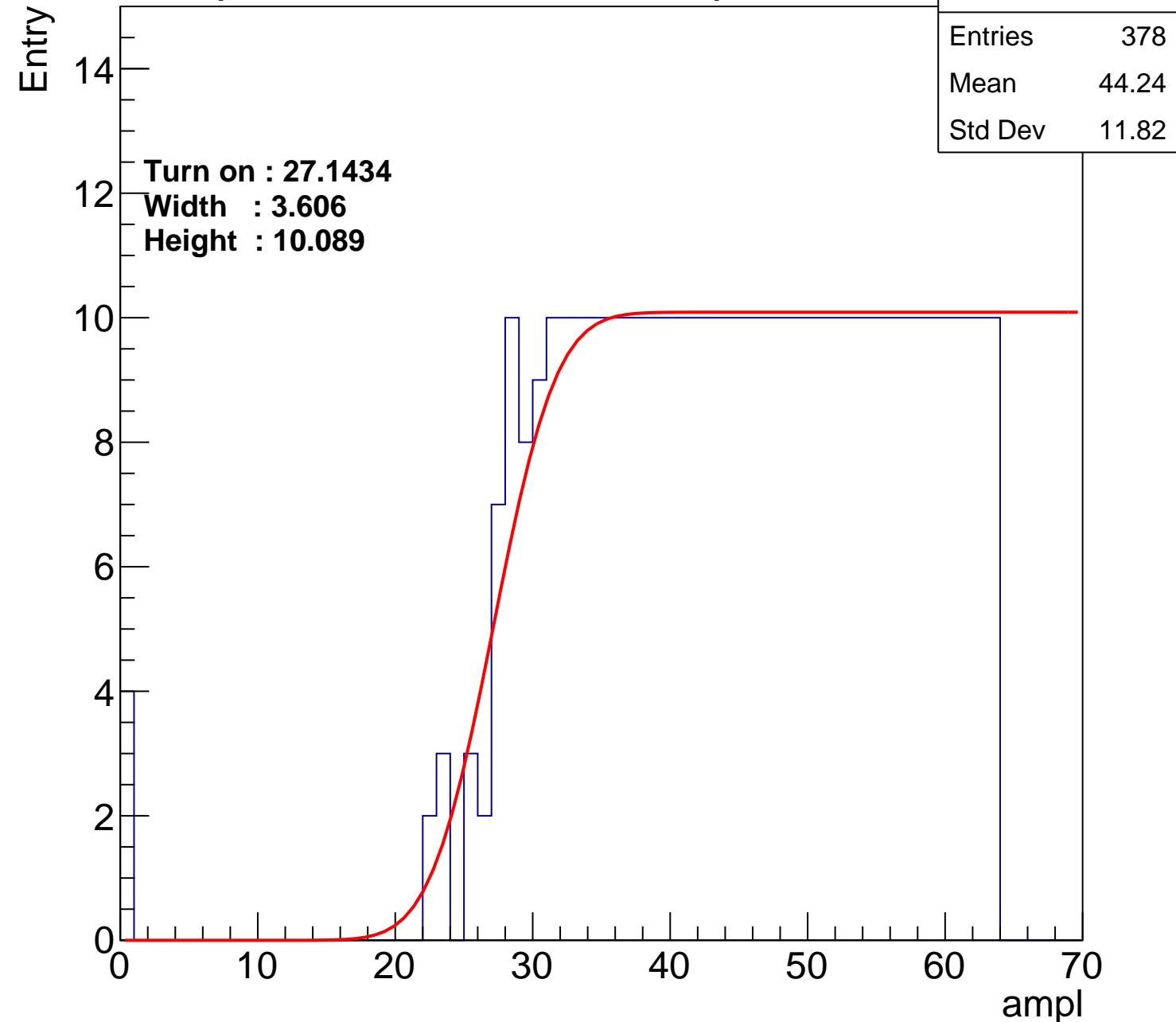
Width : 3.606

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.29
Std Dev	12.24

Turn on : 24.8067

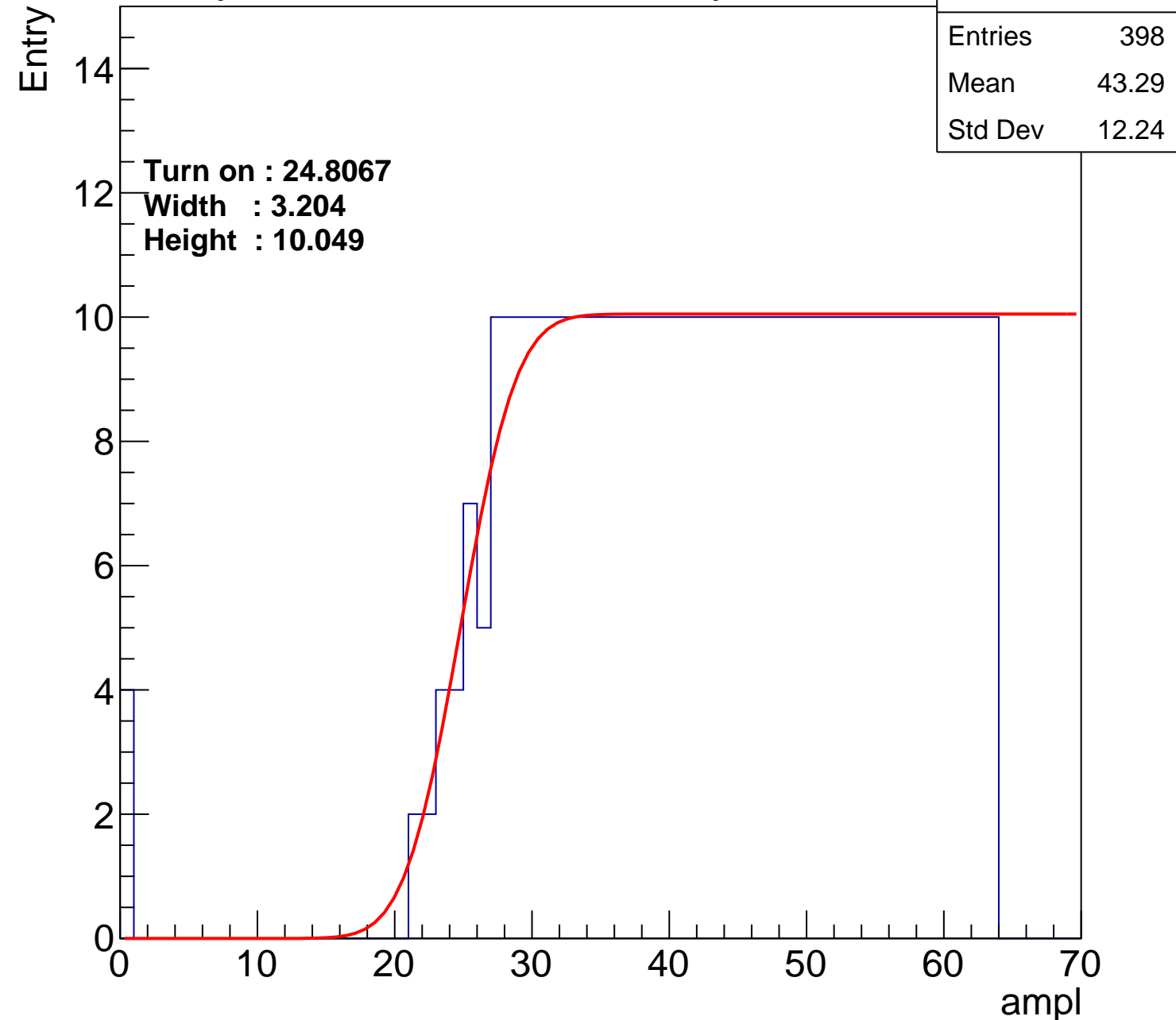
Width : 3.204

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch23

calib_packv5_042523_0143.root, FC#12, port B1

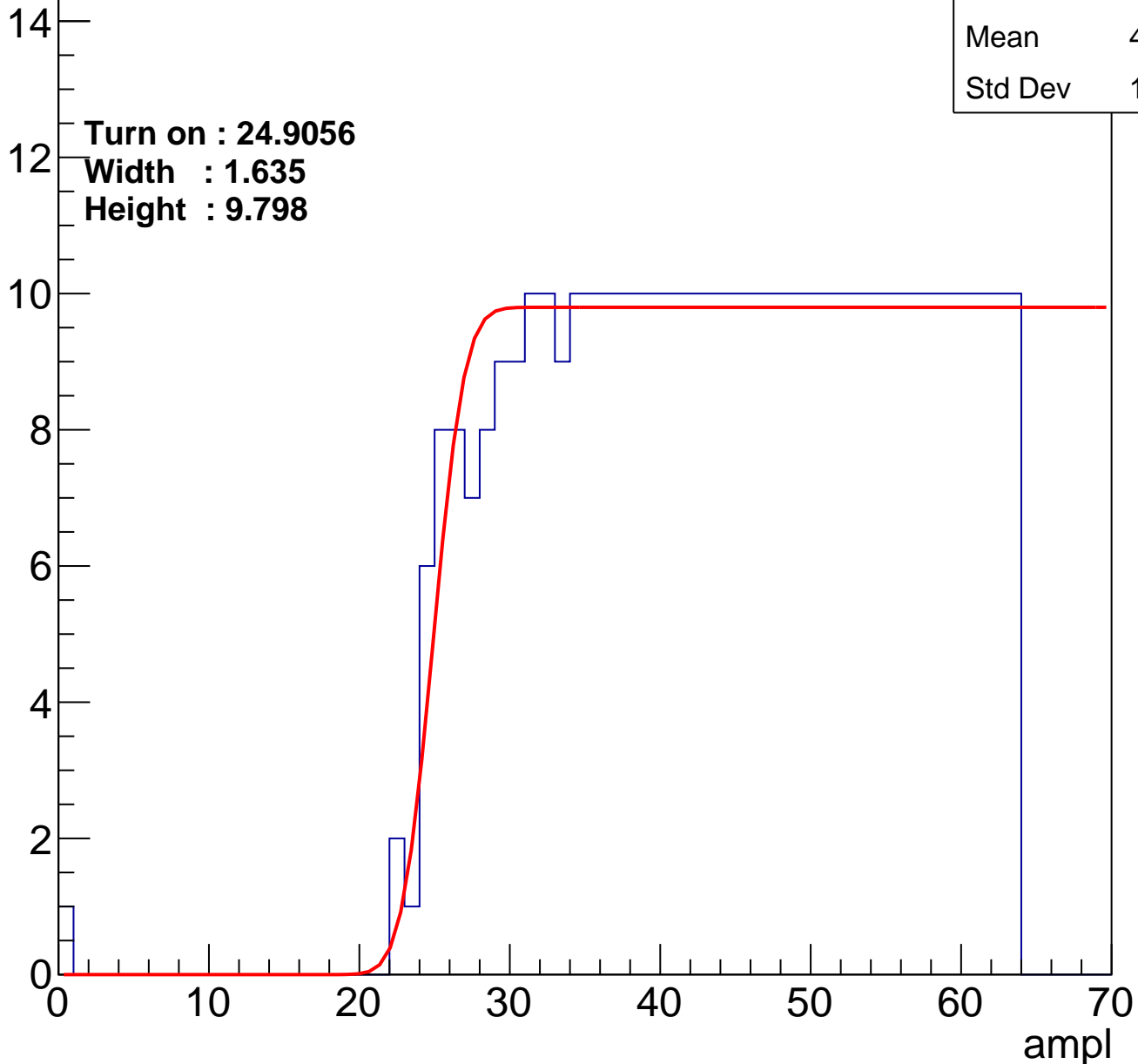
Entries	388
Mean	43.92
Std Dev	11.56

Turn on : 24.9056

Width : 1.635

Height : 9.798

Entry



B0L102S, U13-ch24

calib_packv5_042523_0143.root, FC#12, port B1

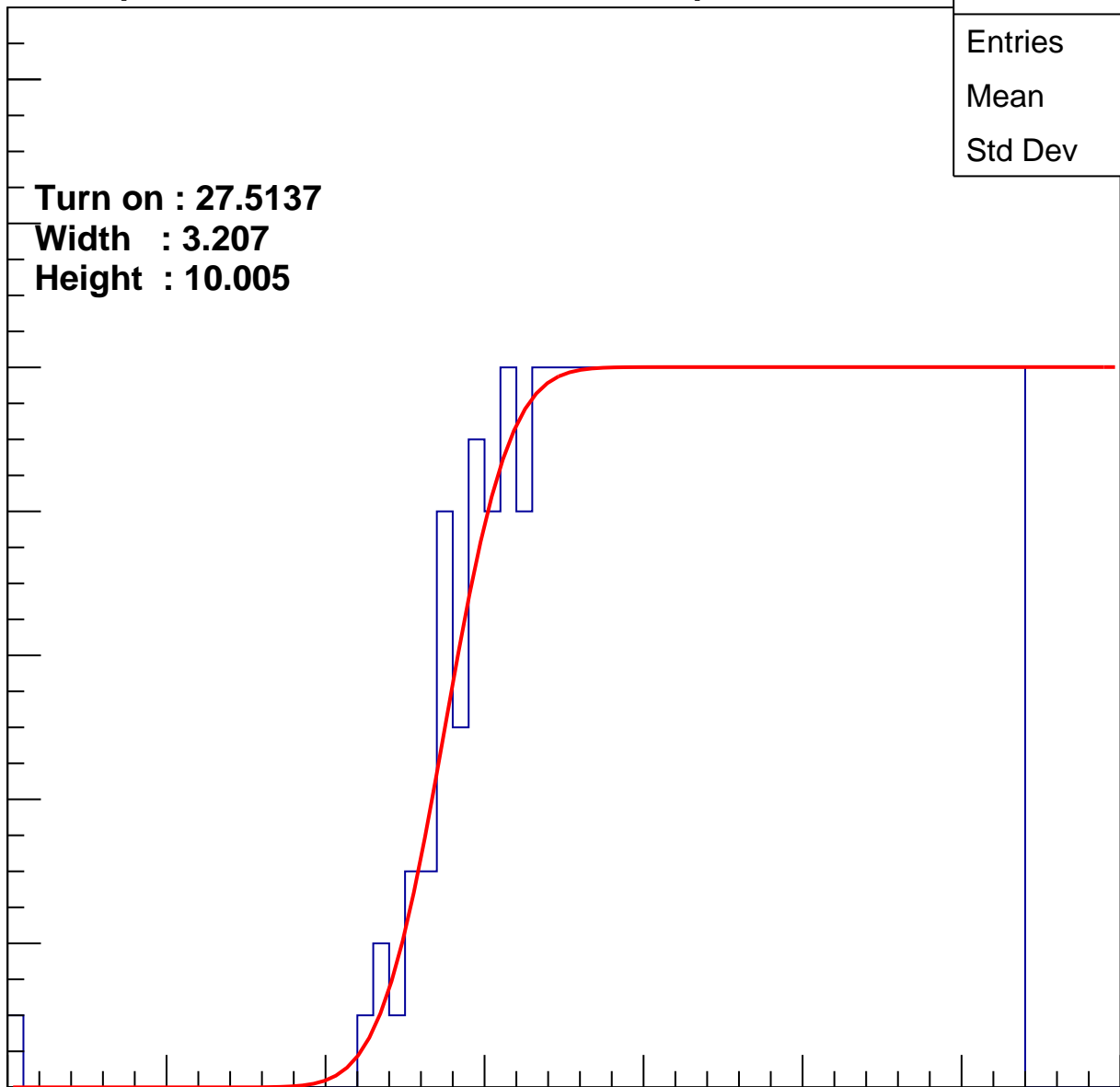
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5137
Width : 3.207
Height : 10.005

Entries	369
Mean	44.85
Std Dev	11.08

ampl



B0L102S, U13-ch25

calib_packv5_042523_0143.root, FC#12, port B1

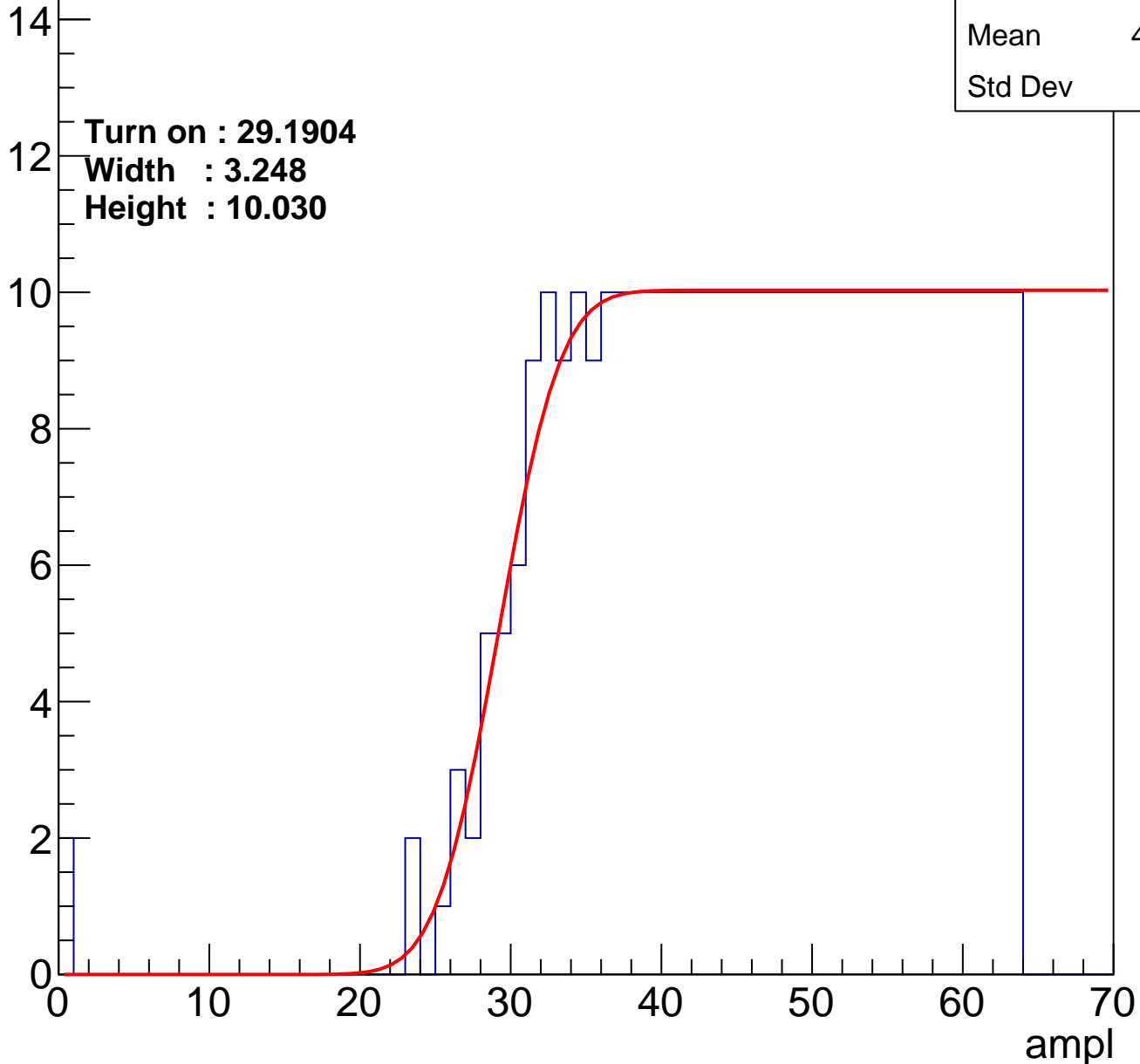
Entries	353
Mean	45.55
Std Dev	10.9

Turn on : 29.1904

Width : 3.248

Height : 10.030

Entry



B0L102S, U13-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.77
Std Dev	11.6

Turn on : 24.9049

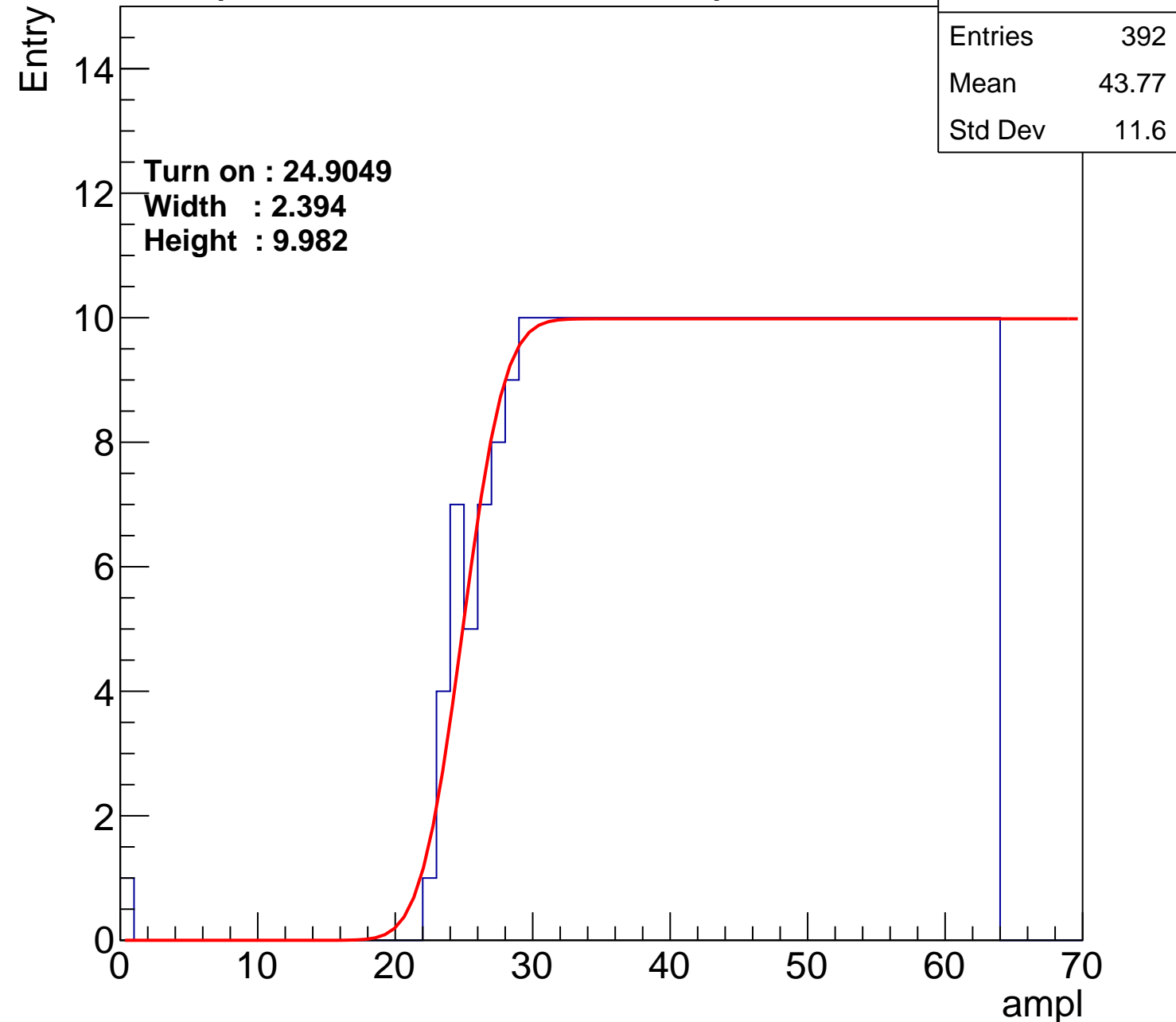
Width : 2.394

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 25.2662

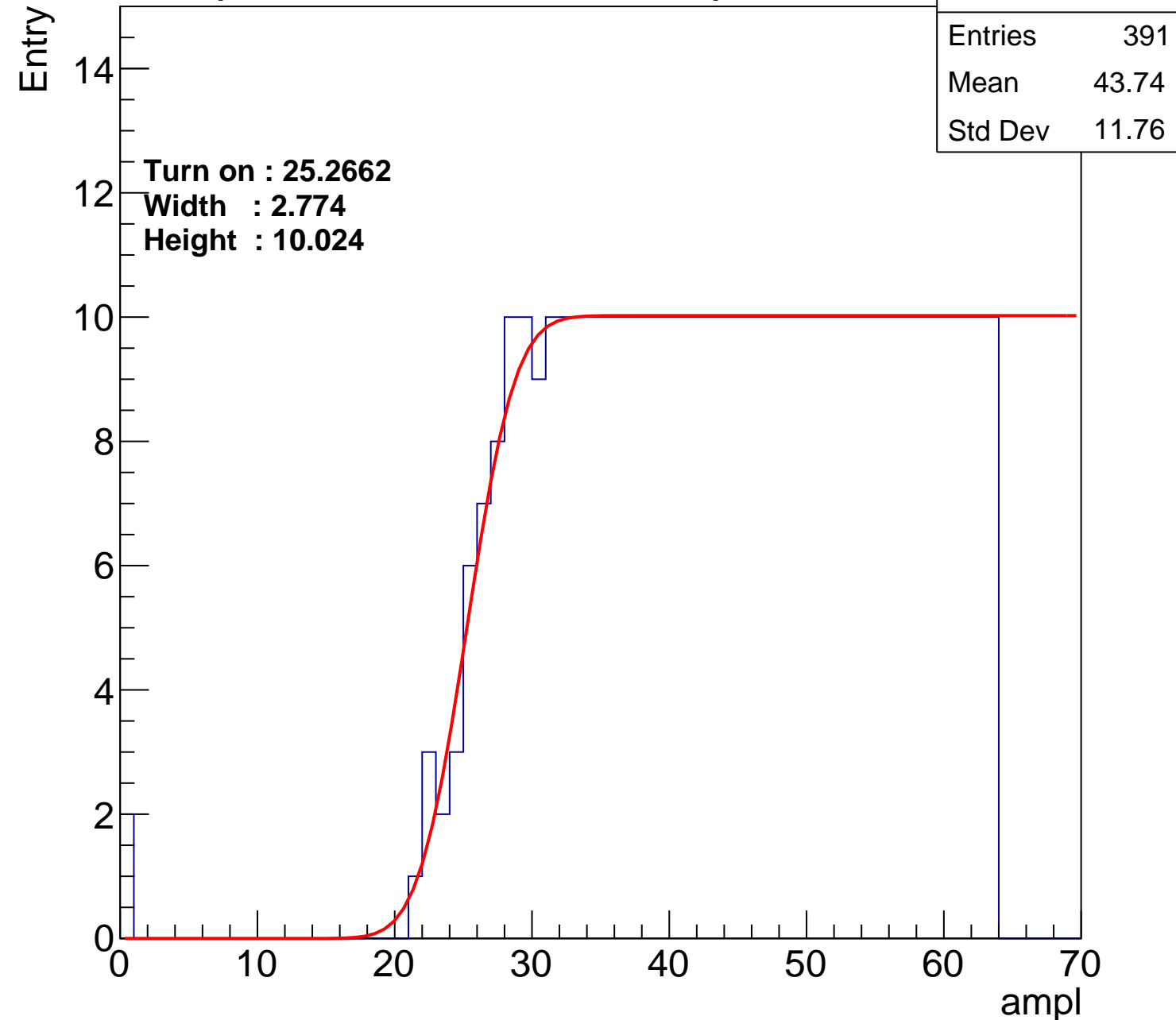
Width : 2.774

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch28

calib_packv5_042523_0143.root, FC#12, port B1

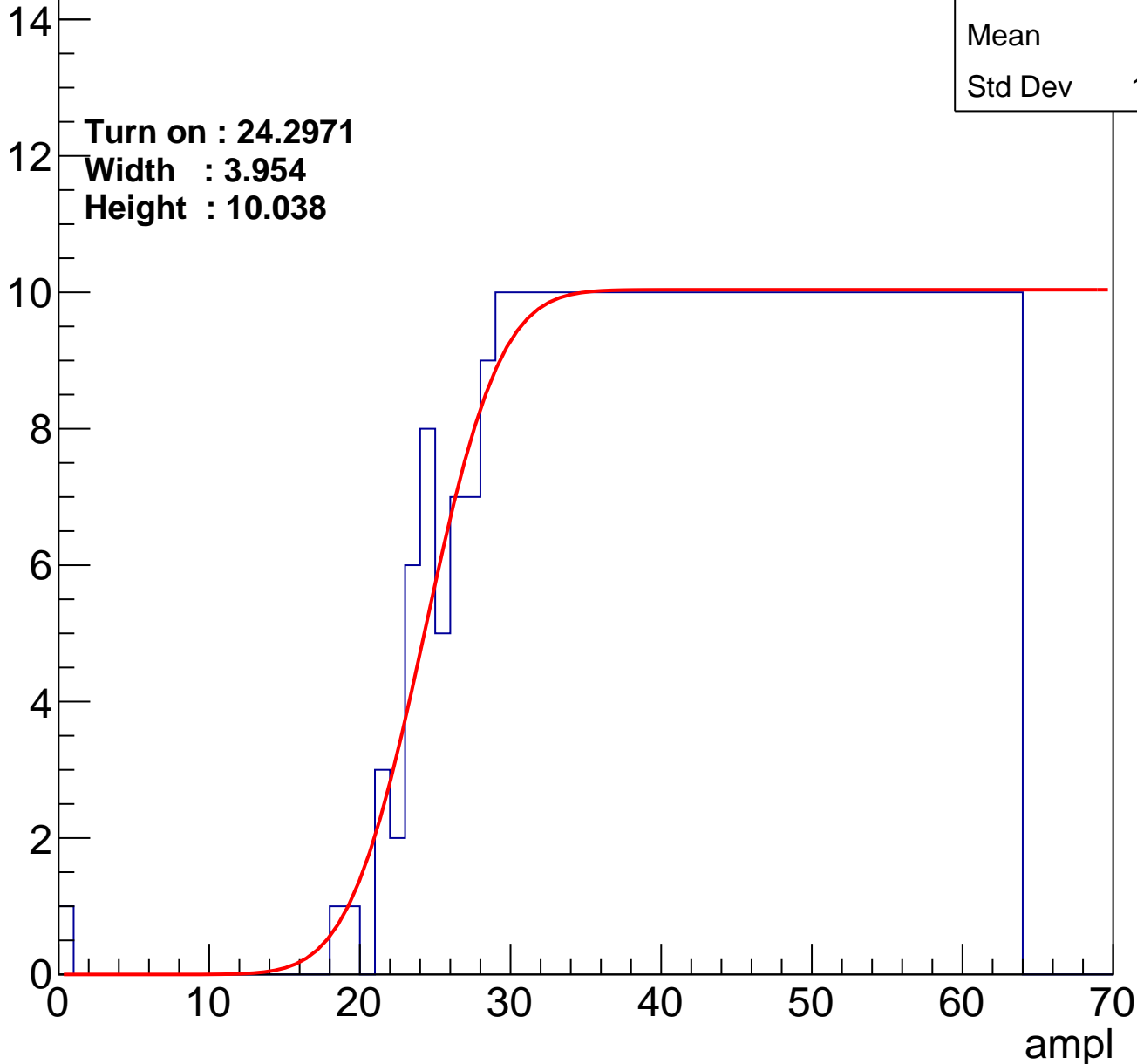
Entries	400
Mean	43.3
Std Dev	11.93

Turn on : 24.2971

Width : 3.954

Height : 10.038

Entry



B0L102S, U13-ch29

calib_packv5_042523_0143.root, FC#12, port B1

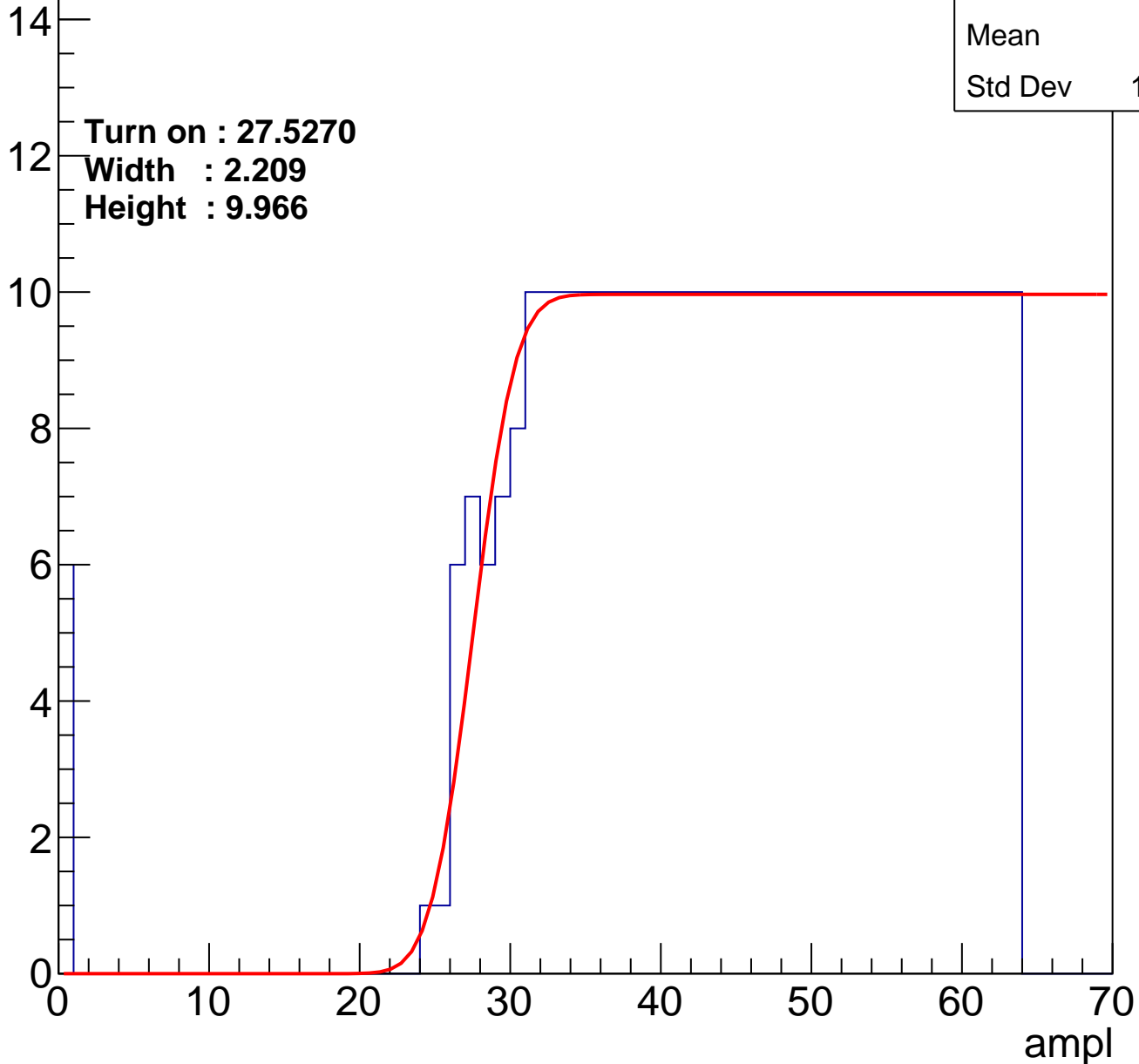
Entries	372
Mean	44.4
Std Dev	12.03

Turn on : 27.5270

Width : 2.209

Height : 9.966

Entry



B0L102S, U13-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.2114

Width : 2.784

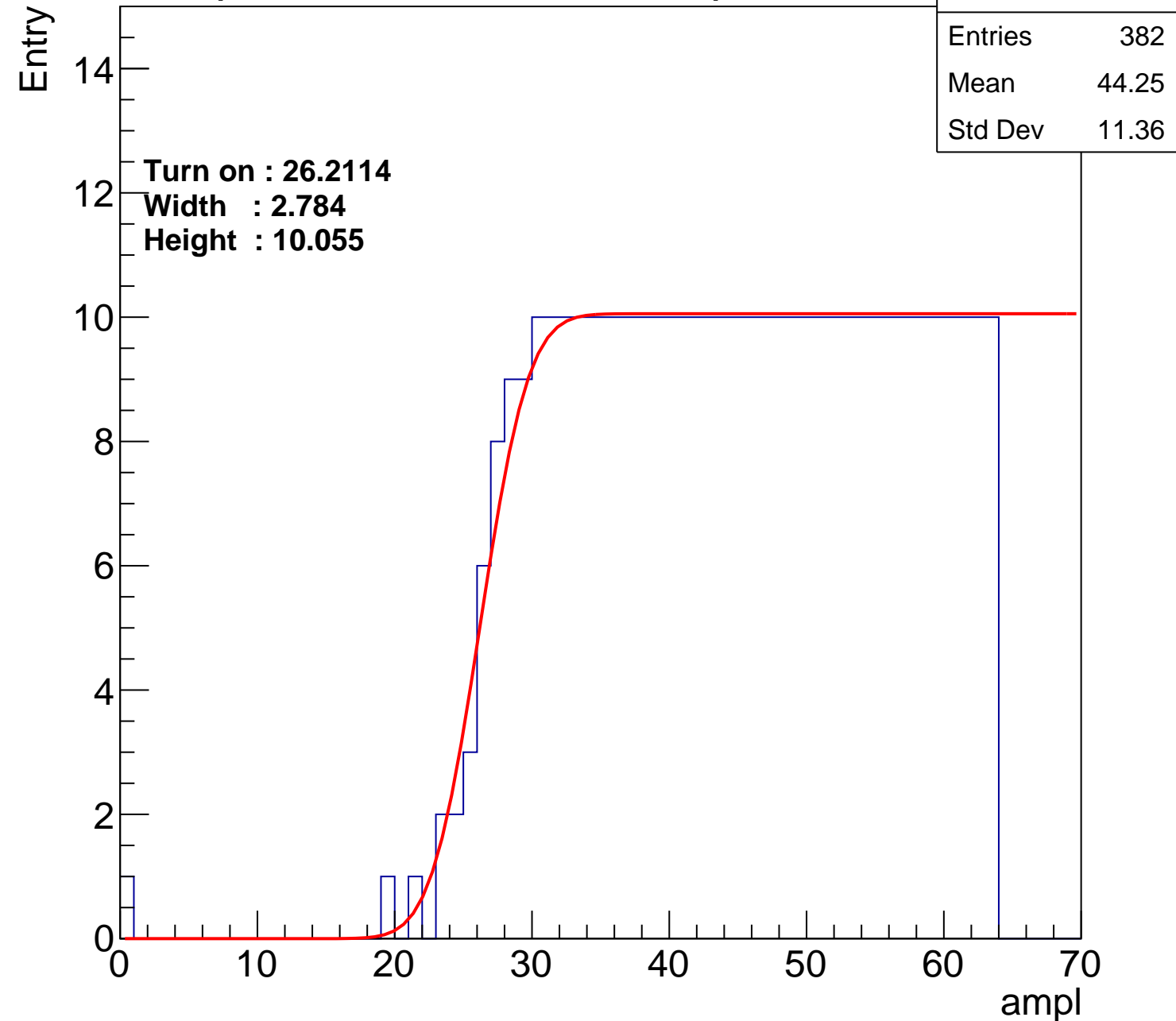
Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U13-ch31

calib_packv5_042523_0143.root, FC#12, port B1

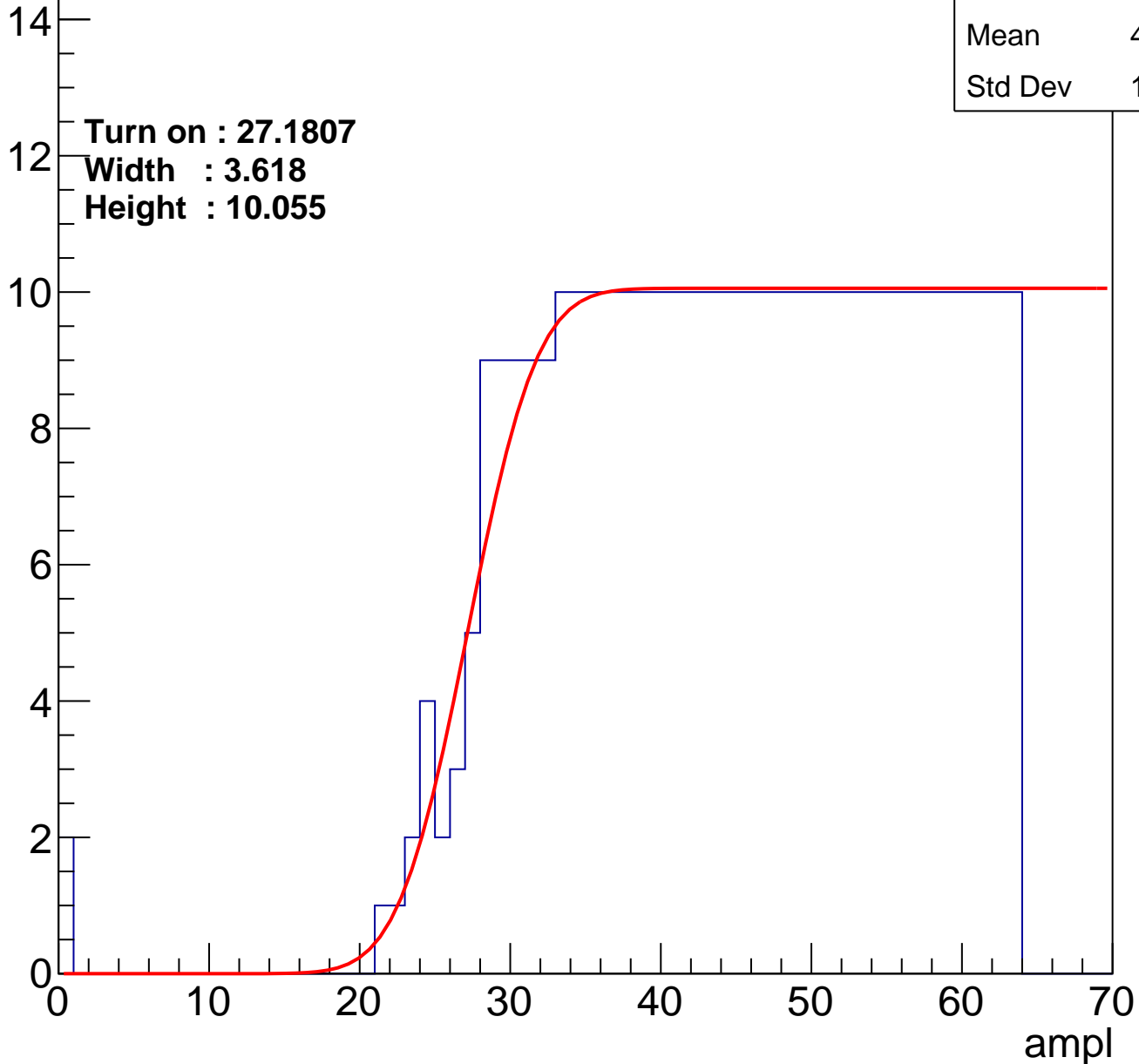
Entries	375
Mean	44.47
Std Dev	11.44

Turn on : 27.1807

Width : 3.618

Height : 10.055

Entry



B0L102S, U13-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.16
Std Dev	10.89

Turn on : 29.0751

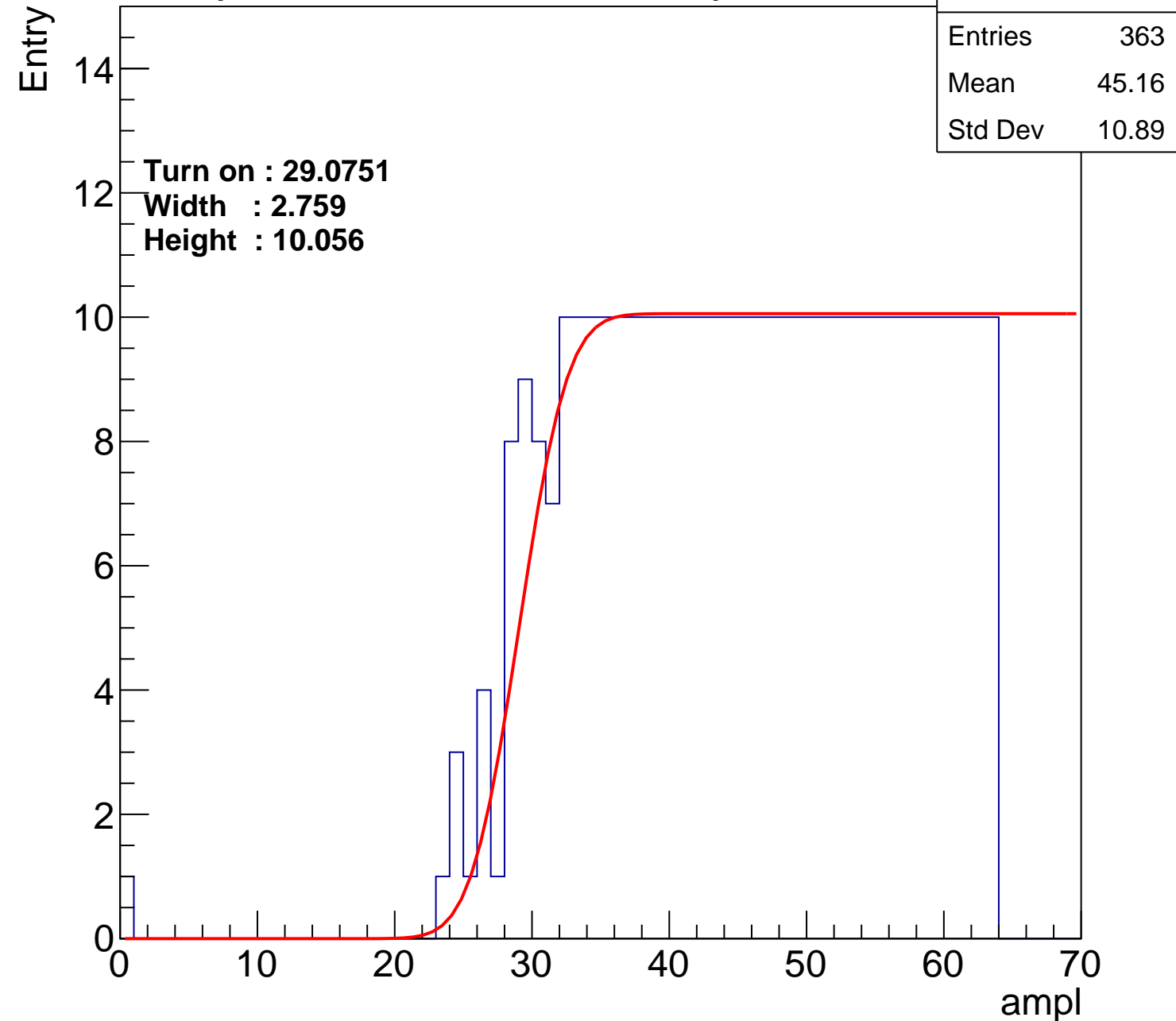
Width : 2.759

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch33

calib_packv5_042523_0143.root, FC#12, port B1

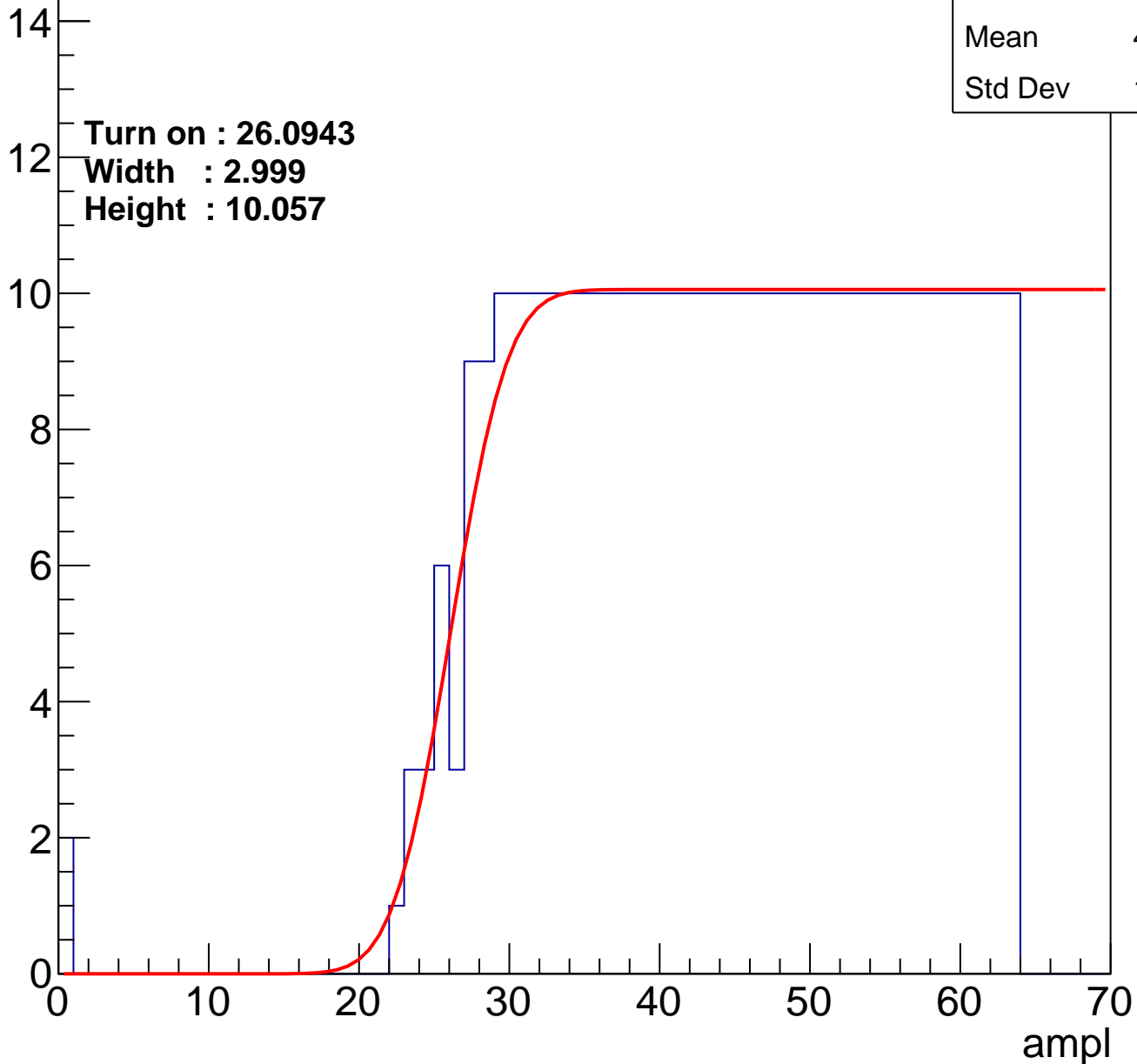
Entries	386
Mean	44.01
Std Dev	11.61

Turn on : 26.0943

Width : 2.999

Height : 10.057

Entry



B0L102S, U13-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.0995

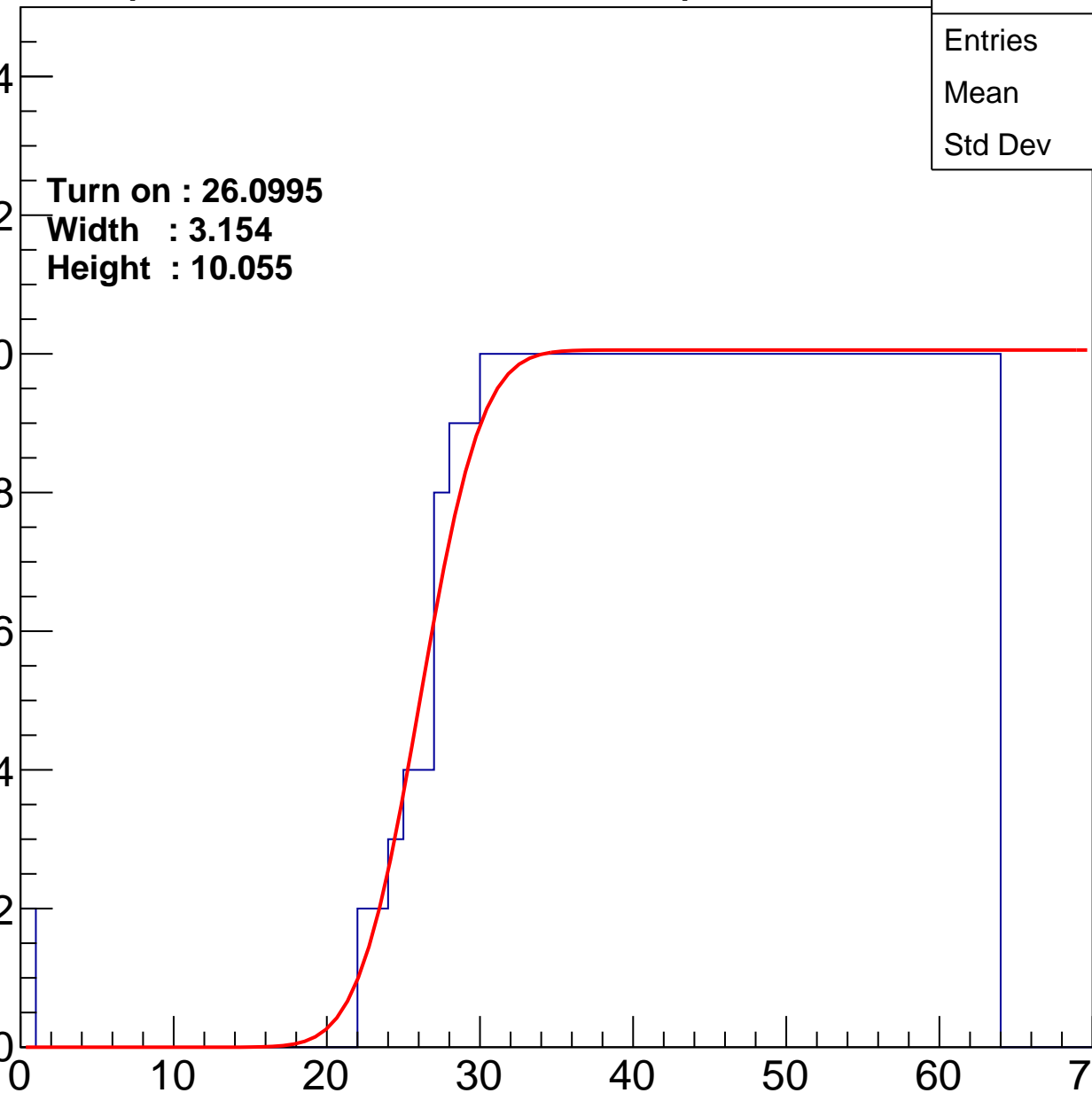
Width : 3.154

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.9995

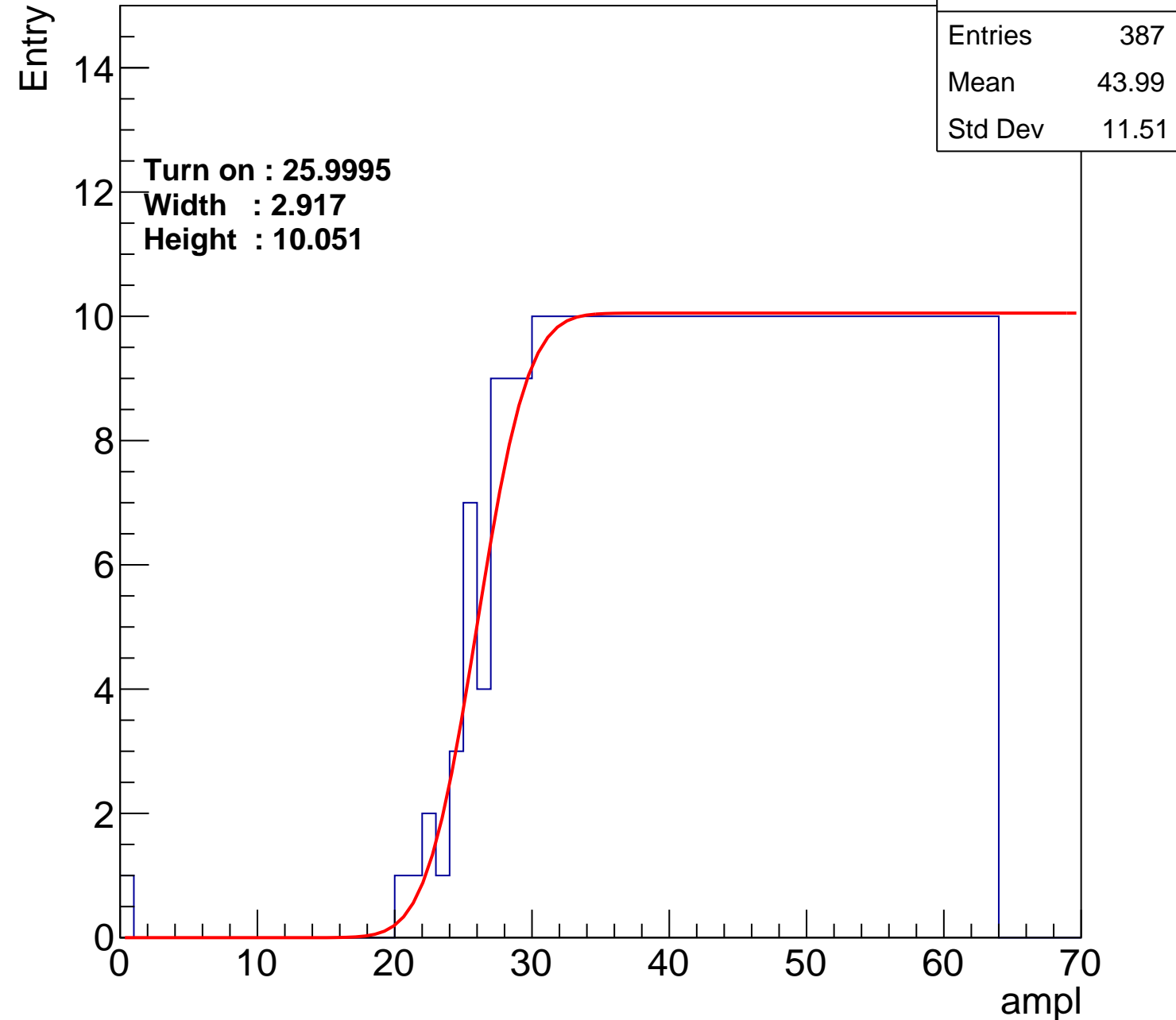
Width : 2.917

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.88
Std Dev	11.67

Turn on : 25.5300

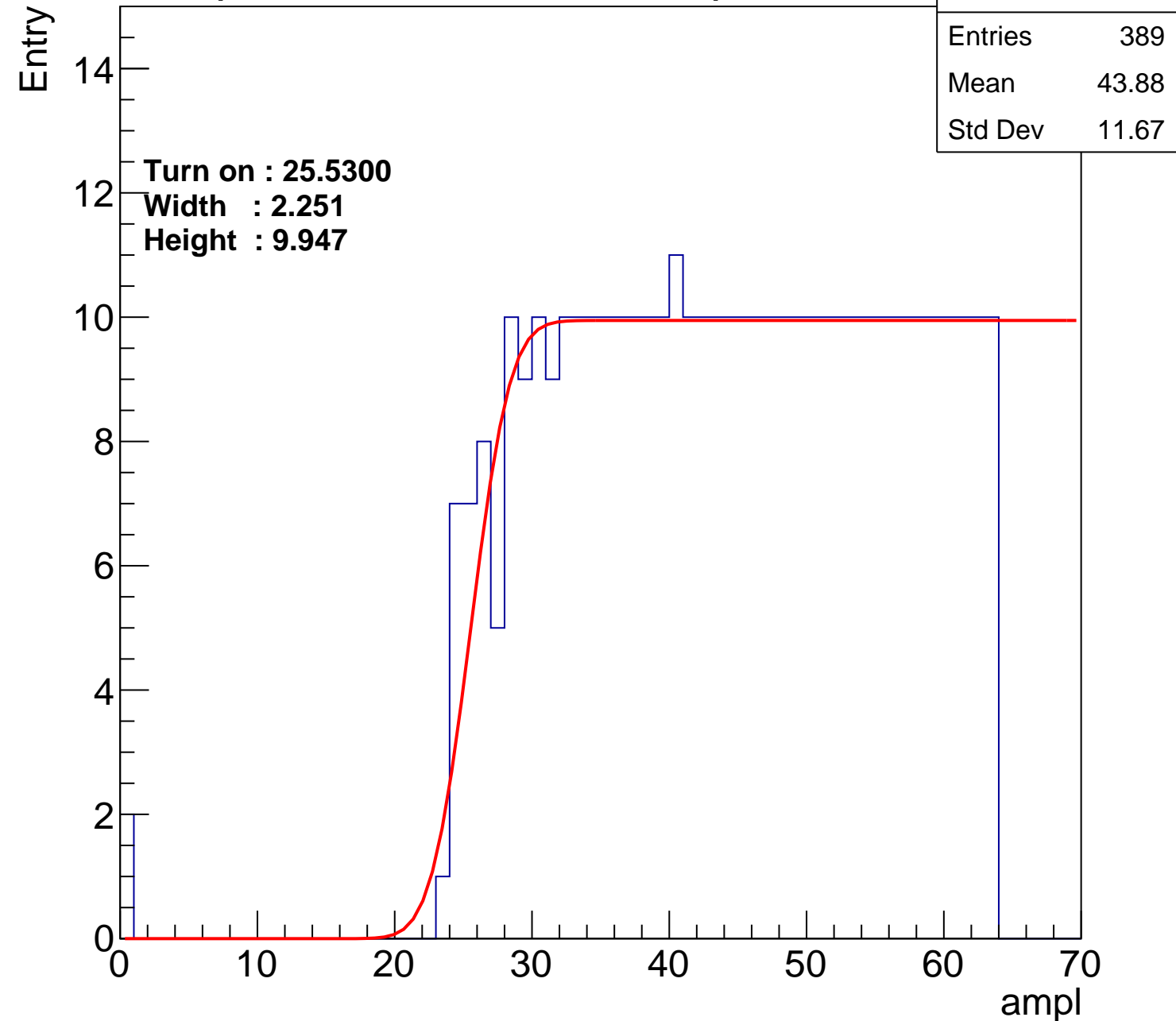
Width : 2.251

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.22
Std Dev	11

Turn on : 27.8108

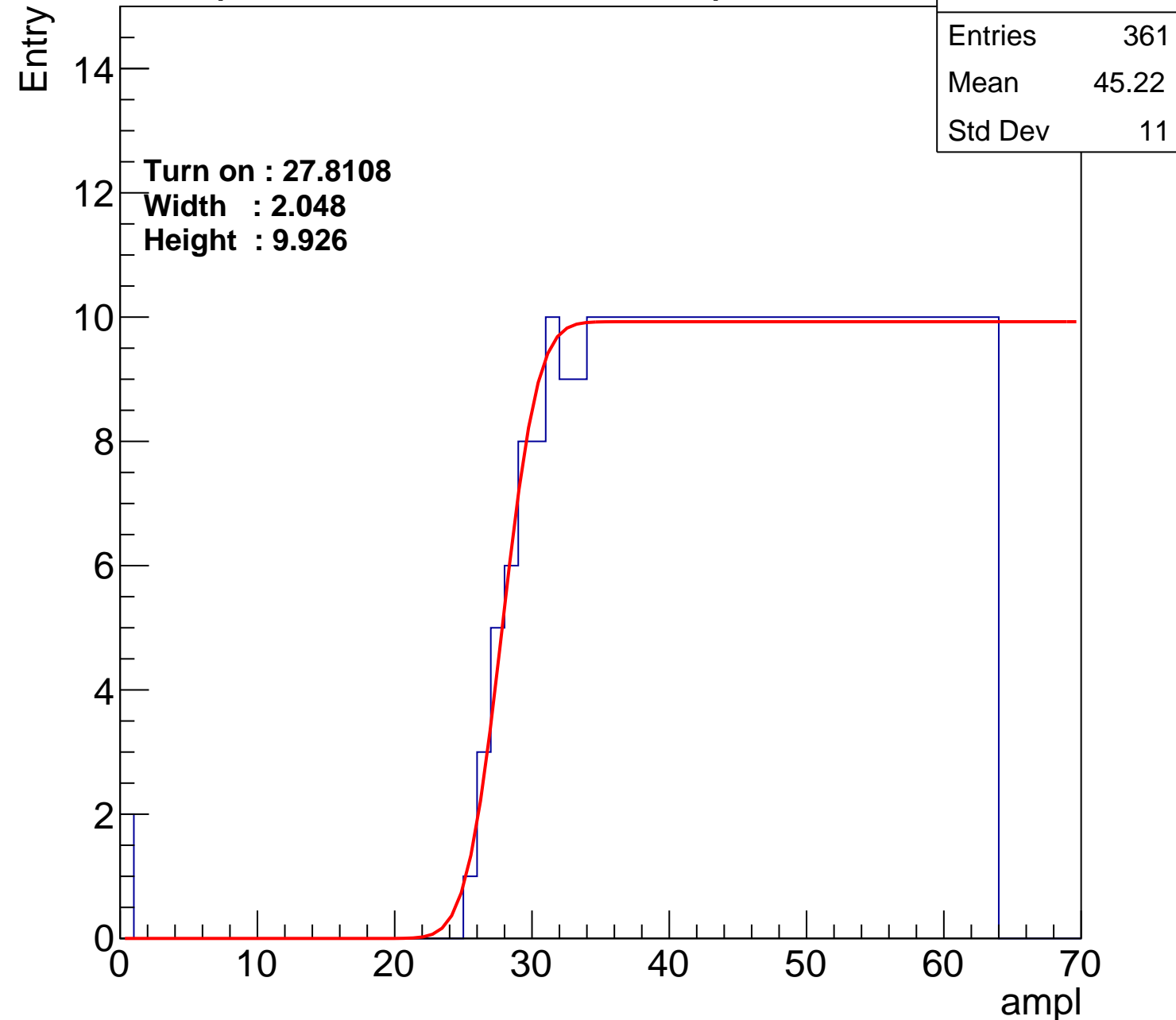
Width : 2.048

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.55
Std Dev	12.14

Turn on : 25.1573

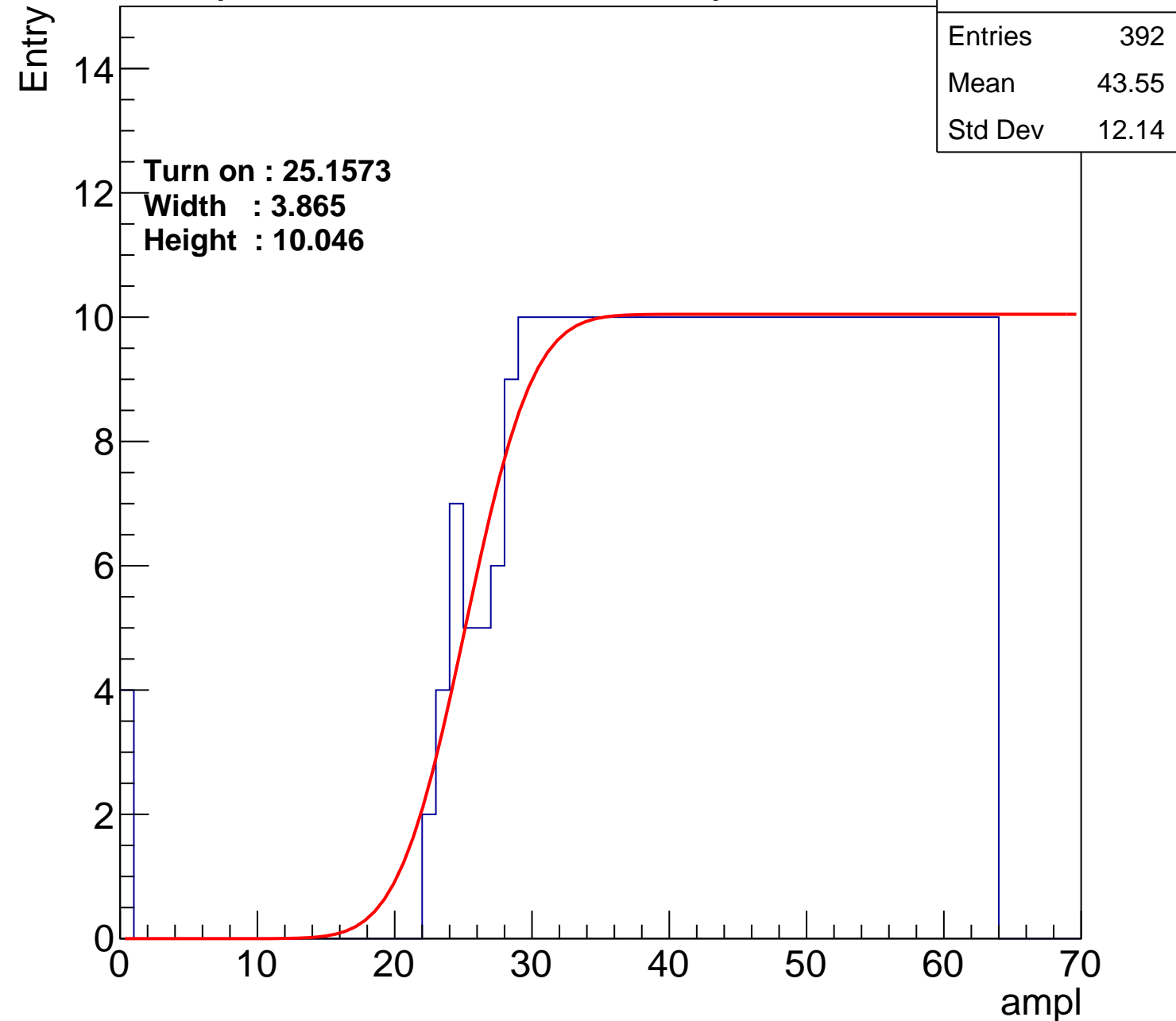
Width : 3.865

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.01
Std Dev	11.65

Turn on : 28.4185

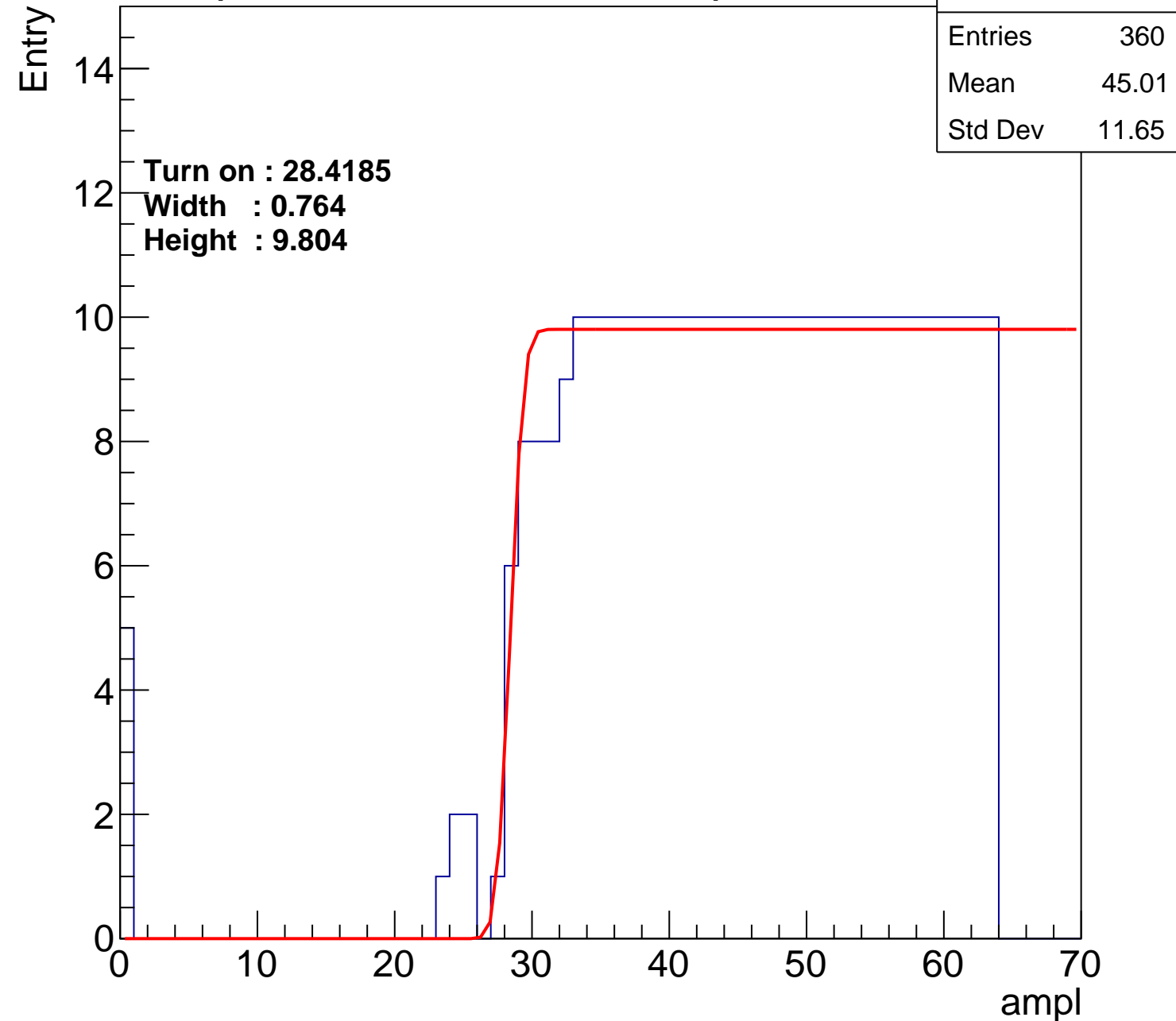
Width : 0.764

Height : 9.804

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.35
Std Dev	11.66

Turn on : 26.8234

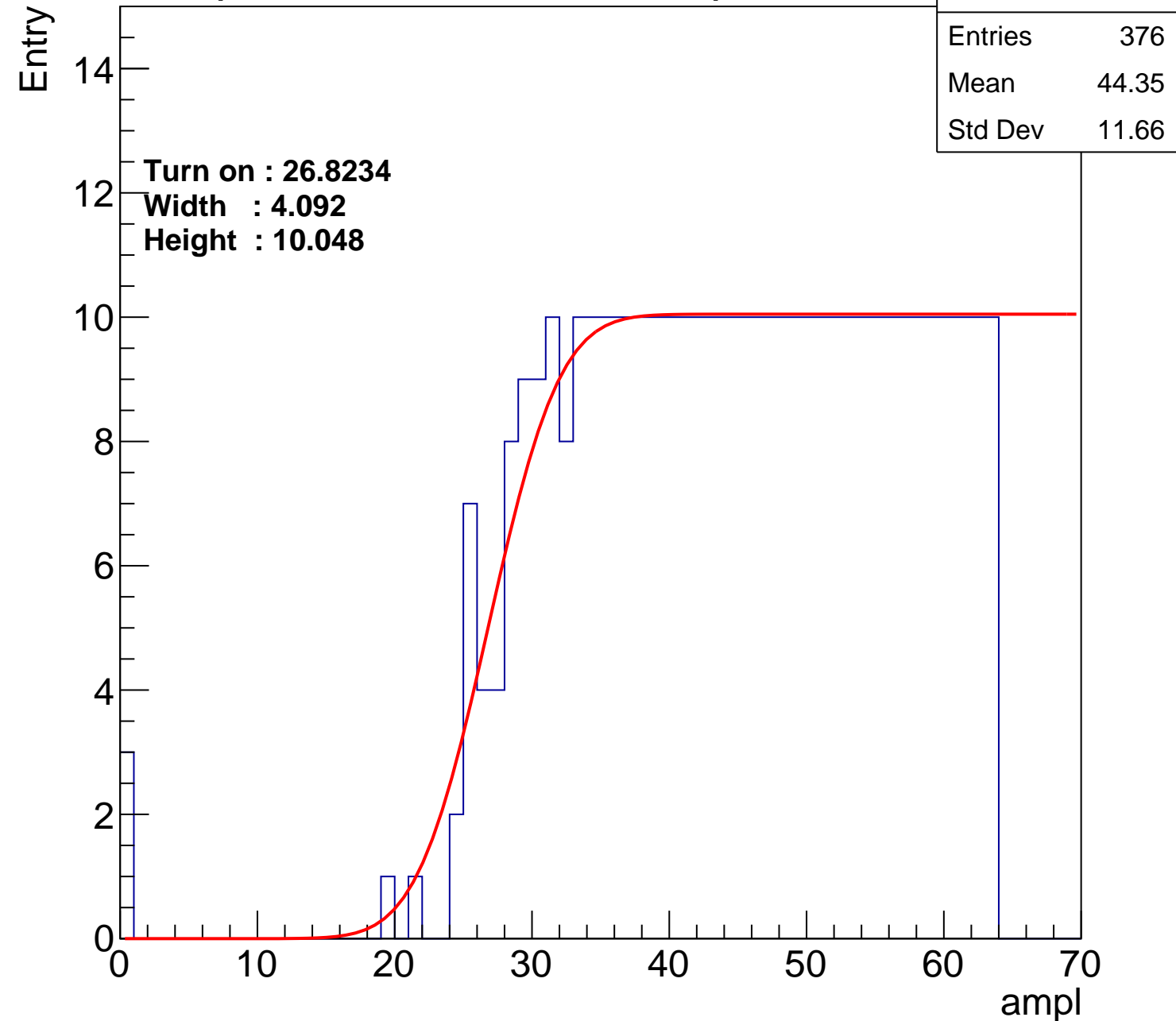
Width : 4.092

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch41

calib_packv5_042523_0143.root, FC#12, port B1

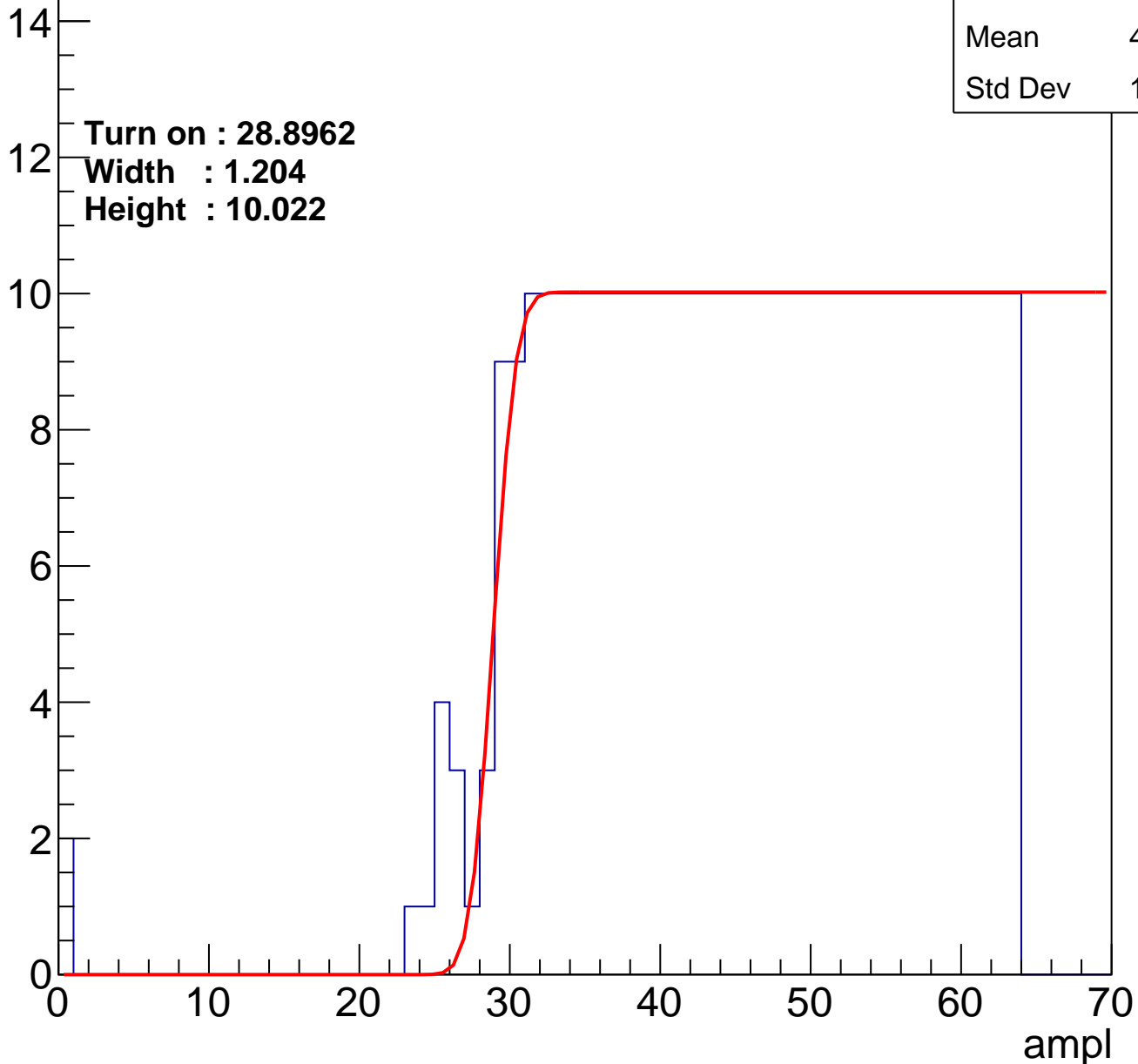
Entries	363
Mean	45.12
Std Dev	11.06

Turn on : 28.8962

Width : 1.204

Height : 10.022

Entry



B0L102S, U13-ch42

calib_packv5_042523_0143.root, FC#12, port B1

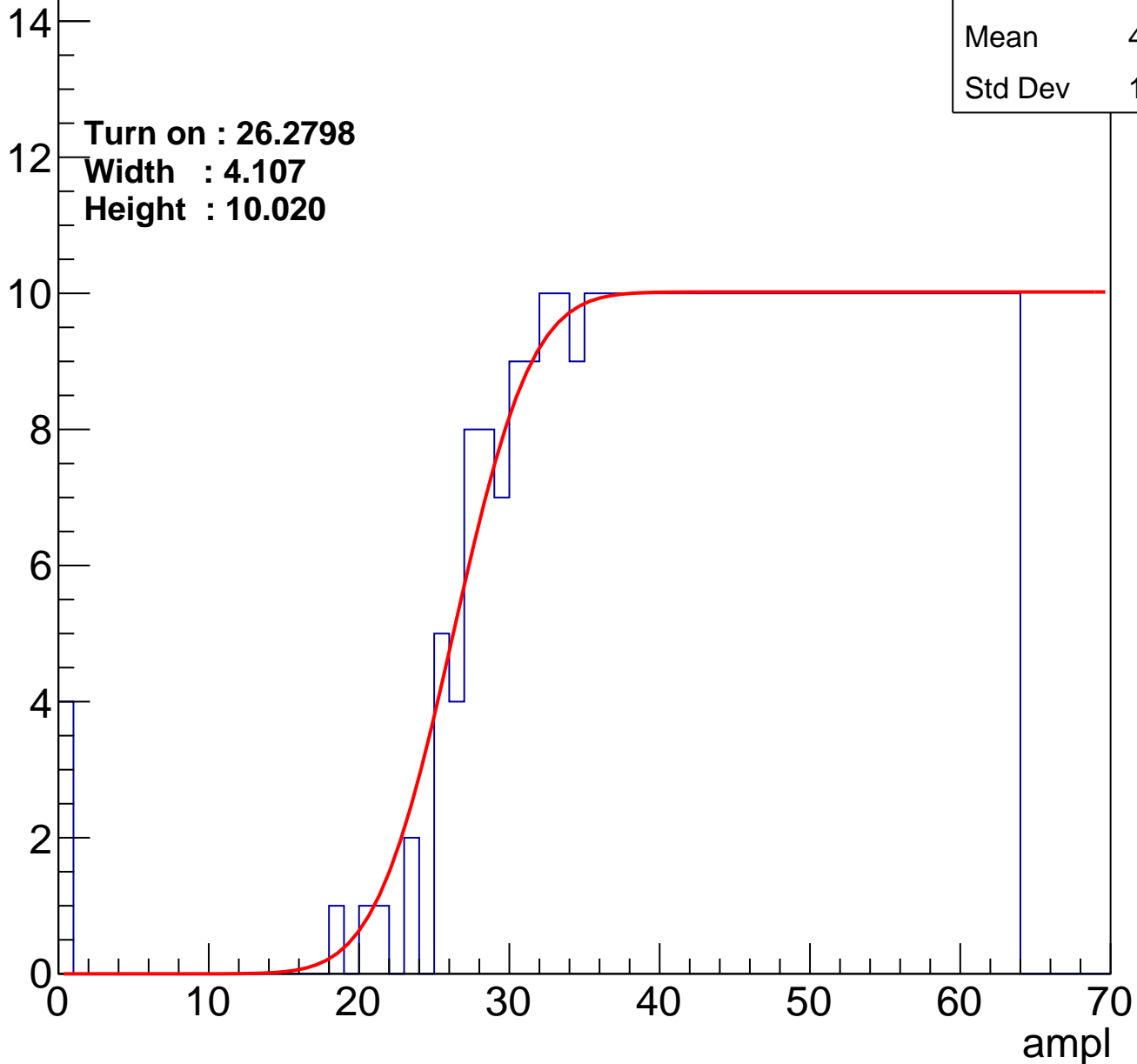
Entries	378
Mean	44.16
Std Dev	11.93

Turn on : 26.2798

Width : 4.107

Height : 10.020

Entry



B0L102S, U13-ch43

calib_packv5_042523_0143.root, FC#12, port B1

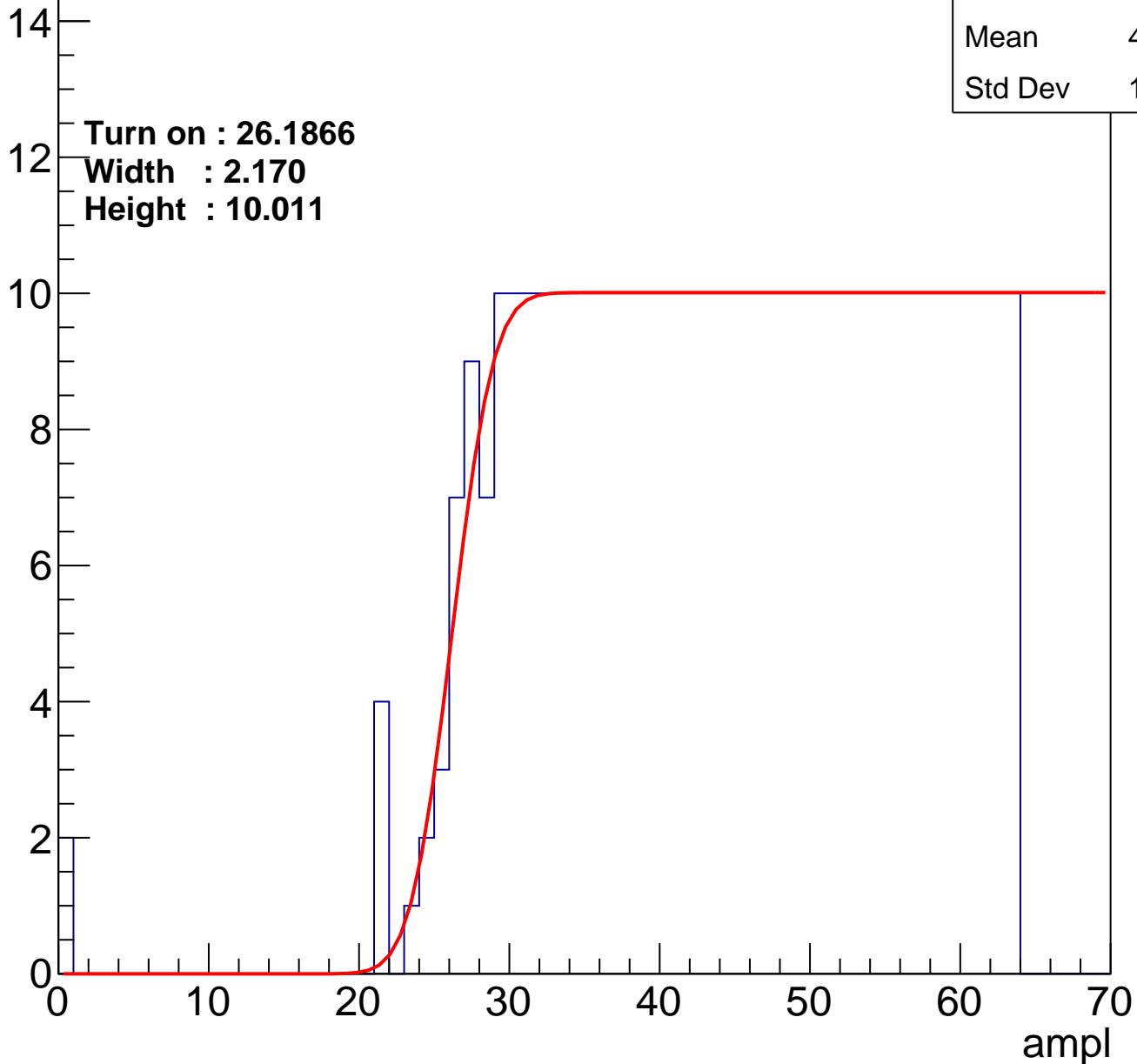
Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 26.1866

Width : 2.170

Height : 10.011

Entry



B0L102S, U13-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.75
Std Dev	11.81

Turn on : 25.5551

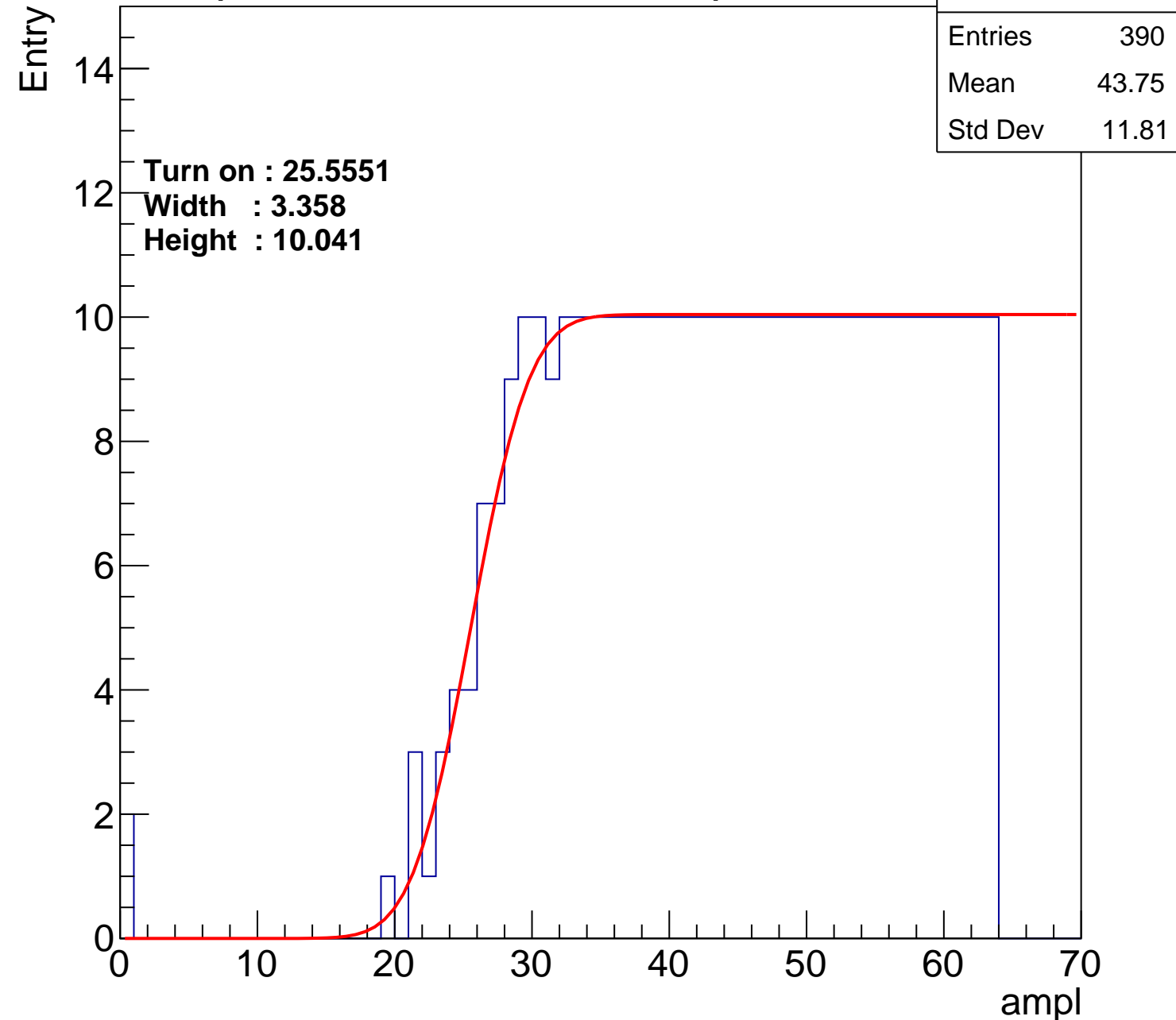
Width : 3.358

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.48
Std Dev	12.21

Turn on : 25.0456

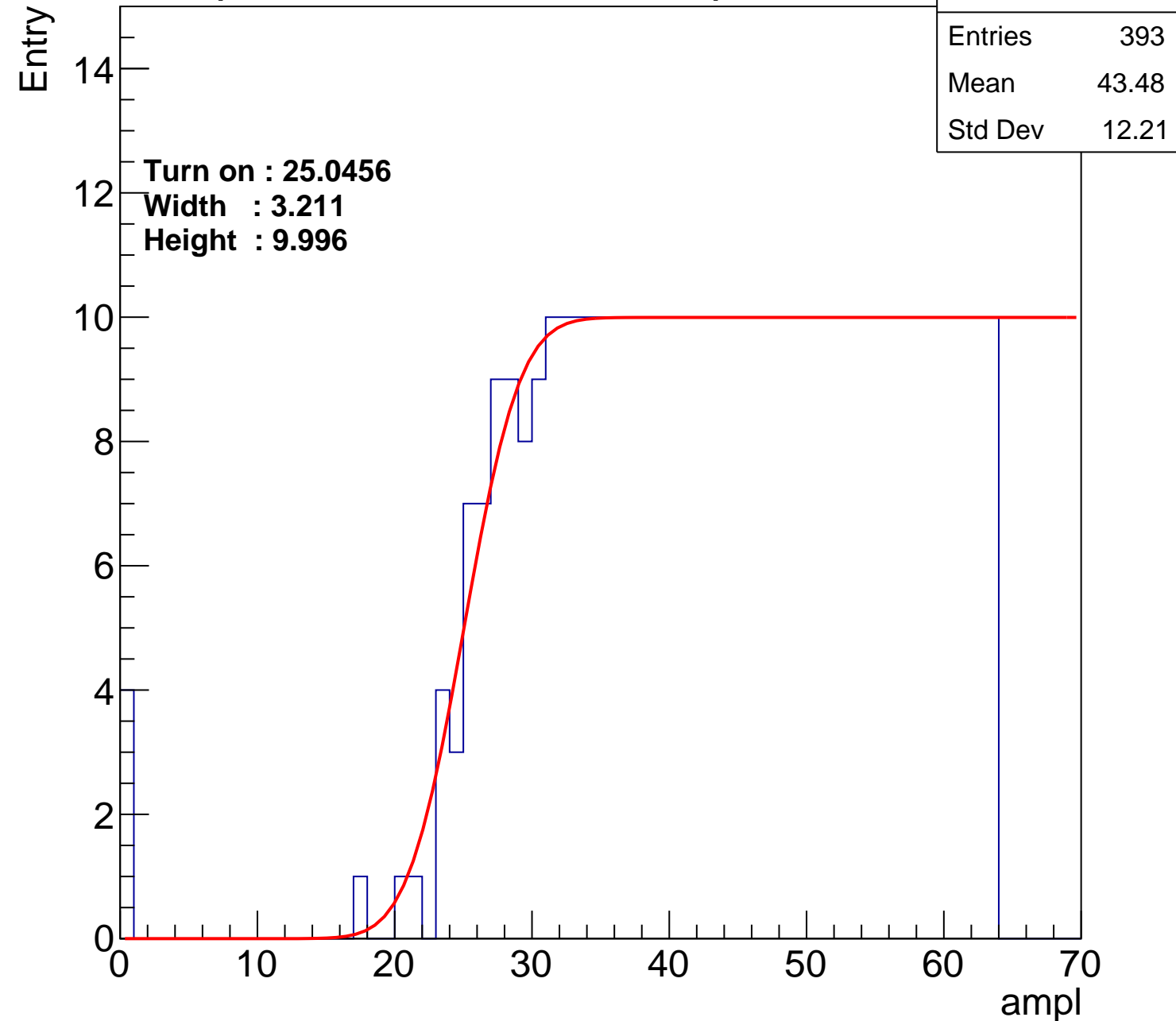
Width : 3.211

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.48
Std Dev	11.44

Turn on : 27.1132

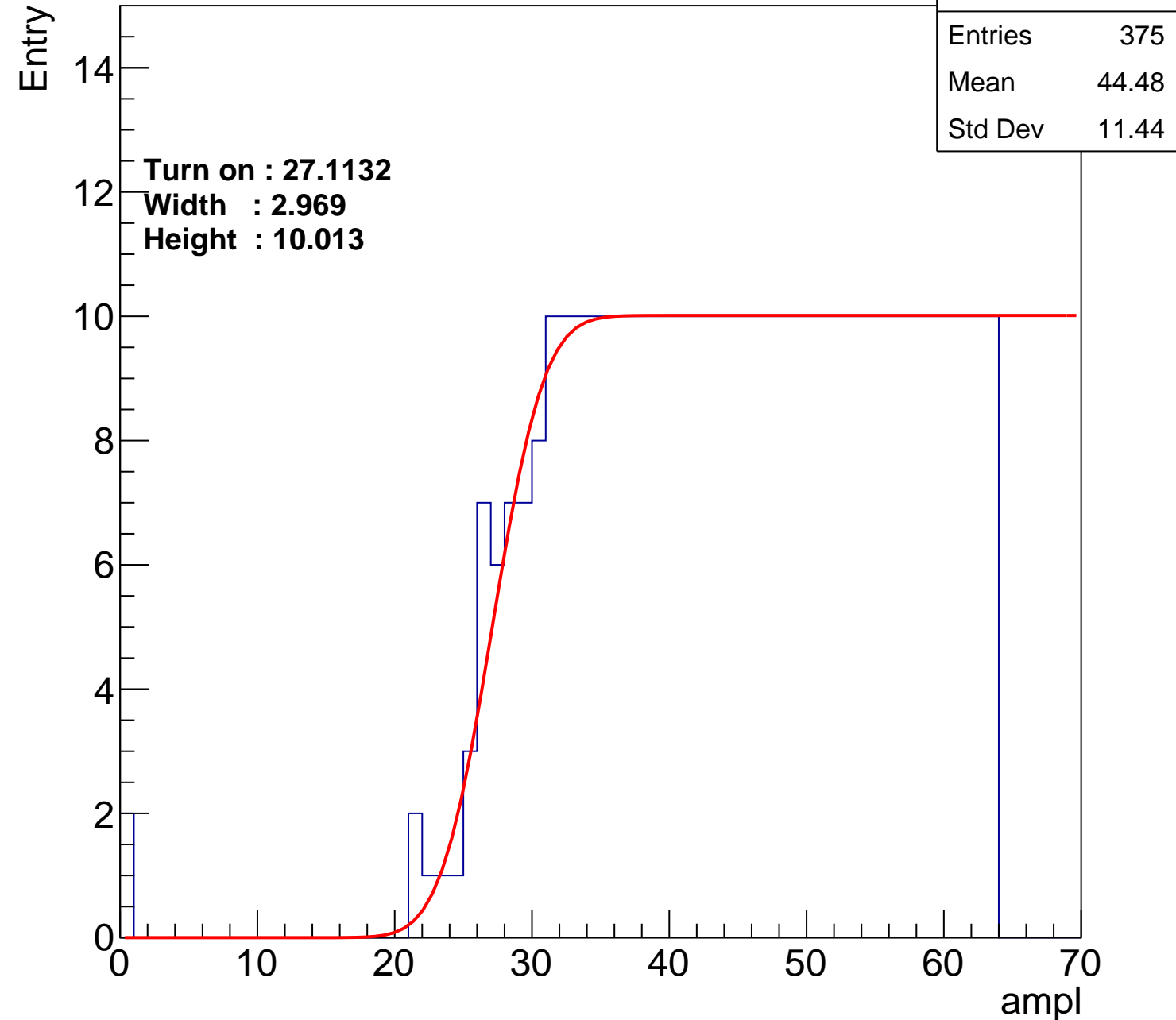
Width : 2.969

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 25.1650

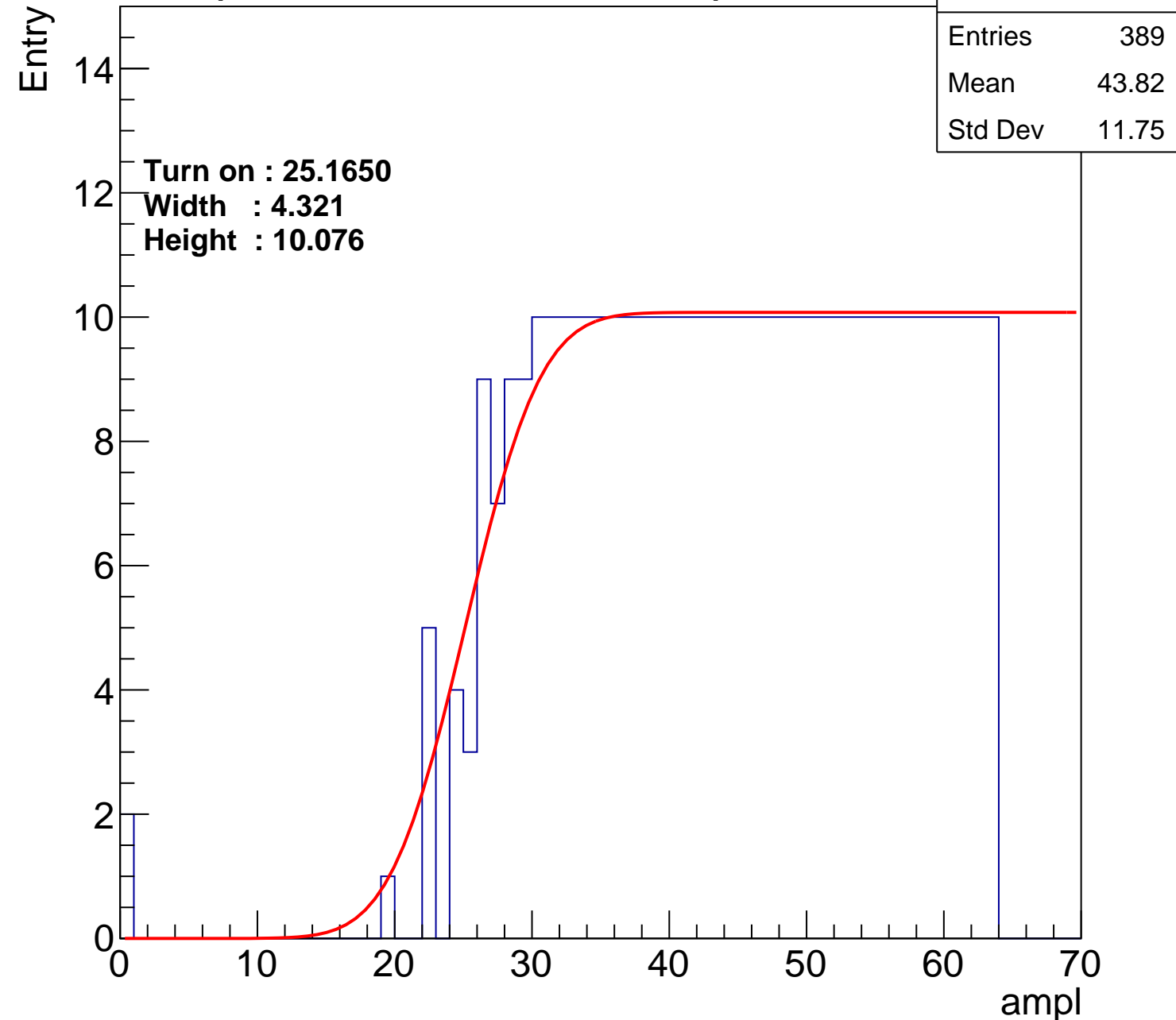
Width : 4.321

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.5966

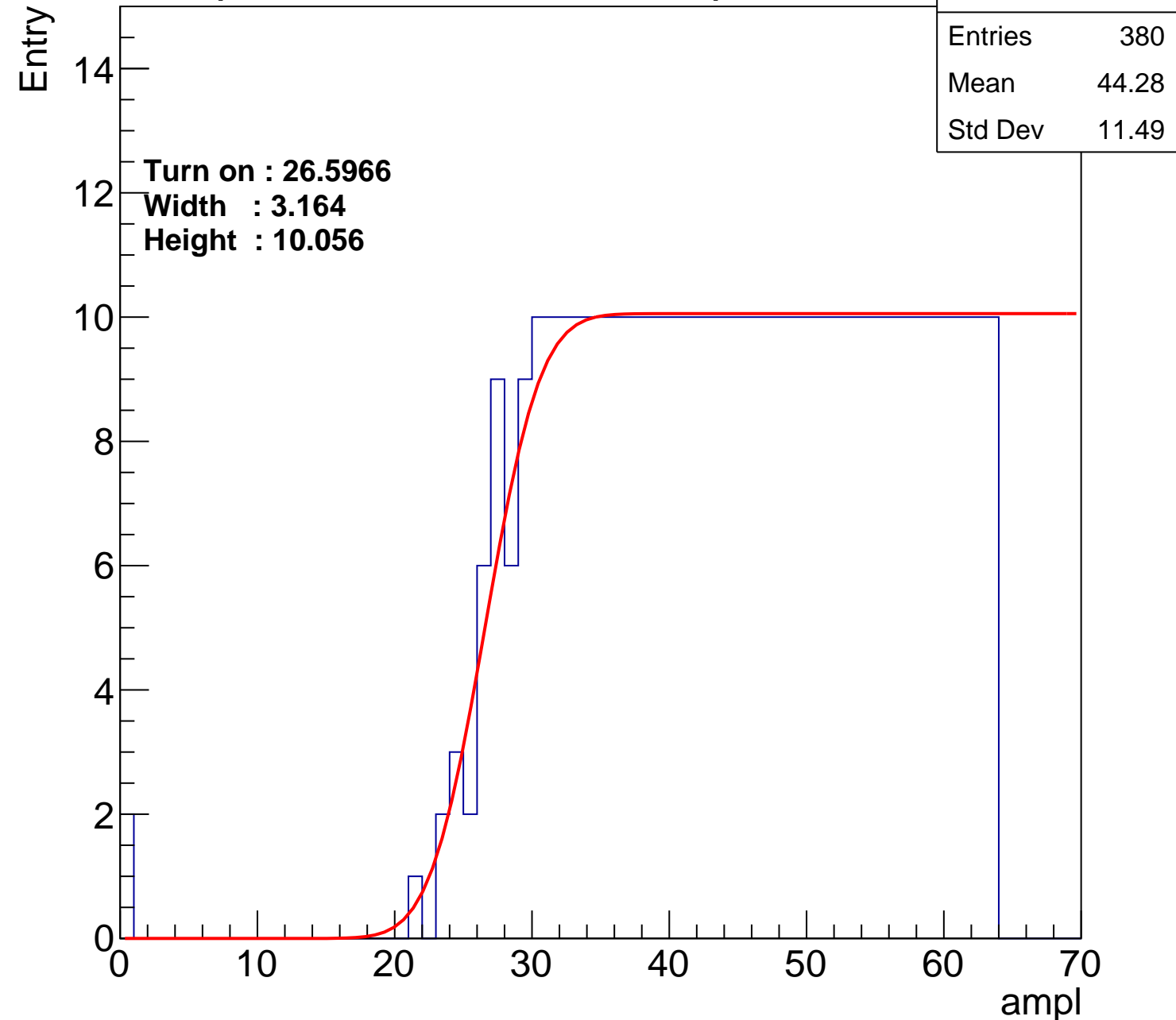
Width : 3.164

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 26.7388

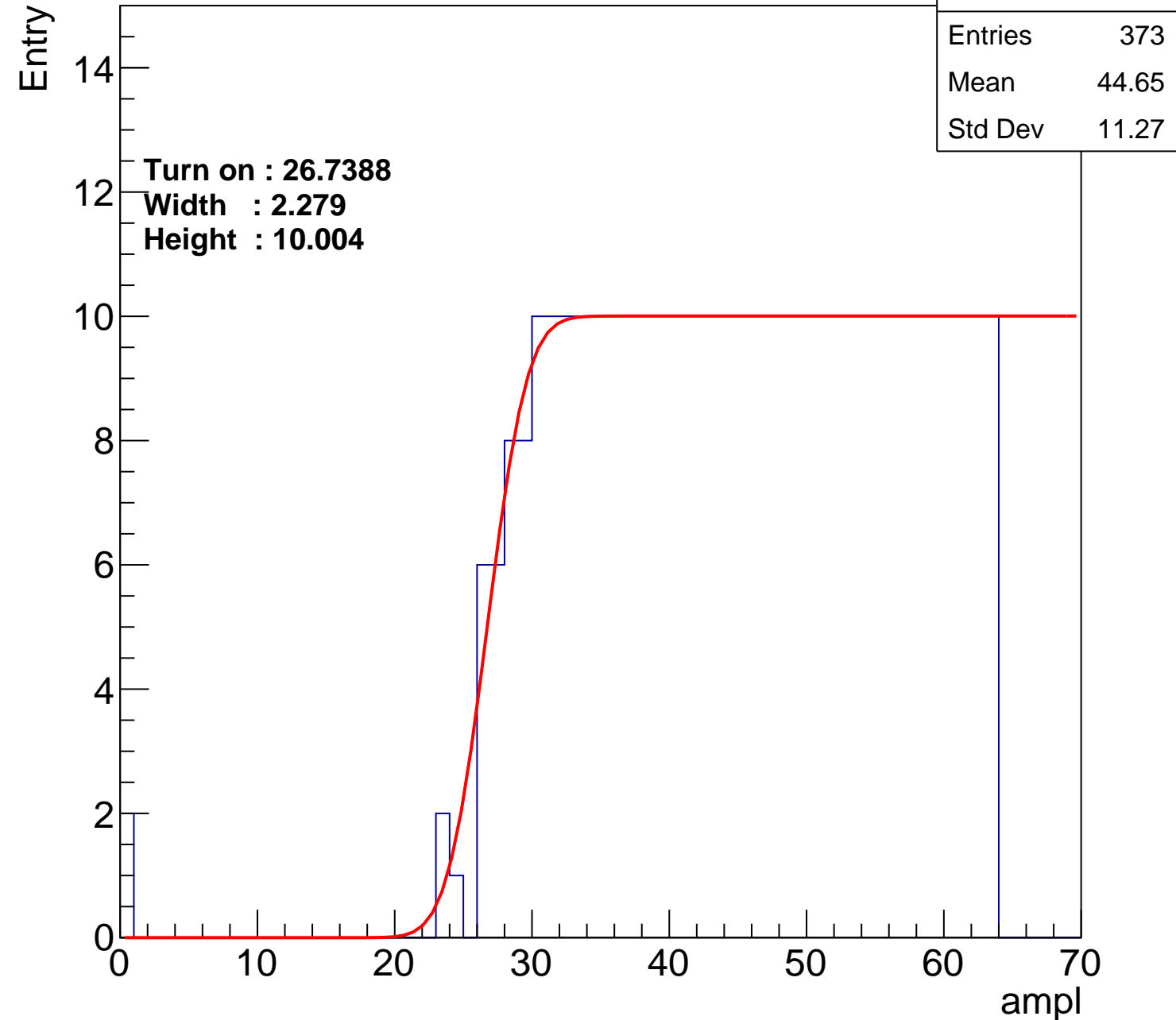
Width : 2.279

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.62

Turn on : 25.9658

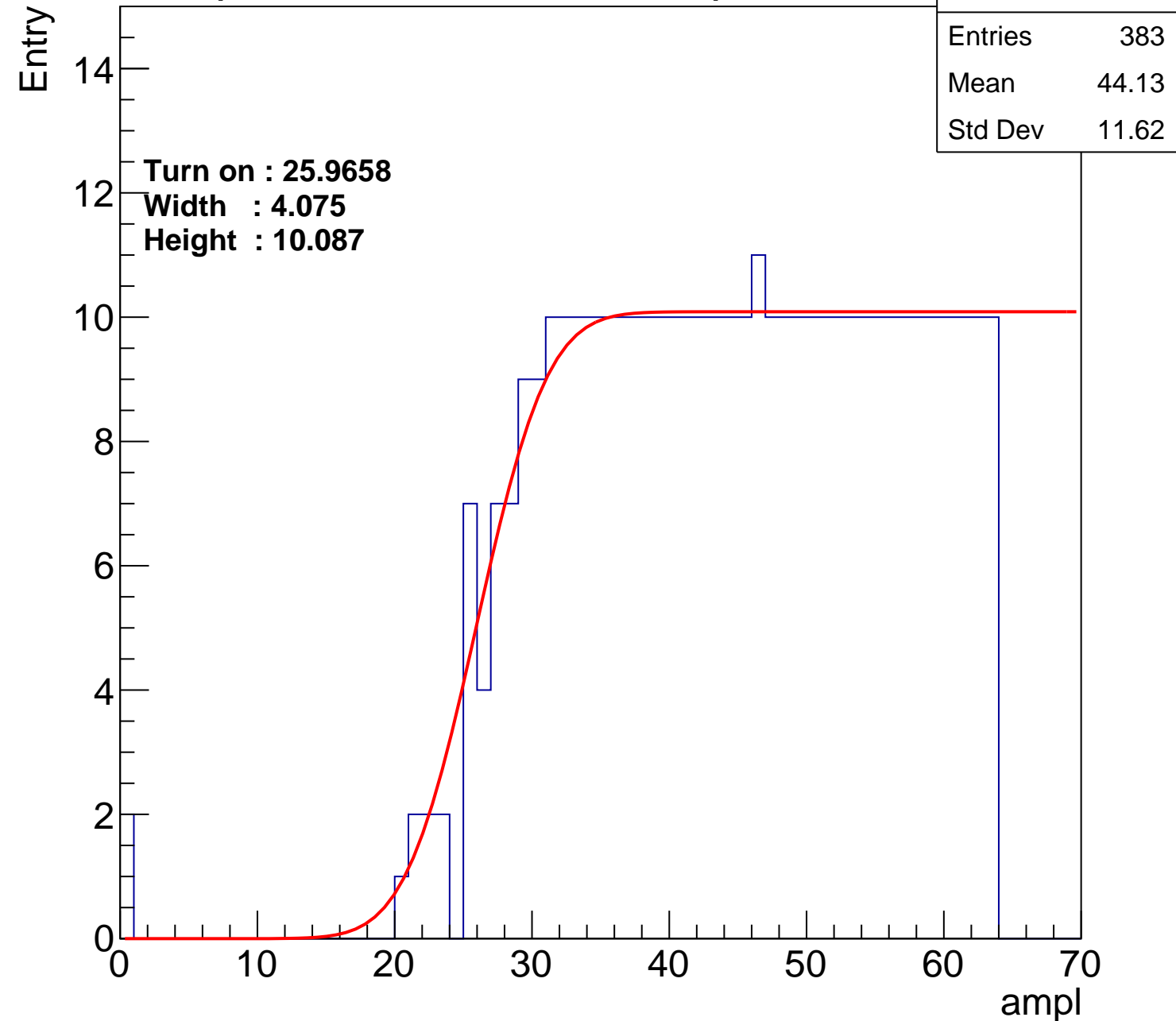
Width : 4.075

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.5483

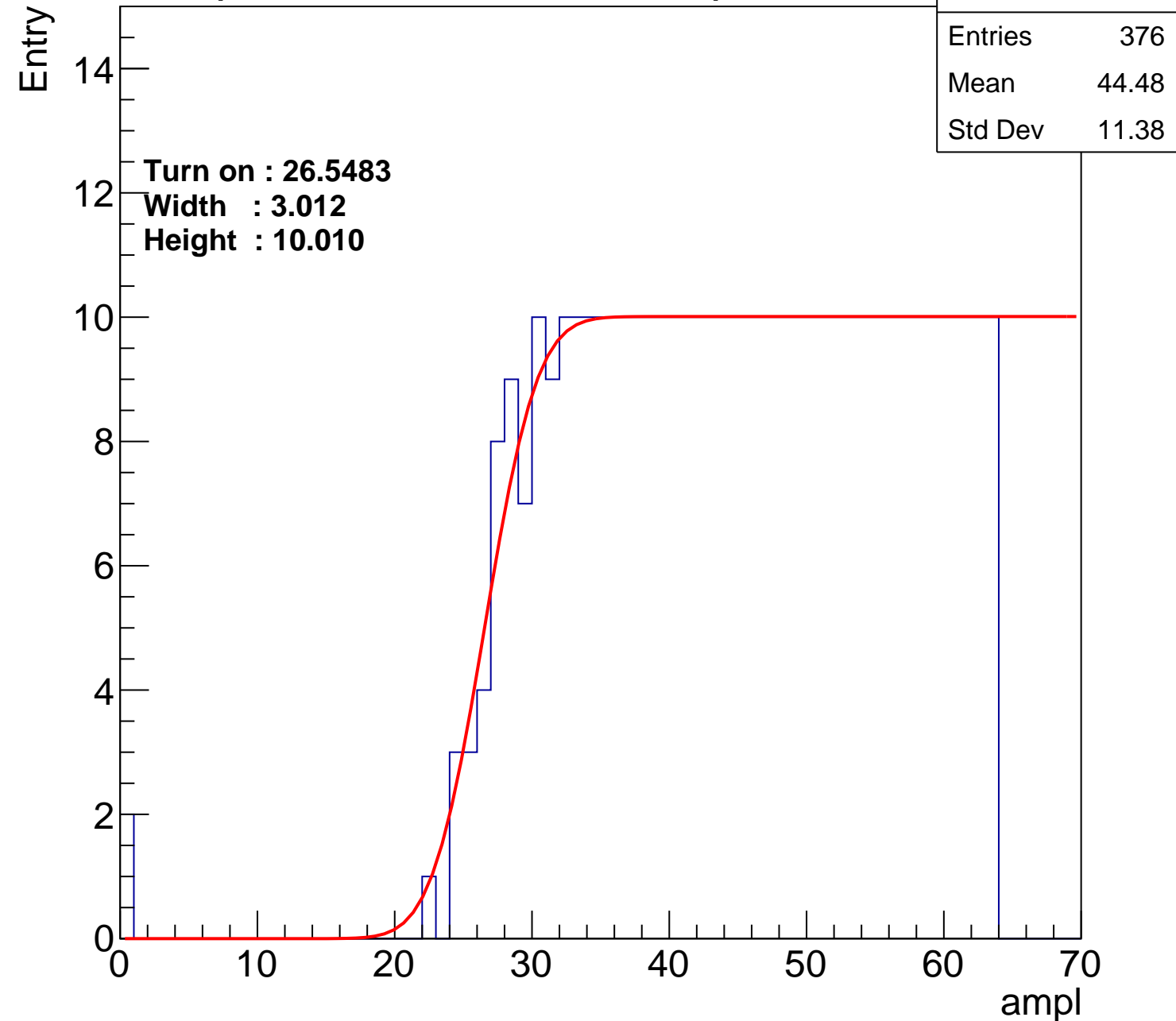
Width : 3.012

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	44.88
Std Dev	11.6

Turn on : 28.3613

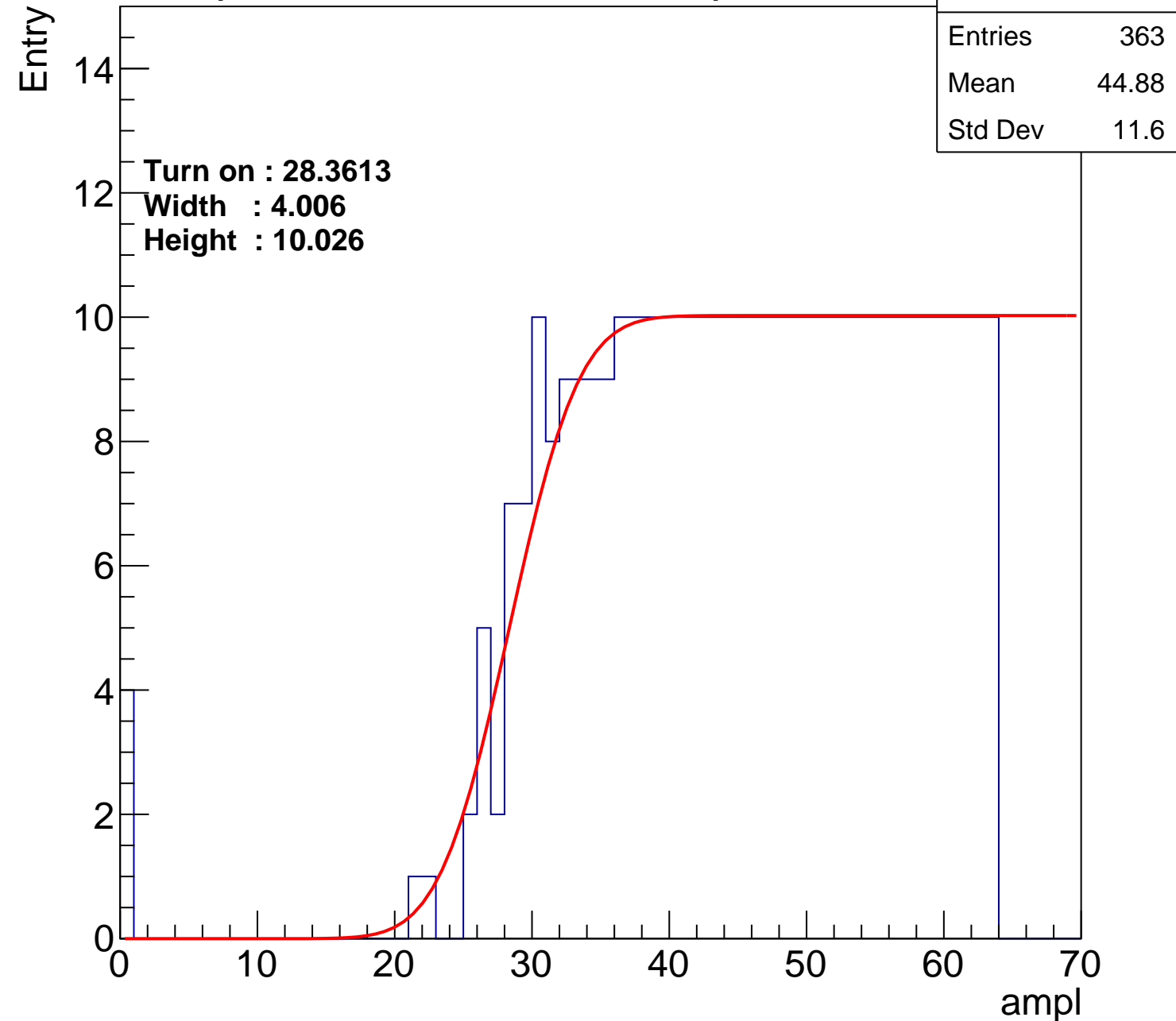
Width : 4.006

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.61
Std Dev	11.51

Turn on : 27.3271

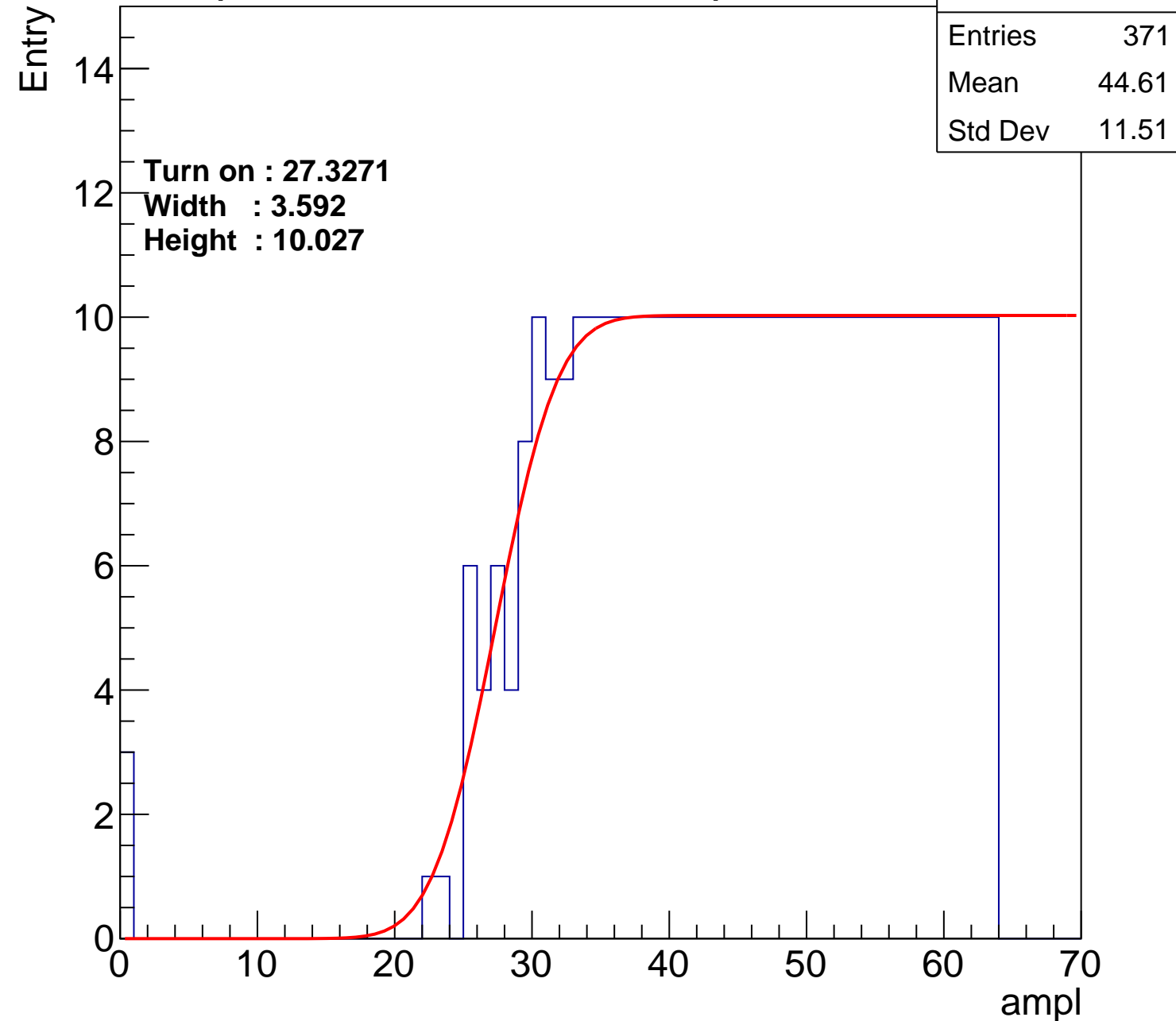
Width : 3.592

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.3690

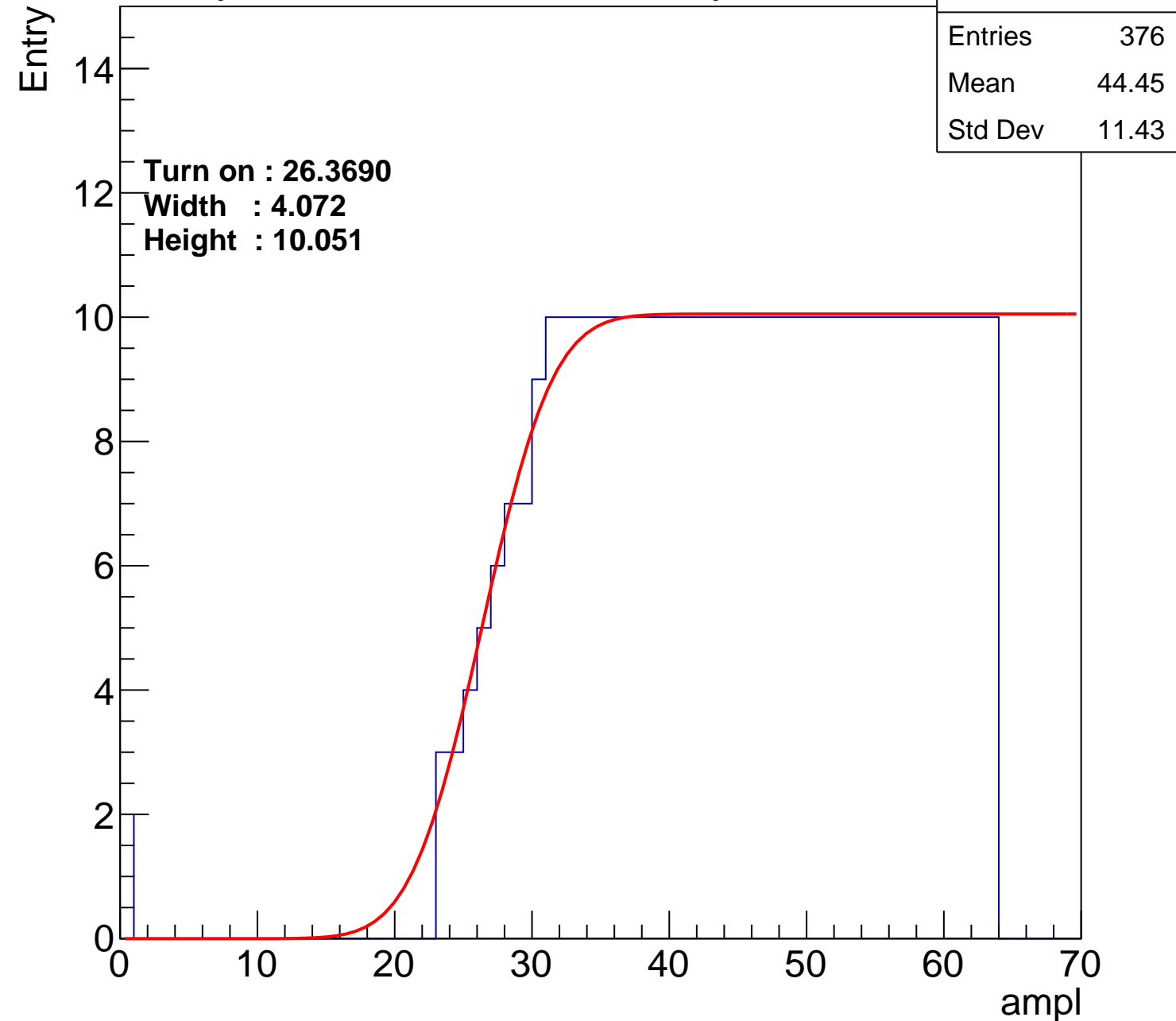
Width : 4.072

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.87

Turn on : 24.9025

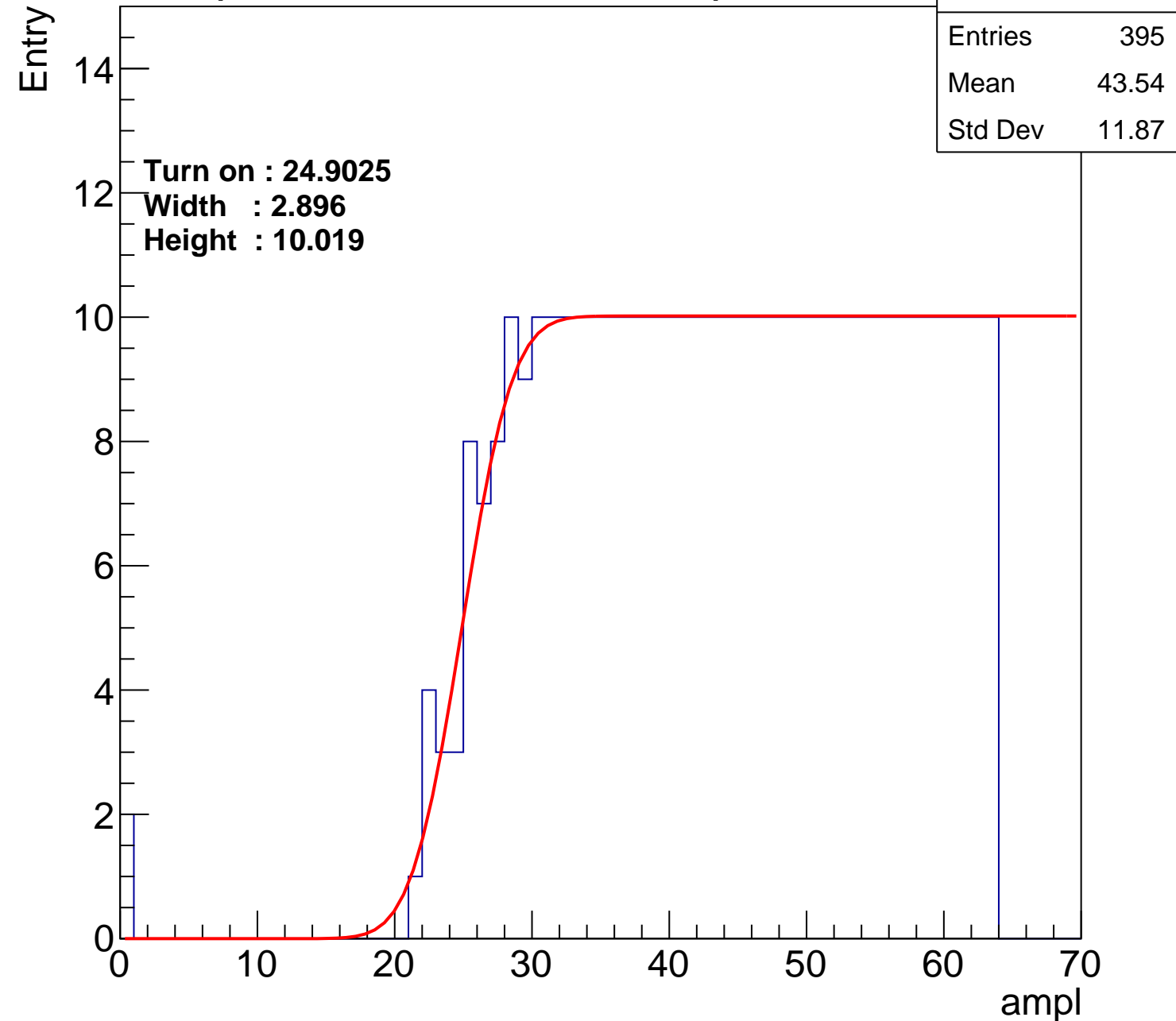
Width : 2.896

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.26
Std Dev	11.51

Turn on : 25.4730

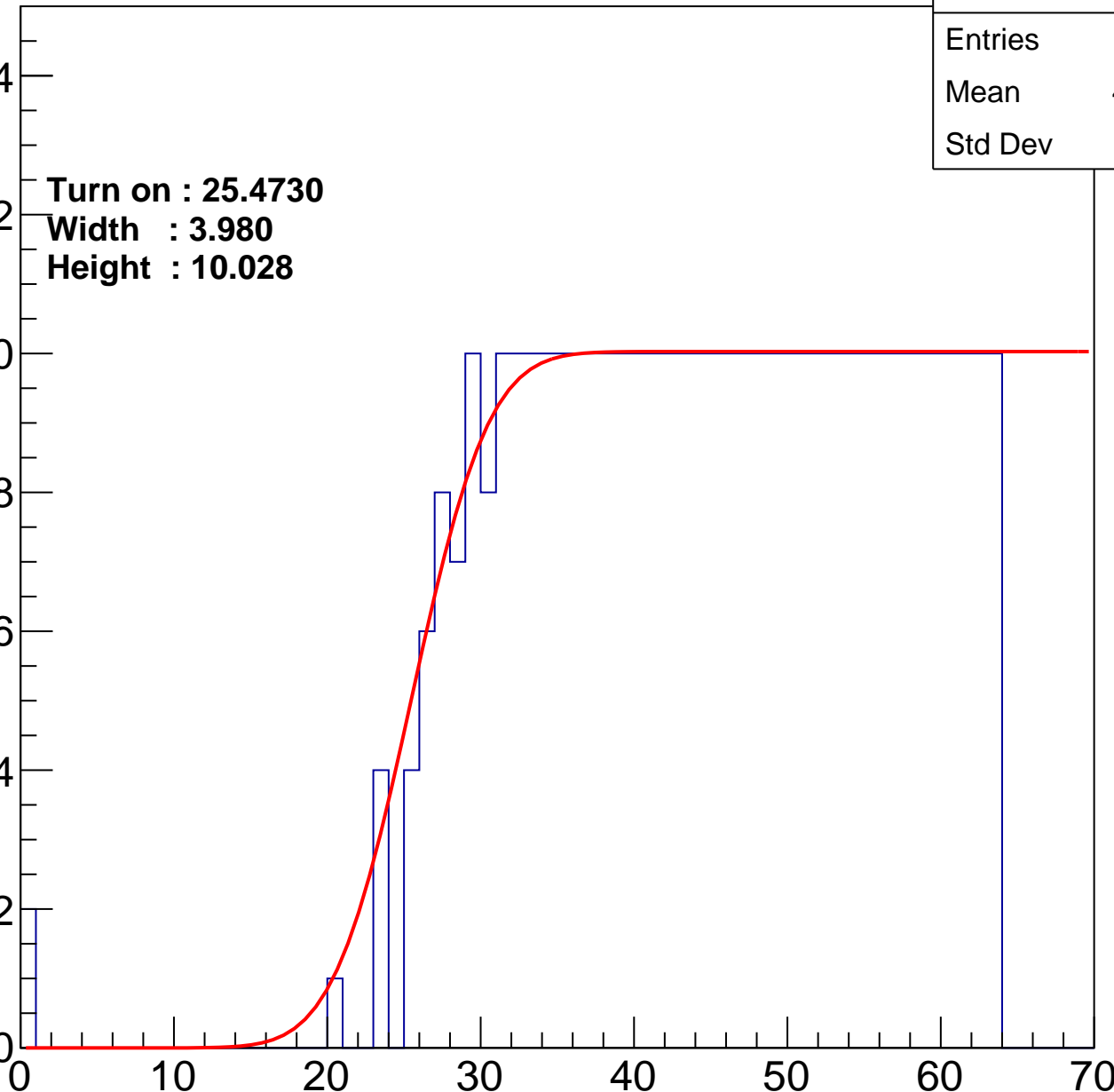
Width : 3.980

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.7765

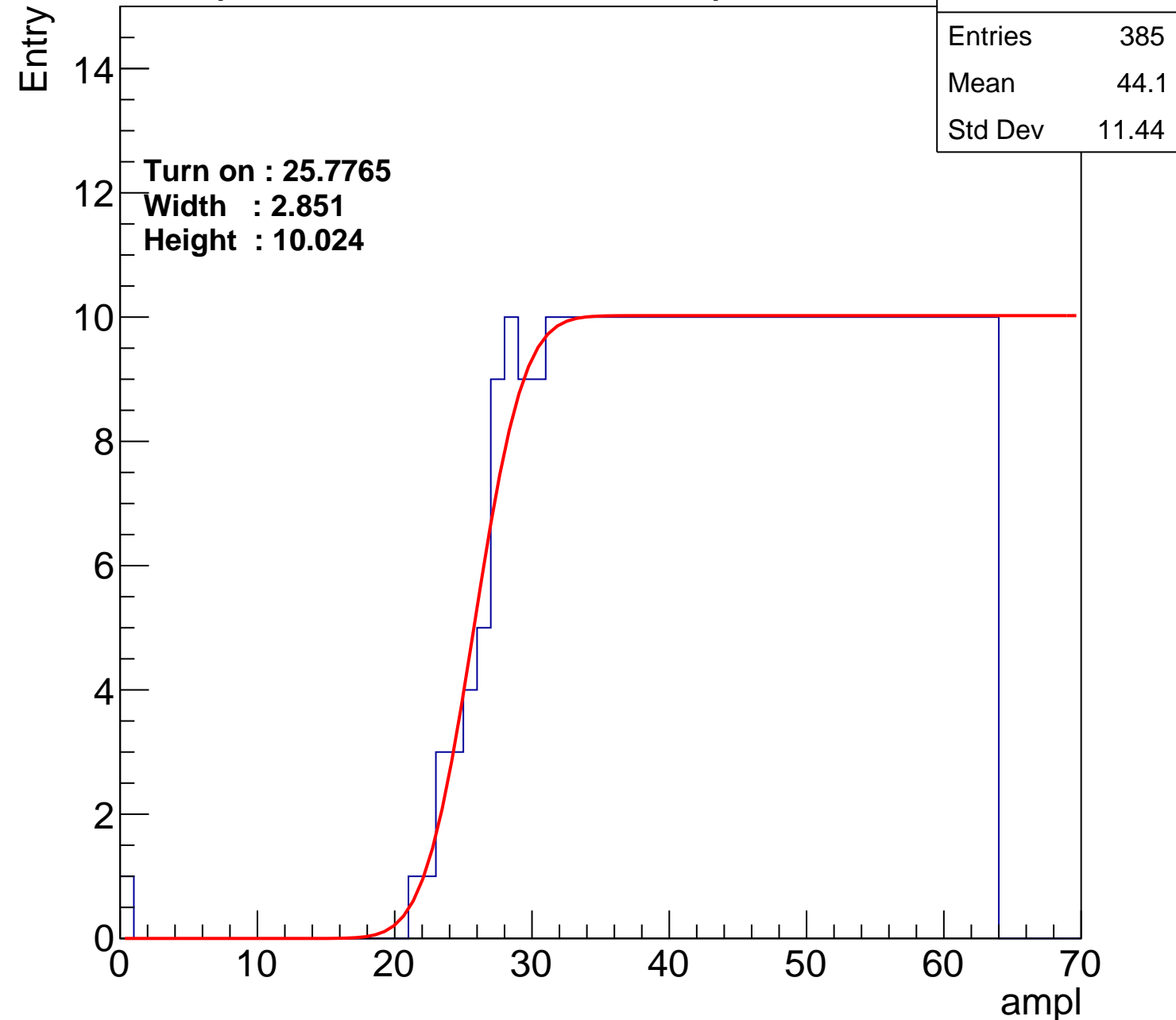
Width : 2.851

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.65
Std Dev	12.4

Turn on : 23.1796

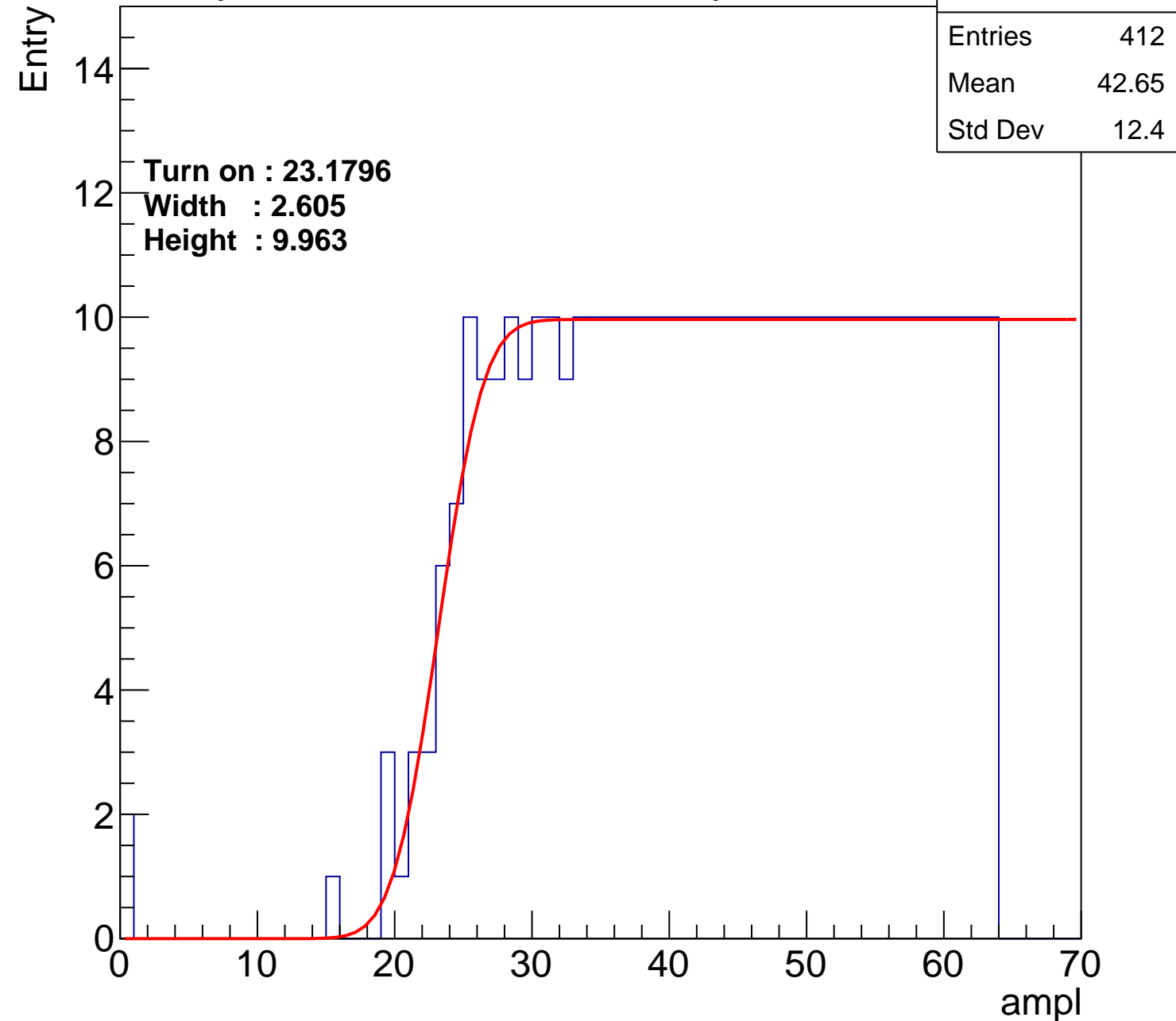
Width : 2.605

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch59

calib_packv5_042523_0143.root, FC#12, port B1

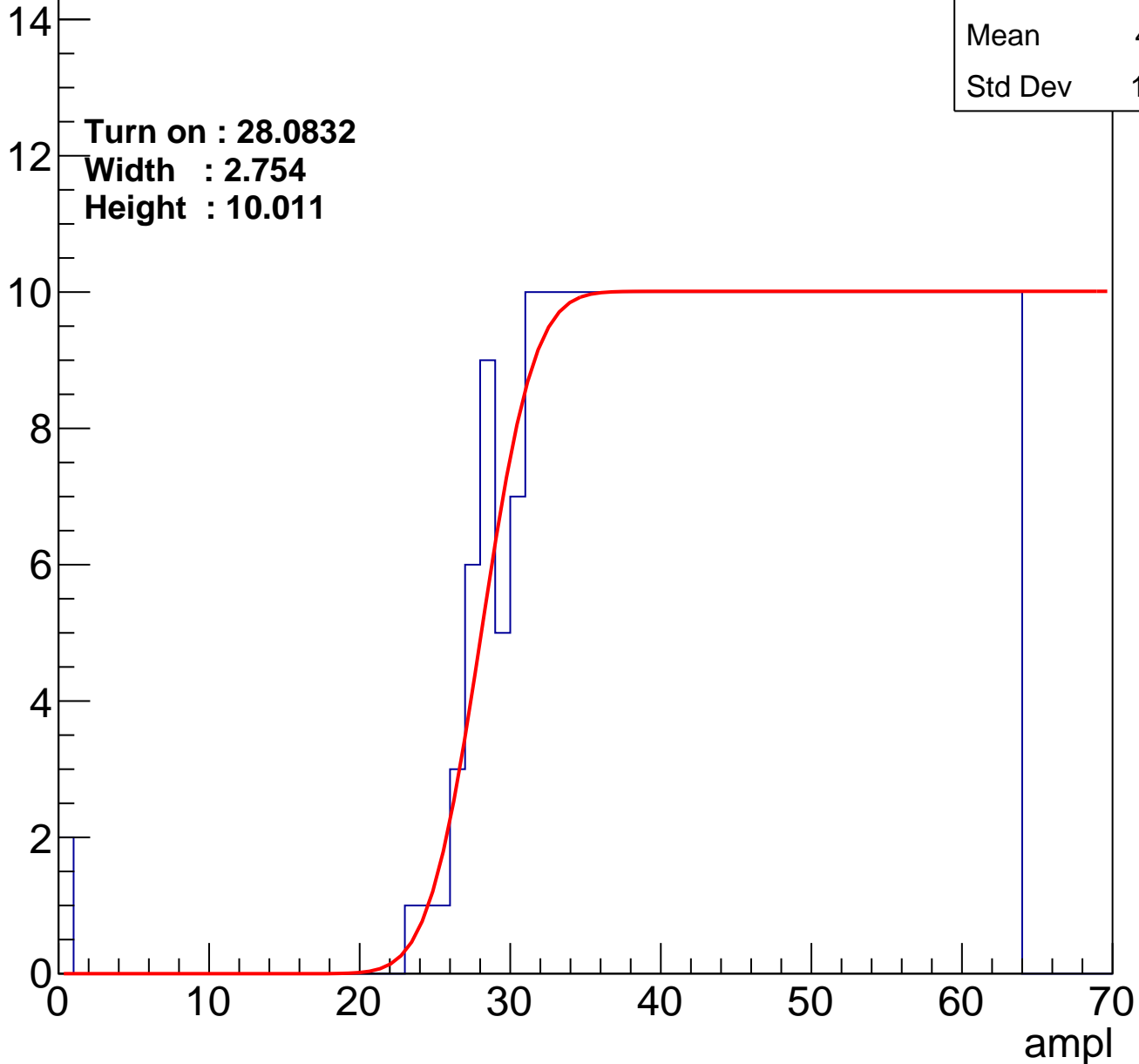
Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 28.0832

Width : 2.754

Height : 10.011

Entry



B0L102S, U13-ch60

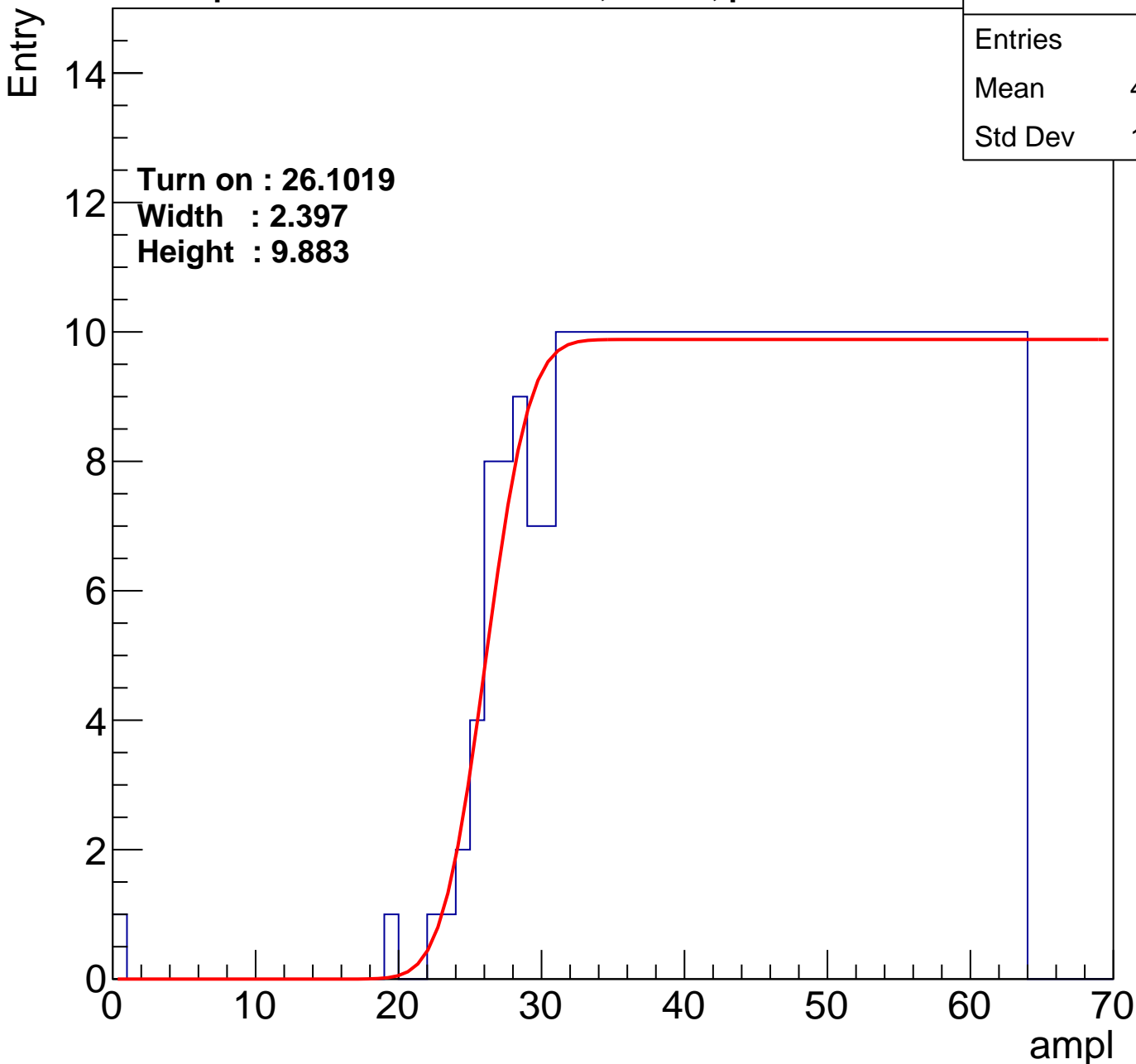
calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.36
Std Dev	11.34

Turn on : 26.1019

Width : 2.397

Height : 9.883



B0L102S, U13-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 27.4805

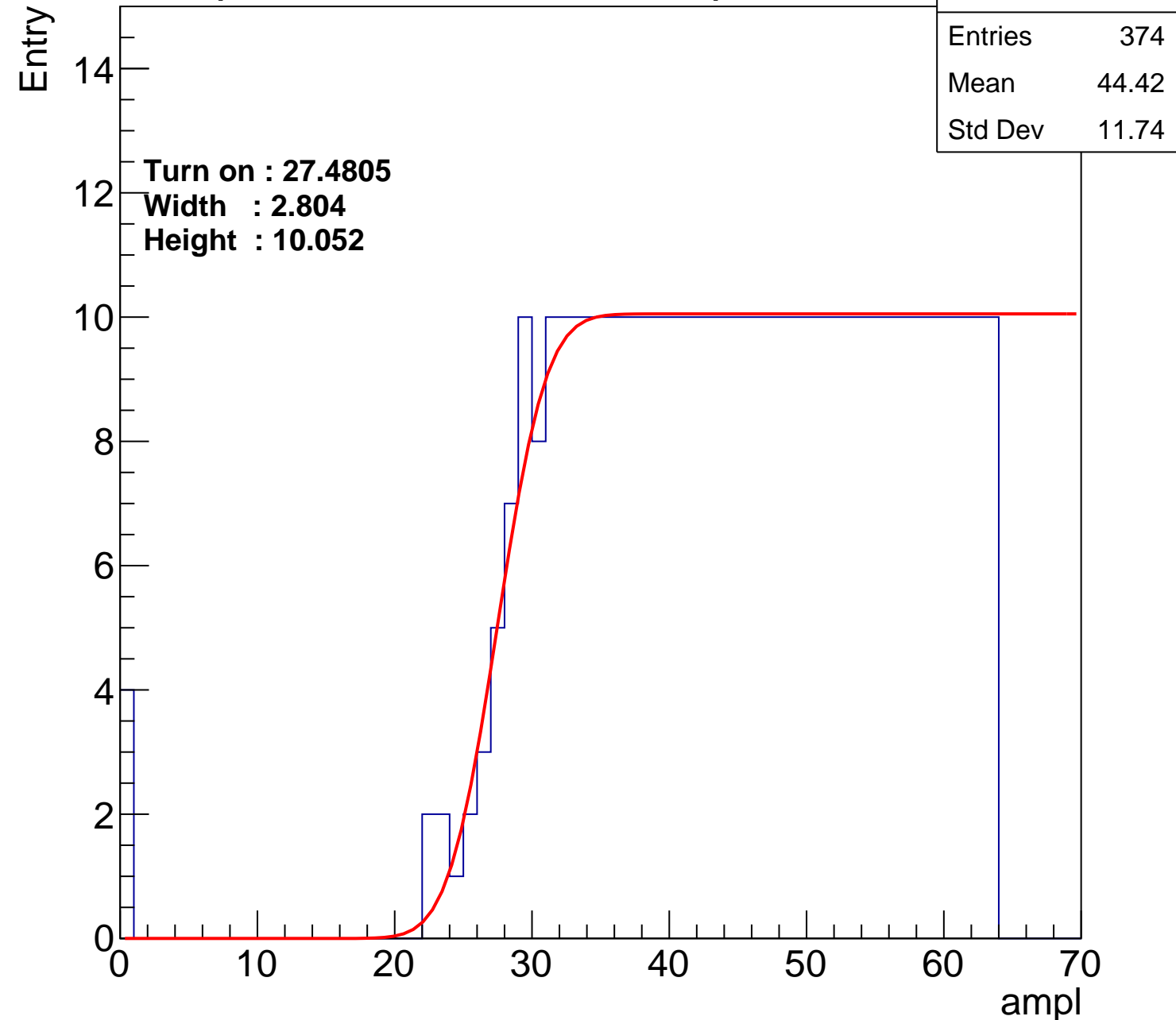
Width : 2.804

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.16
Std Dev	11.44

Turn on : 25.9115

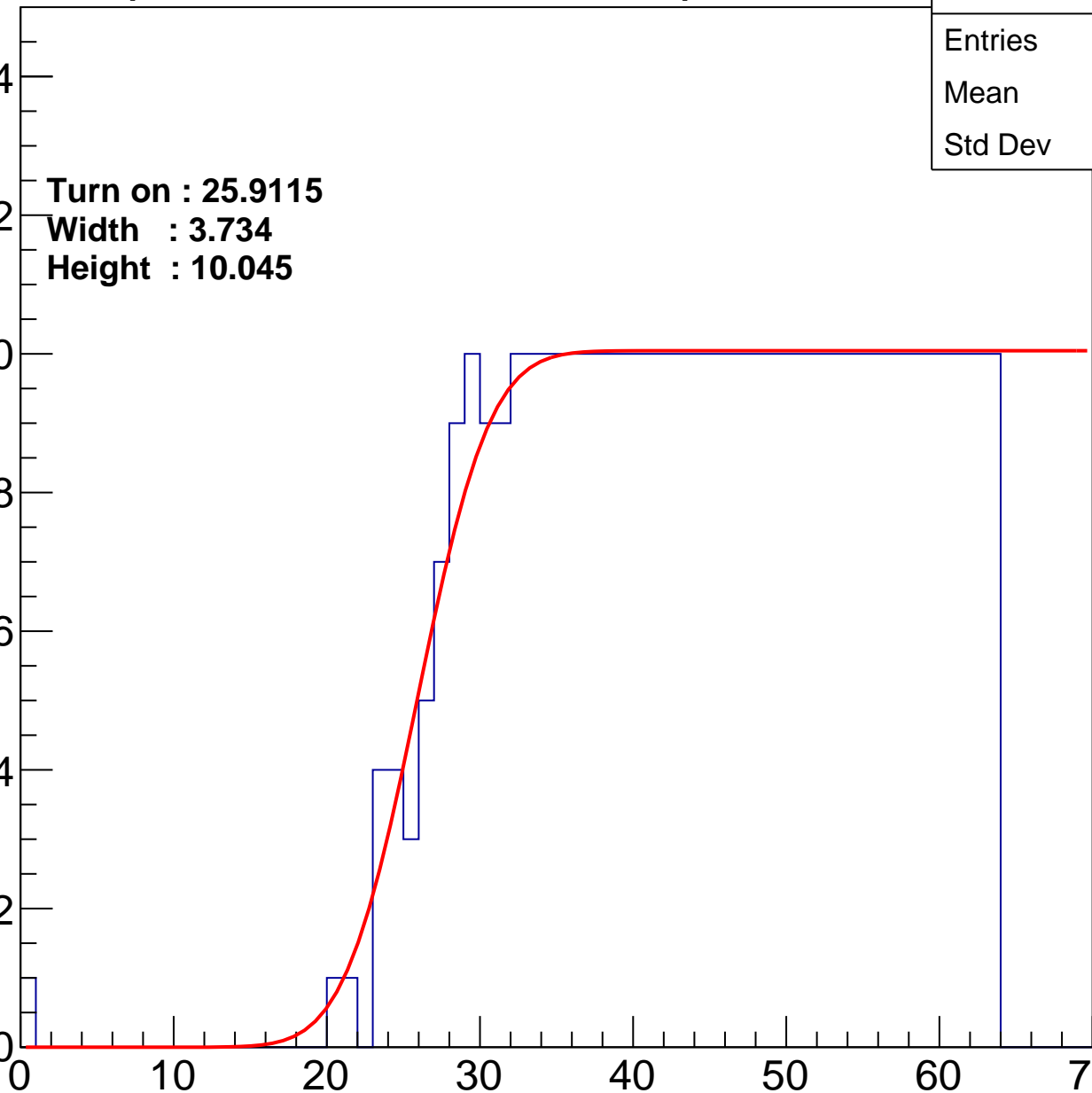
Width : 3.734

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.95
Std Dev	11.02

Turn on : 27.2507

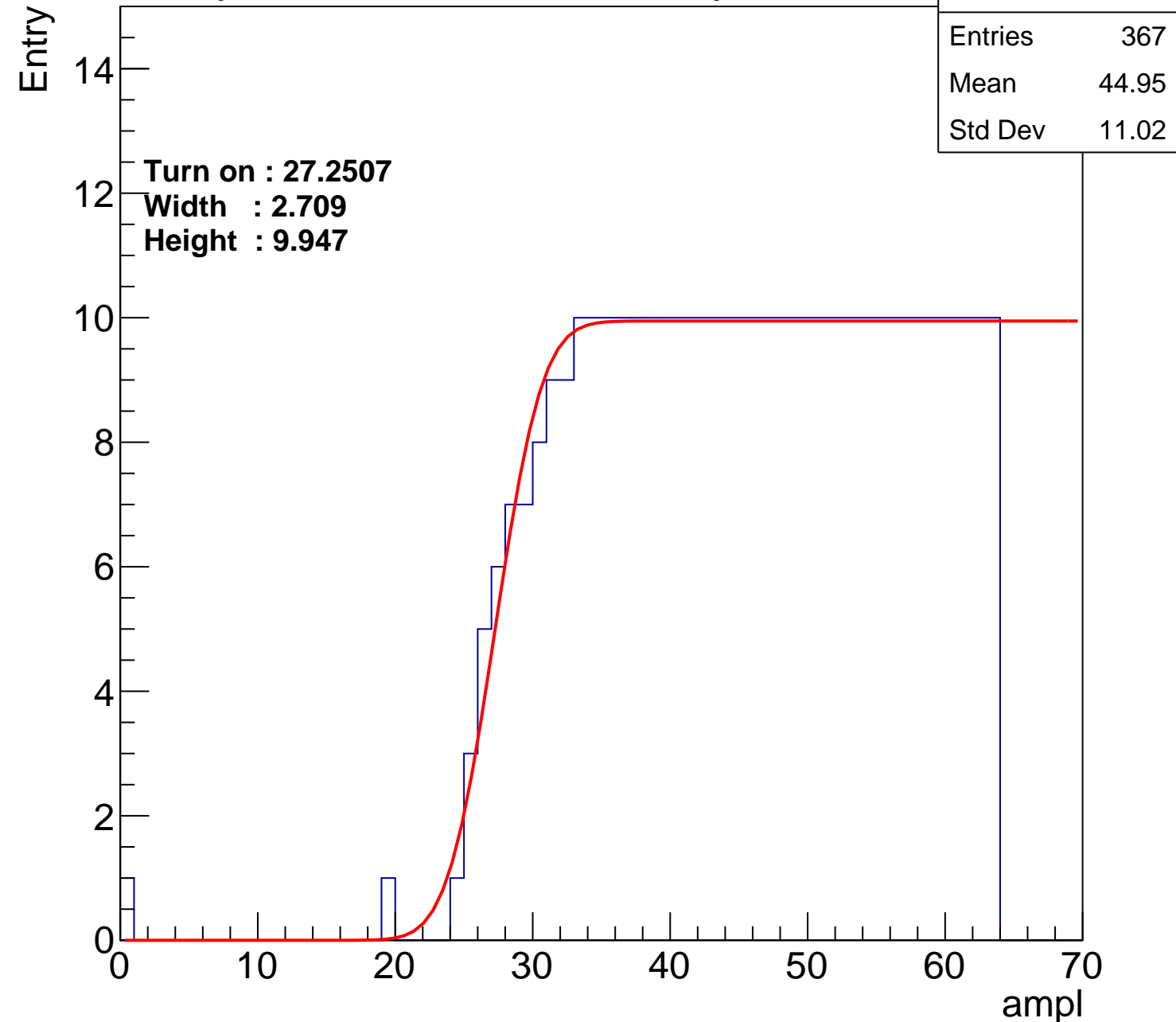
Width : 2.709

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.75
Std Dev	12.24

Turn on : 22.7926

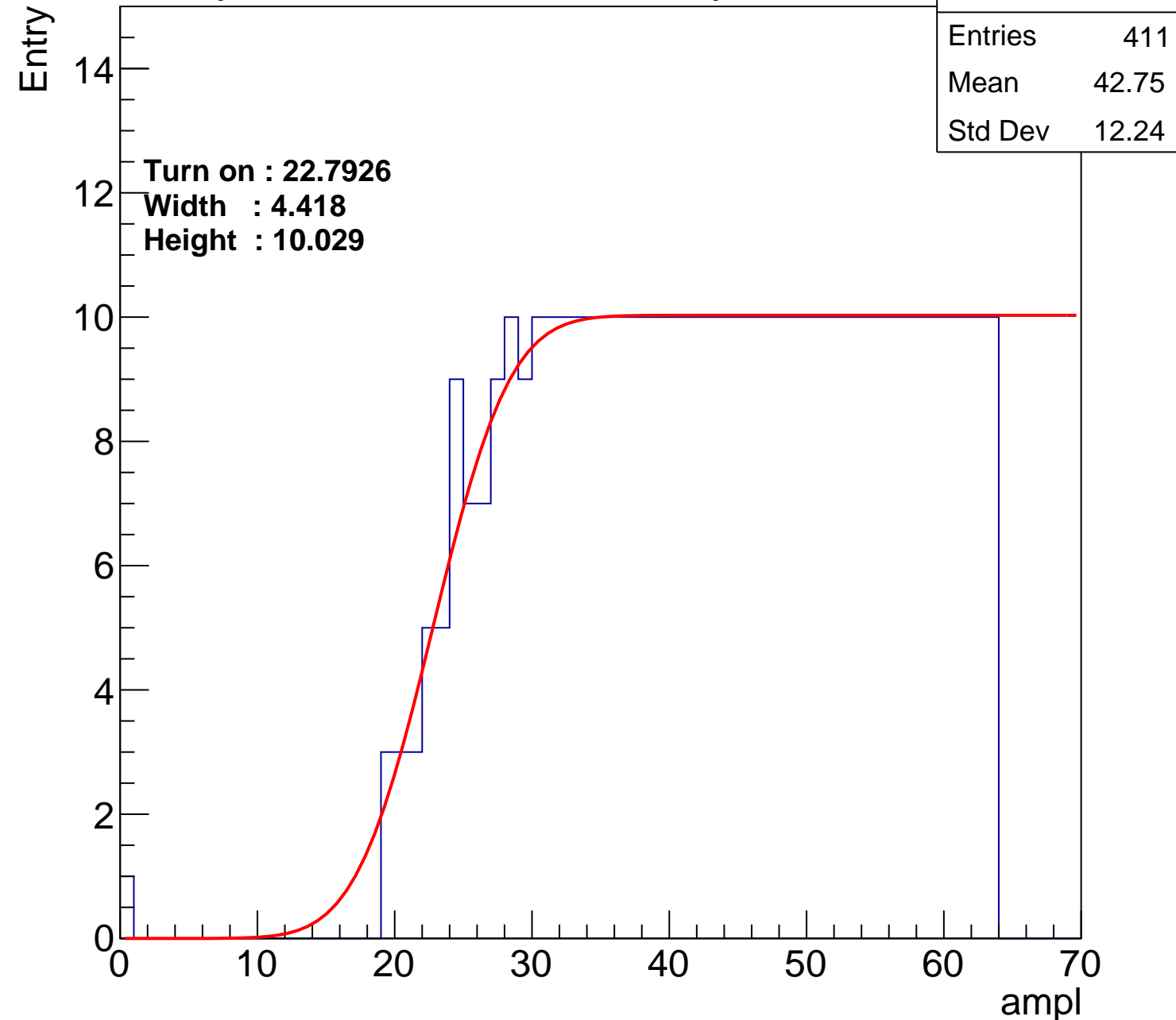
Width : 4.418

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch65

calib_packv5_042523_0143.root, FC#12, port B1

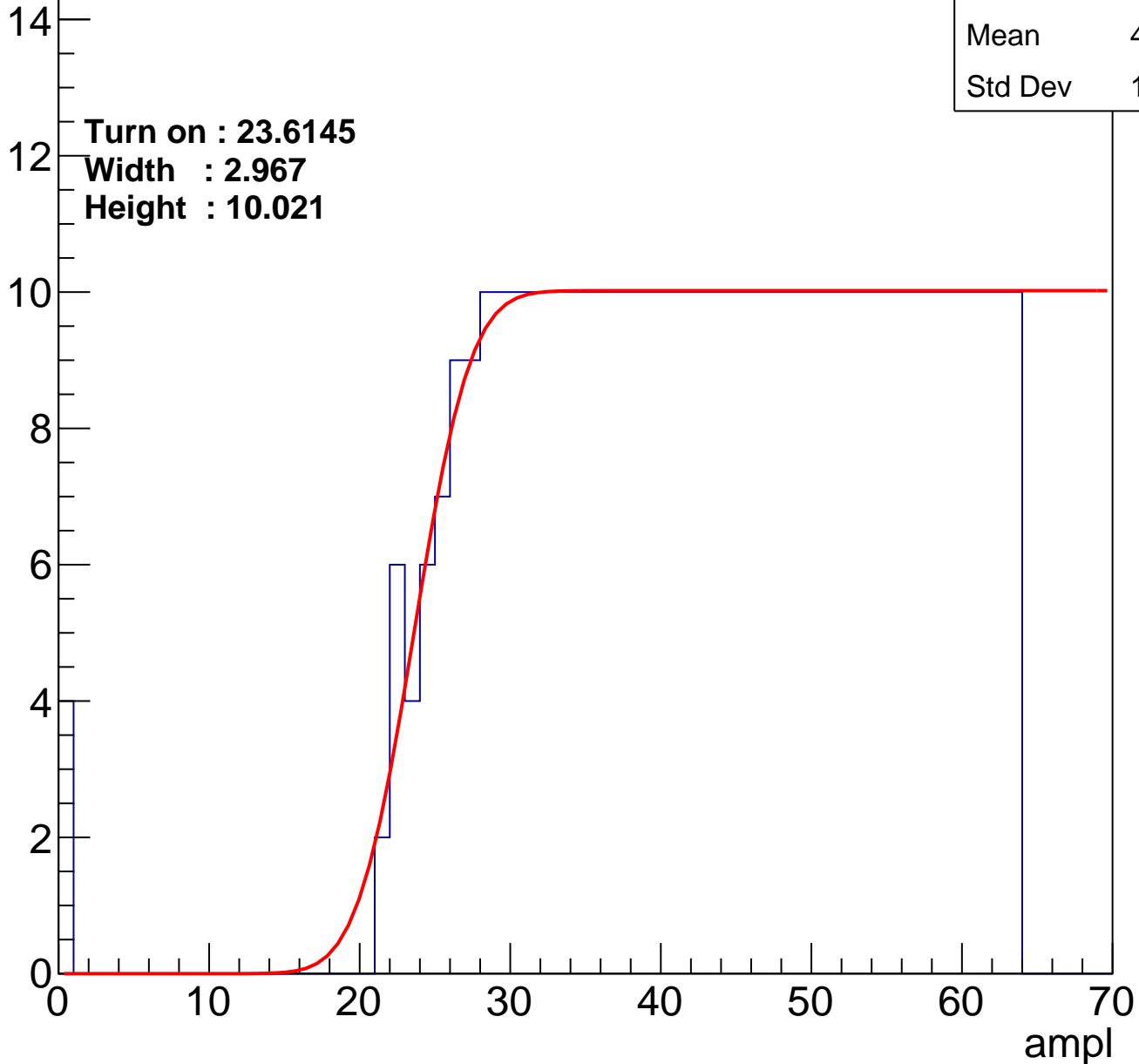
Entries	407
Mean	42.86
Std Dev	12.45

Turn on : 23.6145

Width : 2.967

Height : 10.021

Entry



B0L102S, U13-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.77
Std Dev	11.49

Turn on : 27.5433

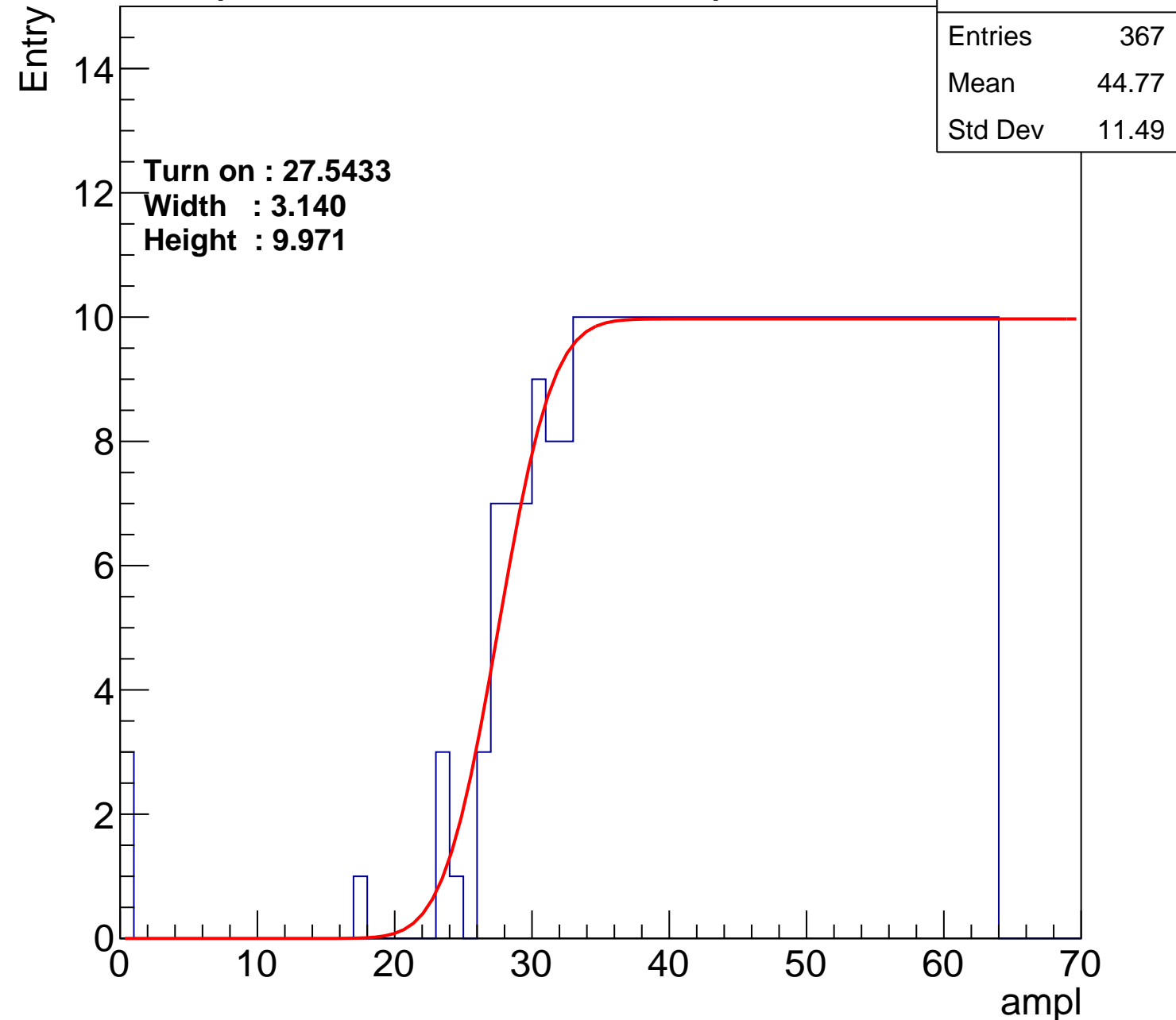
Width : 3.140

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44
Std Dev	11.67

Turn on : 25.4731

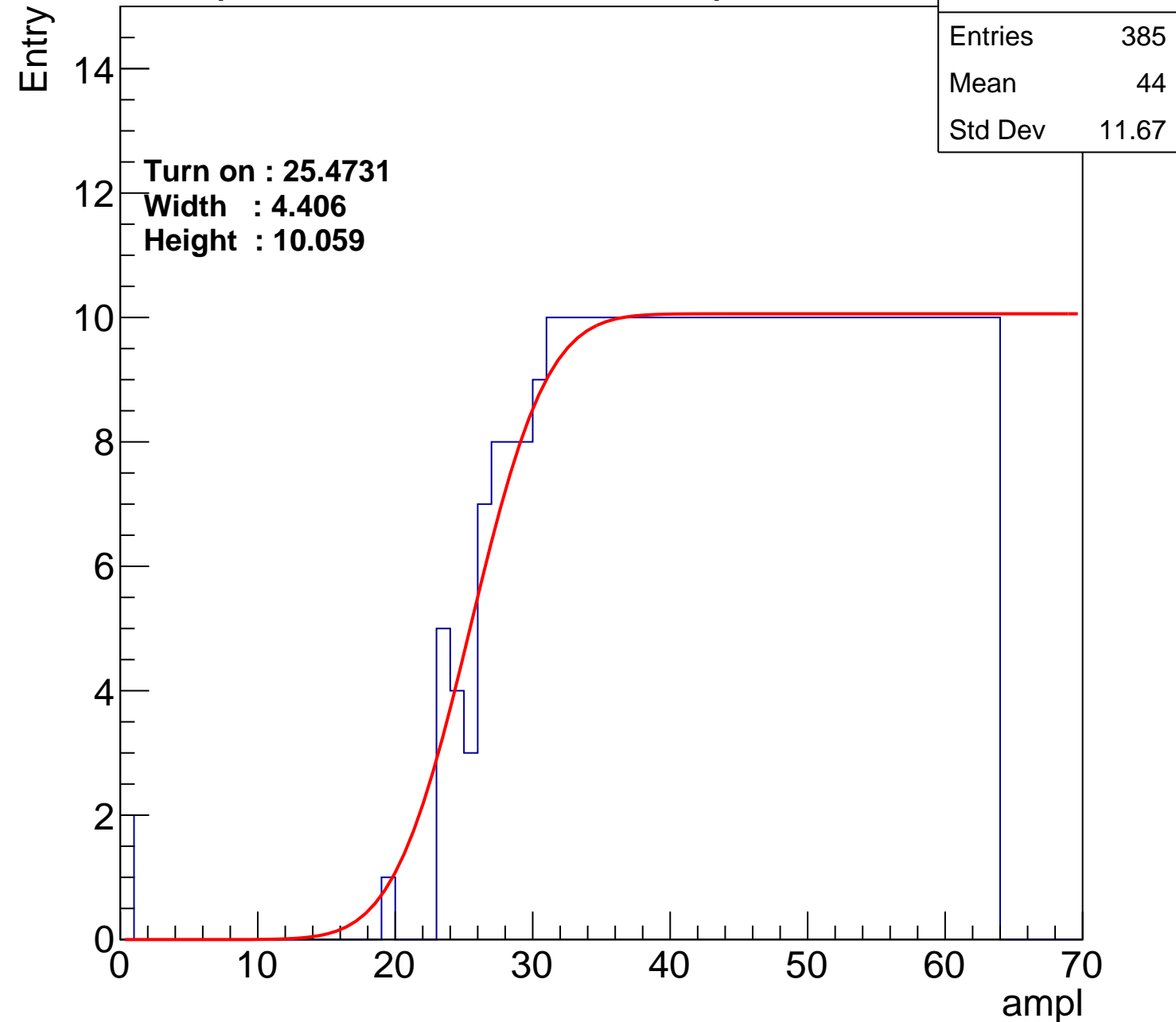
Width : 4.406

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.05
Std Dev	11.94

Turn on : 26.5526

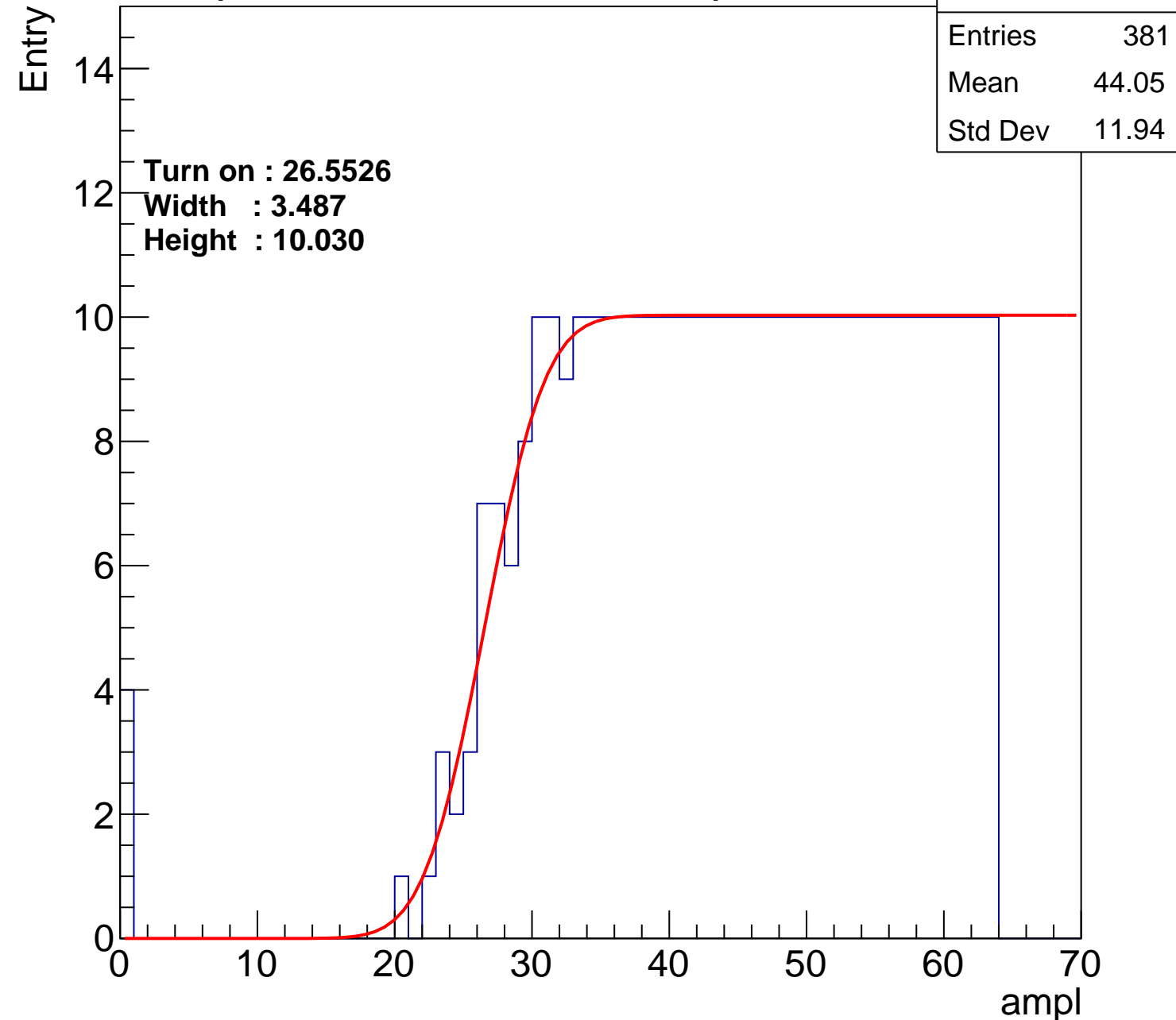
Width : 3.487

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.4437

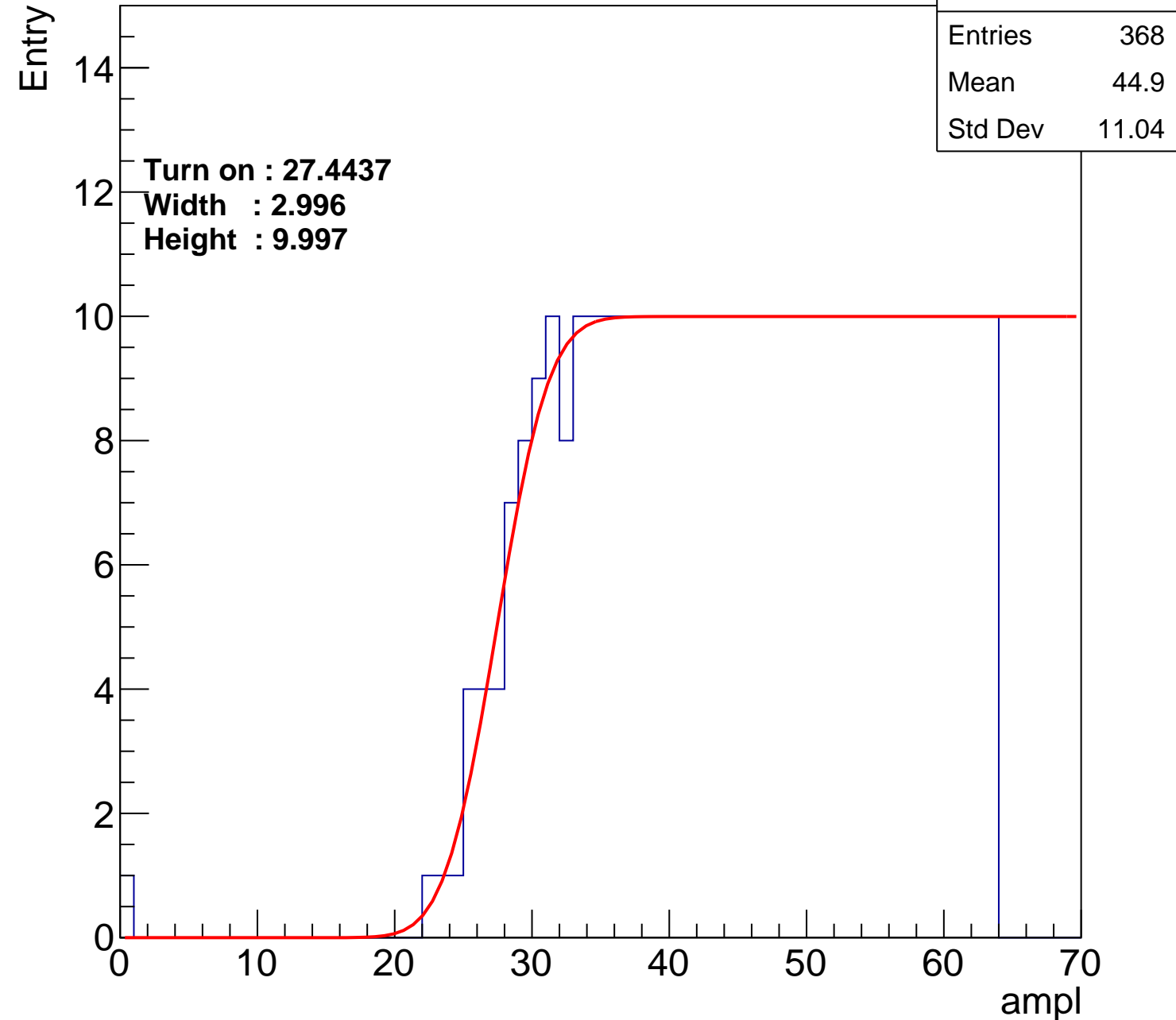
Width : 2.996

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.5
Std Dev	11.77

Turn on : 27.3572

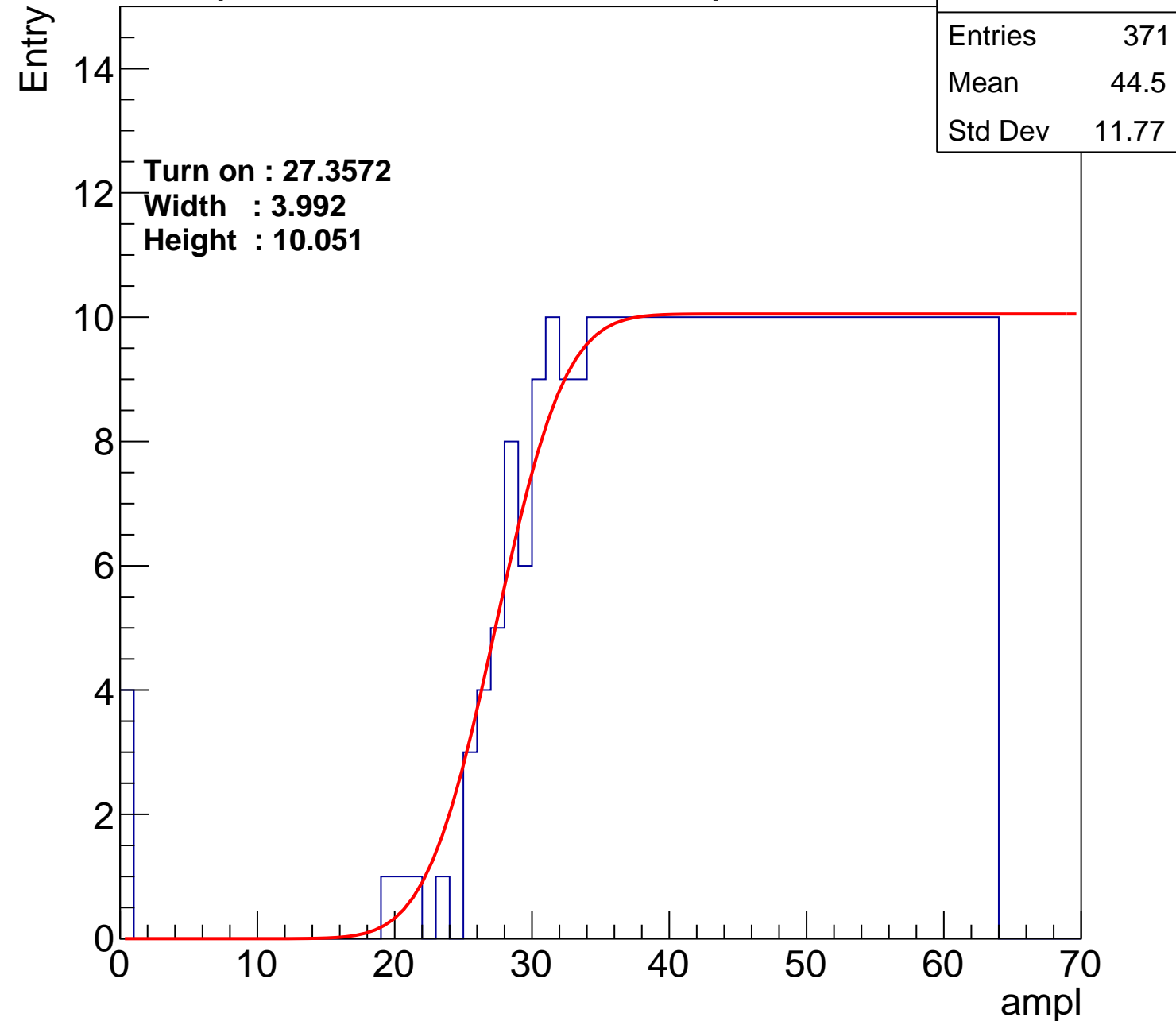
Width : 3.992

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.2988

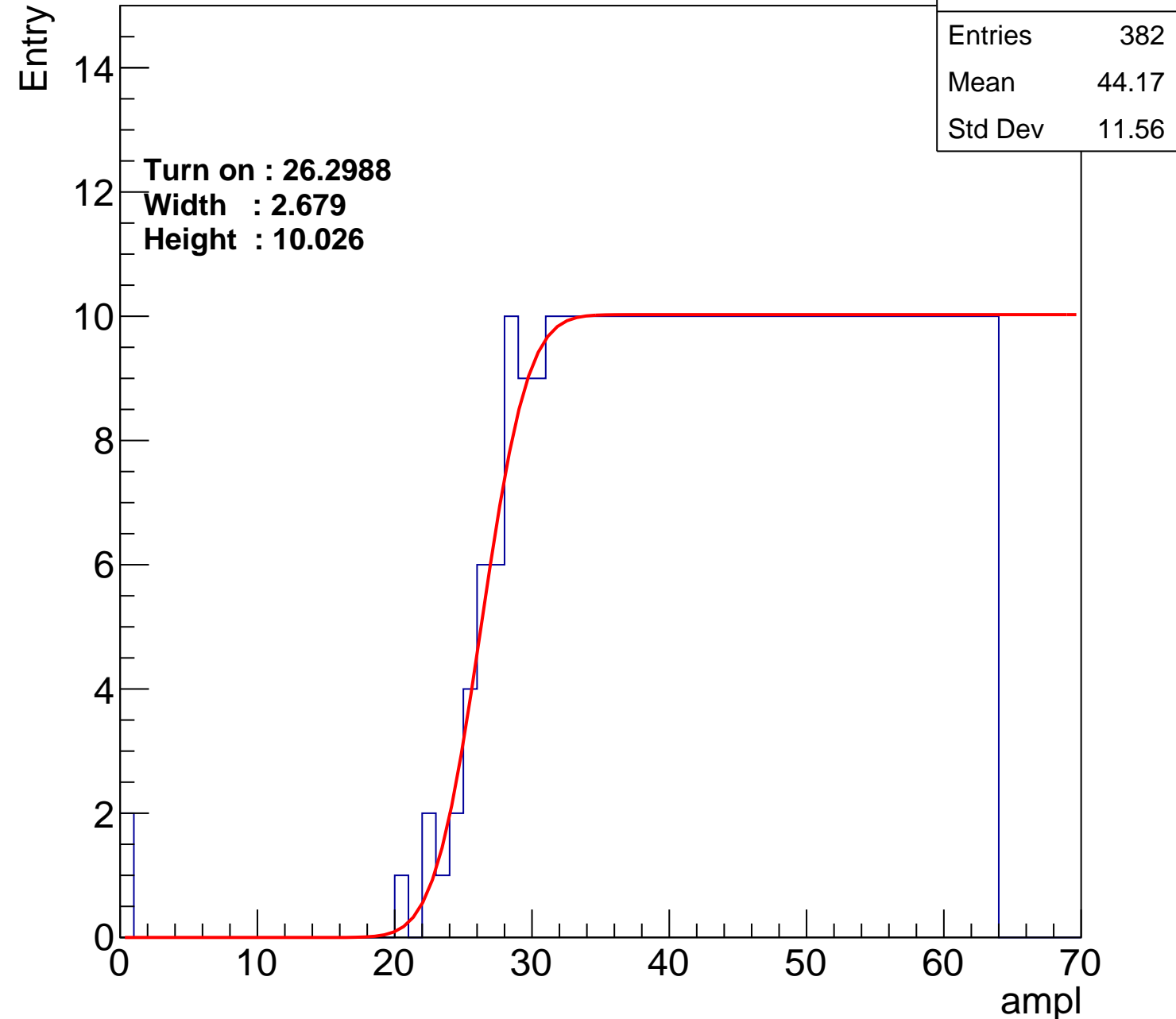
Width : 2.679

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.16
Std Dev	11.45

Turn on : 25.4586

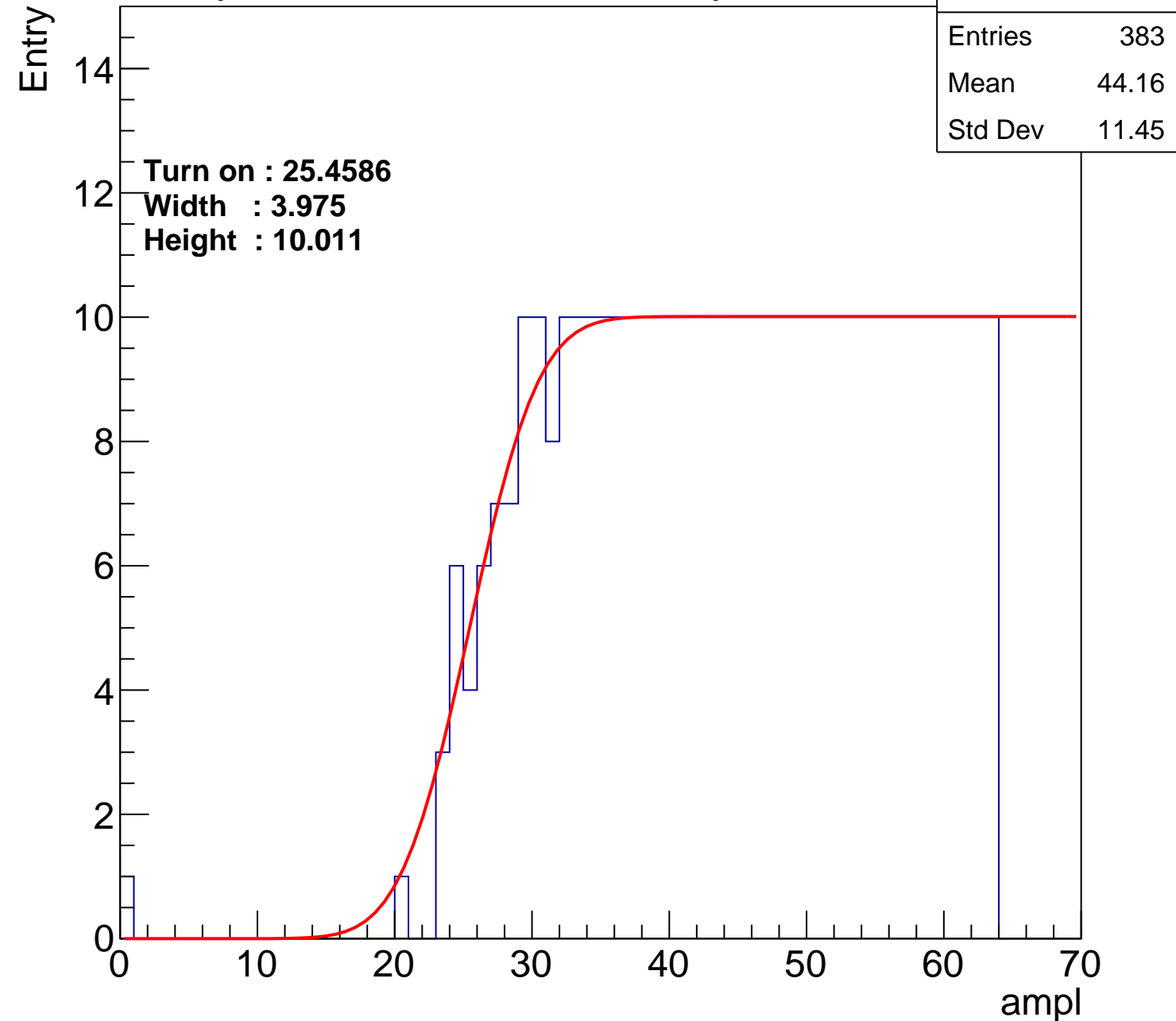
Width : 3.975

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.55
Std Dev	11.42

Turn on : 27.1643

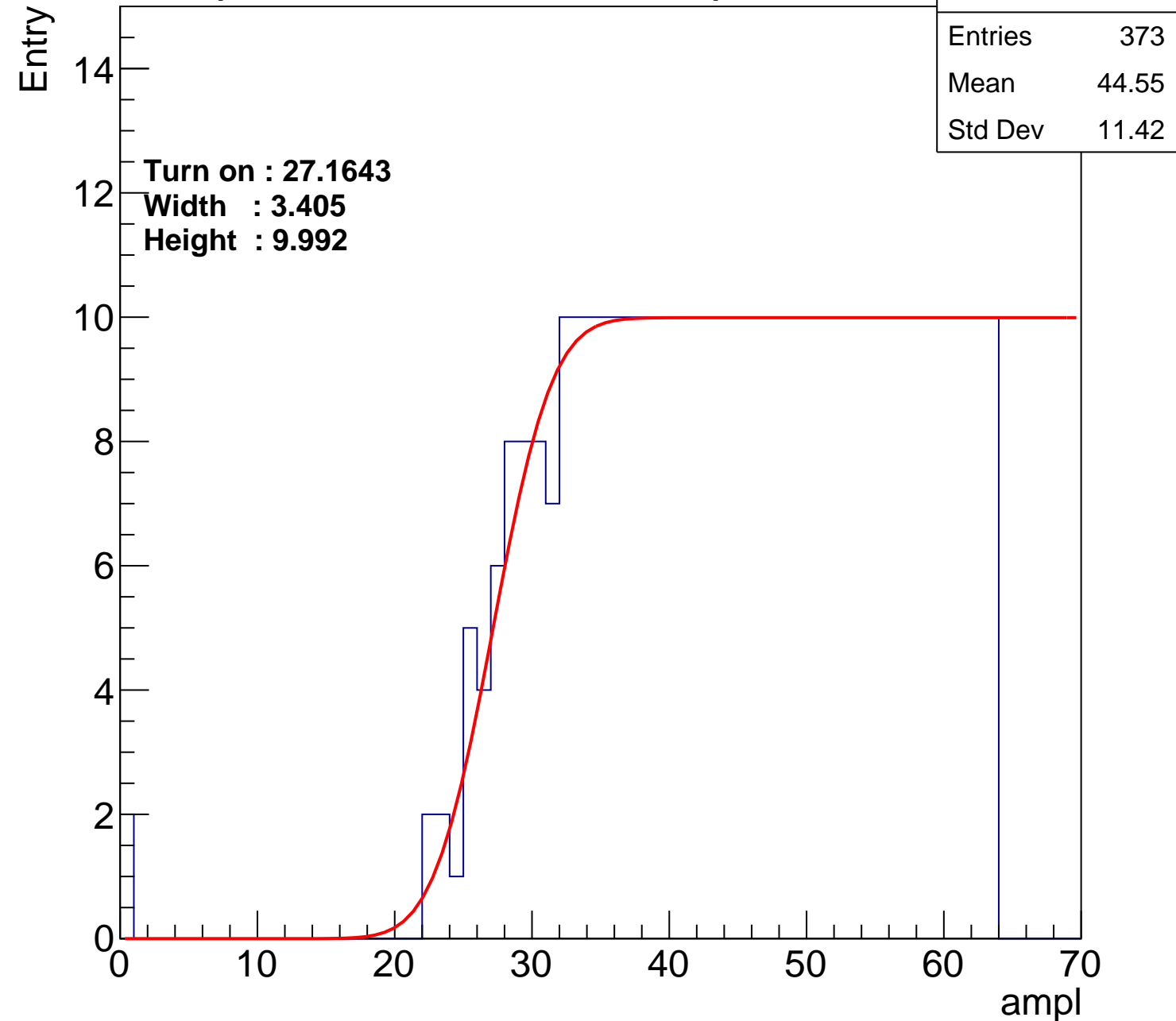
Width : 3.405

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch74

calib_packv5_042523_0143.root, FC#12, port B1

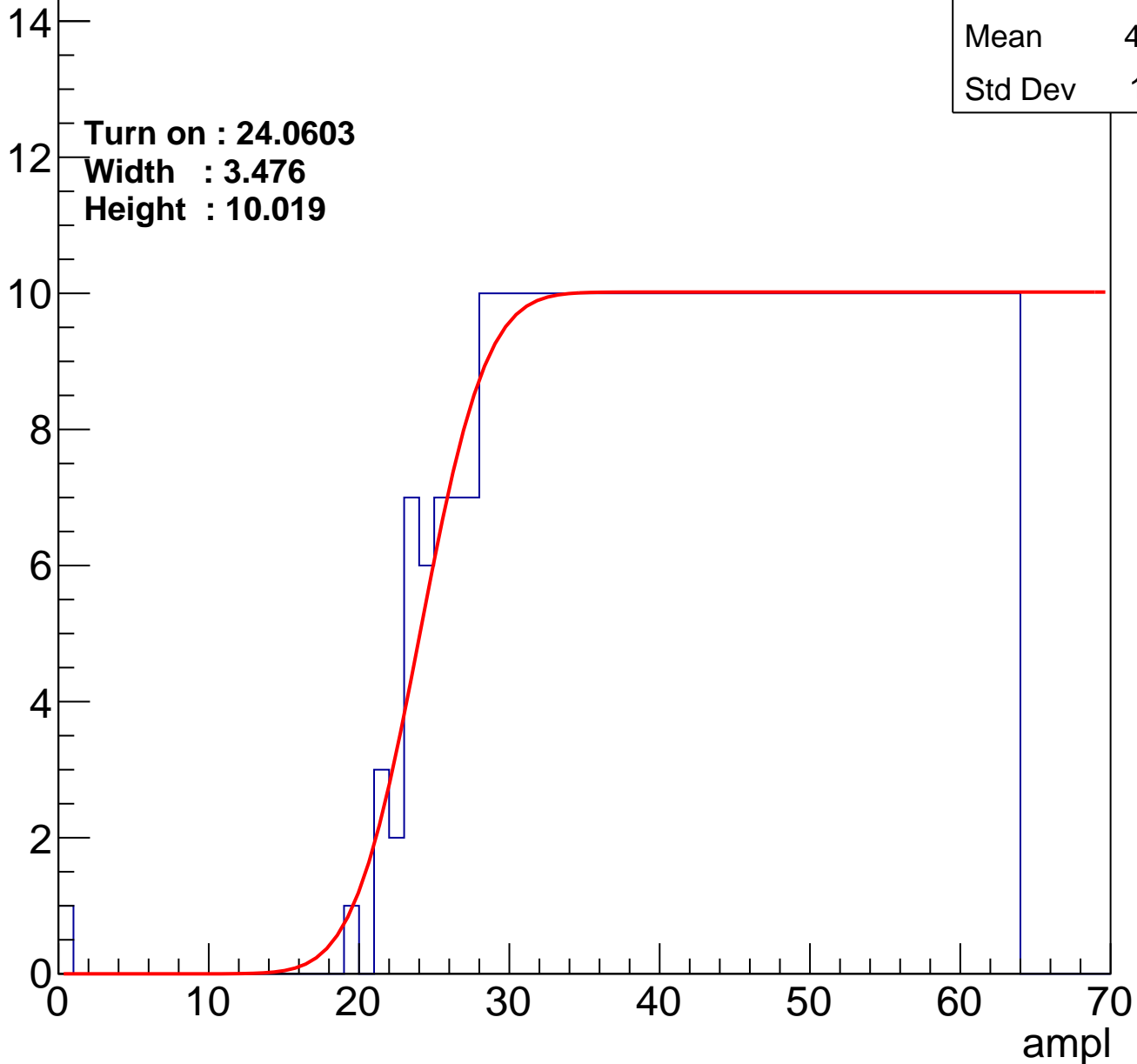
Entries	401
Mean	43.28
Std Dev	11.91

Turn on : 24.0603

Width : 3.476

Height : 10.019

Entry



B0L102S, U13-ch75

calib_packv5_042523_0143.root, FC#12, port B1

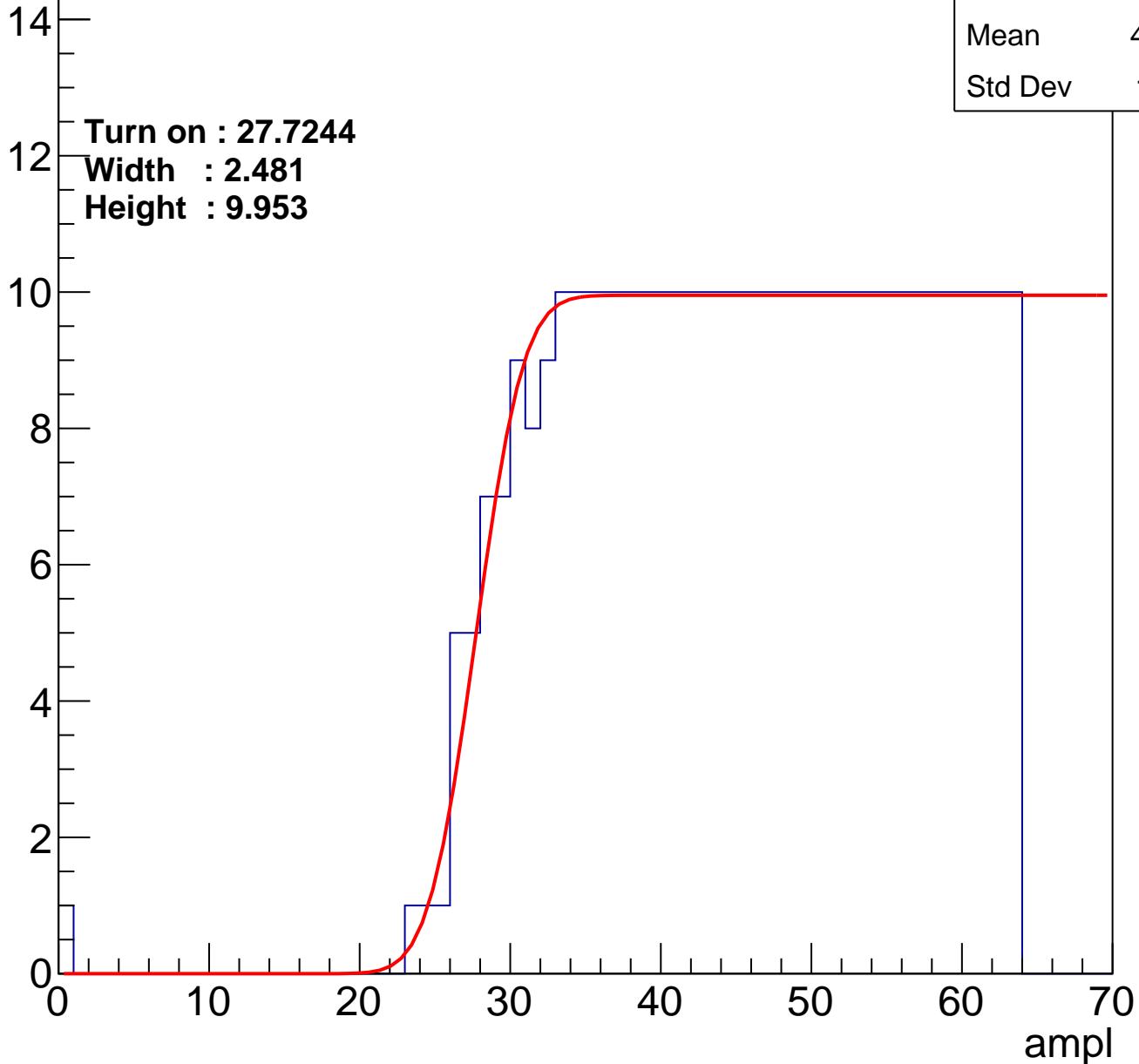
Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 27.7244

Width : 2.481

Height : 9.953

Entry



B0L102S, U13-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.73
Std Dev	11.67

Turn on : 24.9946

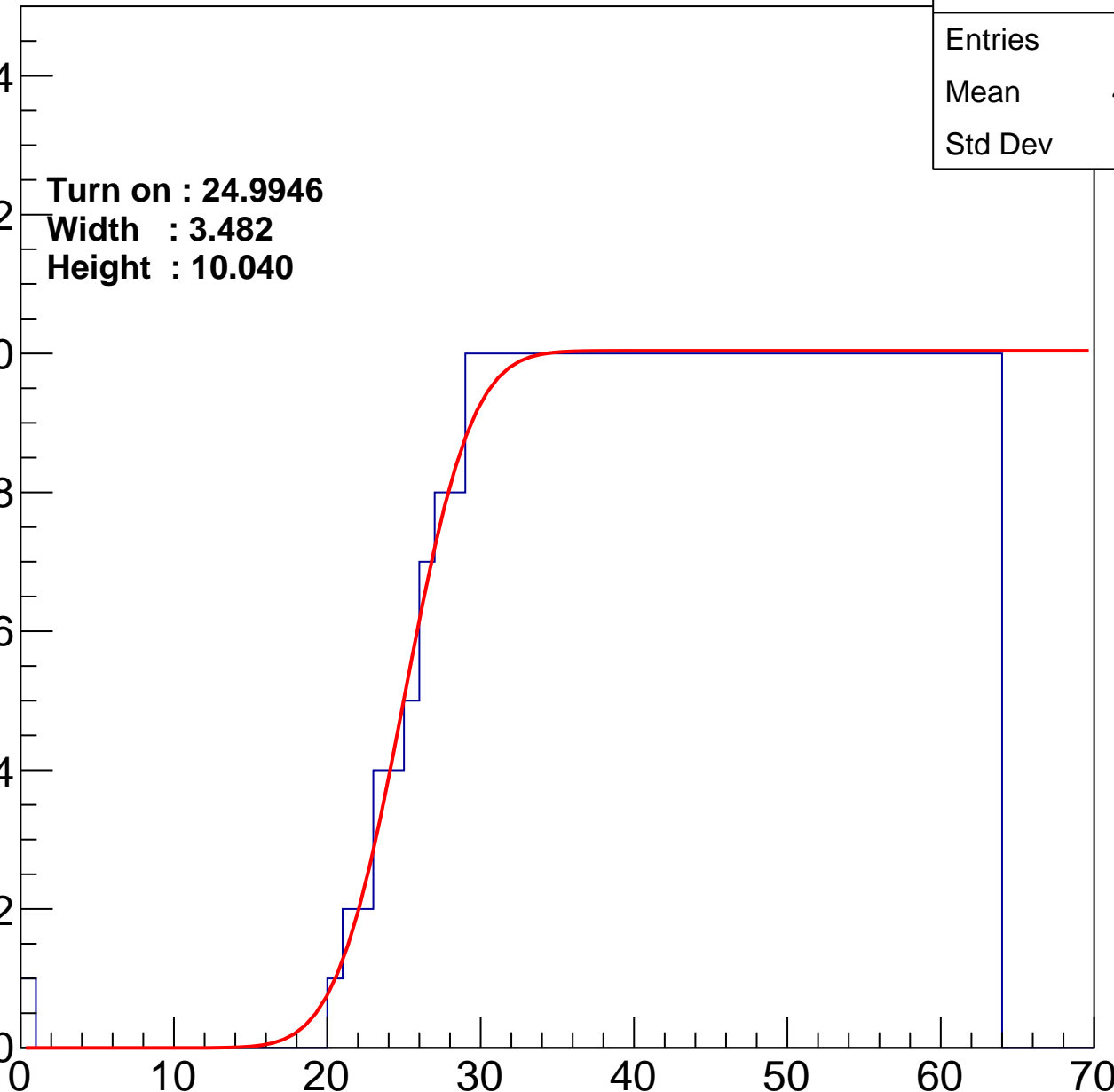
Width : 3.482

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.31
Std Dev	11.97

Turn on : 24.3978

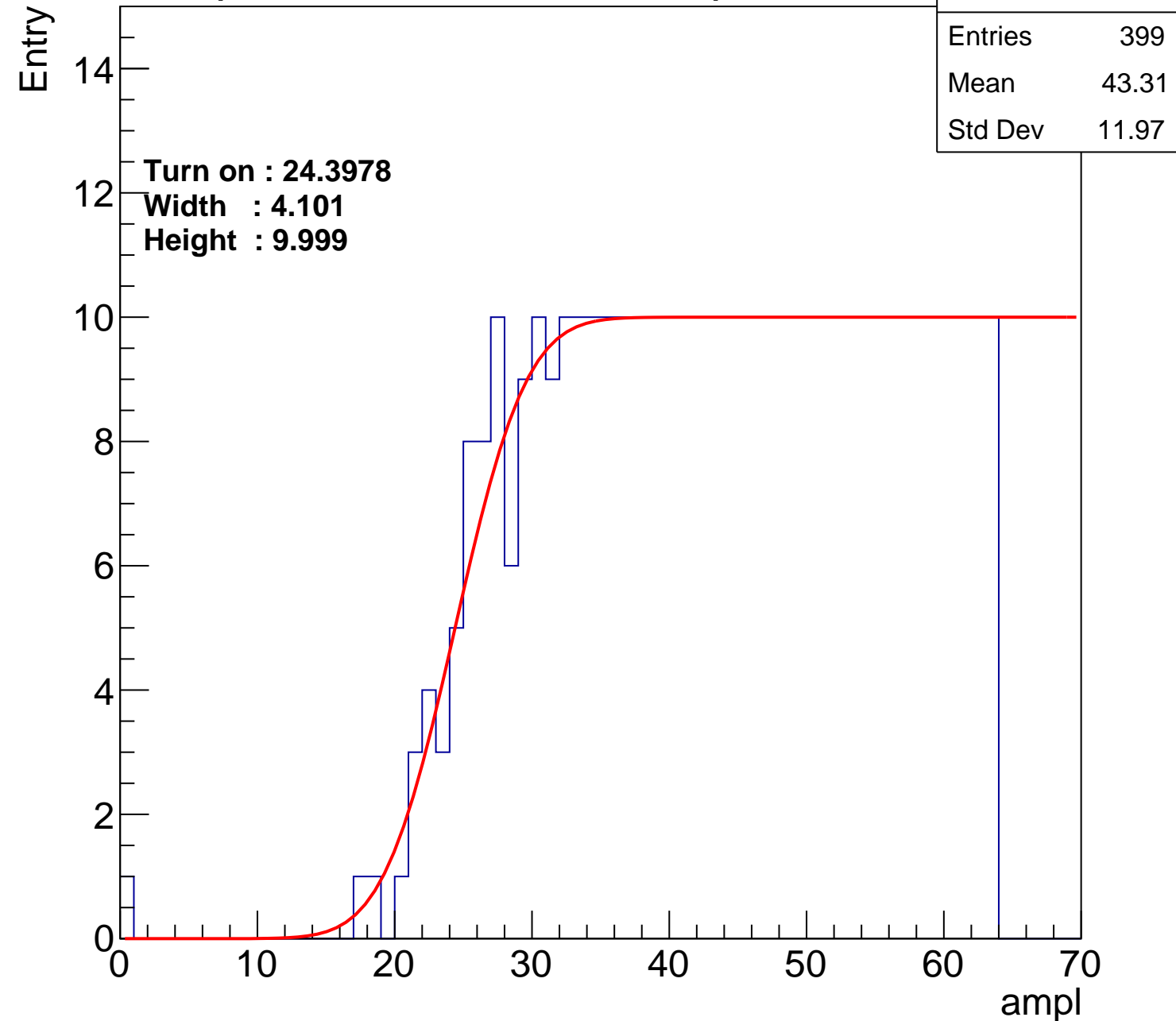
Width : 4.101

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	417
Mean	42.09
Std Dev	13.3

Turn on : 23.4747

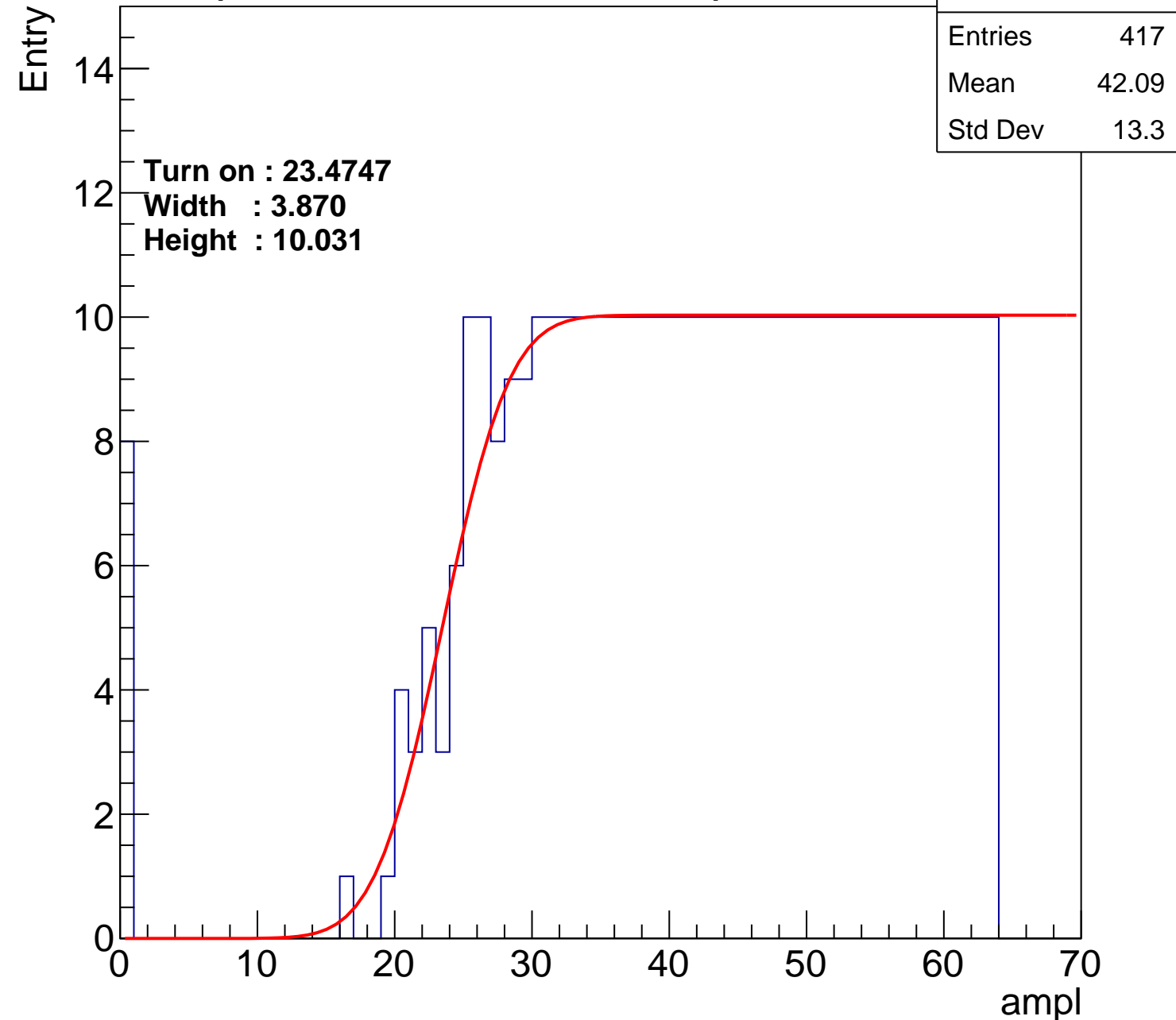
Width : 3.870

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch79

calib_packv5_042523_0143.root, FC#12, port B1

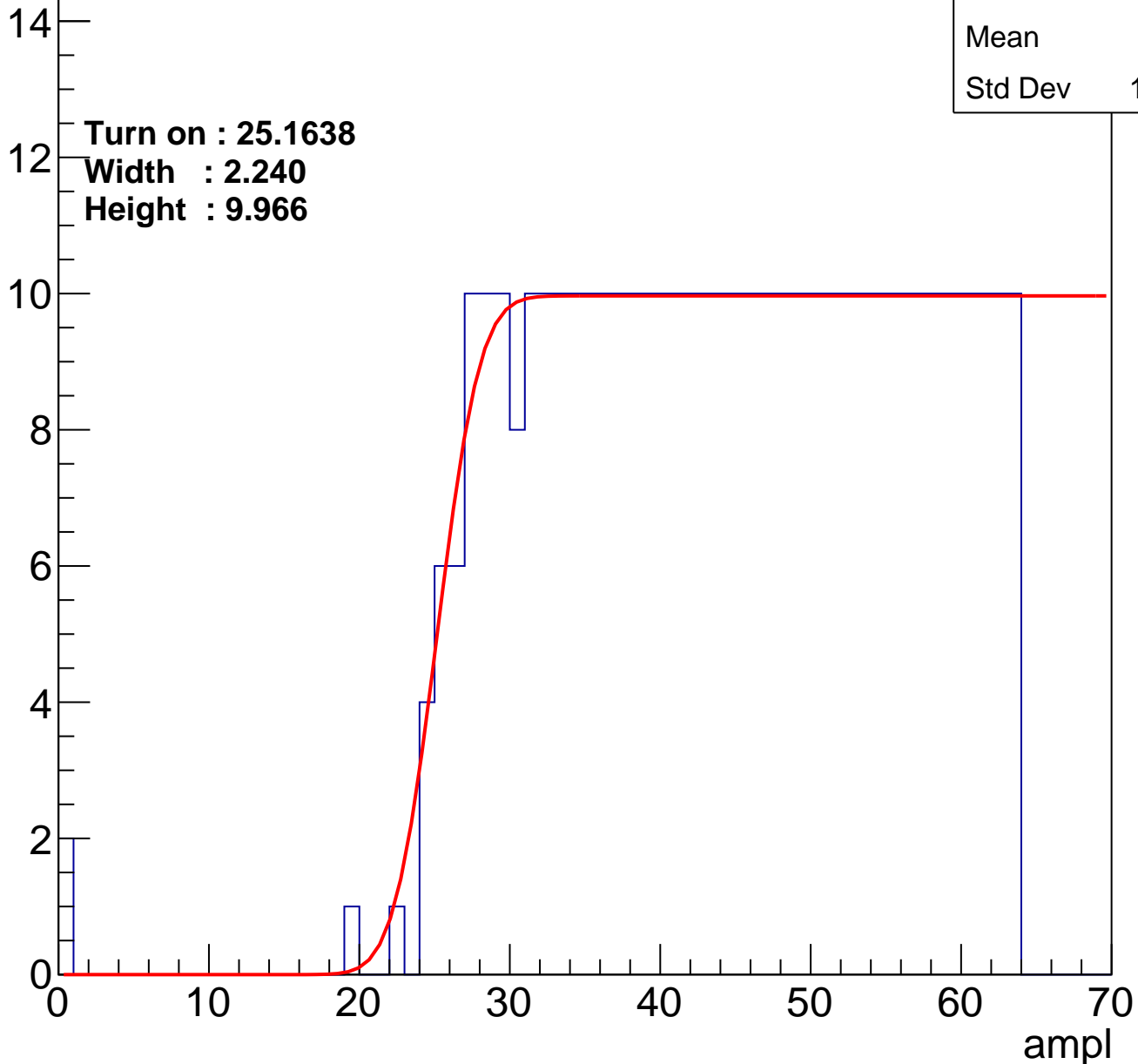
Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.1638

Width : 2.240

Height : 9.966

Entry



B0L102S, U13-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.26
Std Dev	11.83

Turn on : 26.7143

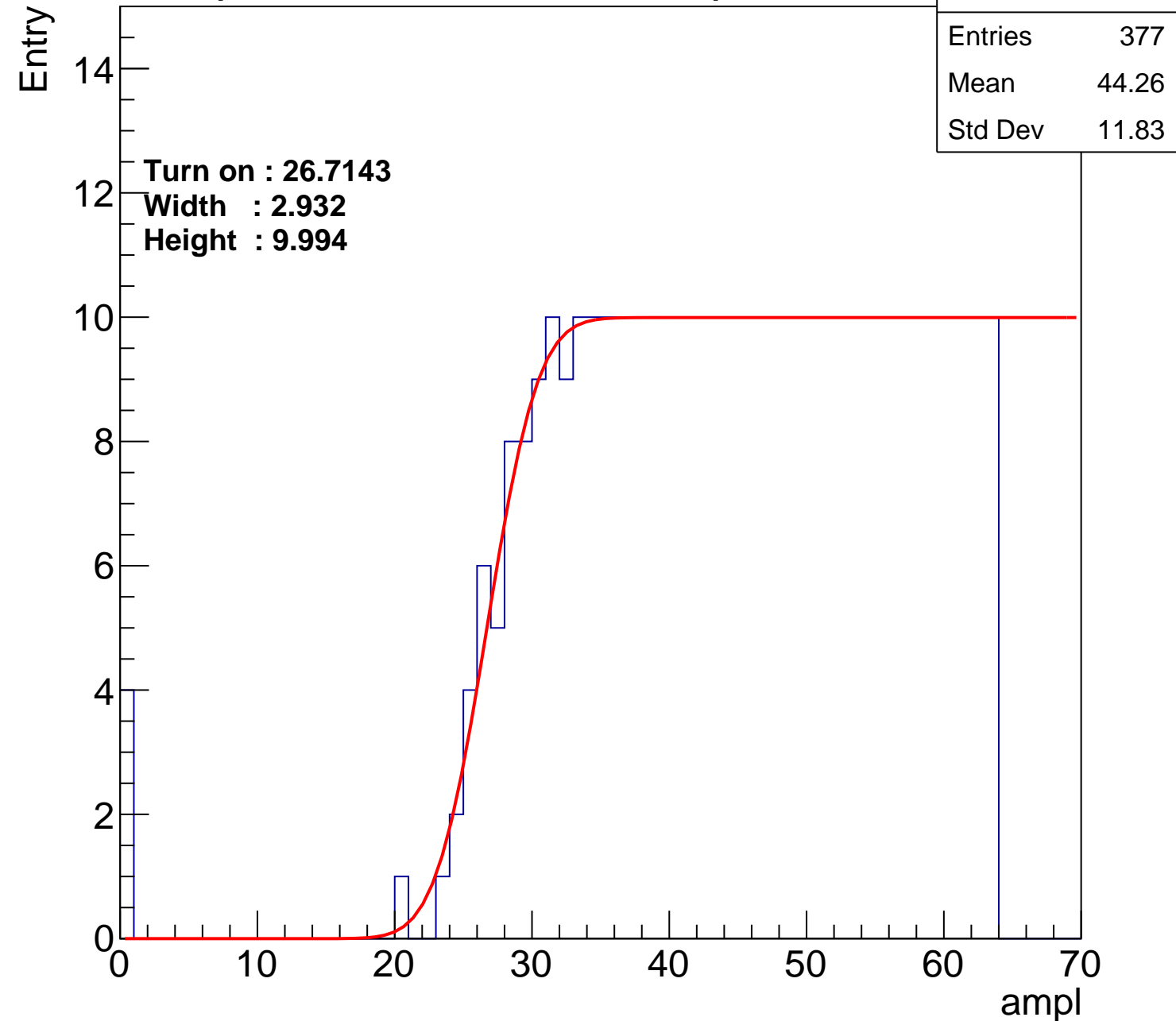
Width : 2.932

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	12.03

Turn on : 26.4703

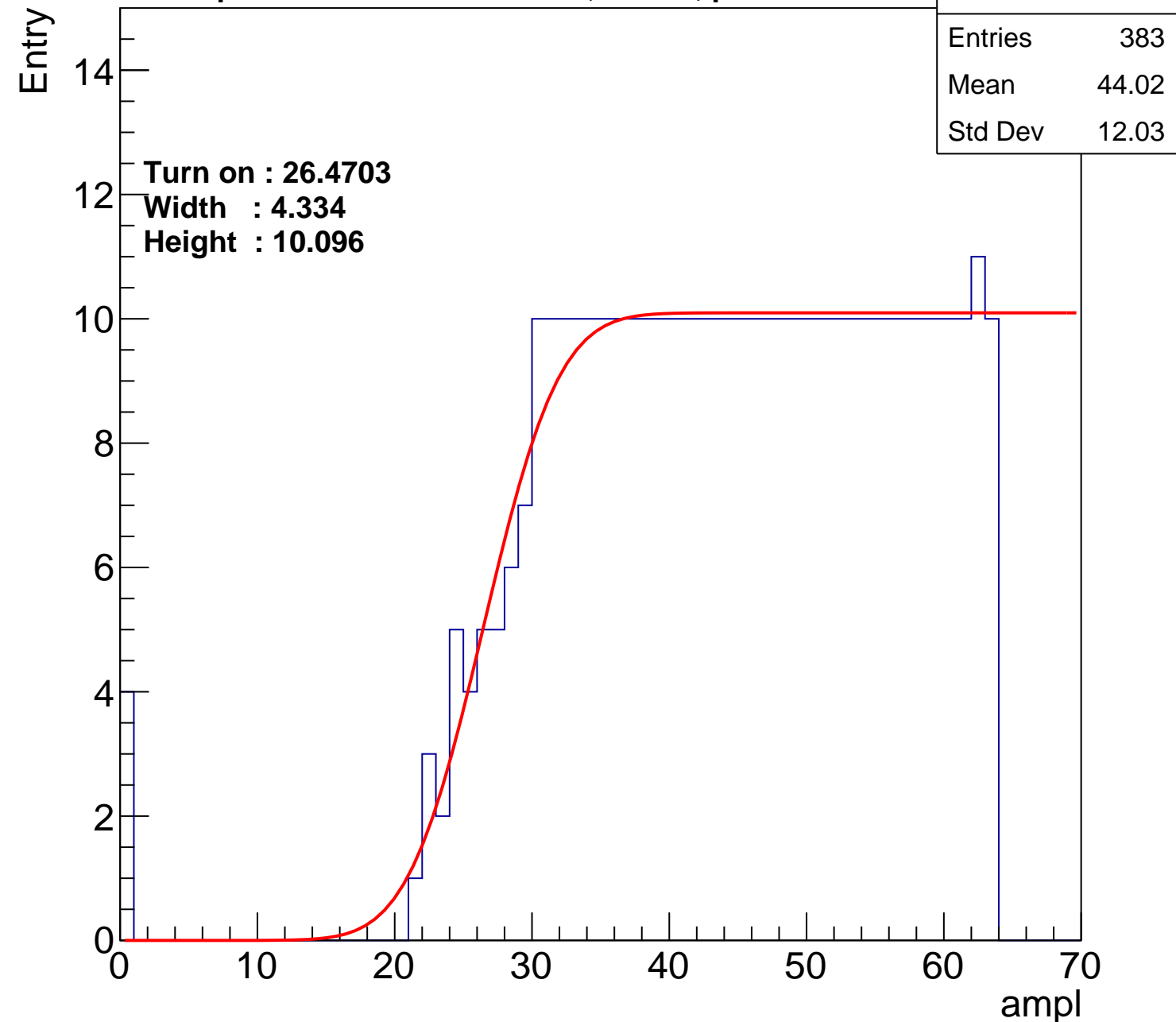
Width : 4.334

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch82

calib_packv5_042523_0143.root, FC#12, port B1

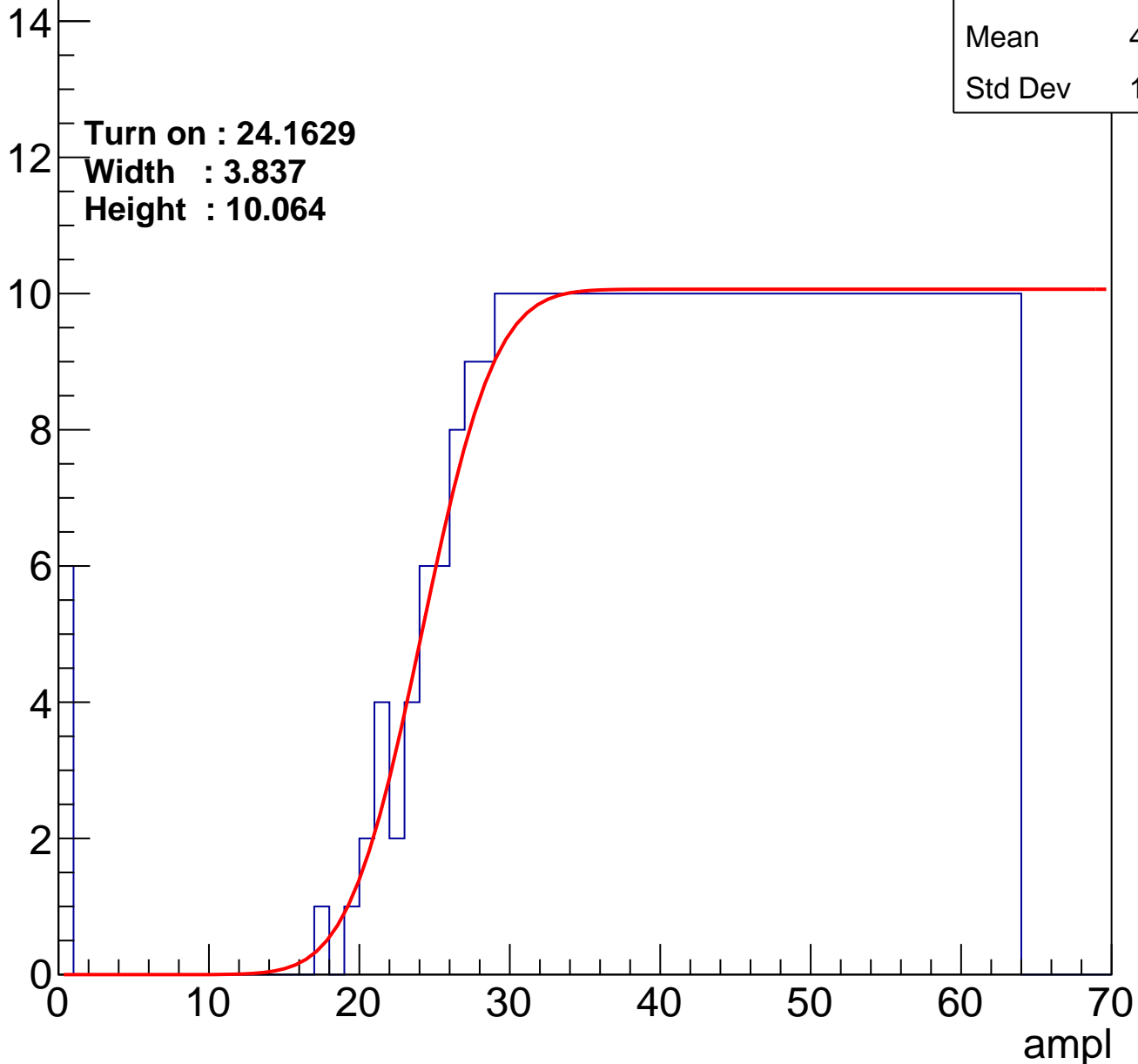
Entries	408
Mean	42.63
Std Dev	12.85

Turn on : 24.1629

Width : 3.837

Height : 10.064

Entry



B0L102S, U13-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.0724

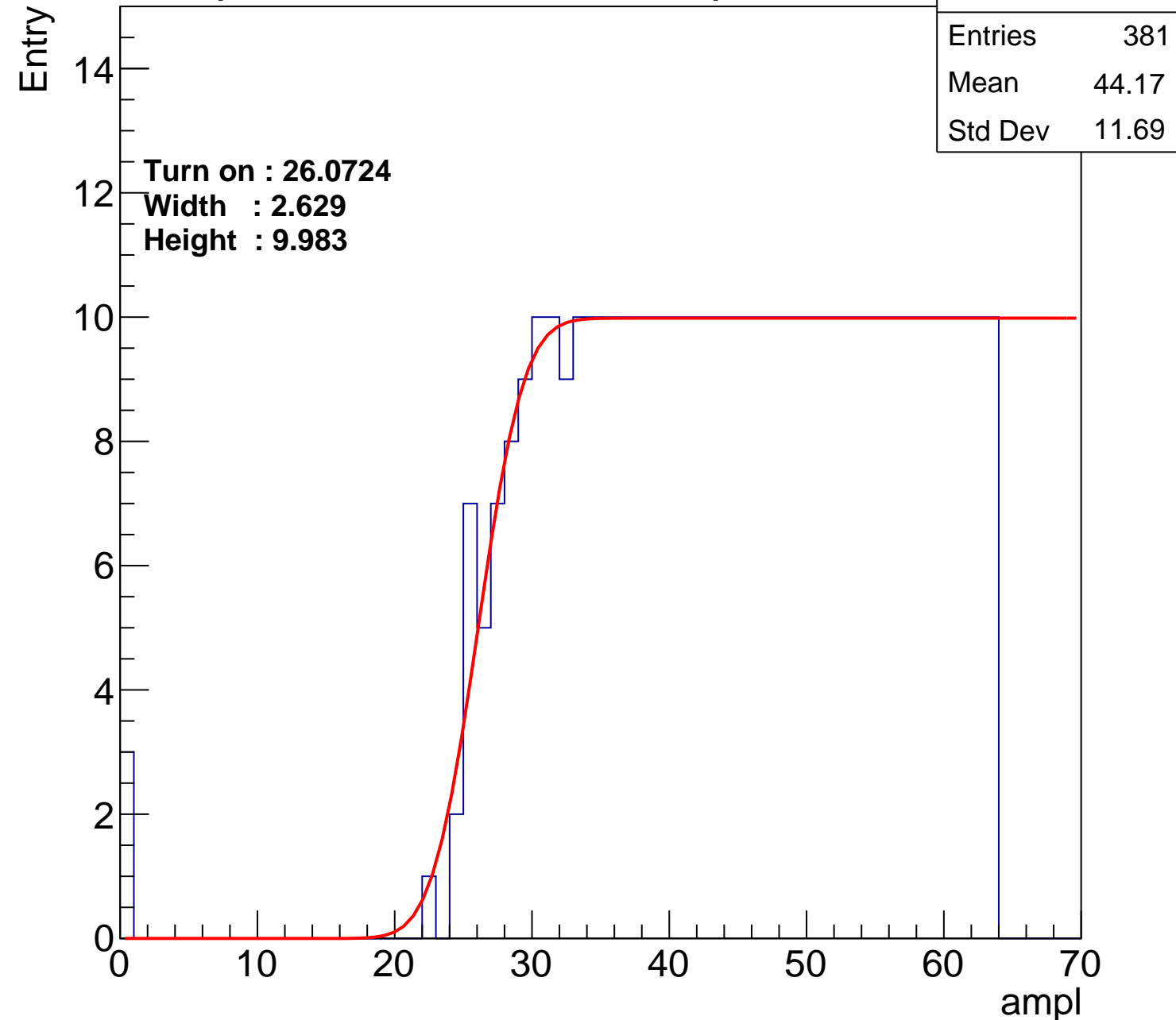
Width : 2.629

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.4752

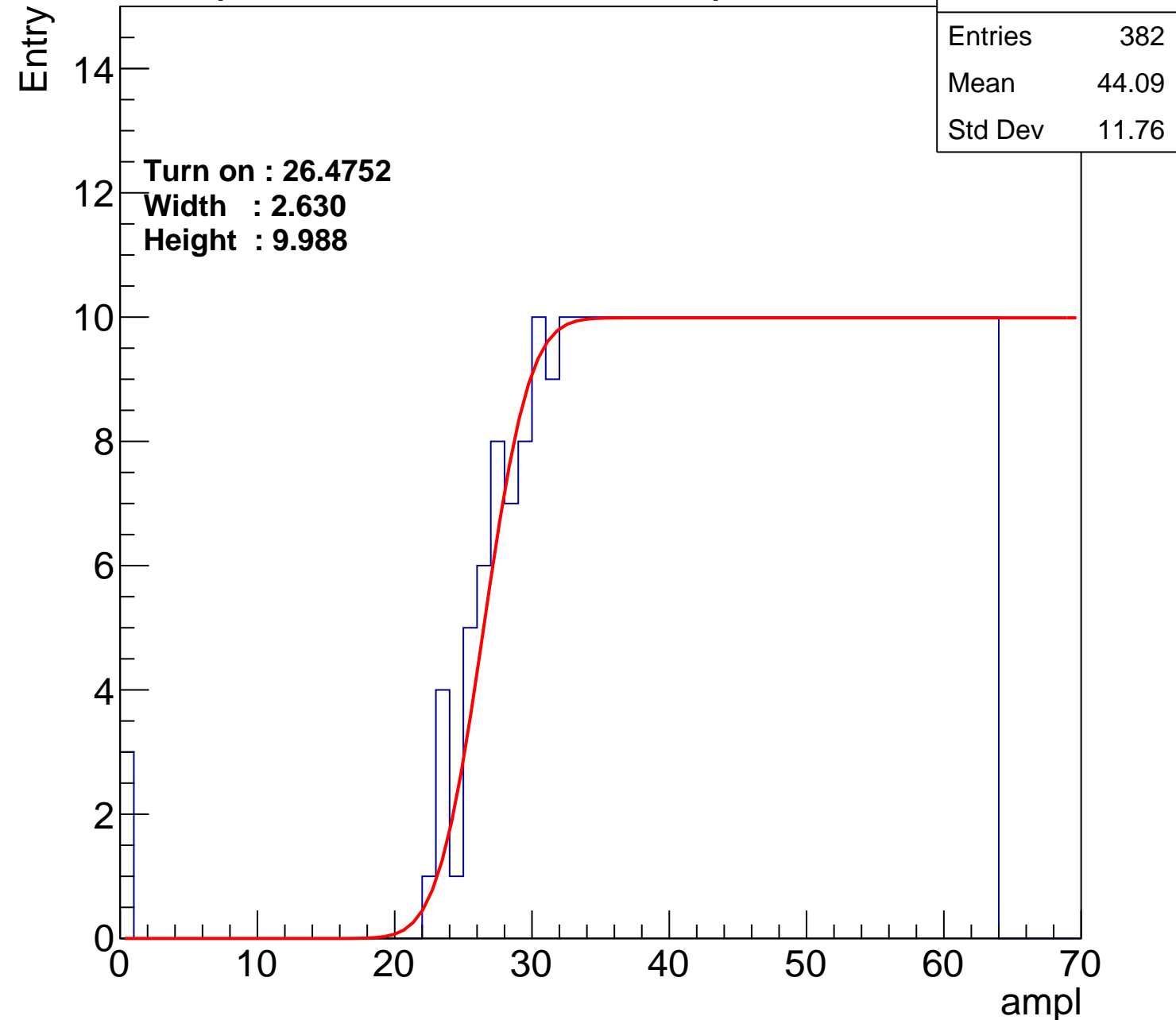
Width : 2.630

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.87
Std Dev	11.9

Turn on : 26.0219

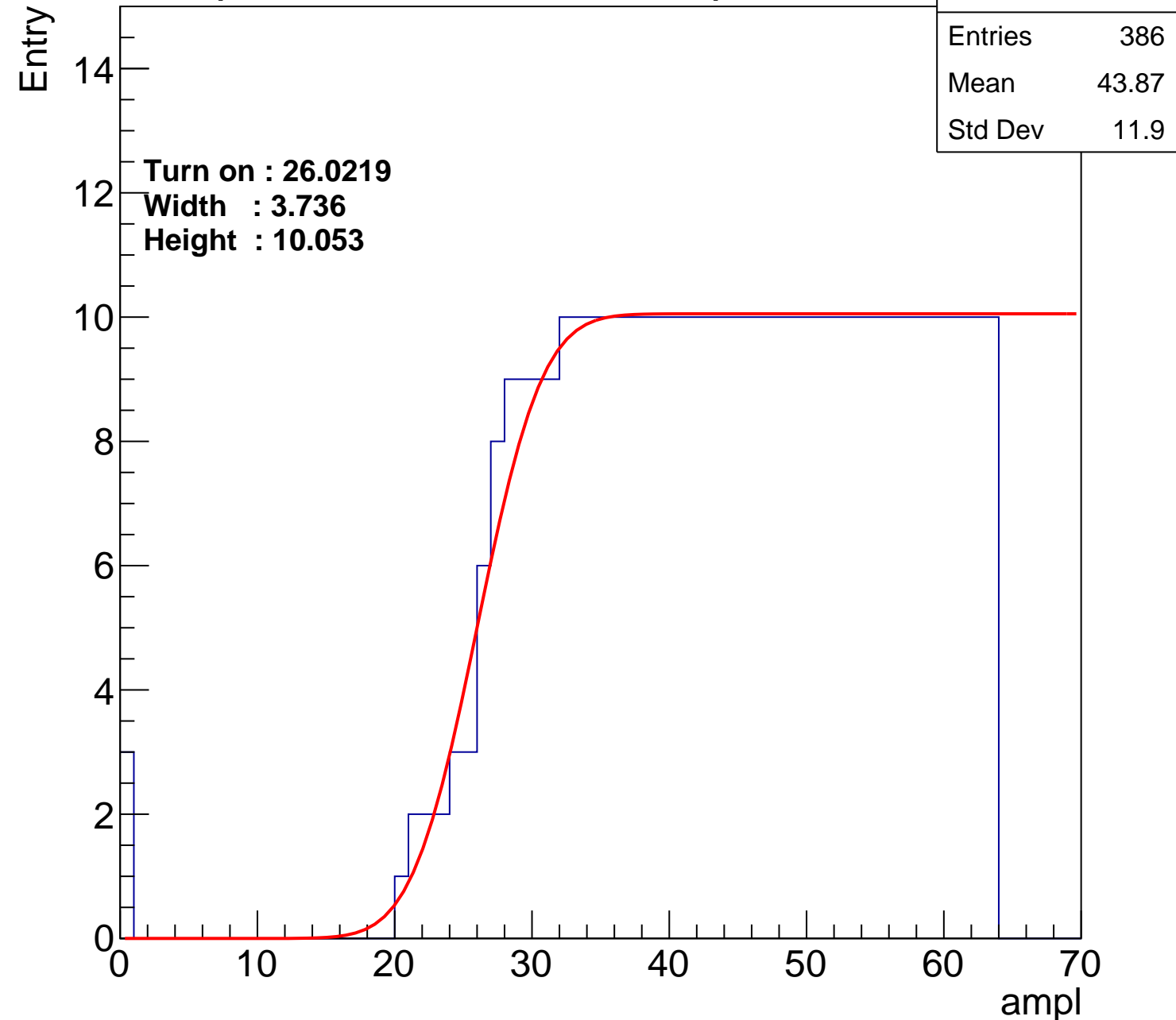
Width : 3.736

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.38
Std Dev	12.1

Turn on : 25.4019

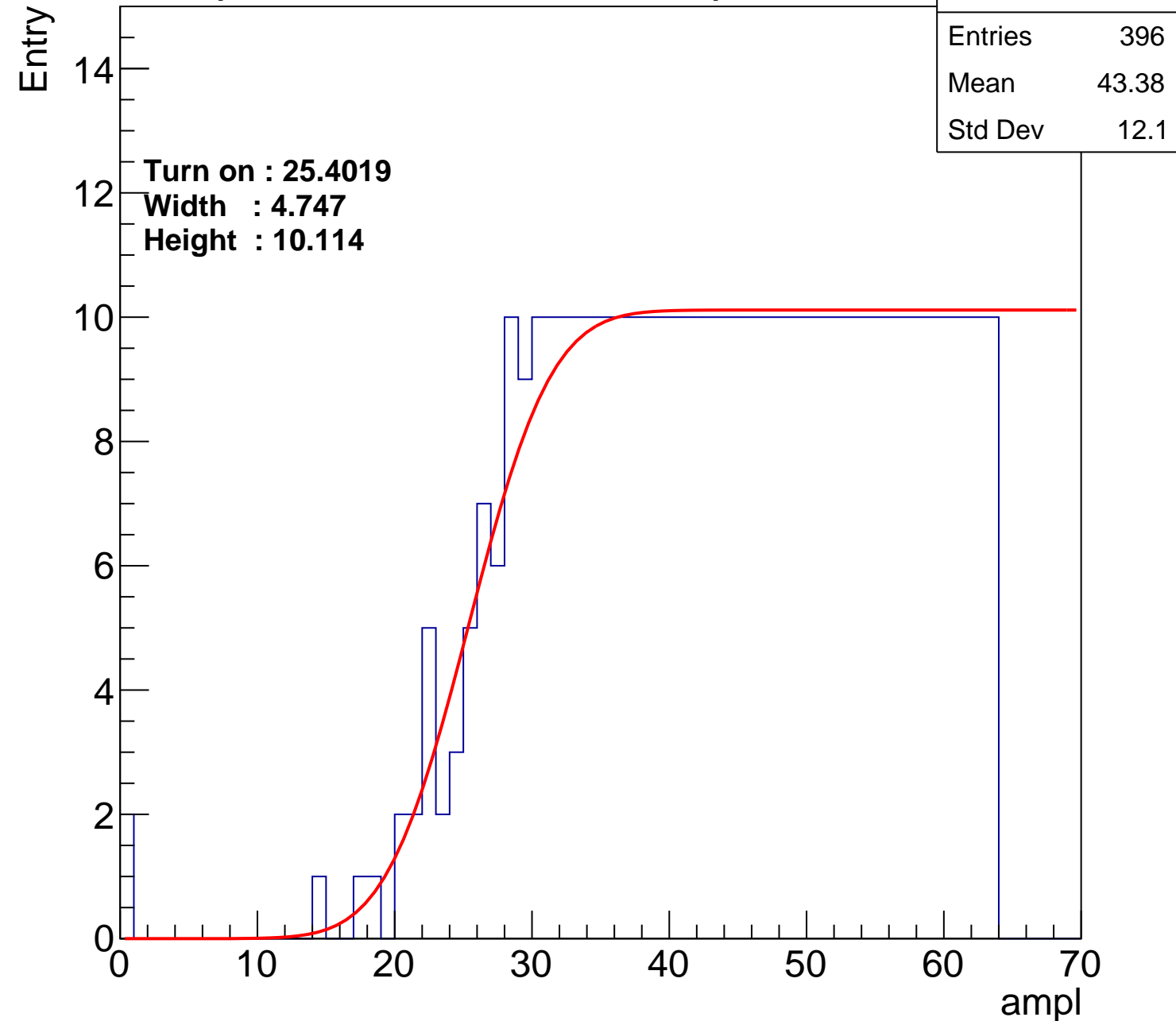
Width : 4.747

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.42
Std Dev	11.47

Turn on : 27.1790

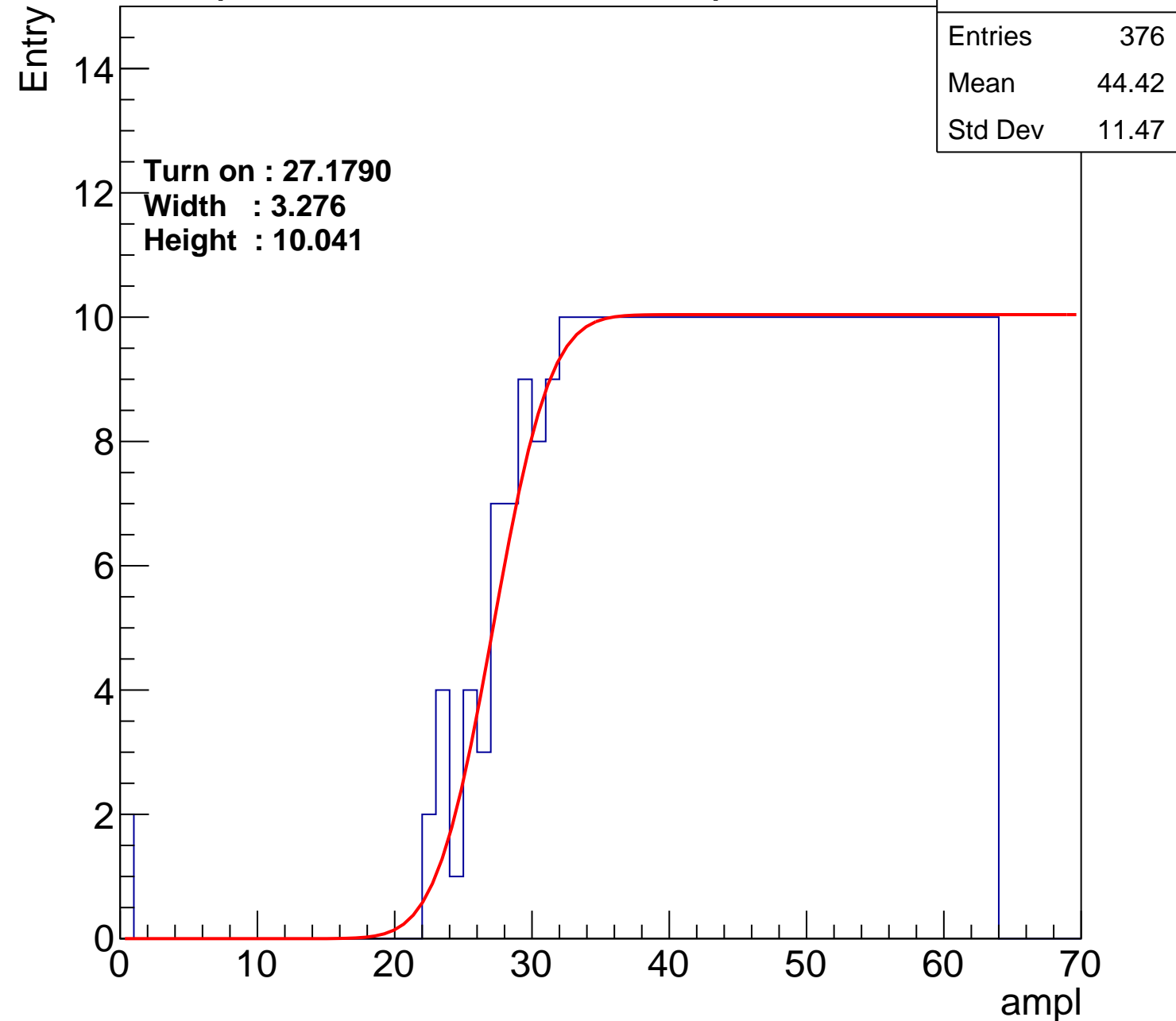
Width : 3.276

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	11.82

Turn on : 26.4806

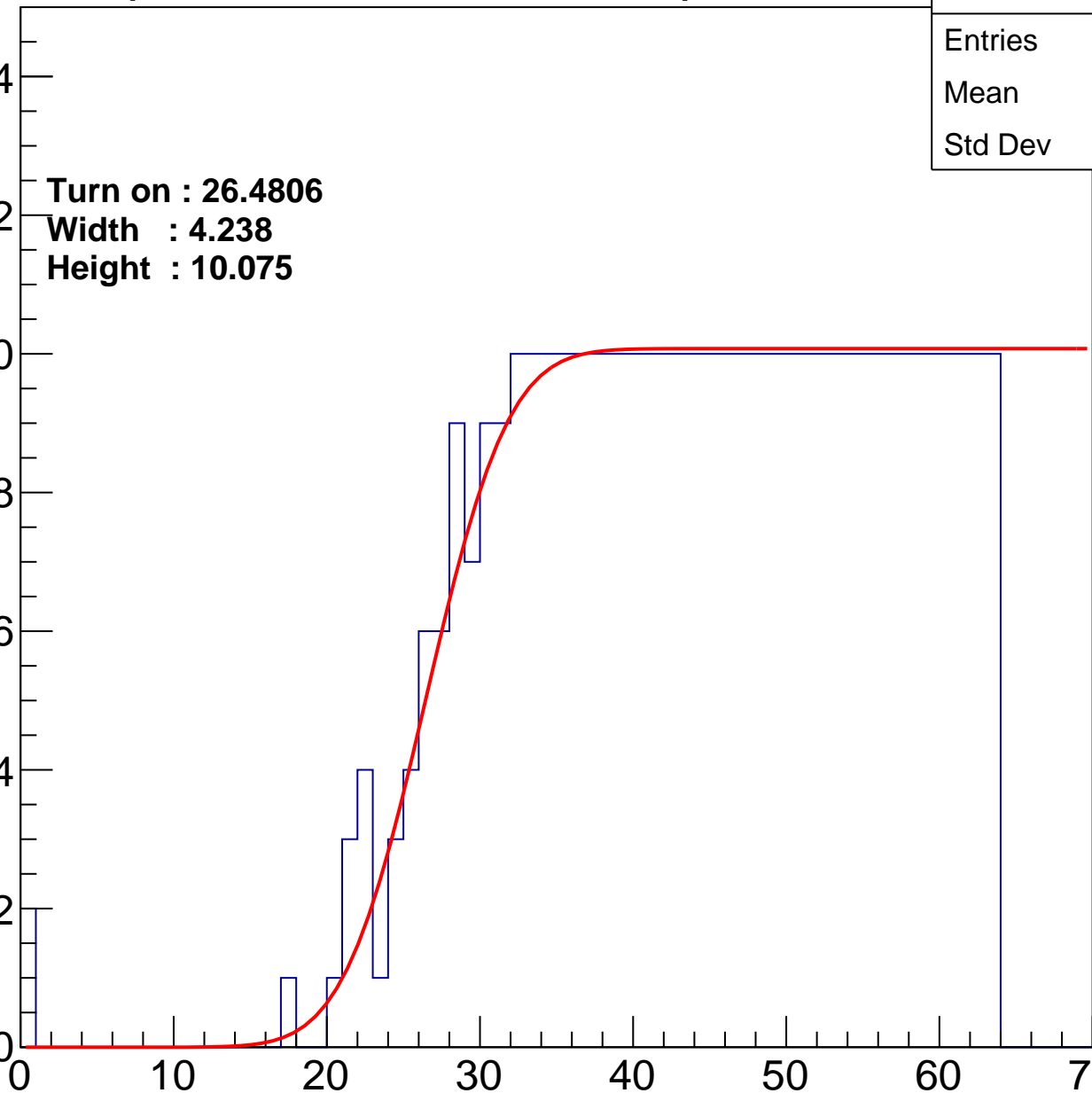
Width : 4.238

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 26.7963

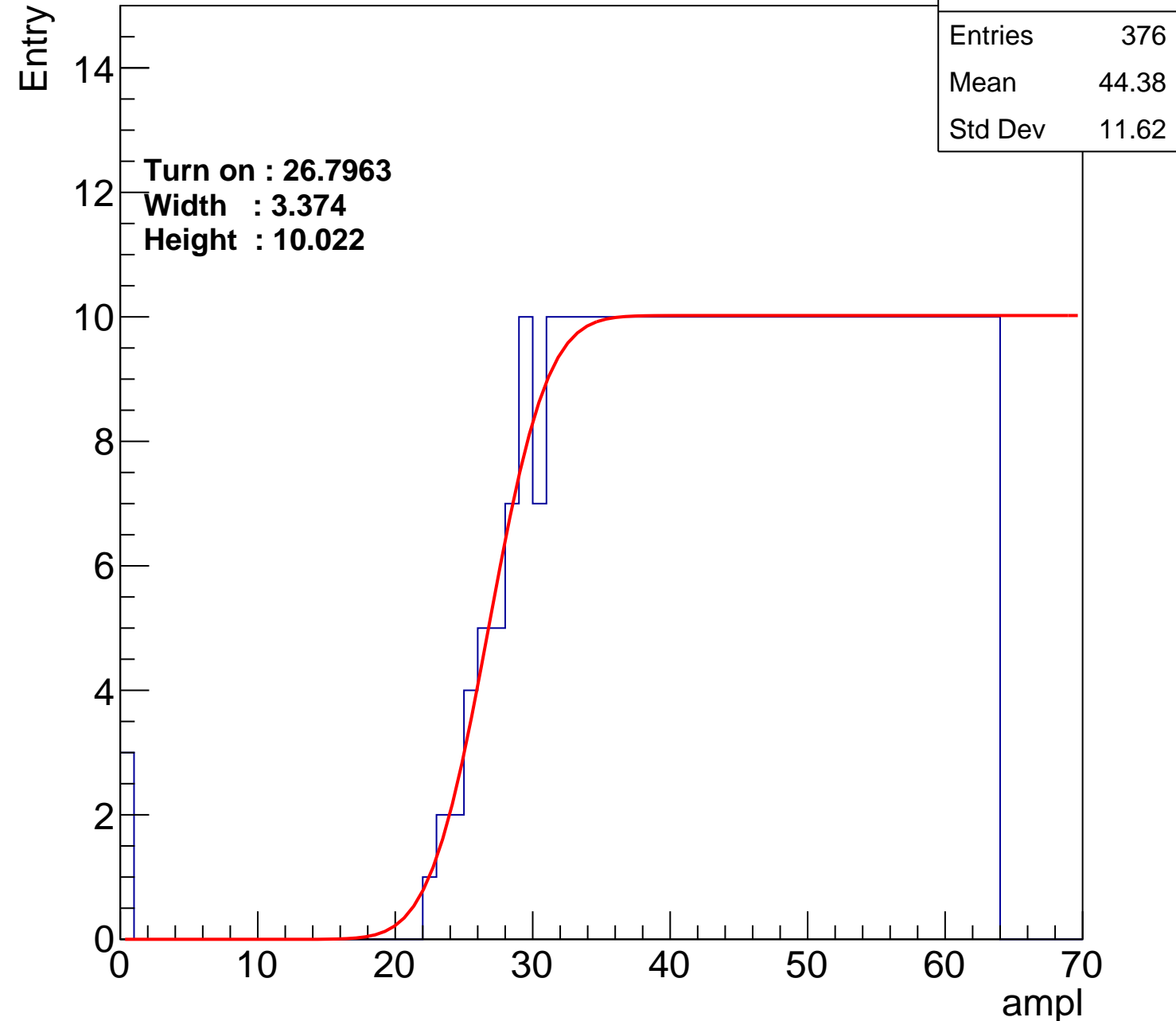
Width : 3.374

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.59
Std Dev	11.49

Turn on : 26.7551

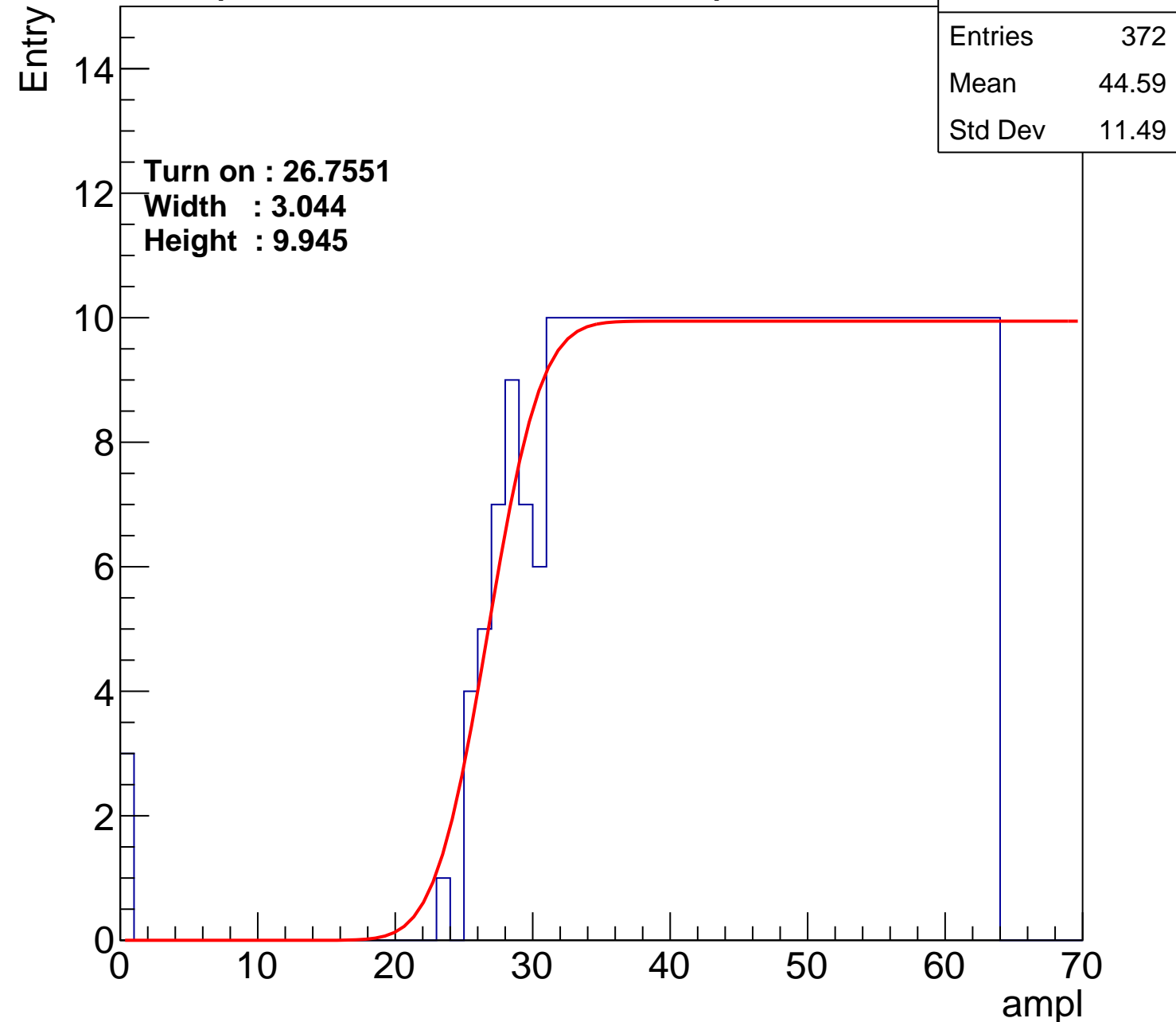
Width : 3.044

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.88
Std Dev	11.88

Turn on : 27.0374

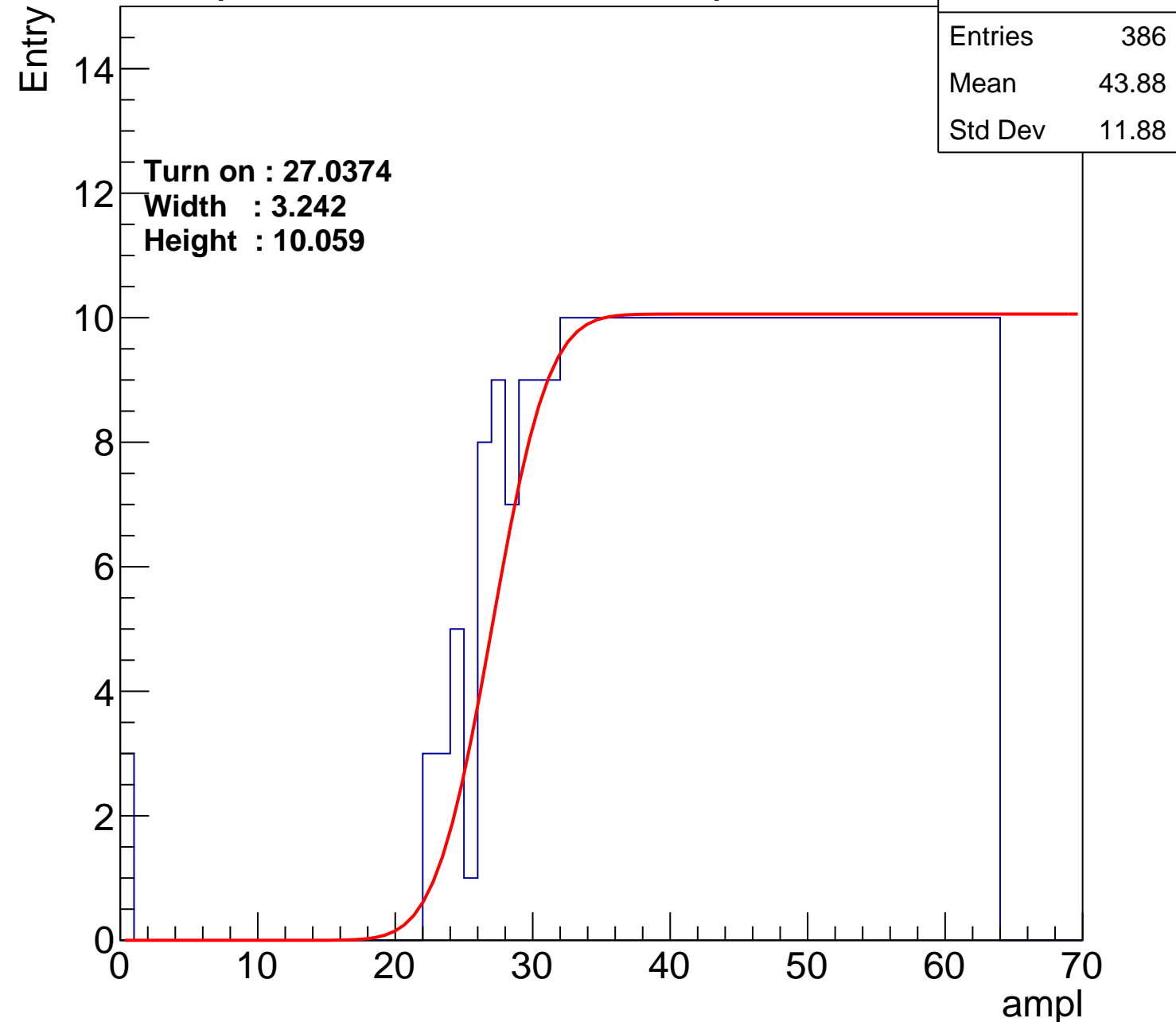
Width : 3.242

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.87
Std Dev	11.89

Turn on : 26.1910

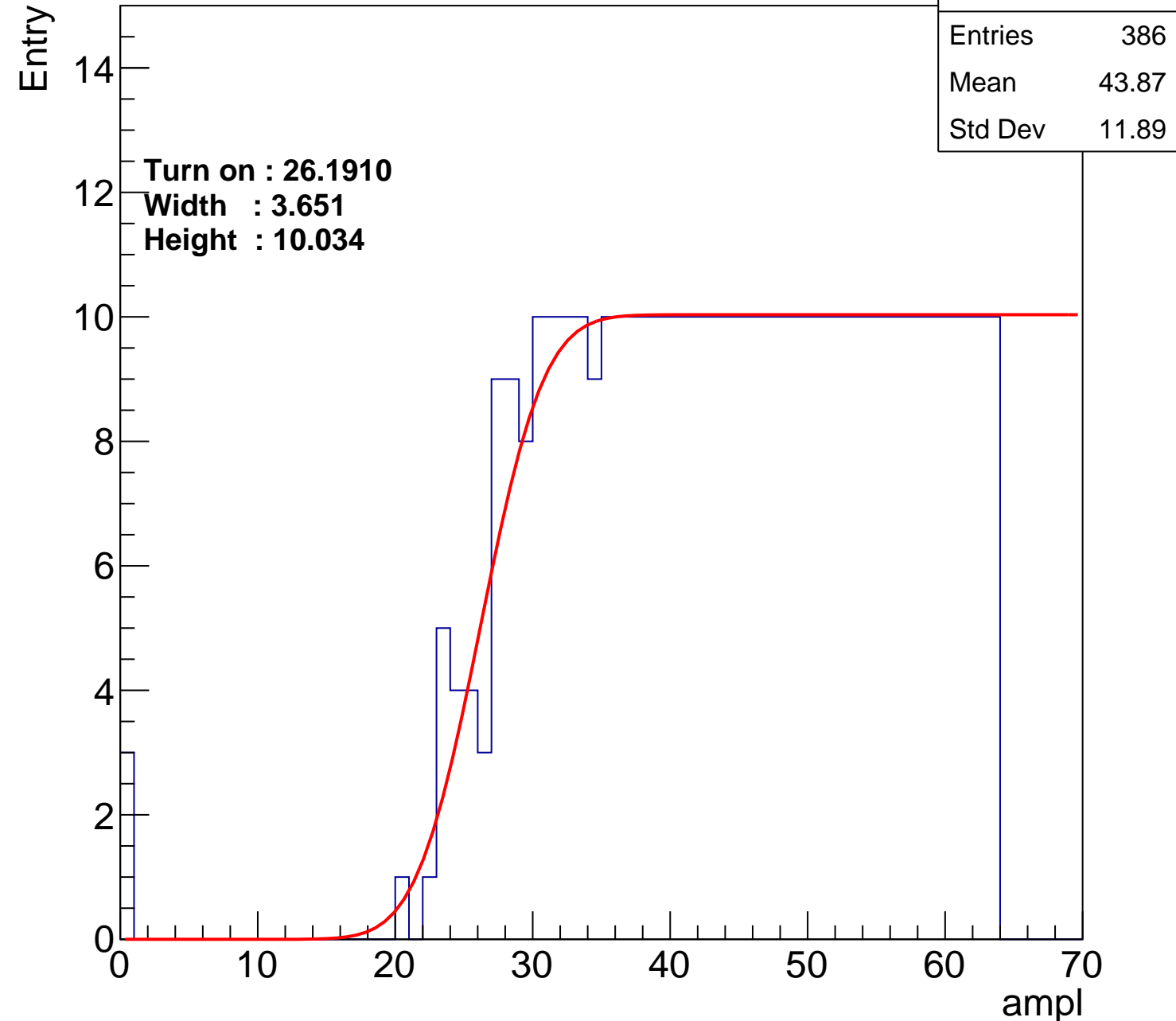
Width : 3.651

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.85
Std Dev	11.03

Turn on : 26.8468

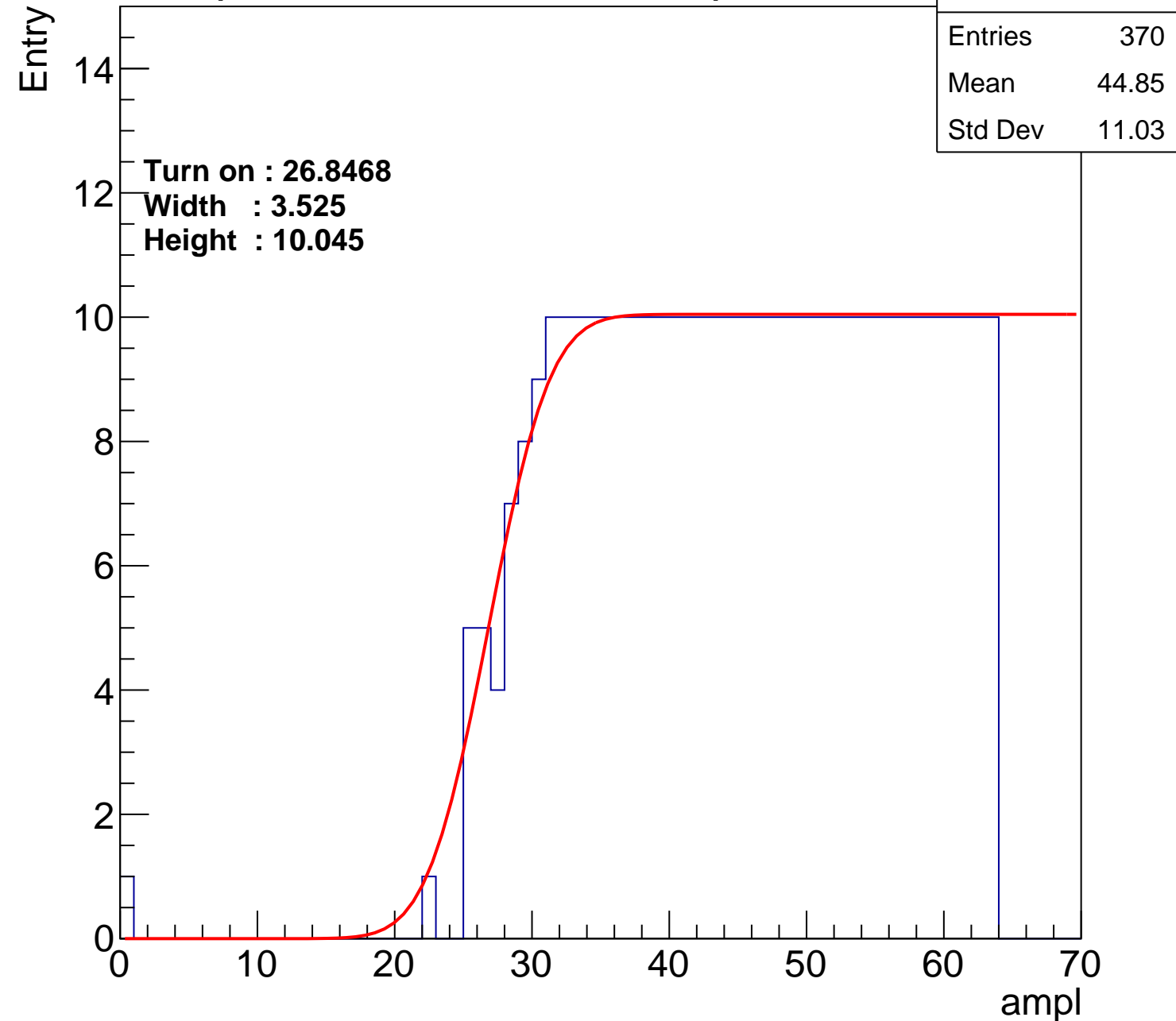
Width : 3.525

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.35
Std Dev	12.3

Turn on : 25.1473

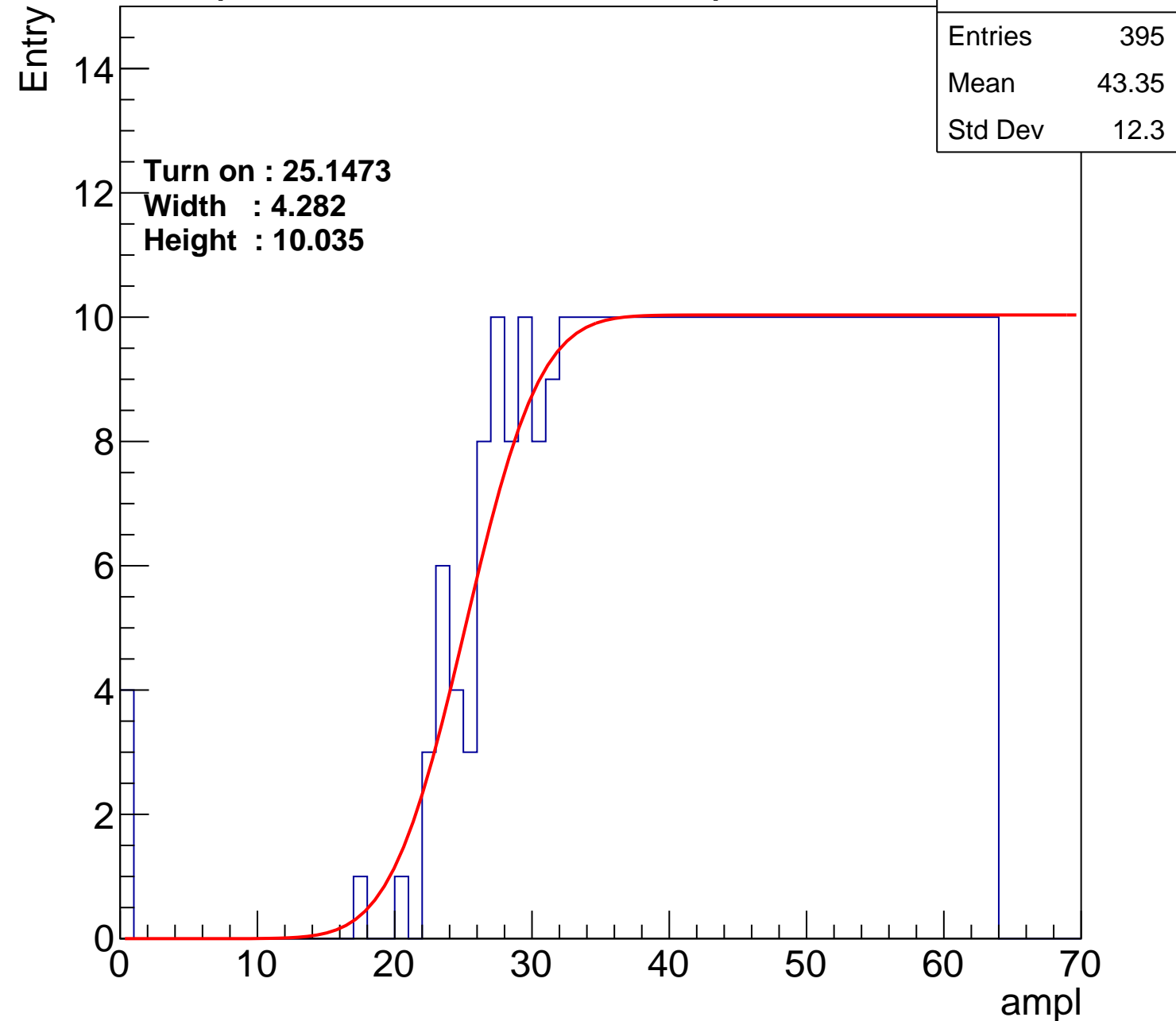
Width : 4.282

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.96
Std Dev	12.4

Turn on : 24.1666

Width : 3.826

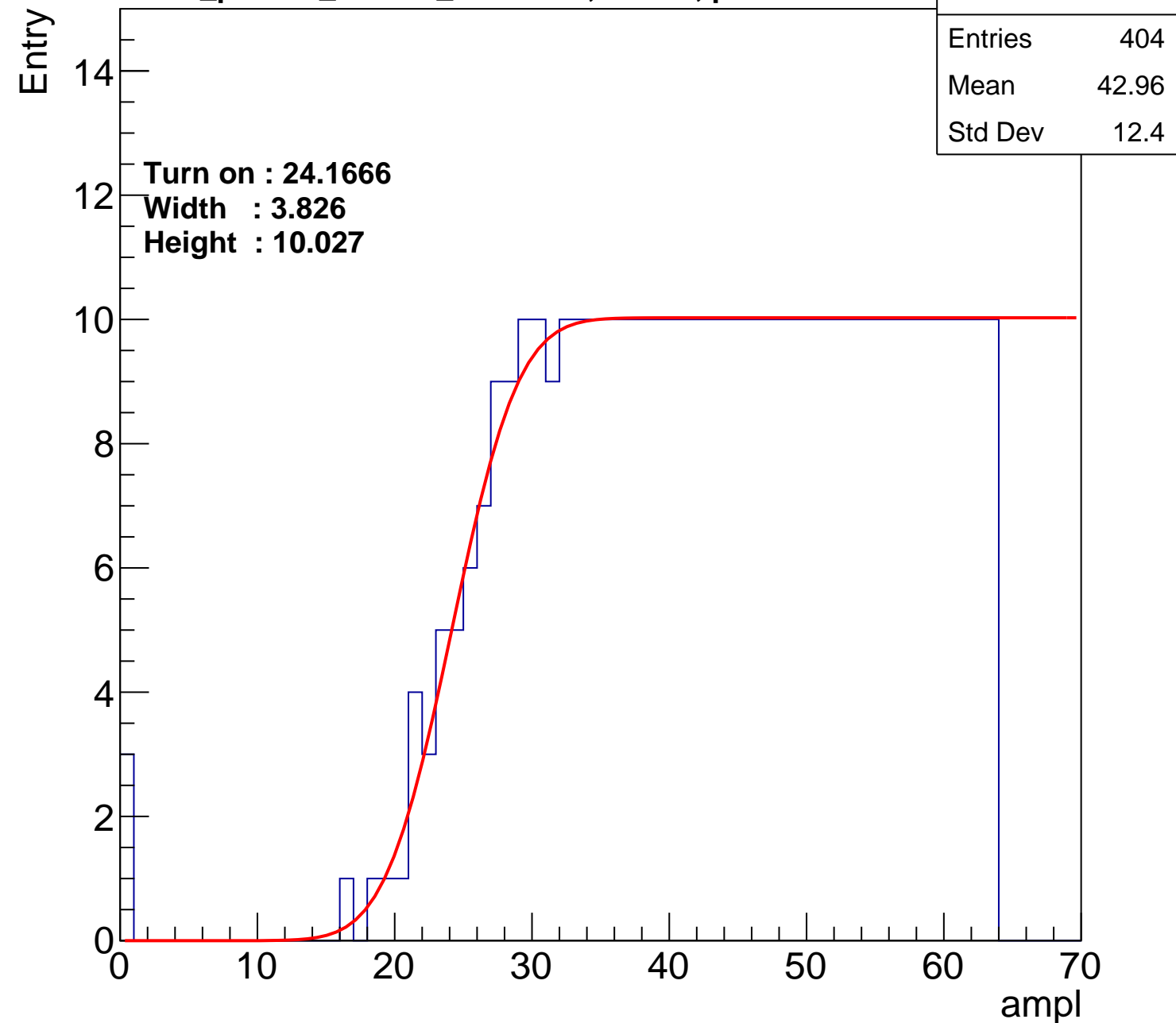
Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U13-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.05
Std Dev	12.11

Turn on : 23.8777

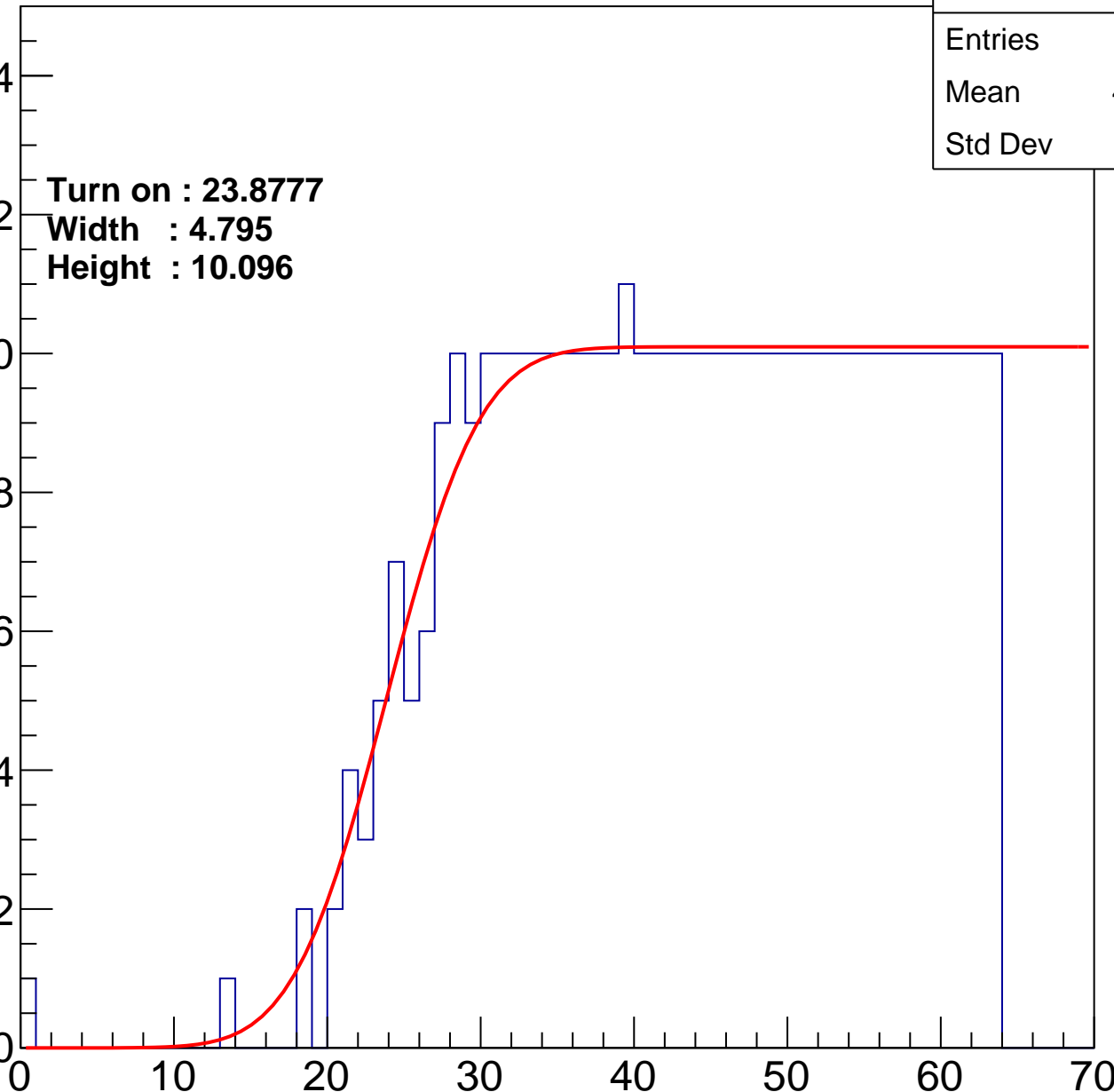
Width : 4.795

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.9223

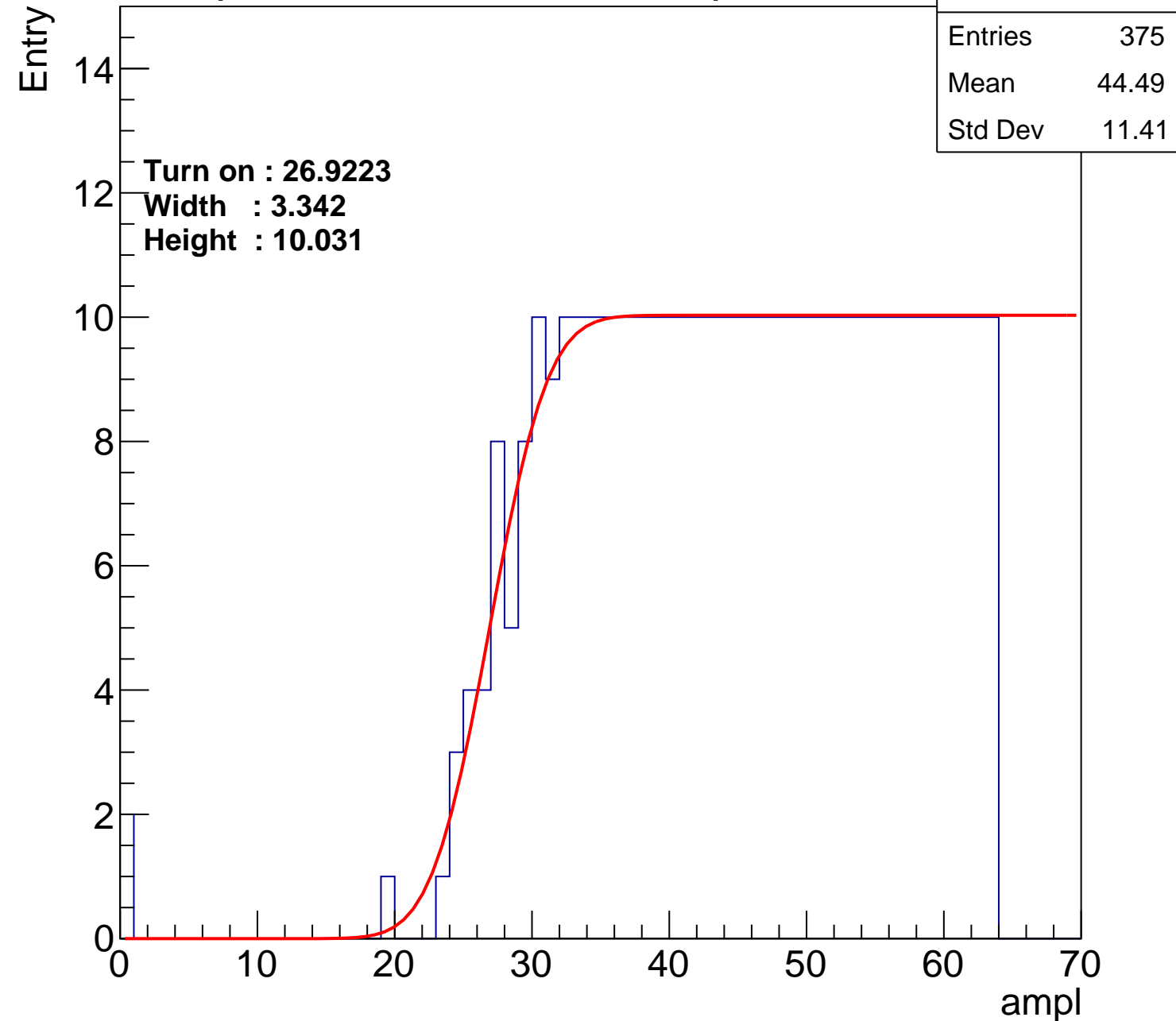
Width : 3.342

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch98

calib_packv5_042523_0143.root, FC#12, port B1

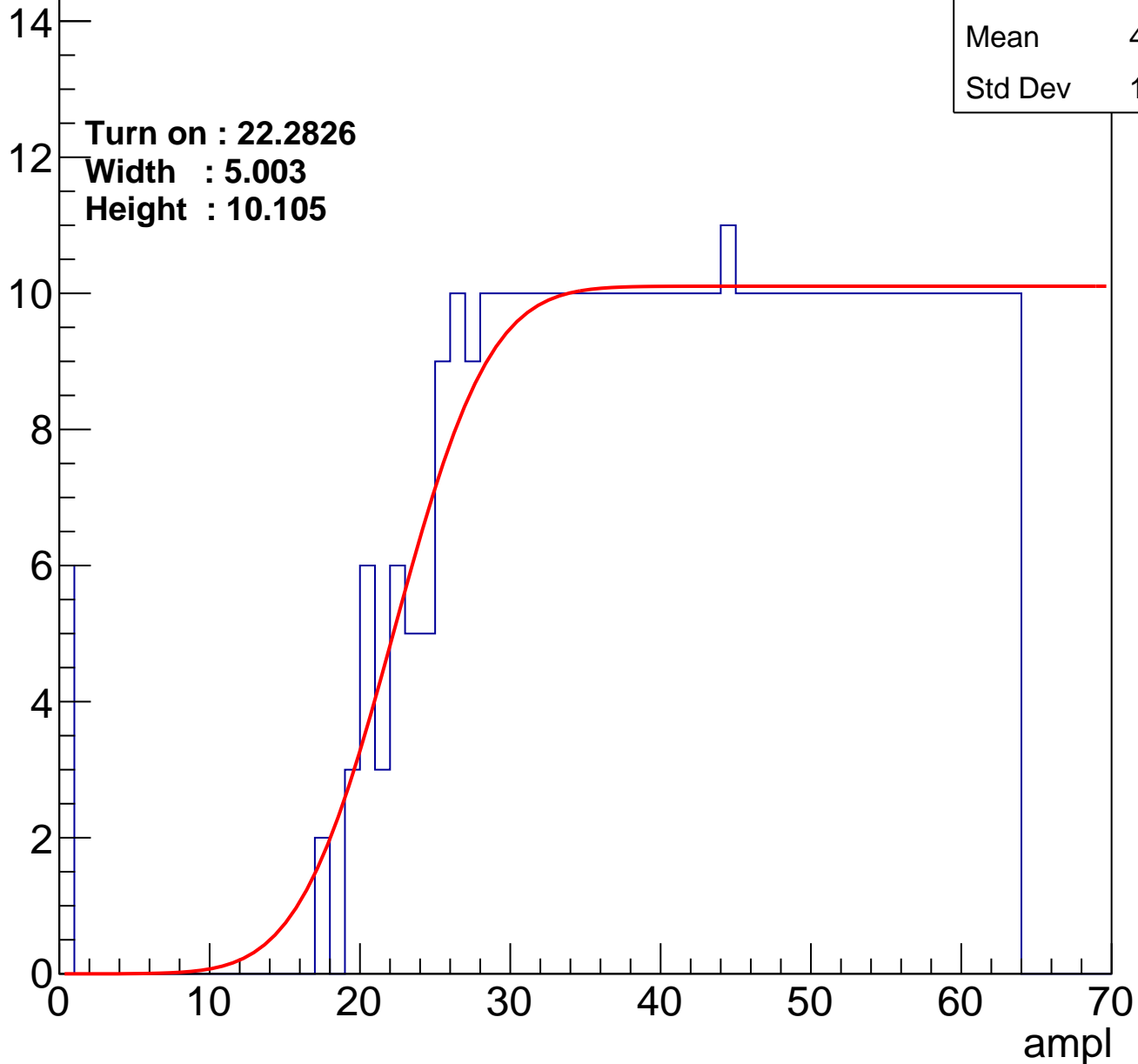
Entries	425
Mean	41.87
Std Dev	13.19

Turn on : 22.2826

Width : 5.003

Height : 10.105

Entry



B0L102S, U13-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.36
Std Dev	11.7

Turn on : 27.5678

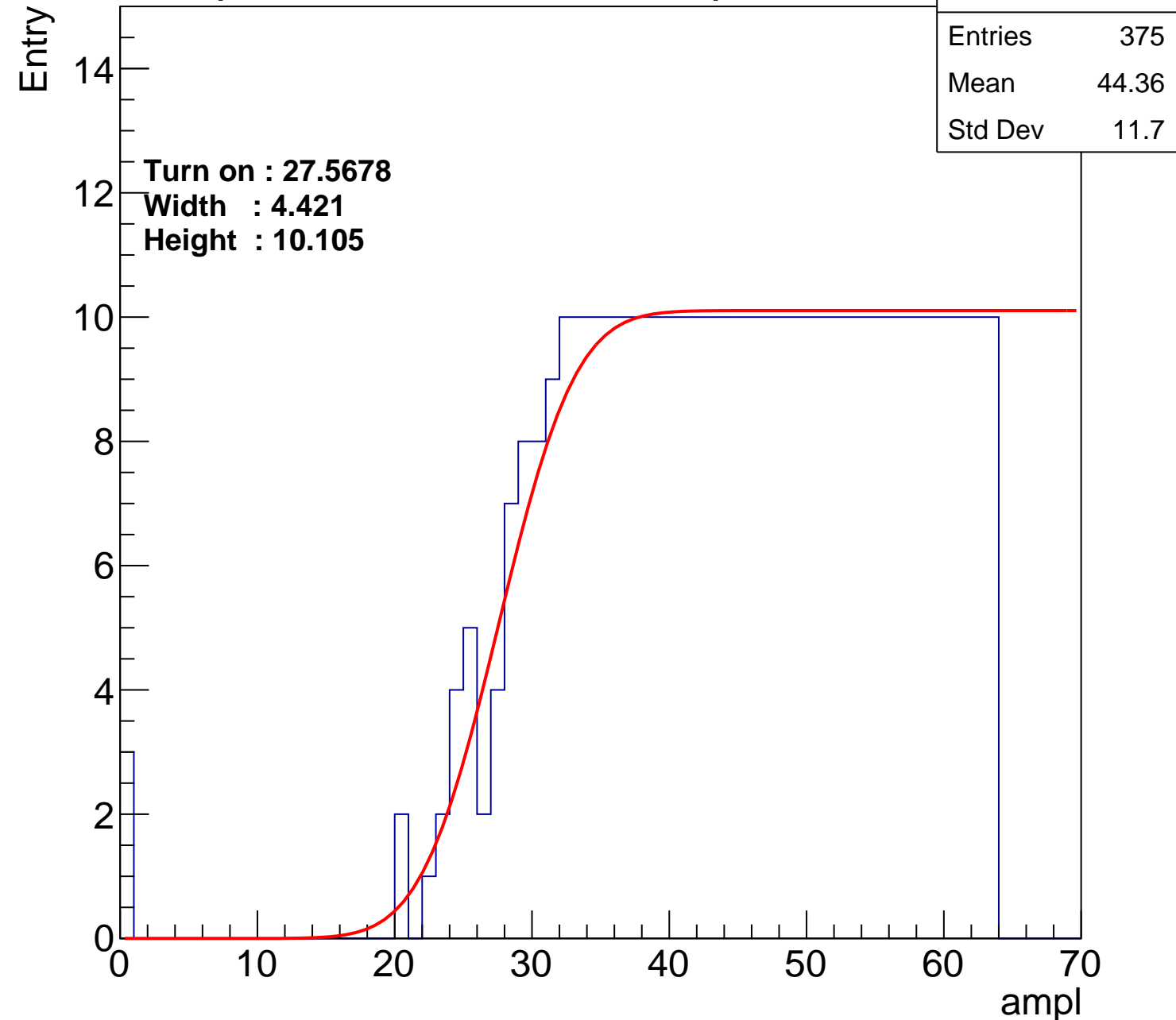
Width : 4.421

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	357
Mean	45.21
Std Dev	11.41

Turn on : 29.0482

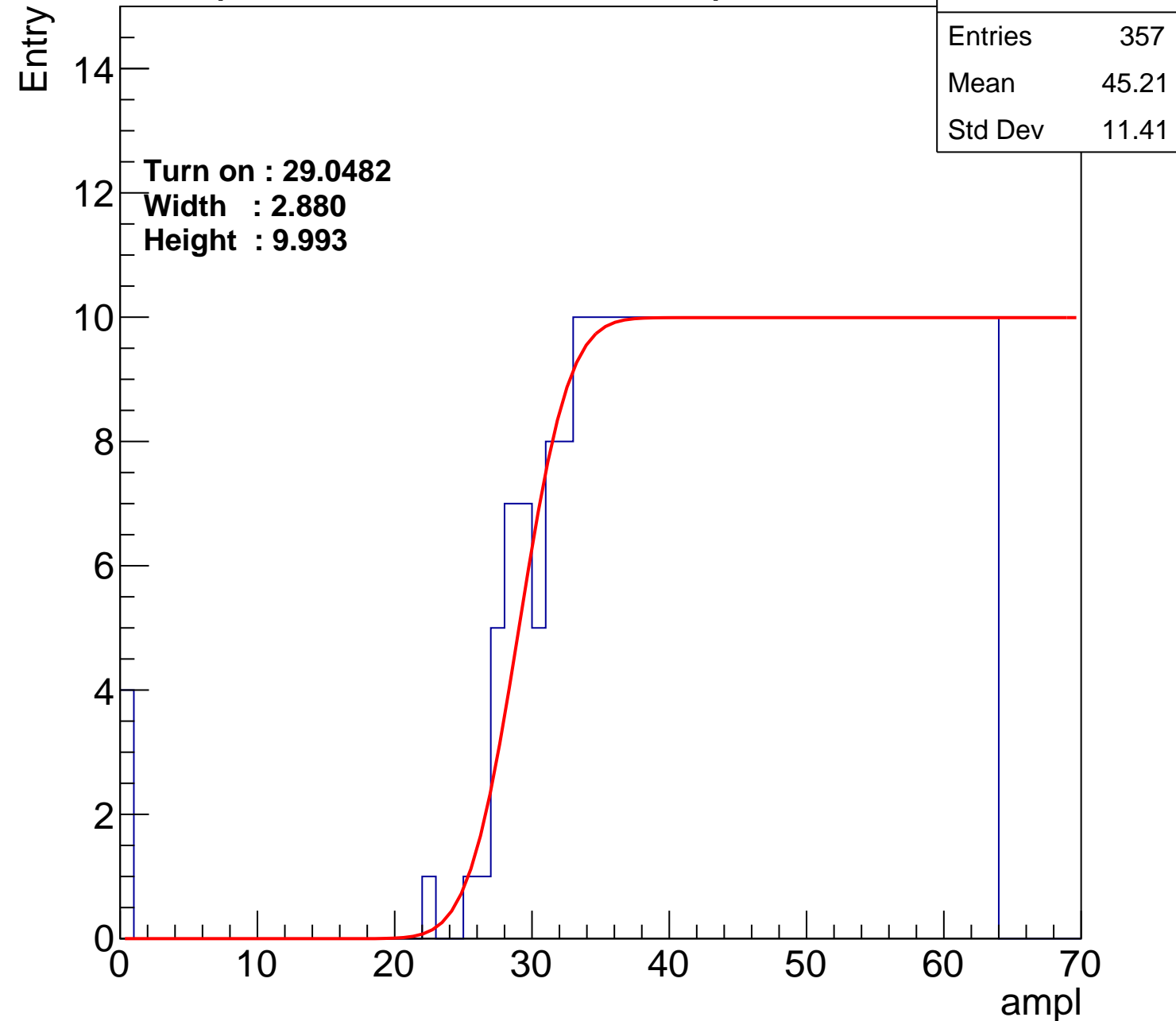
Width : 2.880

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.71
Std Dev	12.42

Turn on : 24.1658

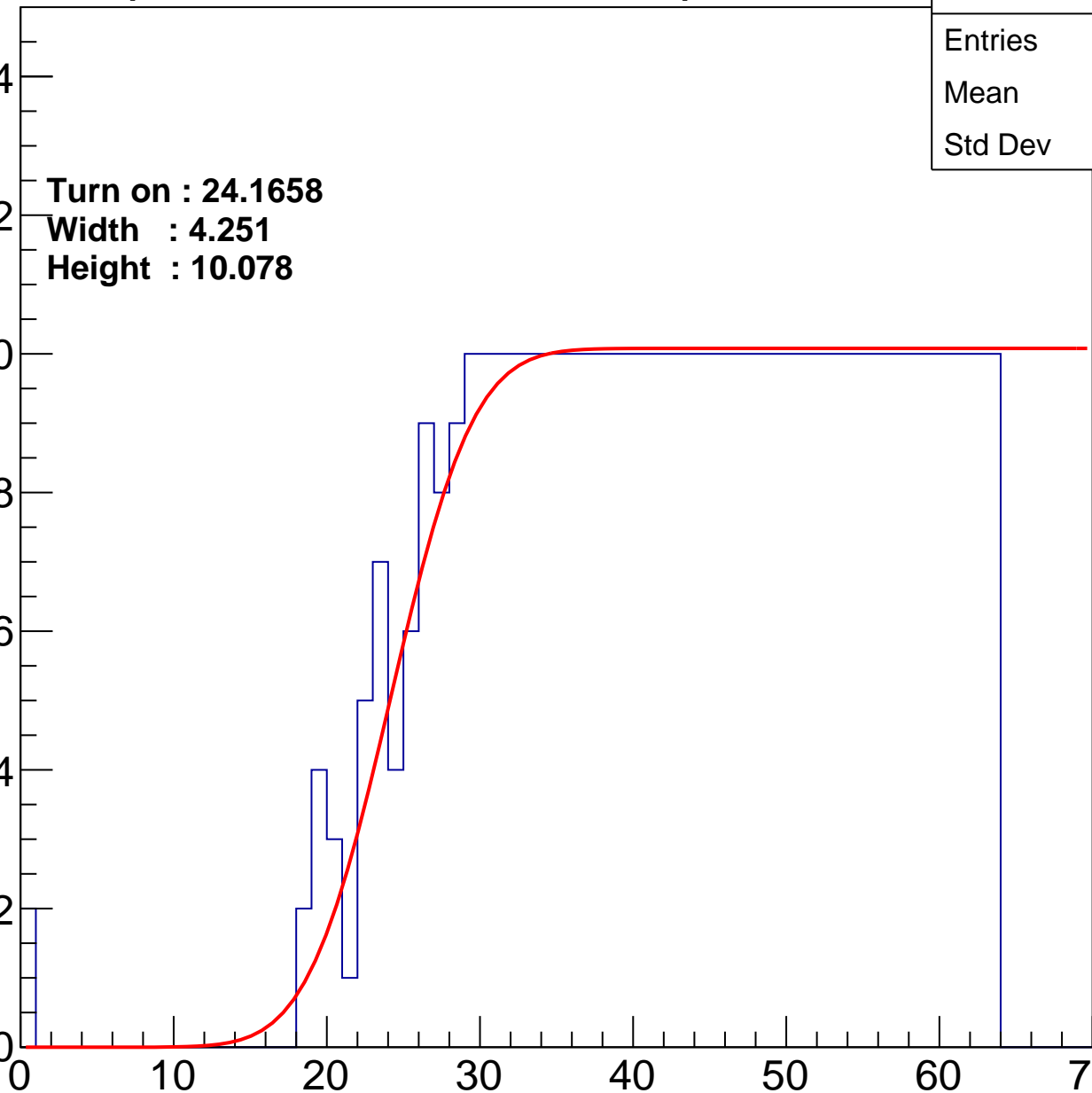
Width : 4.251

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.92
Std Dev	11.26

Turn on : 28.4865

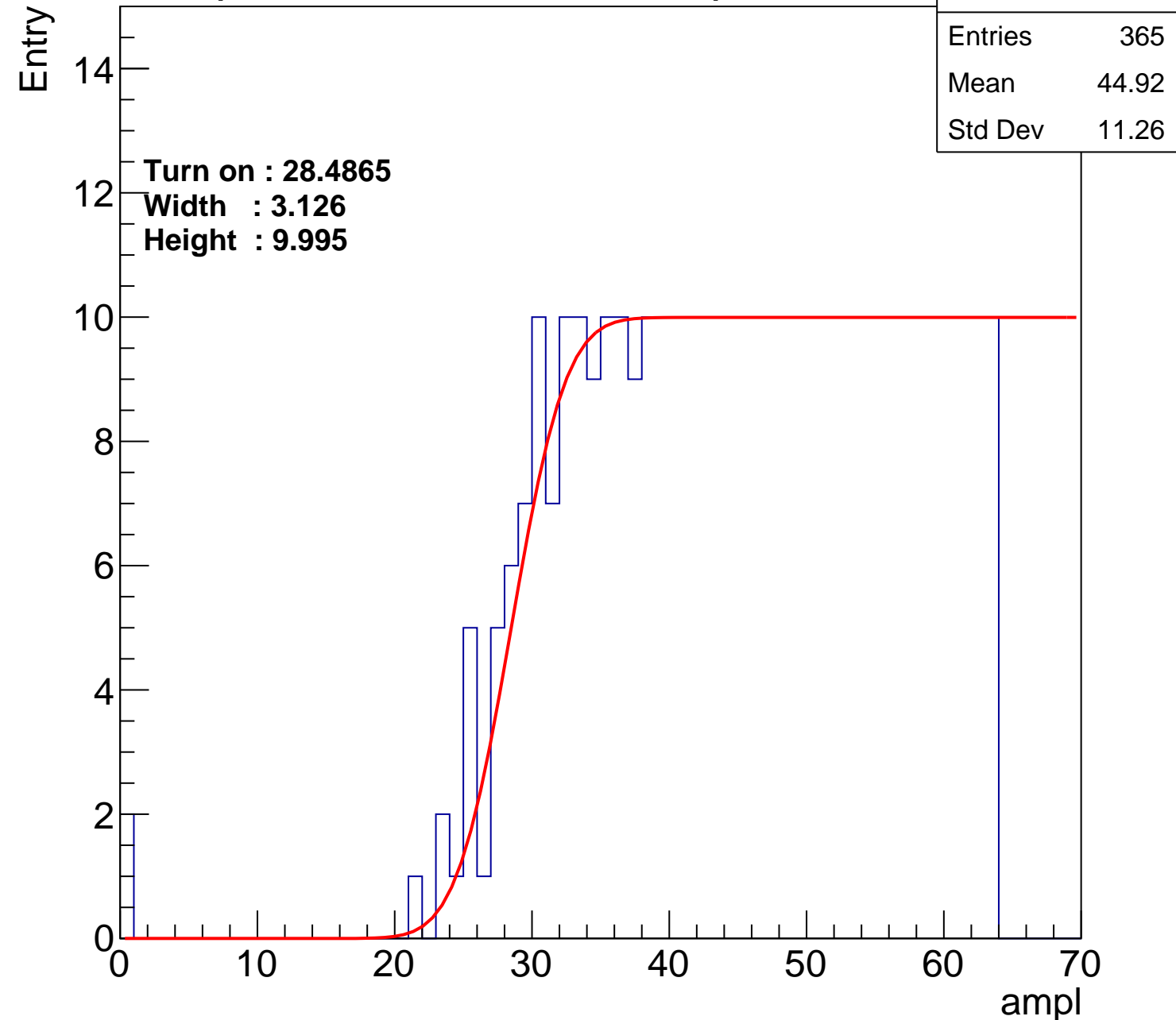
Width : 3.126

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.81
Std Dev	11.58

Turn on : 25.2648

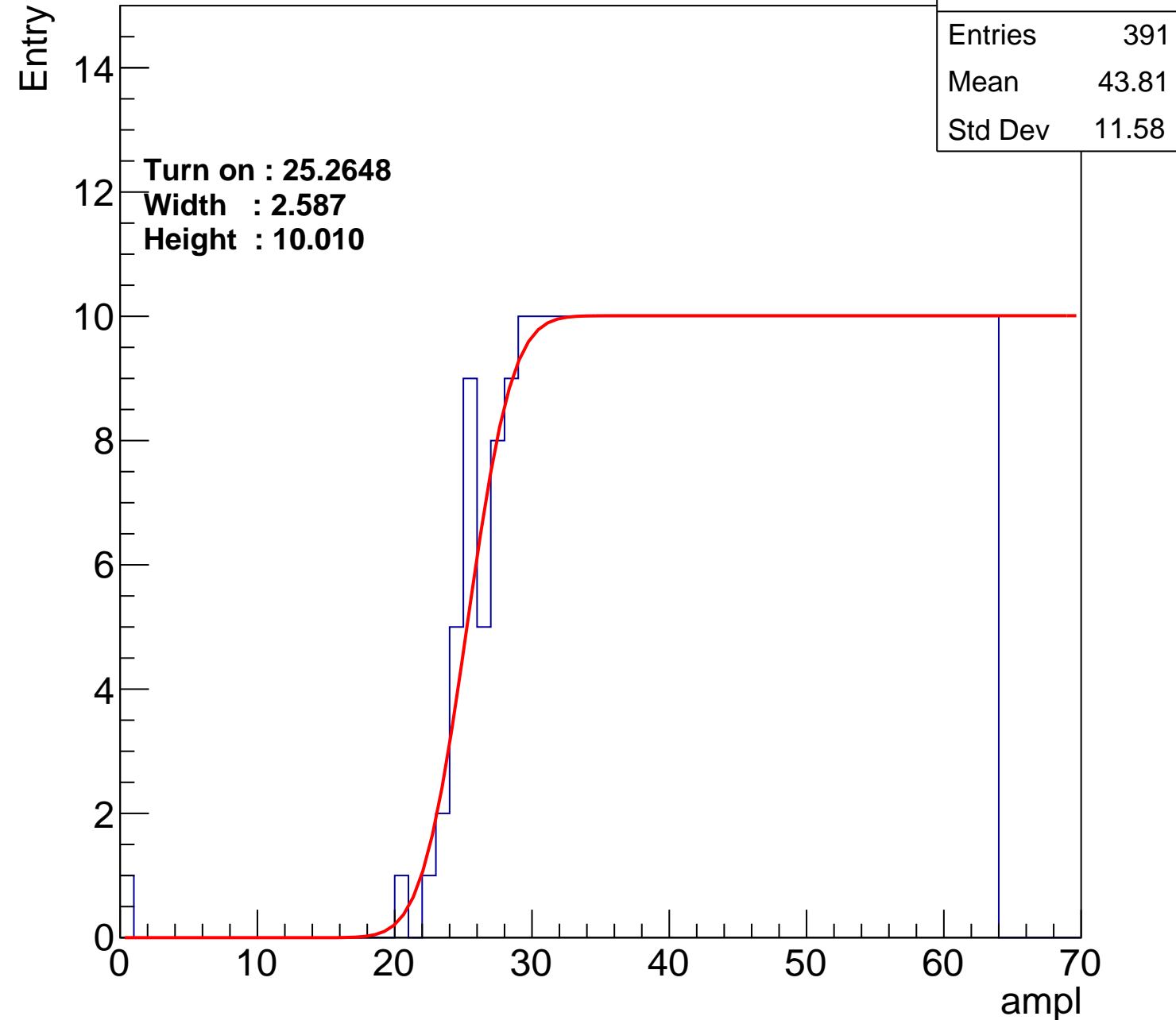
Width : 2.587

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch104

calib_packv5_042523_0143.root, FC#12, port B1

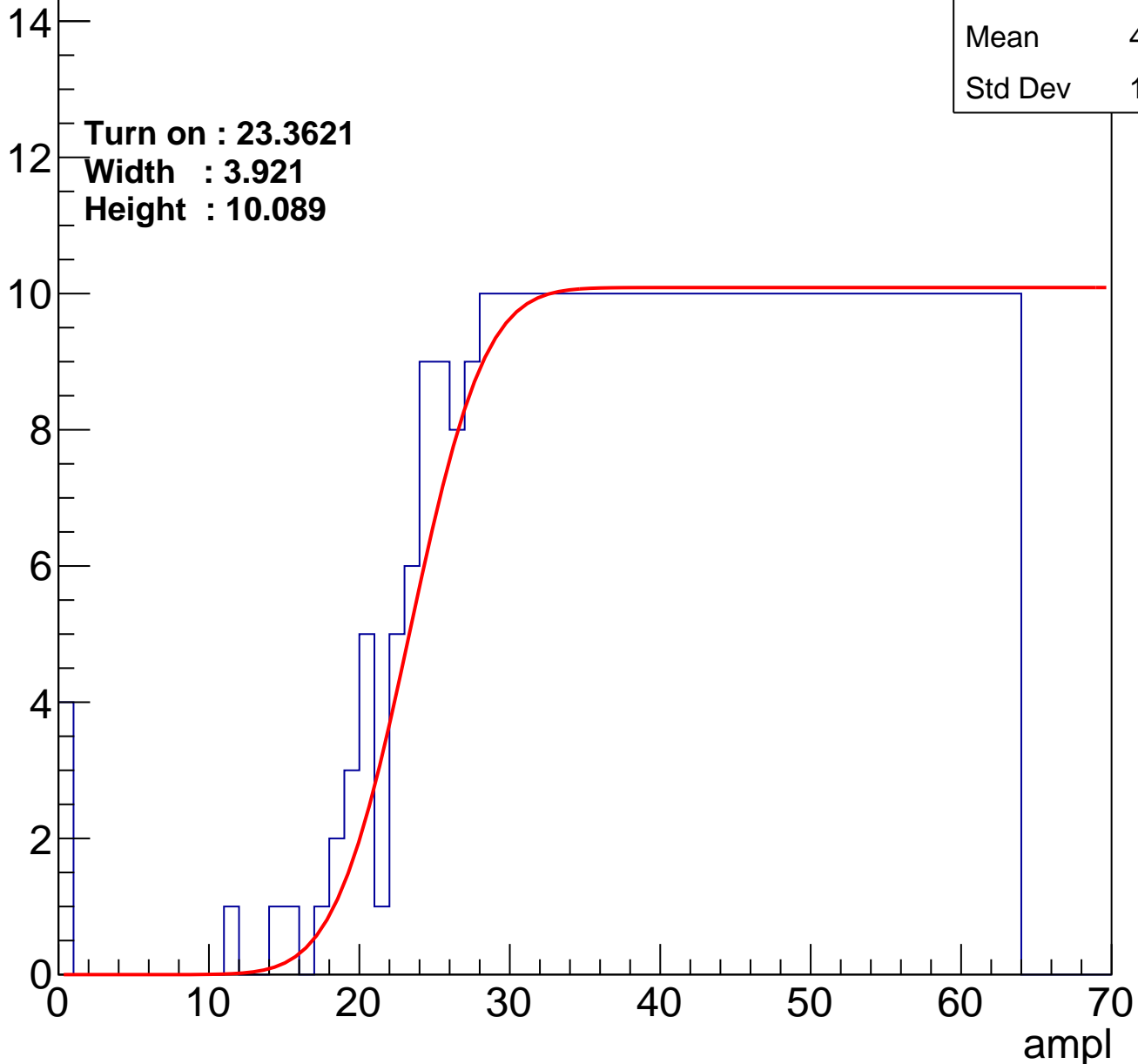
Entries	425
Mean	41.86
Std Dev	13.08

Turn on : 23.3621

Width : 3.921

Height : 10.089

Entry



B0L102S, U13-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.47
Std Dev	11.57

Turn on : 26.8980

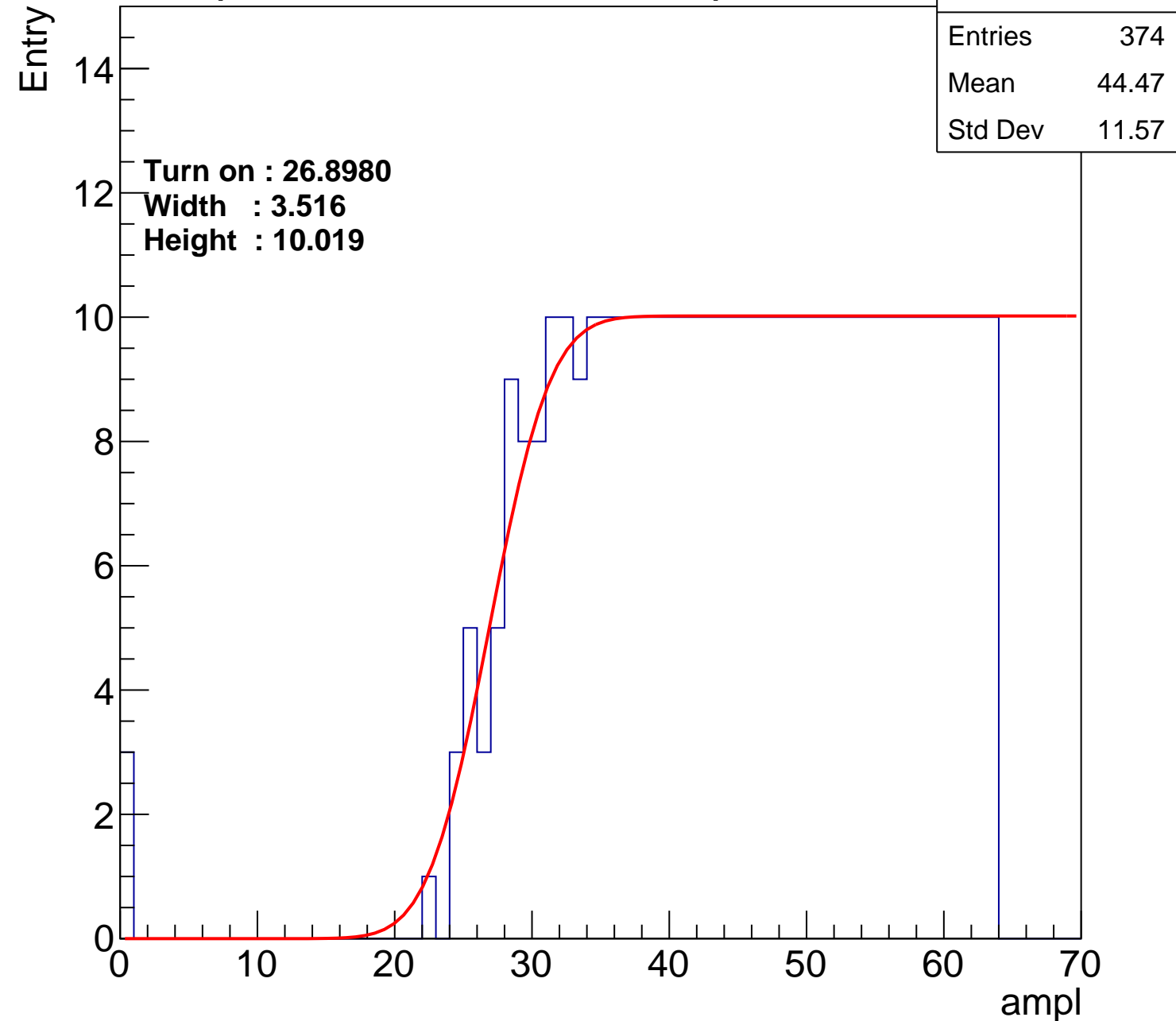
Width : 3.516

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.36
Std Dev	12.27

Turn on : 25.8602

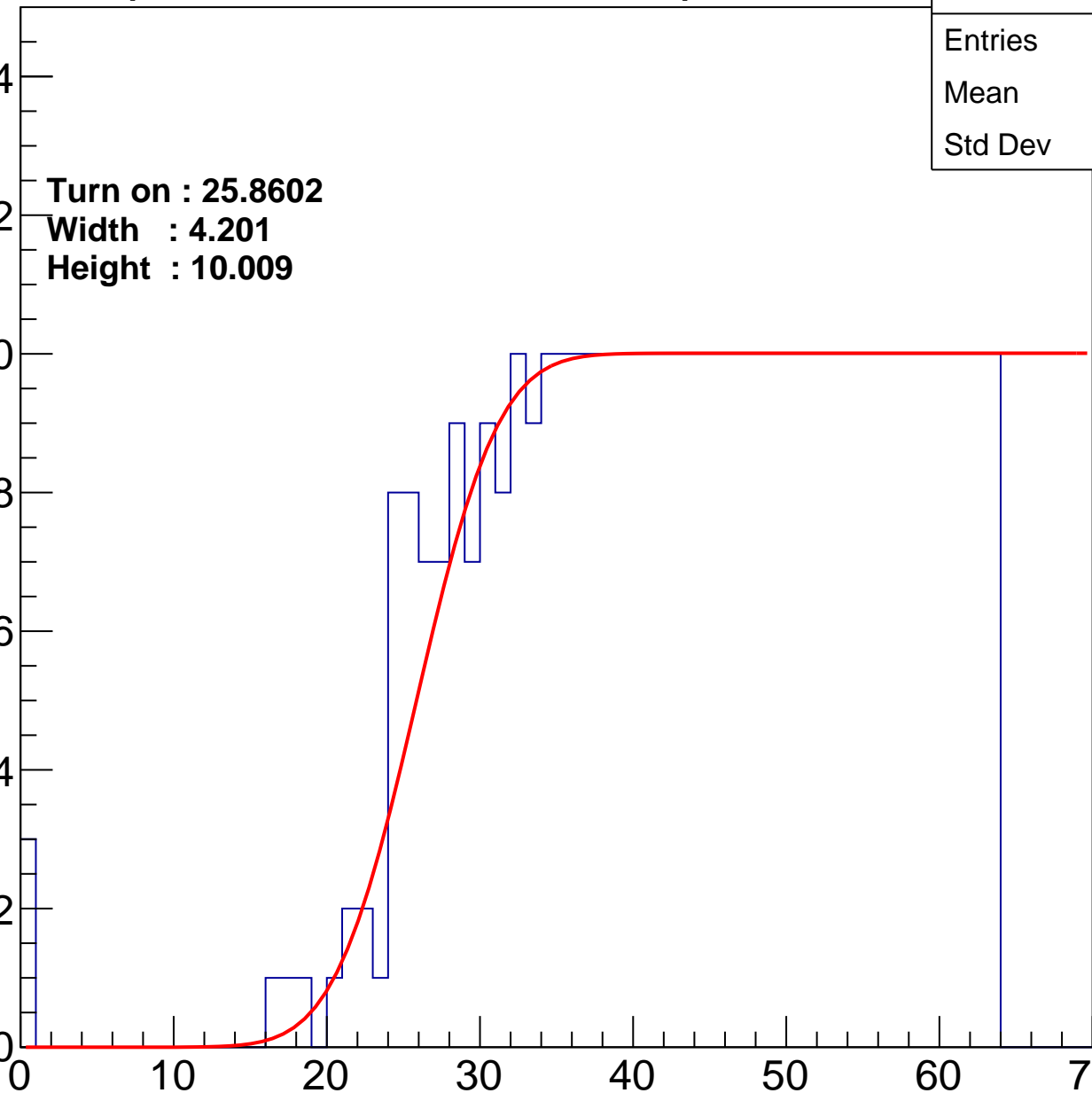
Width : 4.201

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.5249

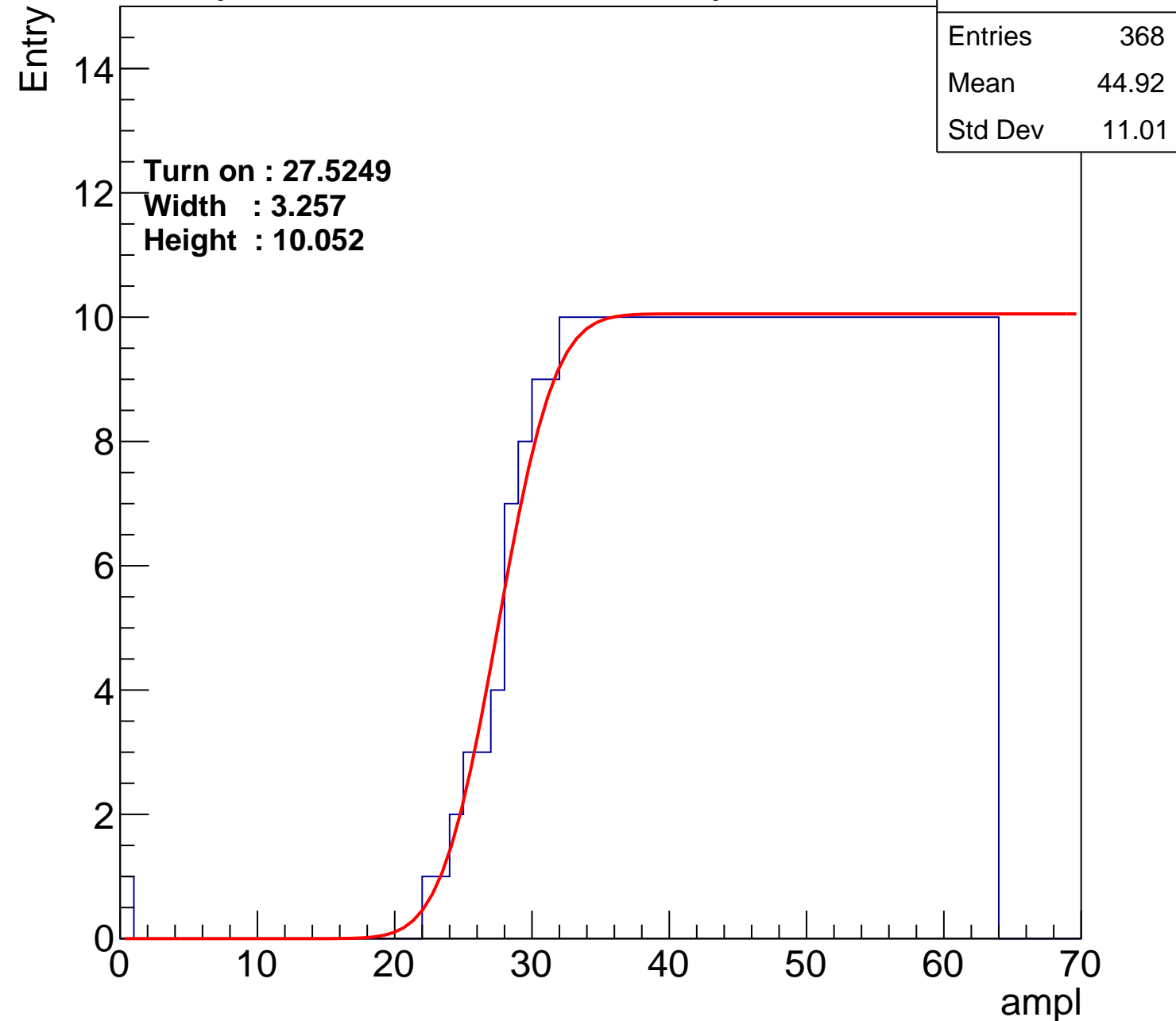
Width : 3.257

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.82
Std Dev	12.1

Turn on : 26.5740

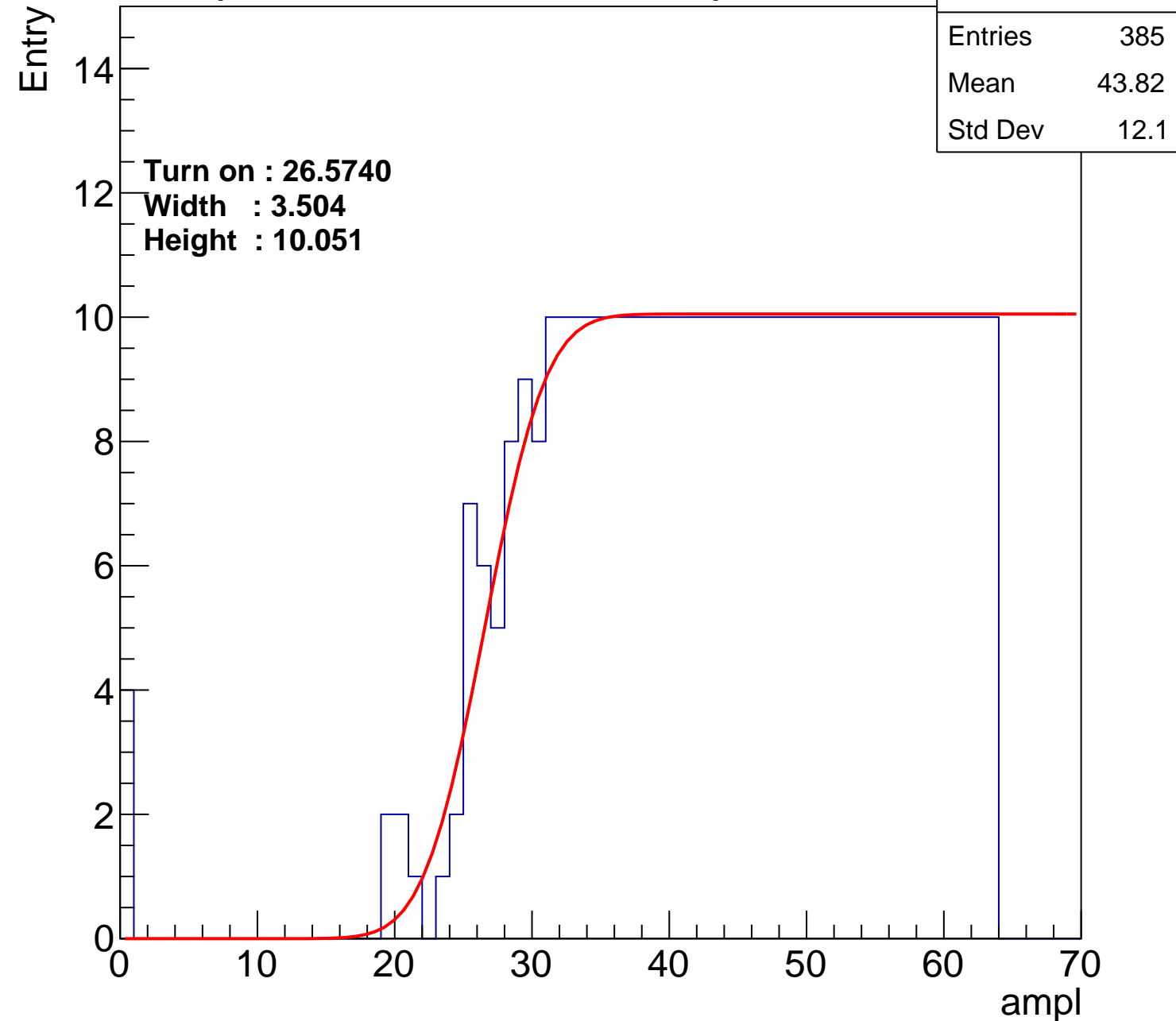
Width : 3.504

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.17
Std Dev	12.04

Turn on : 27.2895

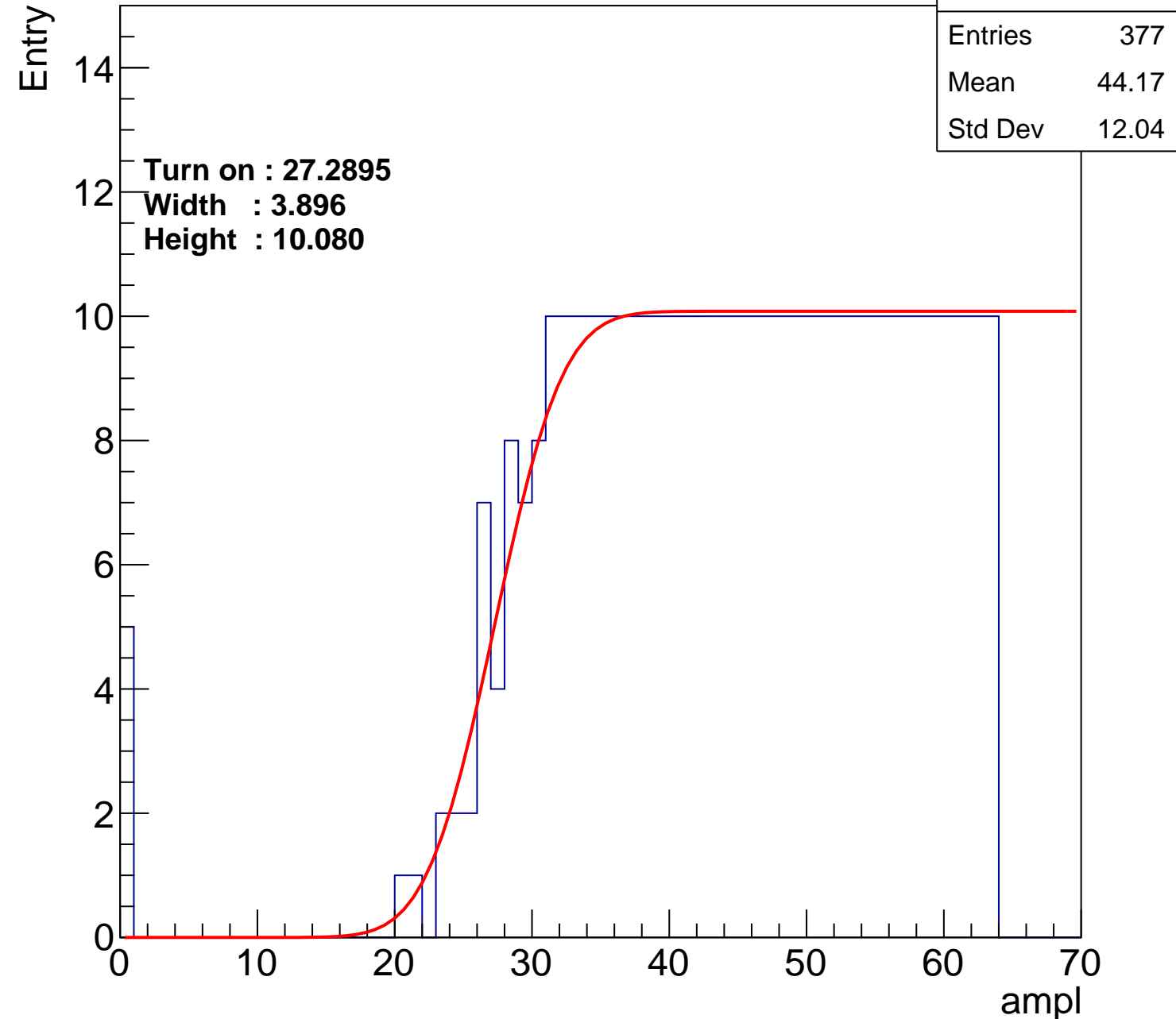
Width : 3.896

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	43.07
Std Dev	12.32

Turn on : 24.0484

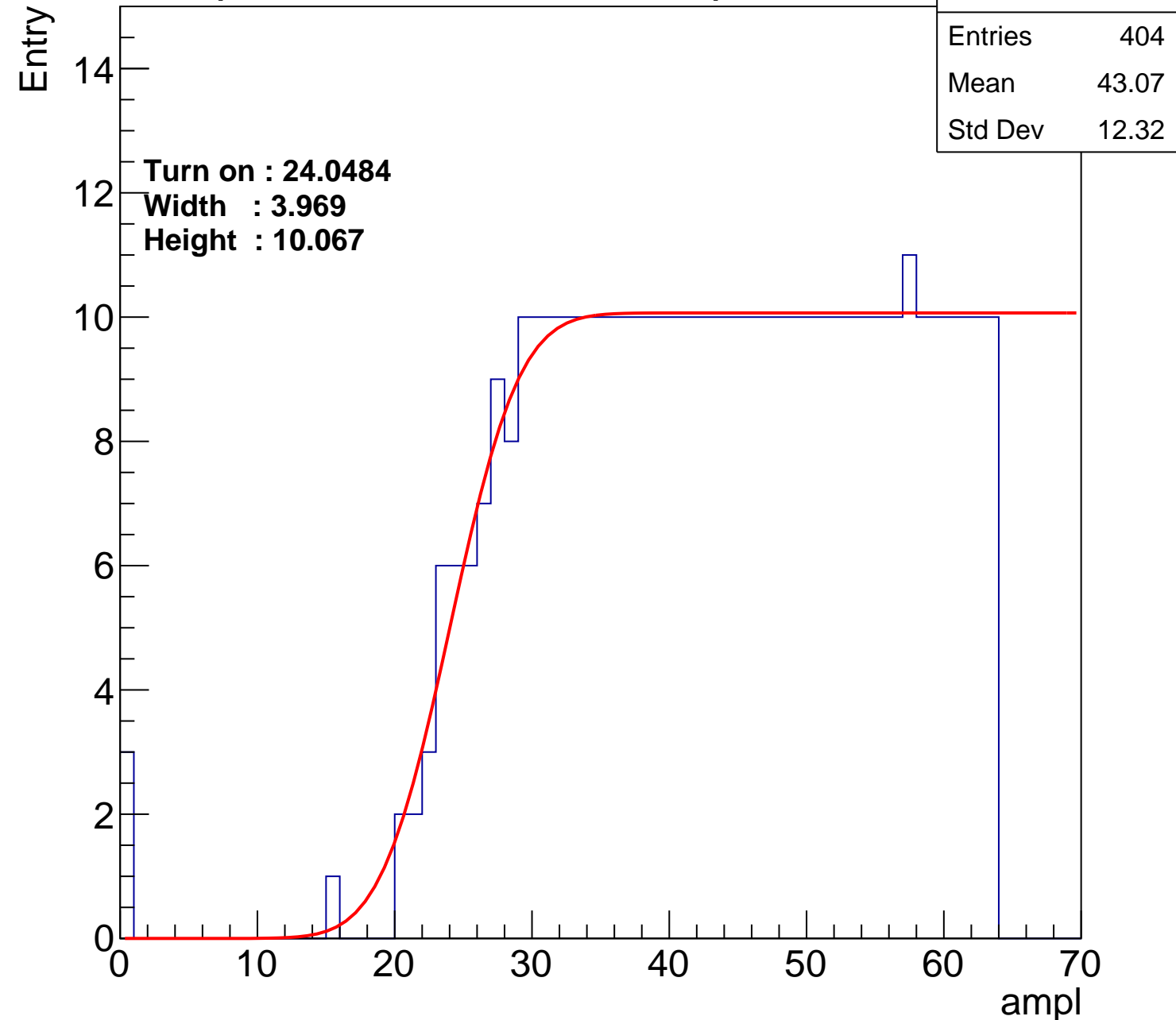
Width : 3.969

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.21
Std Dev	12.3

Turn on : 23.3995

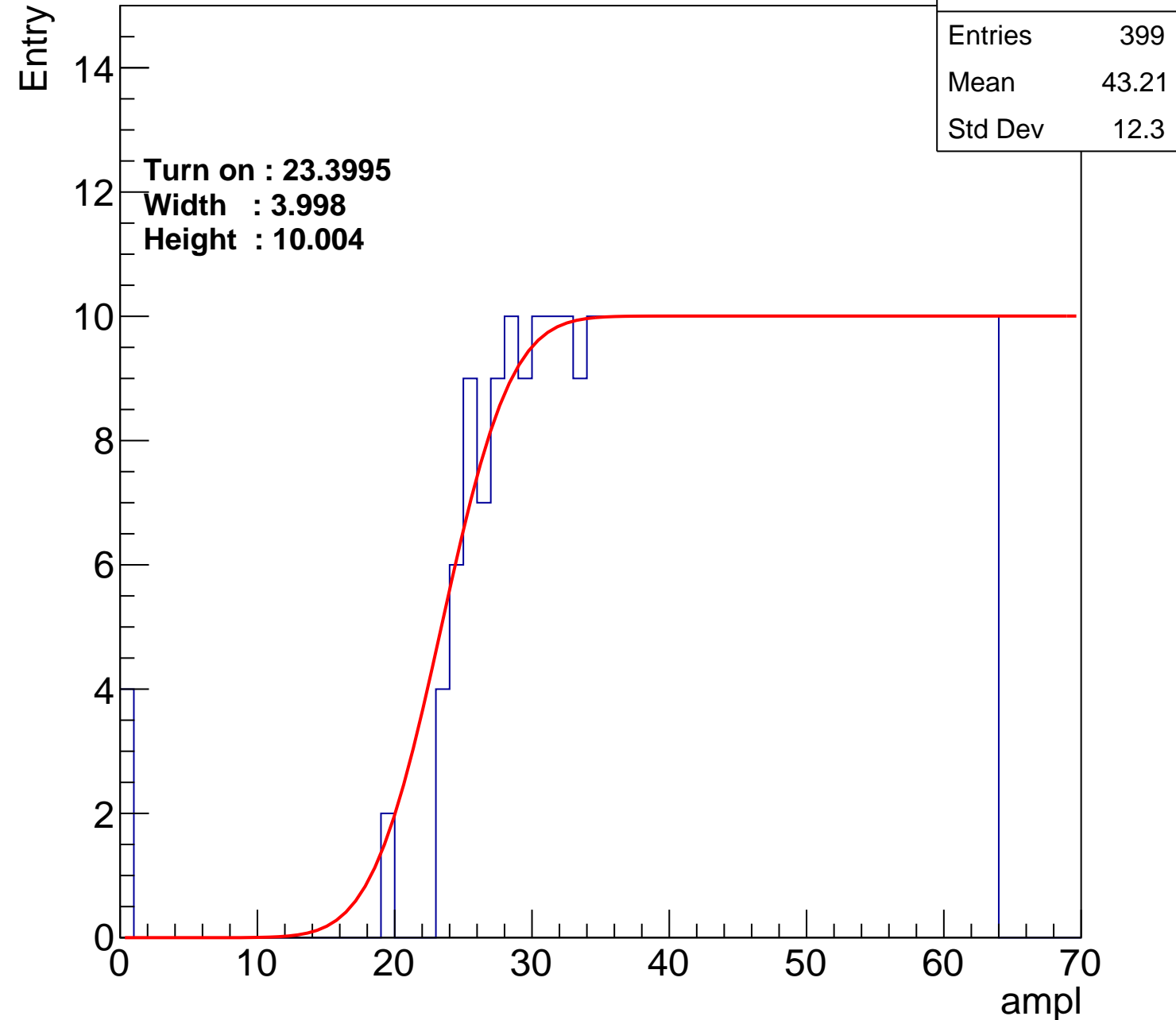
Width : 3.998

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.14
Std Dev	12.33

Turn on : 25.4247

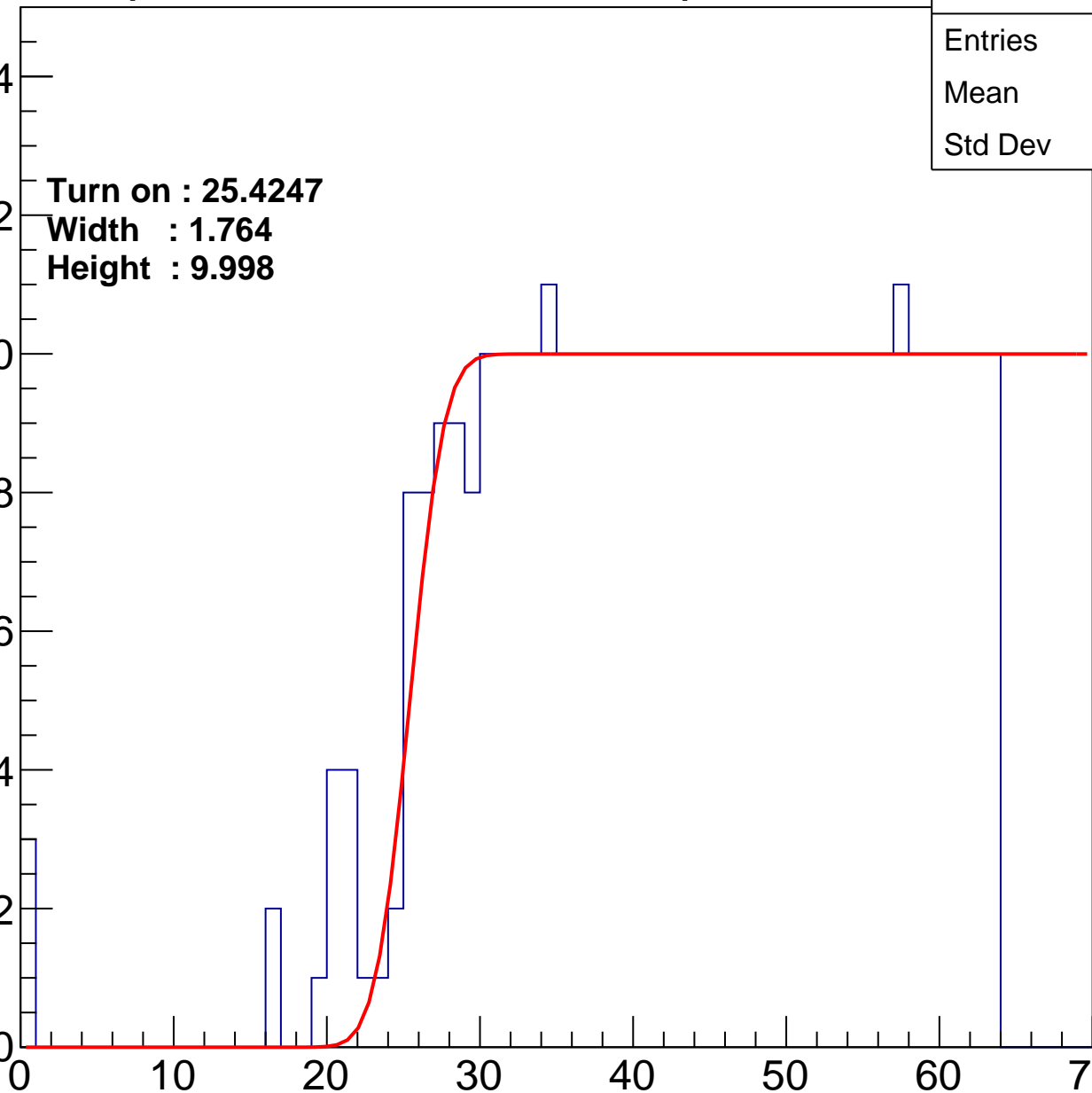
Width : 1.764

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.42
Std Dev	11.47

Turn on : 26.9572

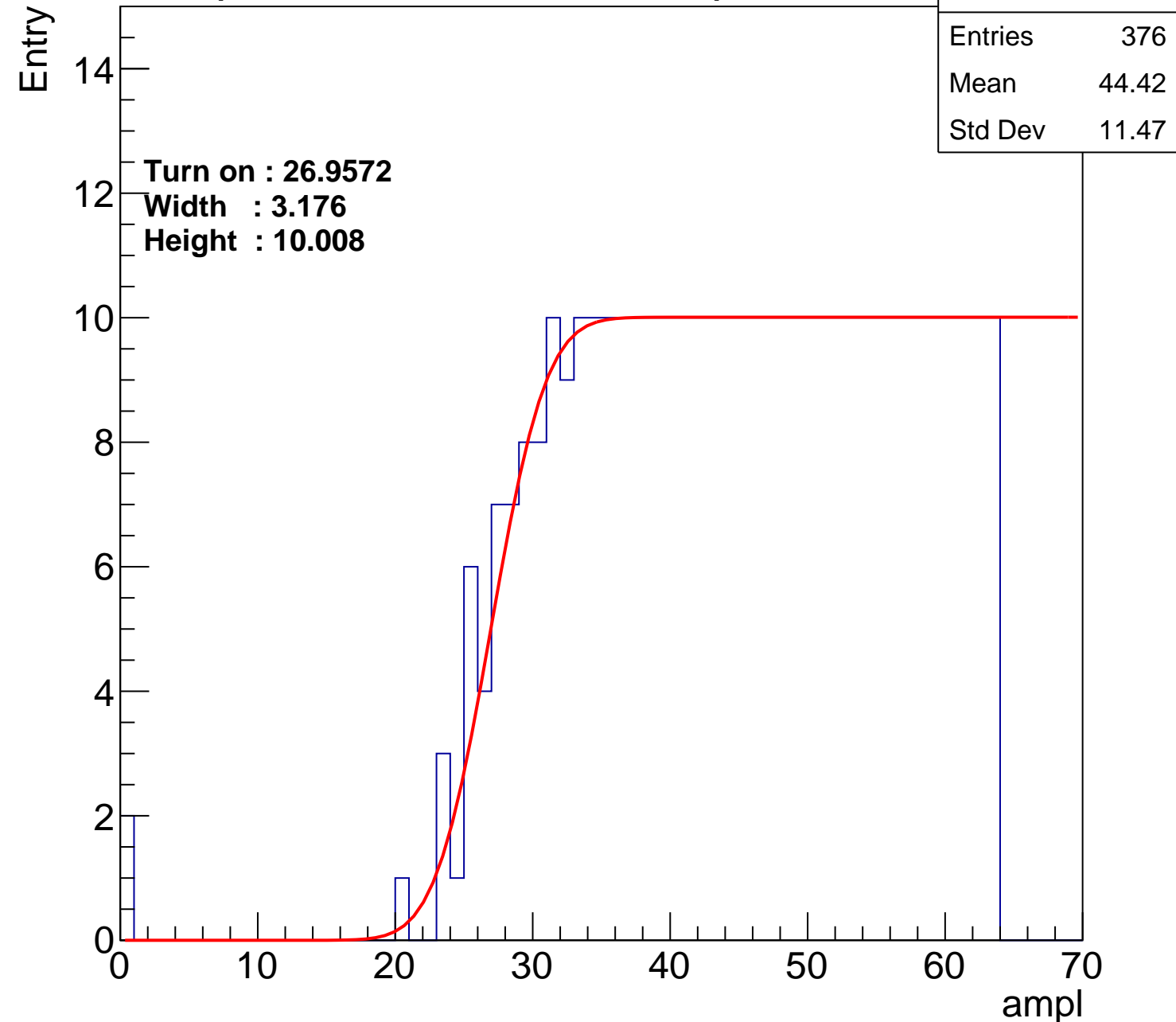
Width : 3.176

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.36
Std Dev	11.7

Turn on : 27.2049

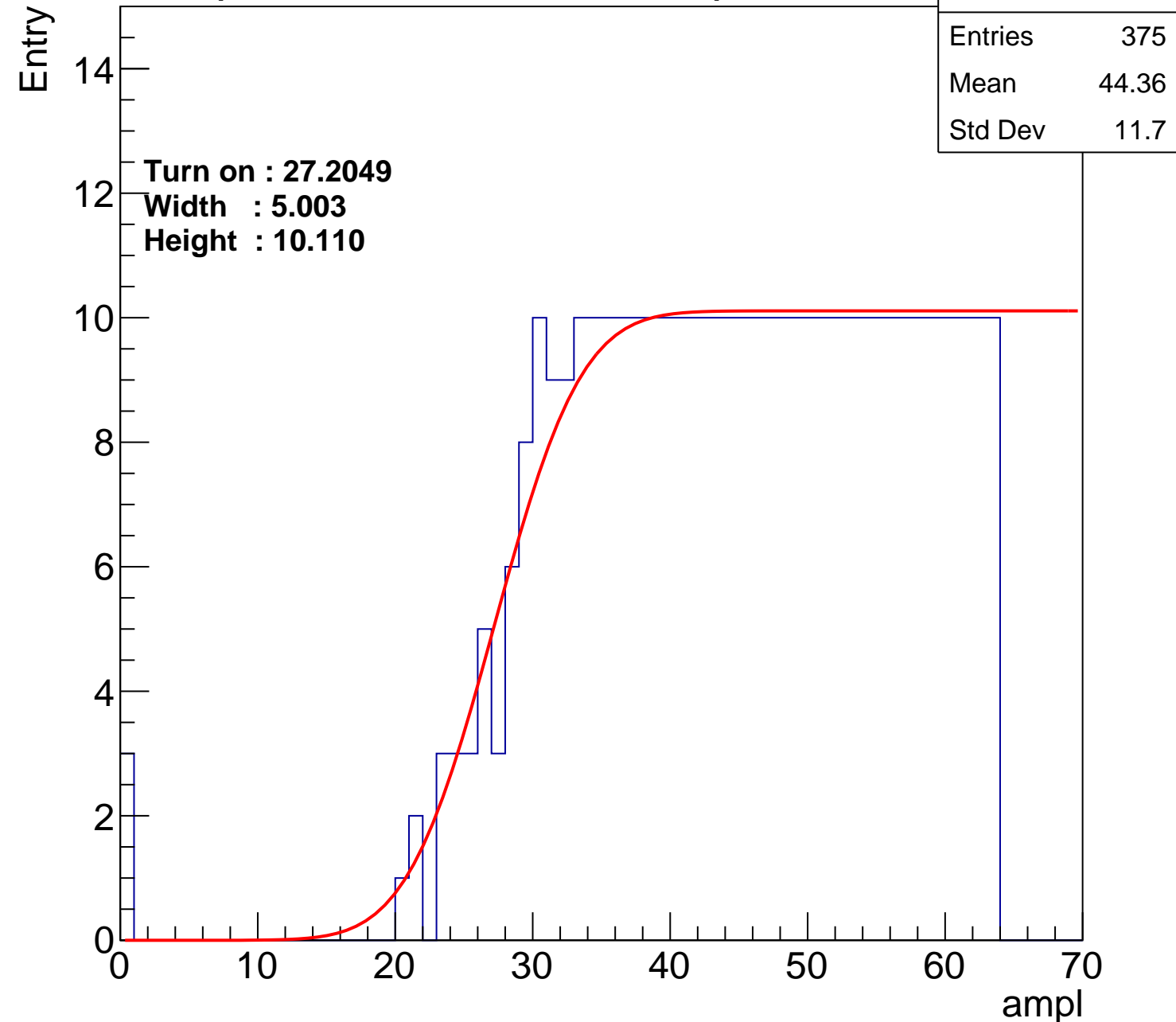
Width : 5.003

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.73
Std Dev	12.07

Turn on : 25.6713

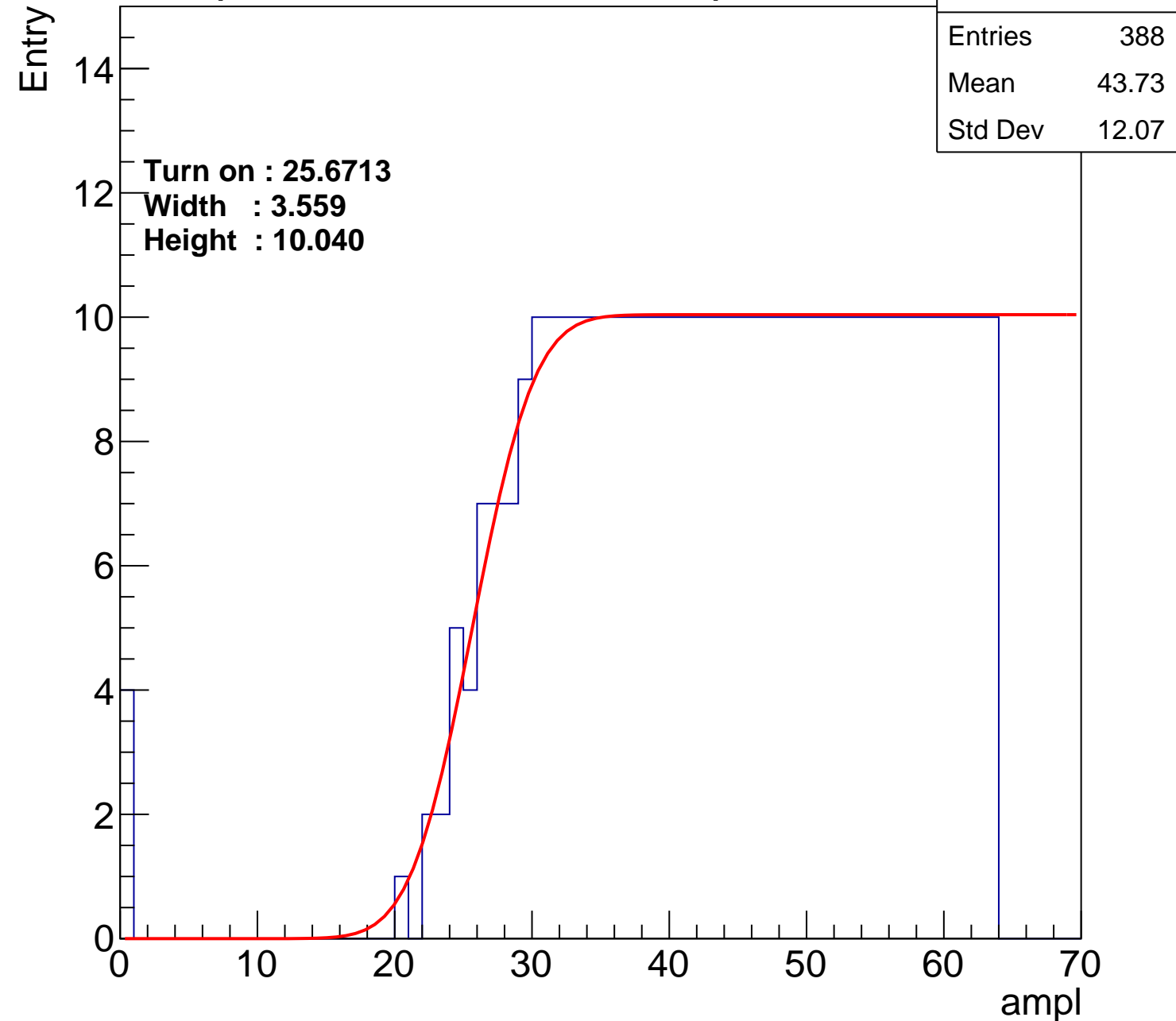
Width : 3.559

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.49
Std Dev	11.6

Turn on : 26.8278

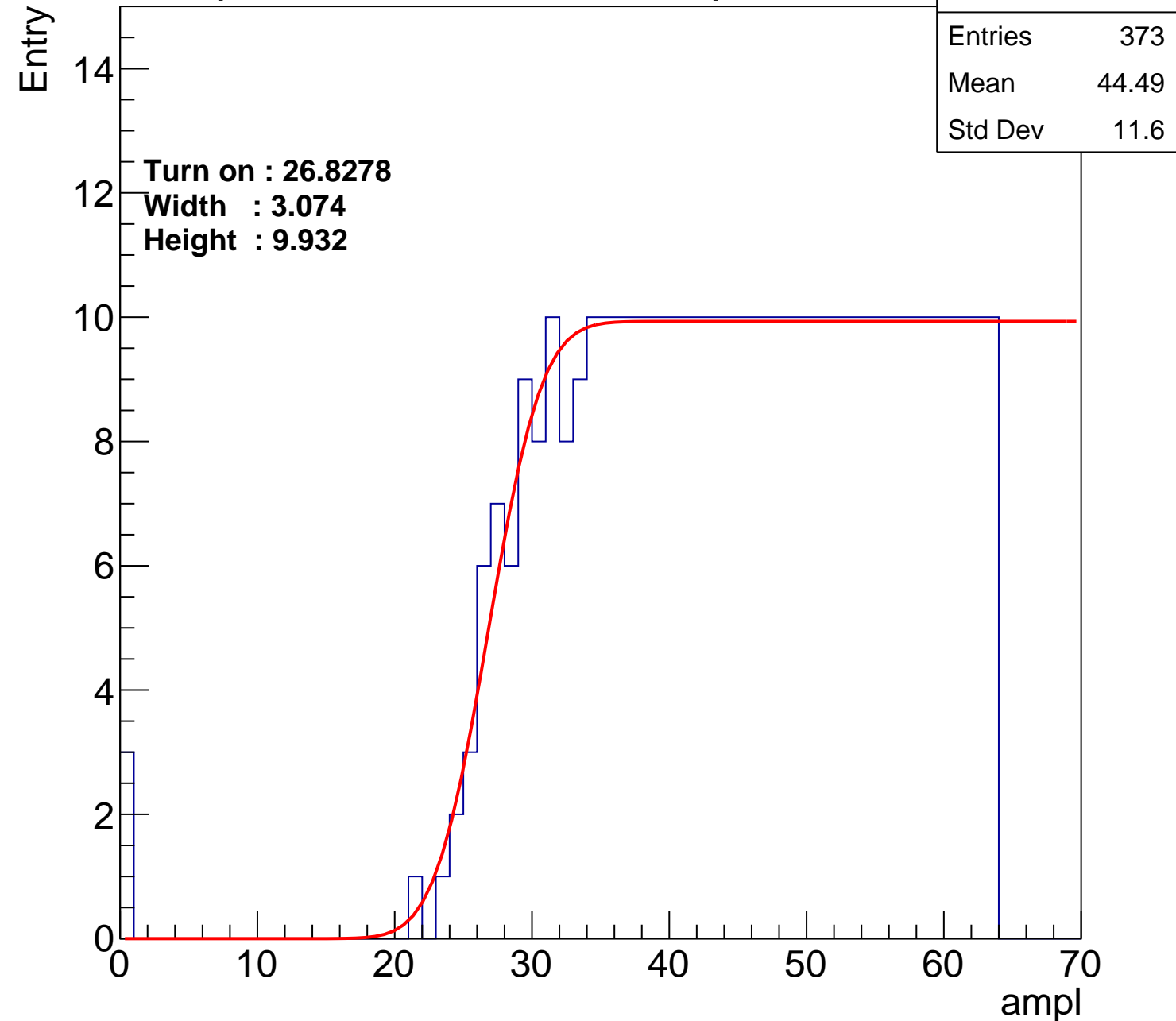
Width : 3.074

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.69
Std Dev	11.36

Turn on : 27.6020

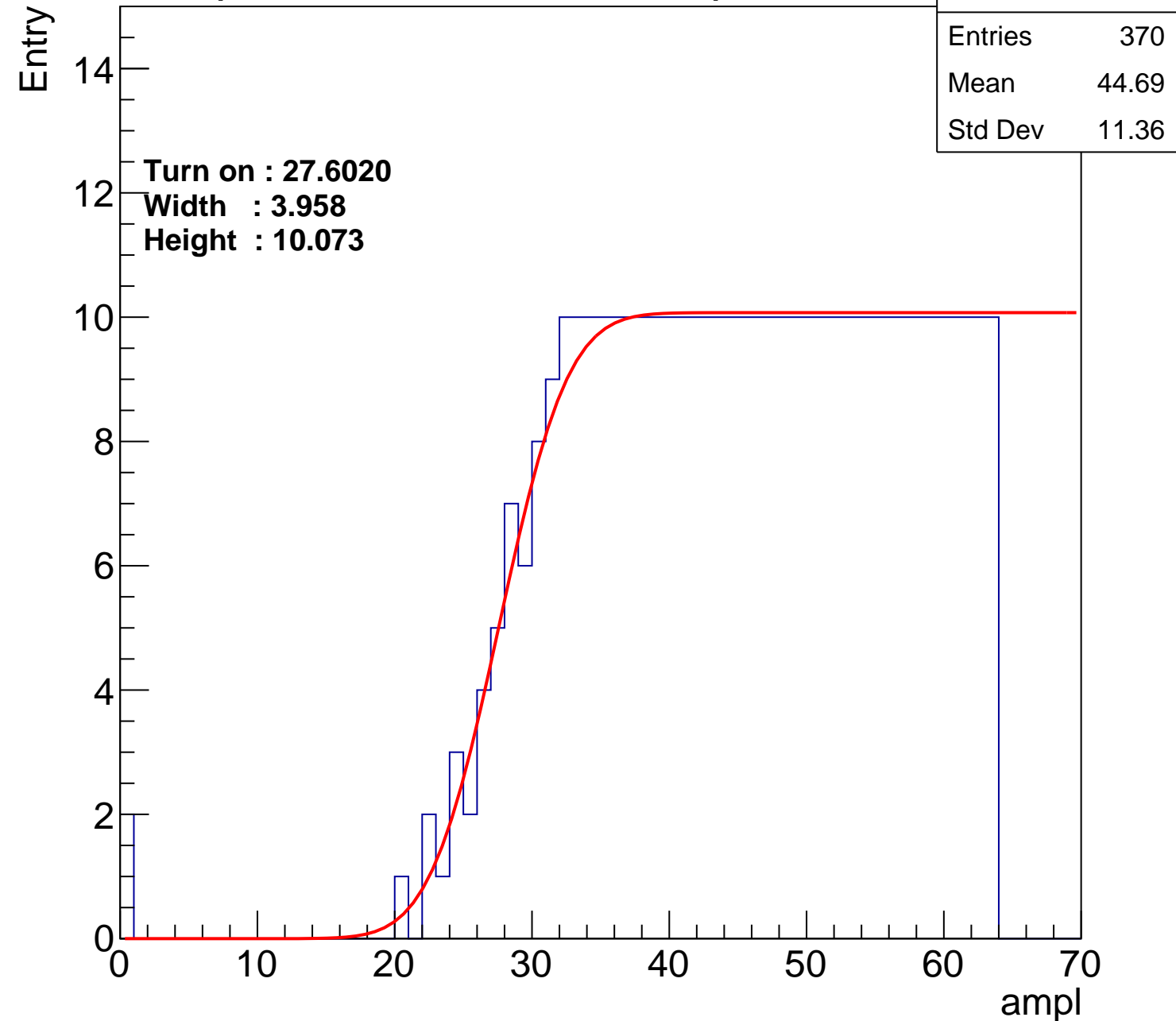
Width : 3.958

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch118

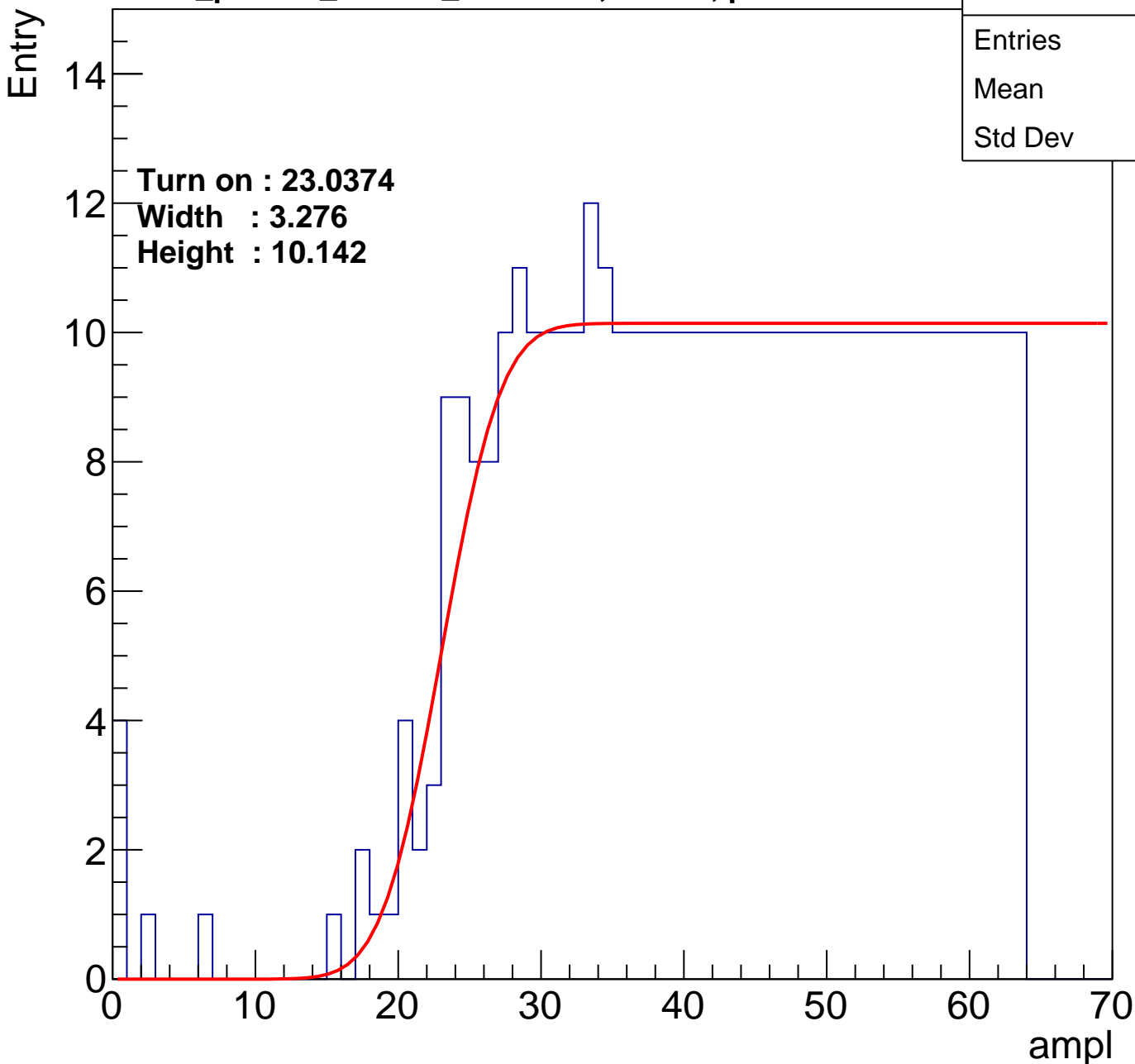
calib_packv5_042523_0143.root, FC#12, port B1

Entries	428
Mean	41.8
Std Dev	13.1

Turn on : 23.0374

Width : 3.276

Height : 10.142



B0L102S, U13-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.16
Std Dev	11.63

Turn on : 26.5429

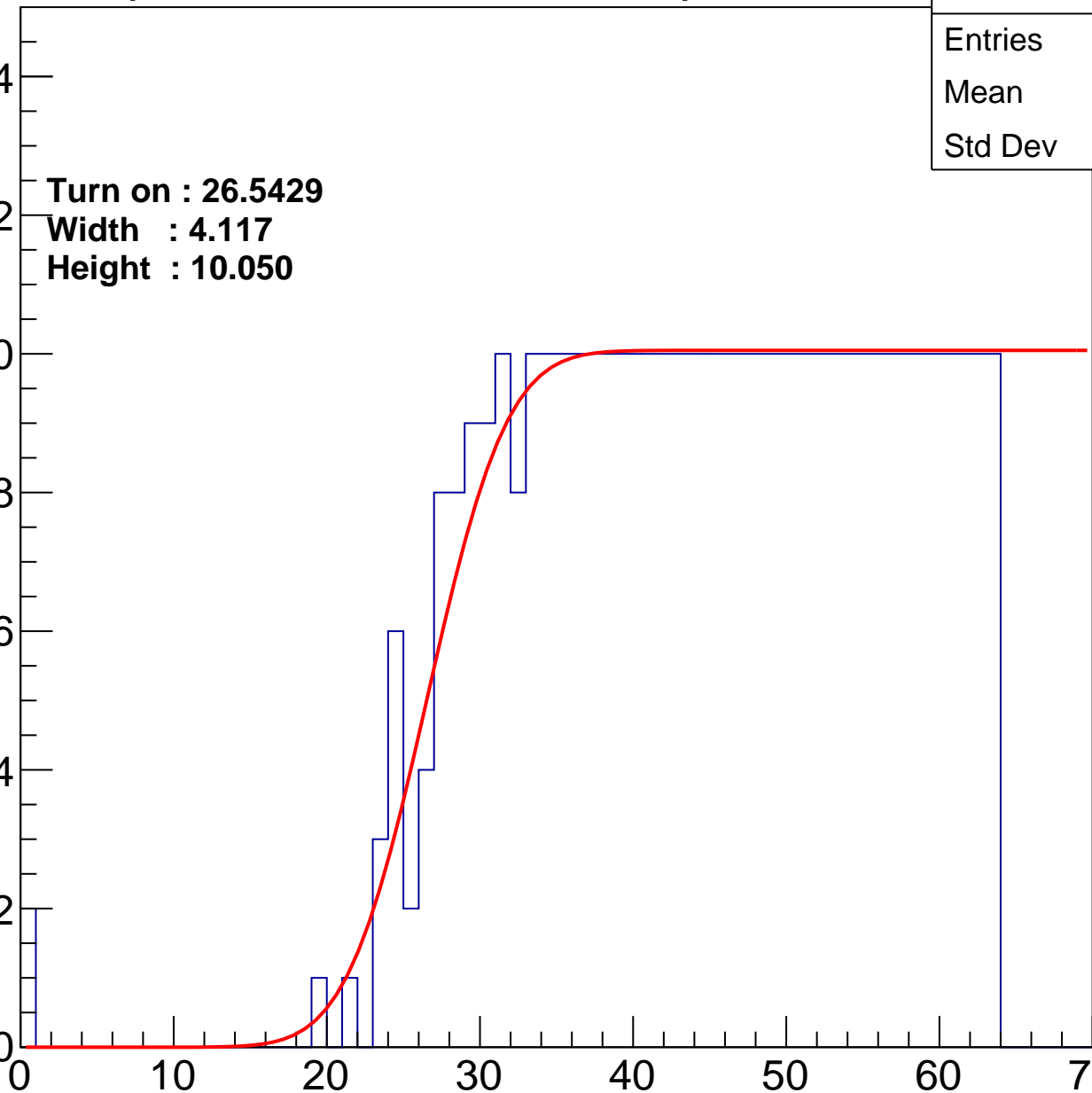
Width : 4.117

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch120

calib_packv5_042523_0143.root, FC#12, port B1

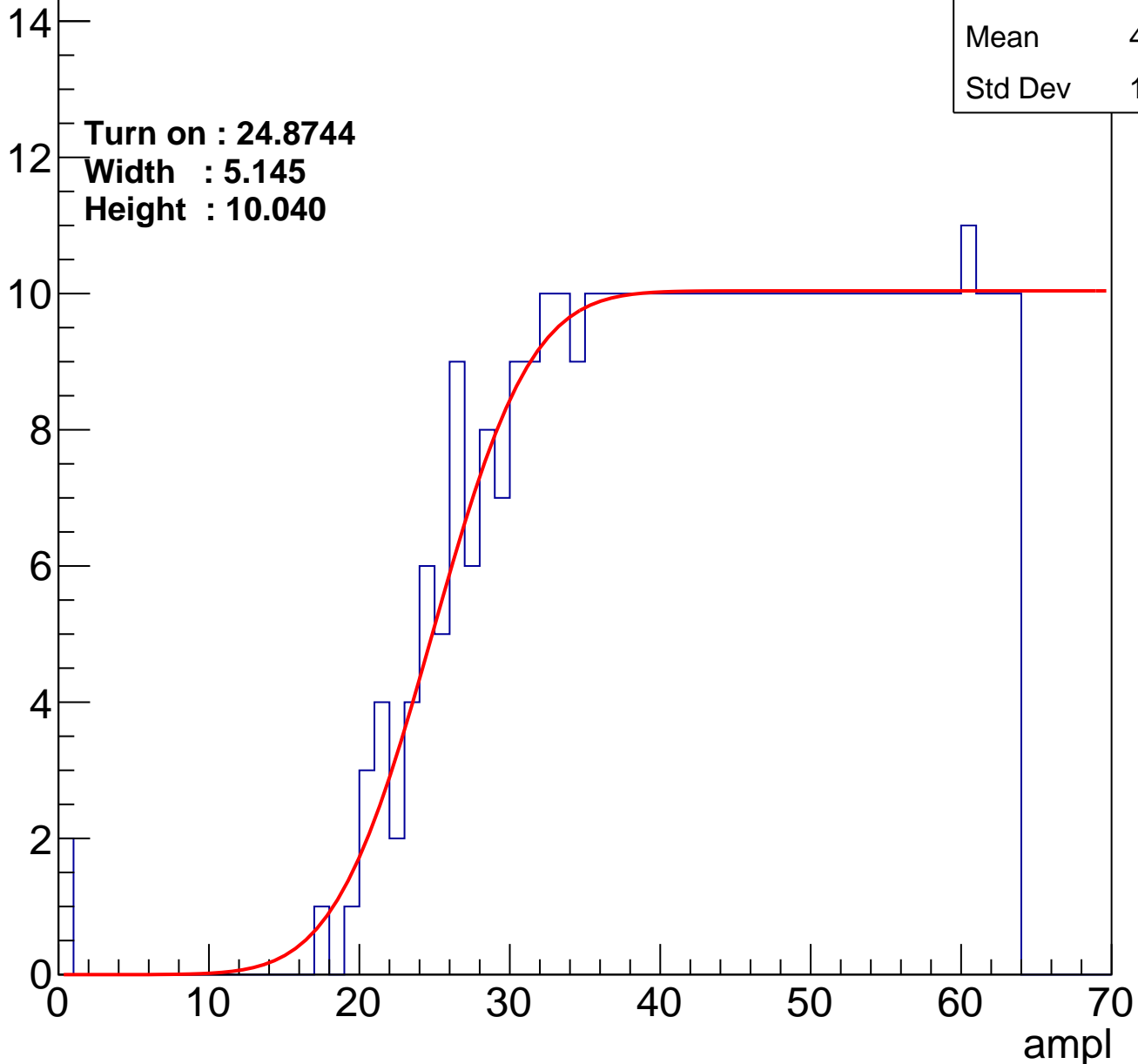
Entries	396
Mean	43.39
Std Dev	12.18

Turn on : 24.8744

Width : 5.145

Height : 10.040

Entry



B0L102S, U13-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 27.6651

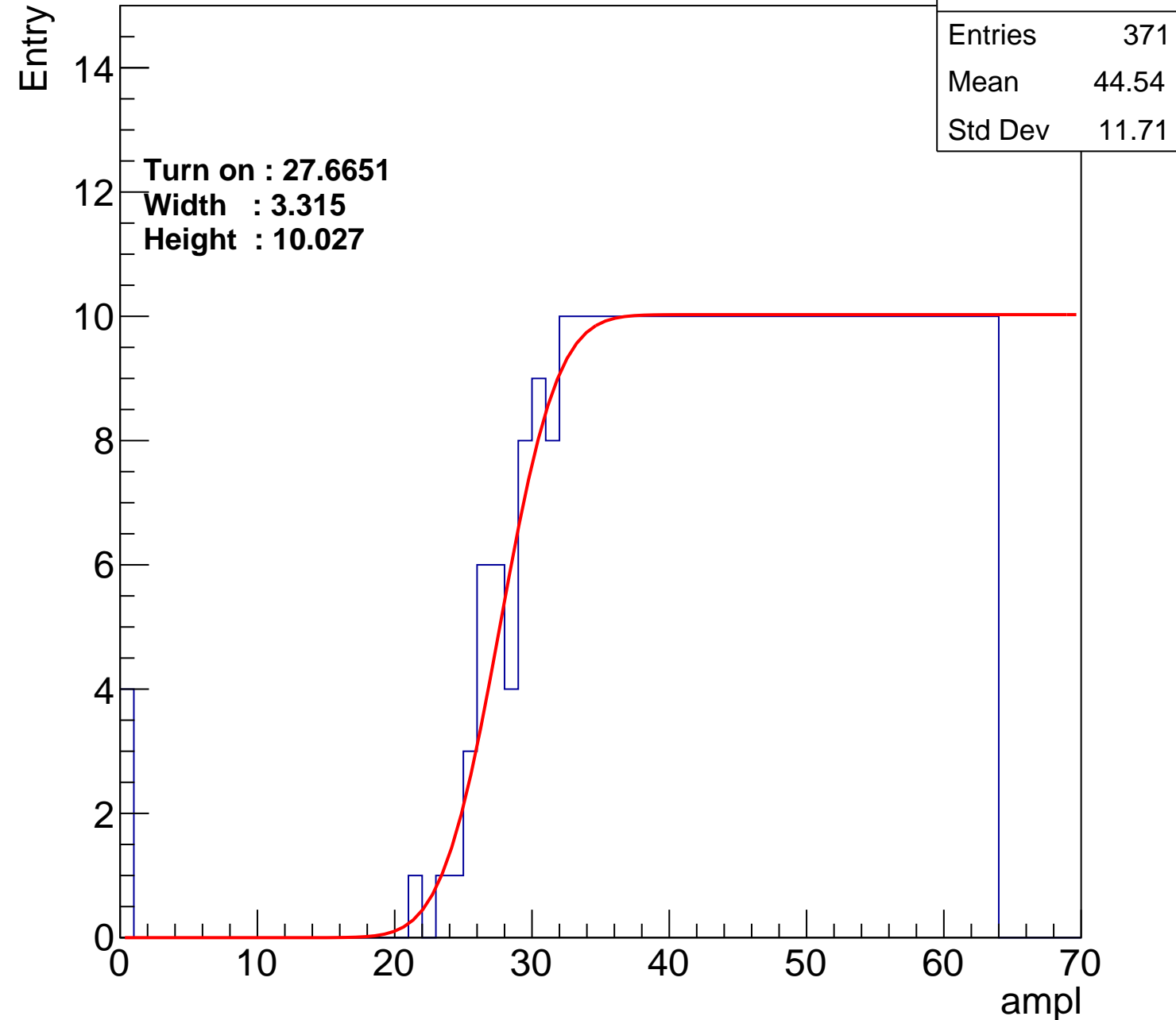
Width : 3.315

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.31
Std Dev	11.4

Turn on : 27.2449

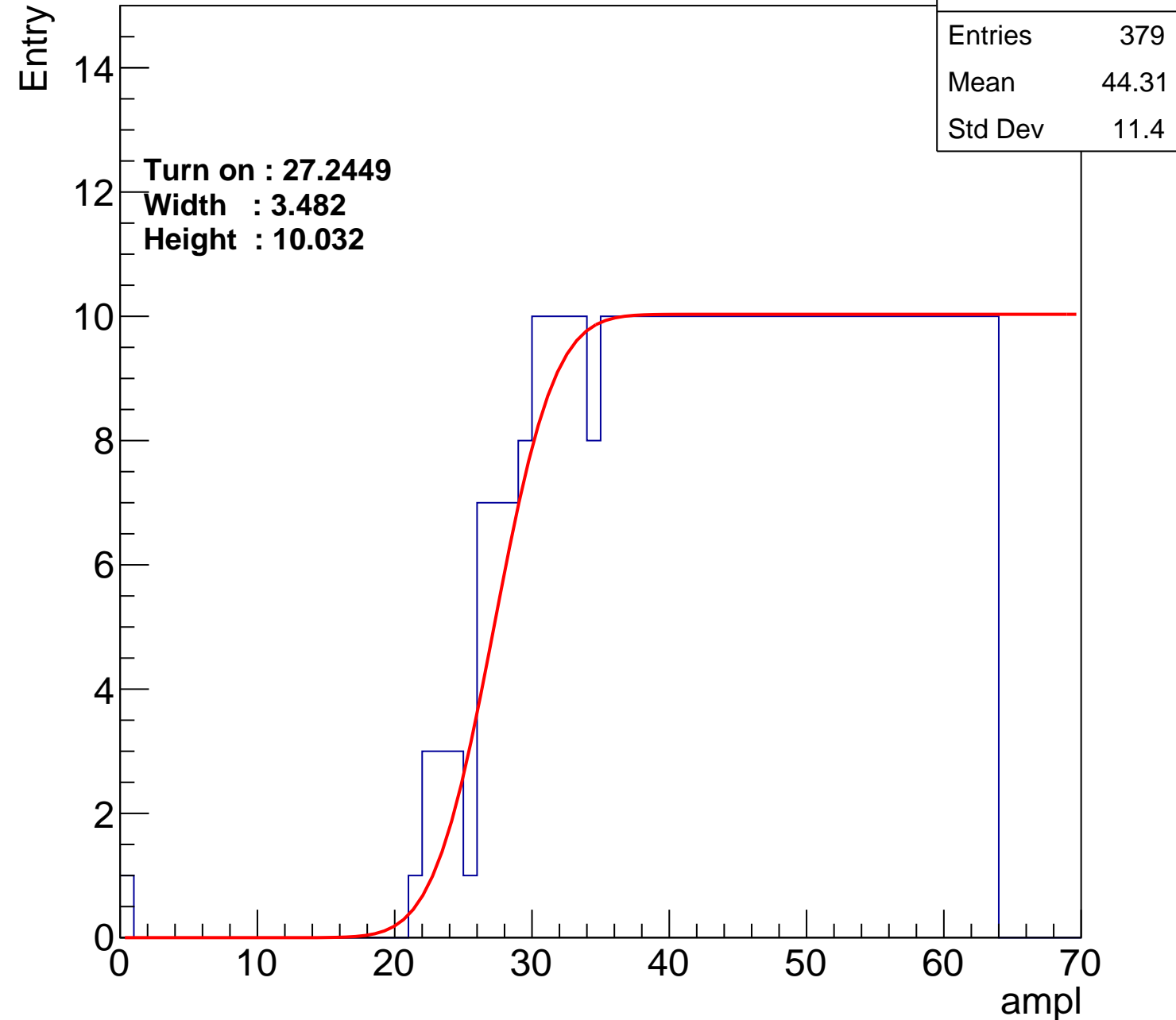
Width : 3.482

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.25
Std Dev	11.98

Turn on : 27.5446

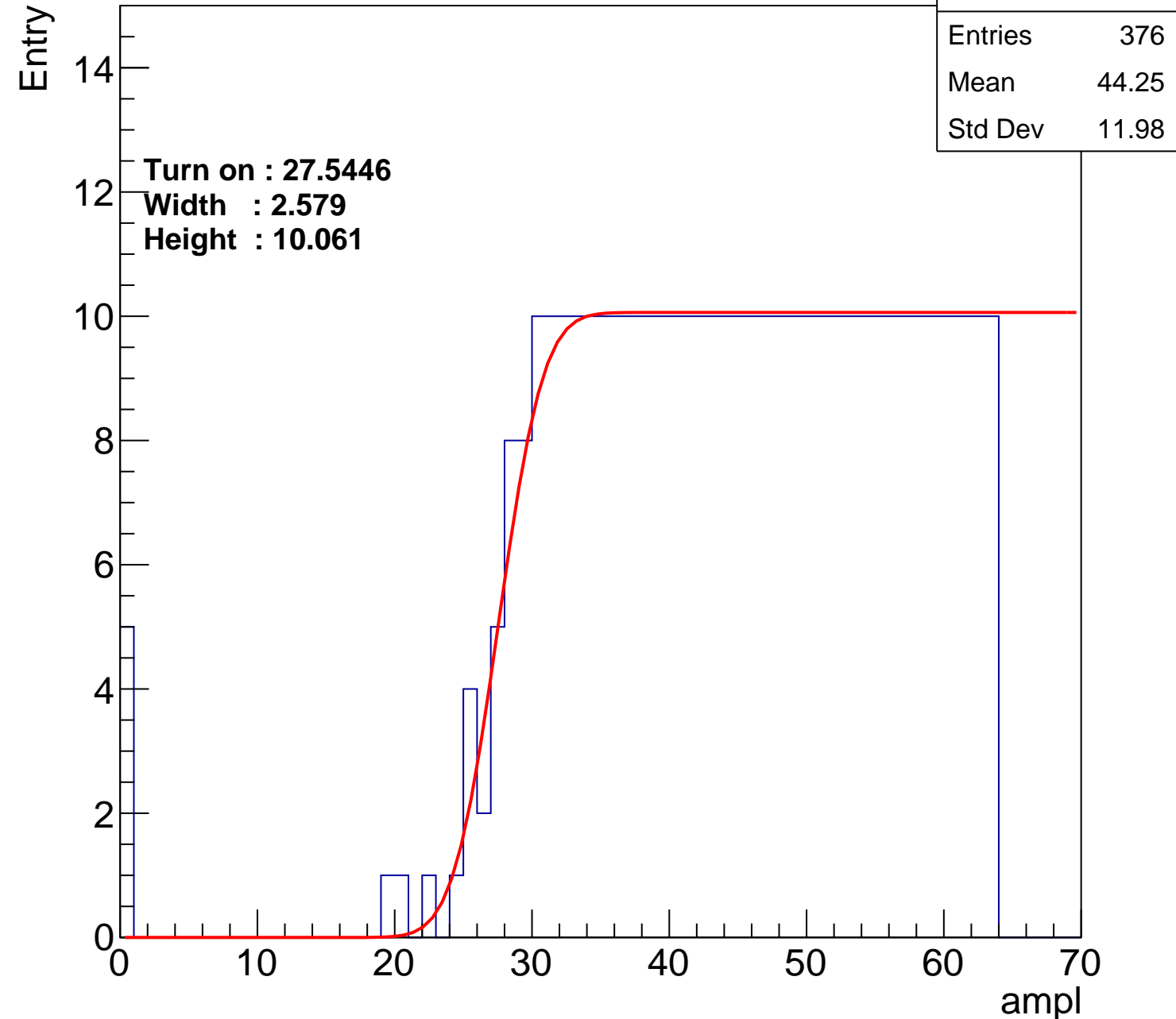
Width : 2.579

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.71
Std Dev	12.56

Turn on : 23.9785

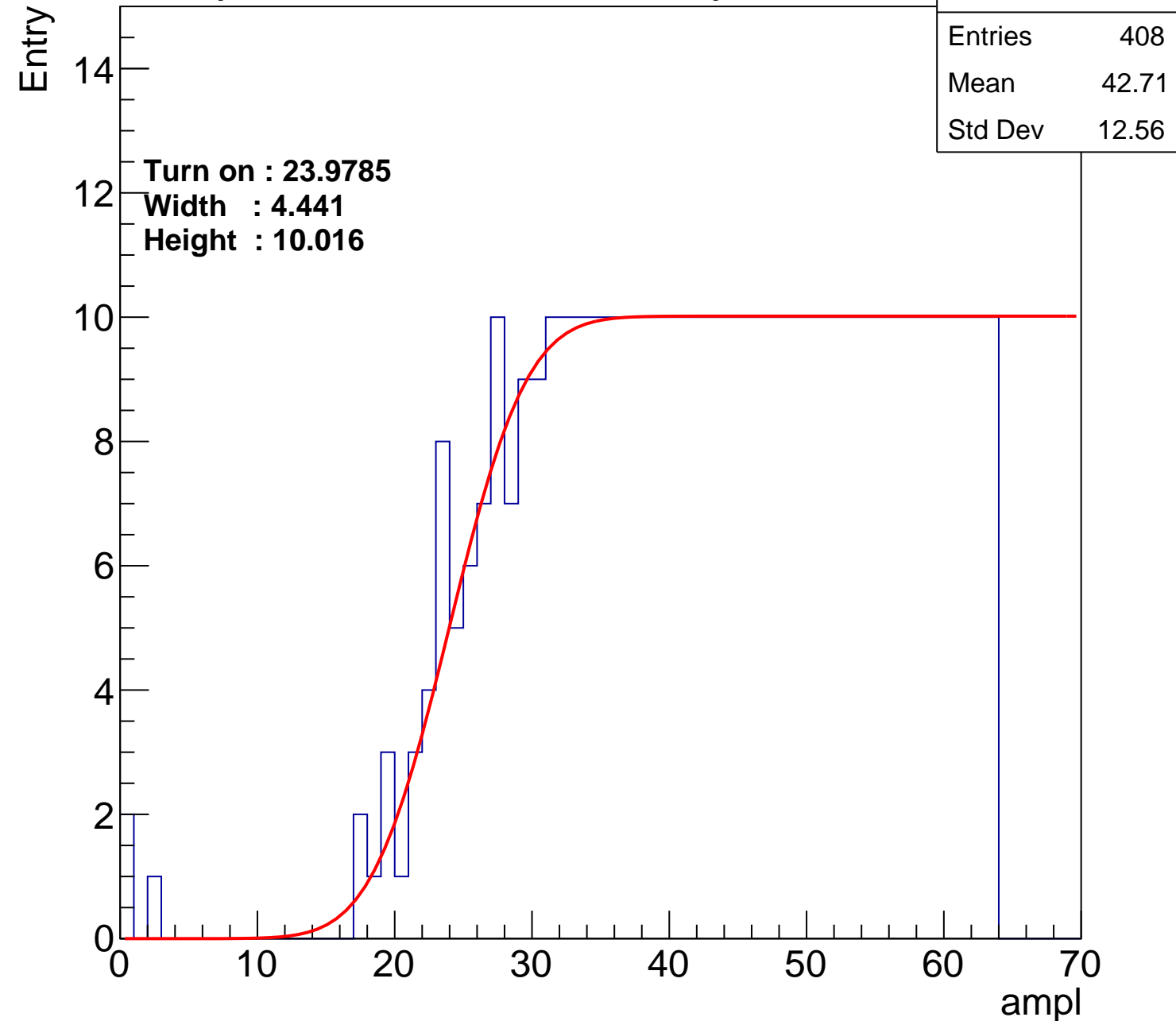
Width : 4.441

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 26.1226

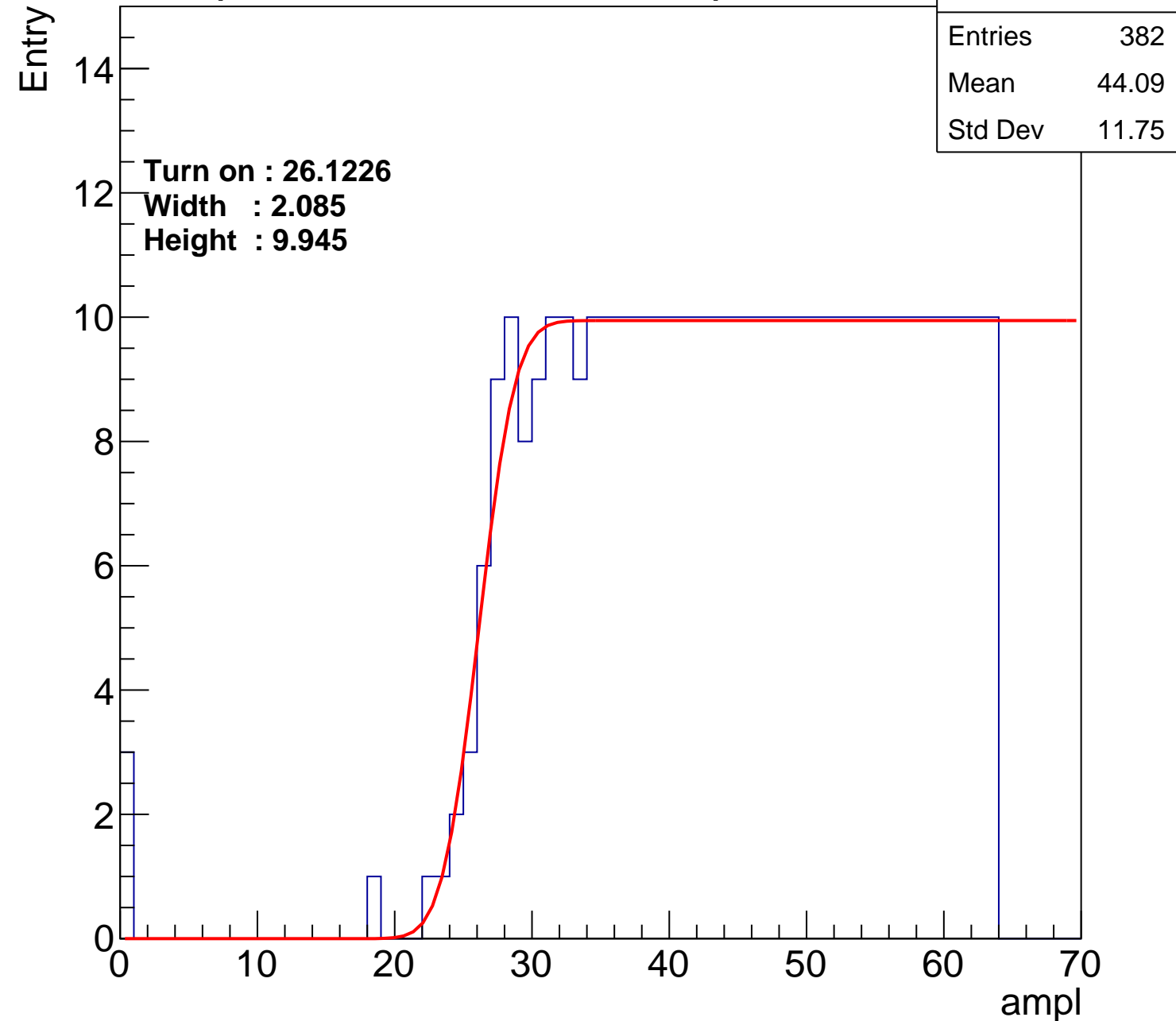
Width : 2.085

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	447
Mean	41.03
Std Dev	13.7

Turn on : 21.2049

Width : 3.122

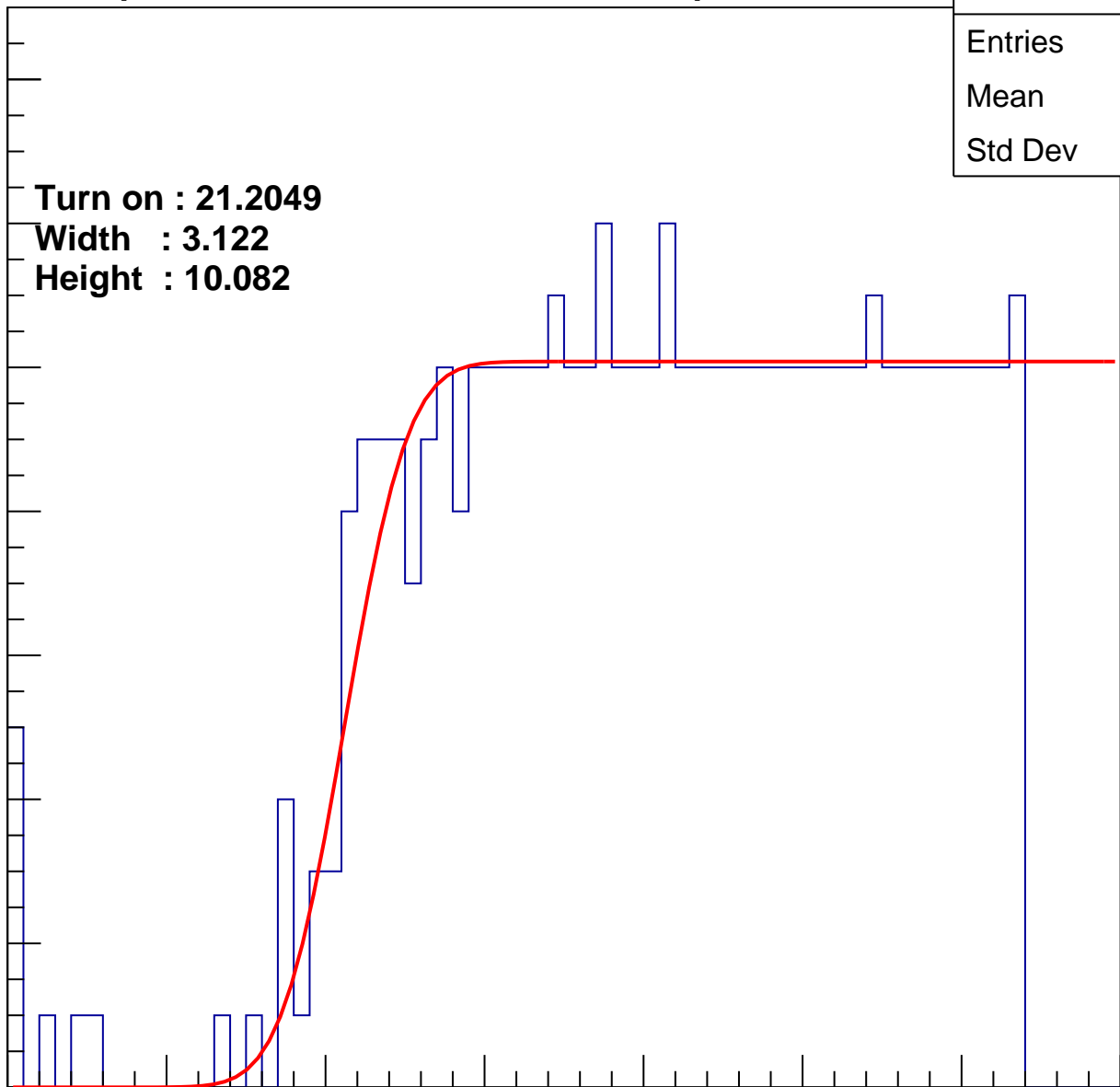
Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U13-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.22
Std Dev	12.4

Turn on : 25.3726

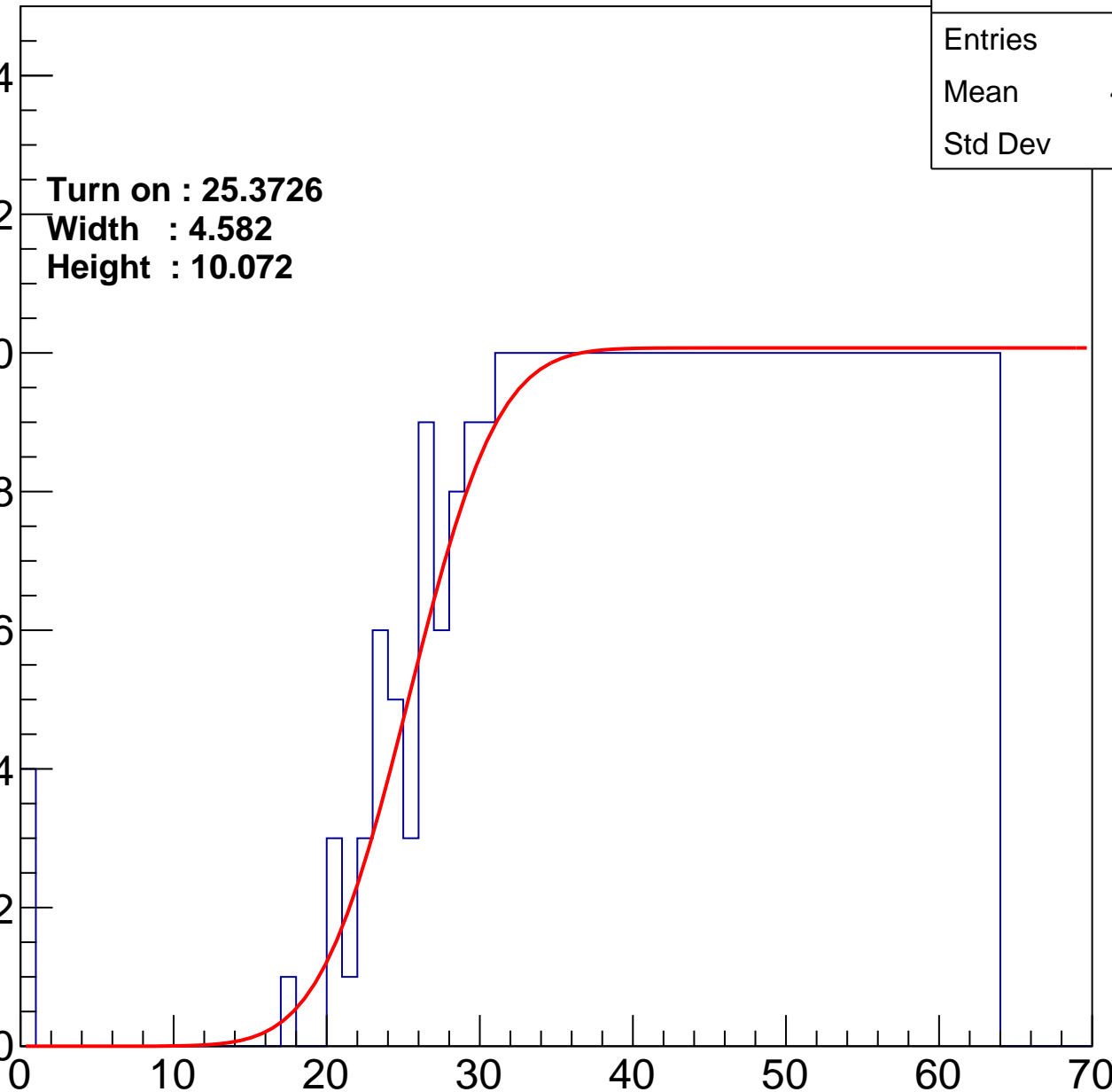
Width : 4.582

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U13-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.22
Std Dev	12.4

Turn on : 25.3726

Width : 4.582

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

