



# B0L002S, U8-ch0, adc0

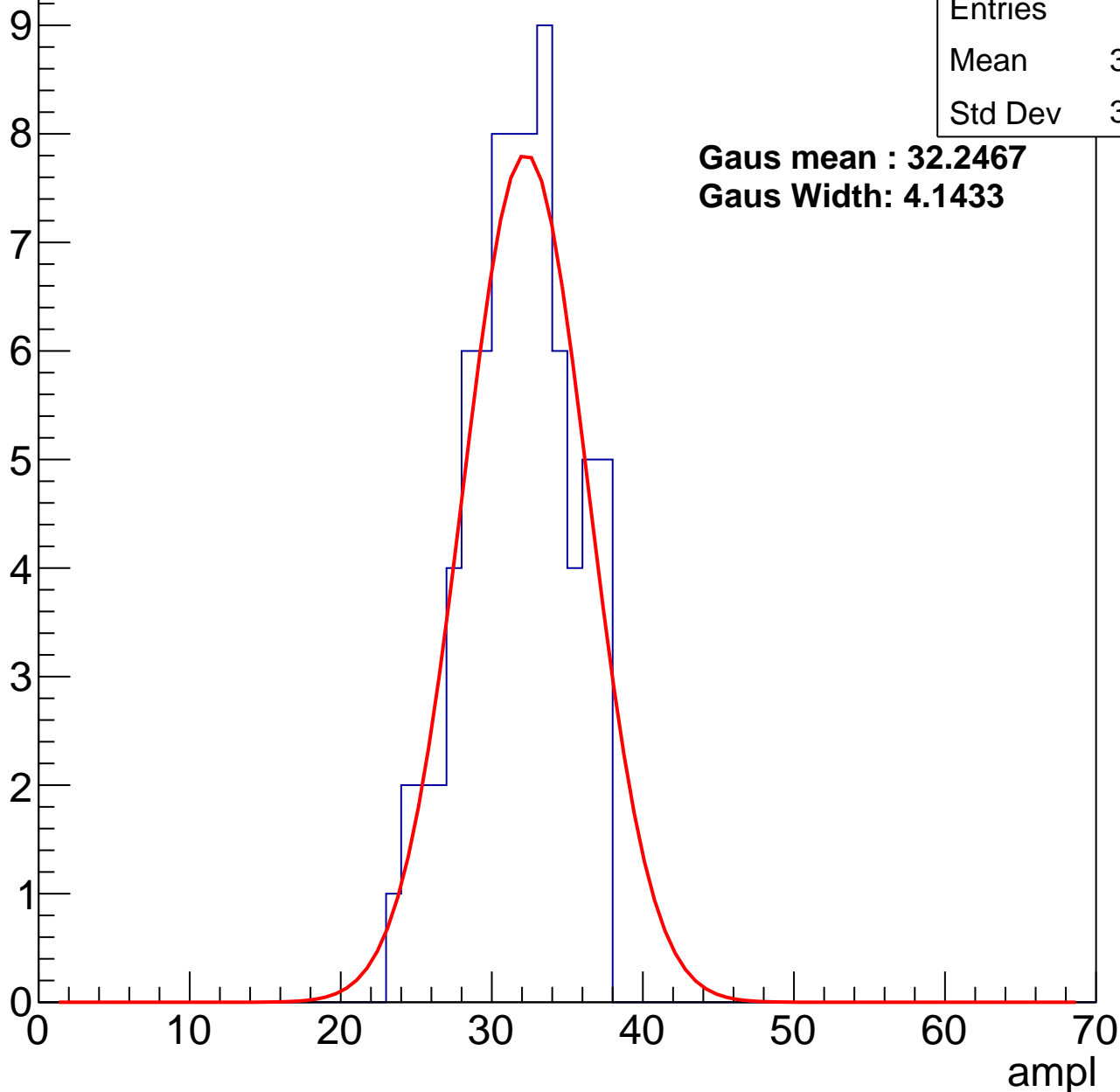
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.22
Std Dev	3.436

**Gaus mean : 32.2467**

**Gaus Width: 4.1433**

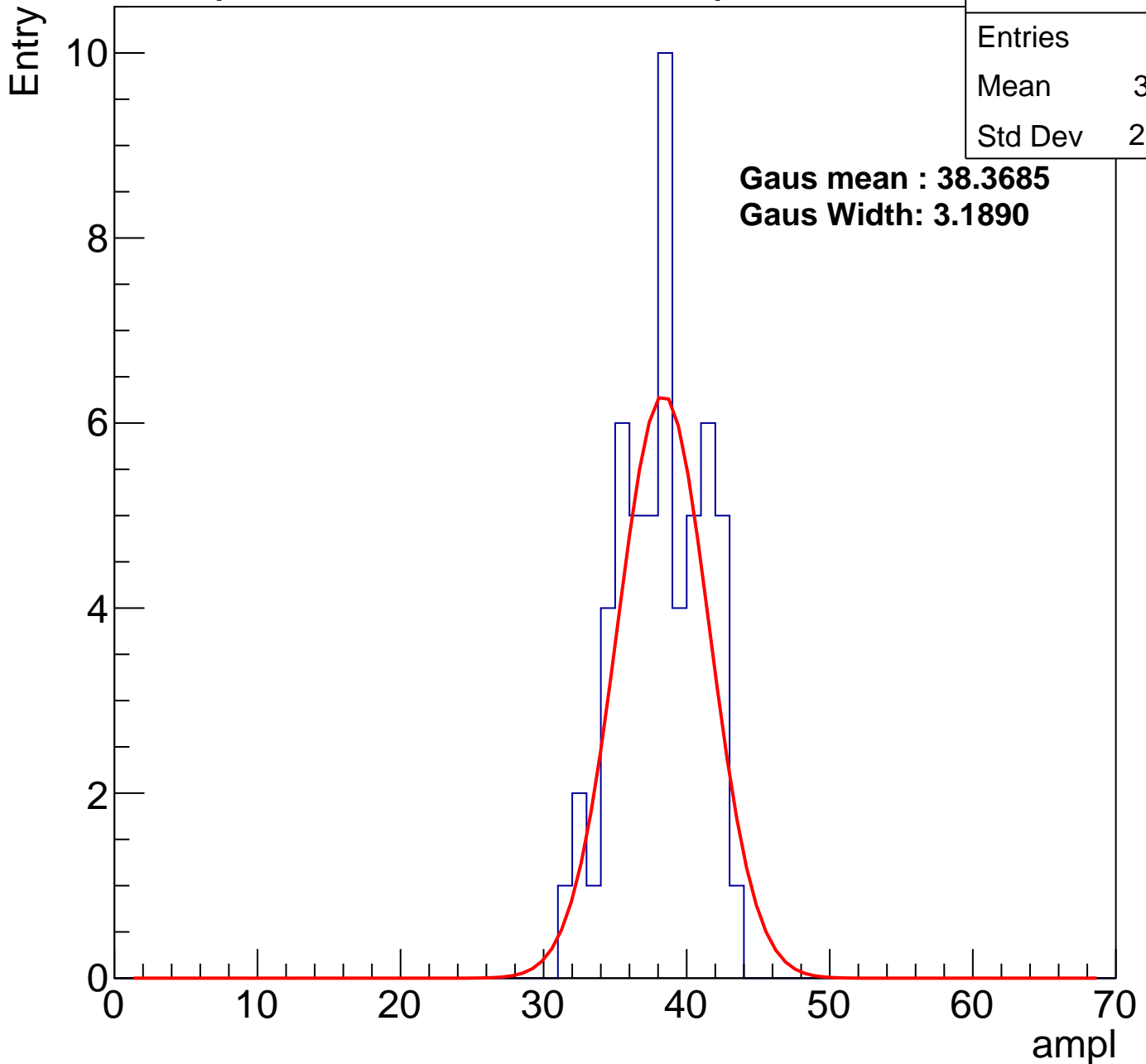


# B0L002S, U8-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	55
Mean	37.71
Std Dev	2.915

**Gaus mean : 38.3685**  
**Gaus Width: 3.1890**



# B0L002S, U8-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	81
Mean	44.95
Std Dev	3.559

**Gaus mean : 44.8032**

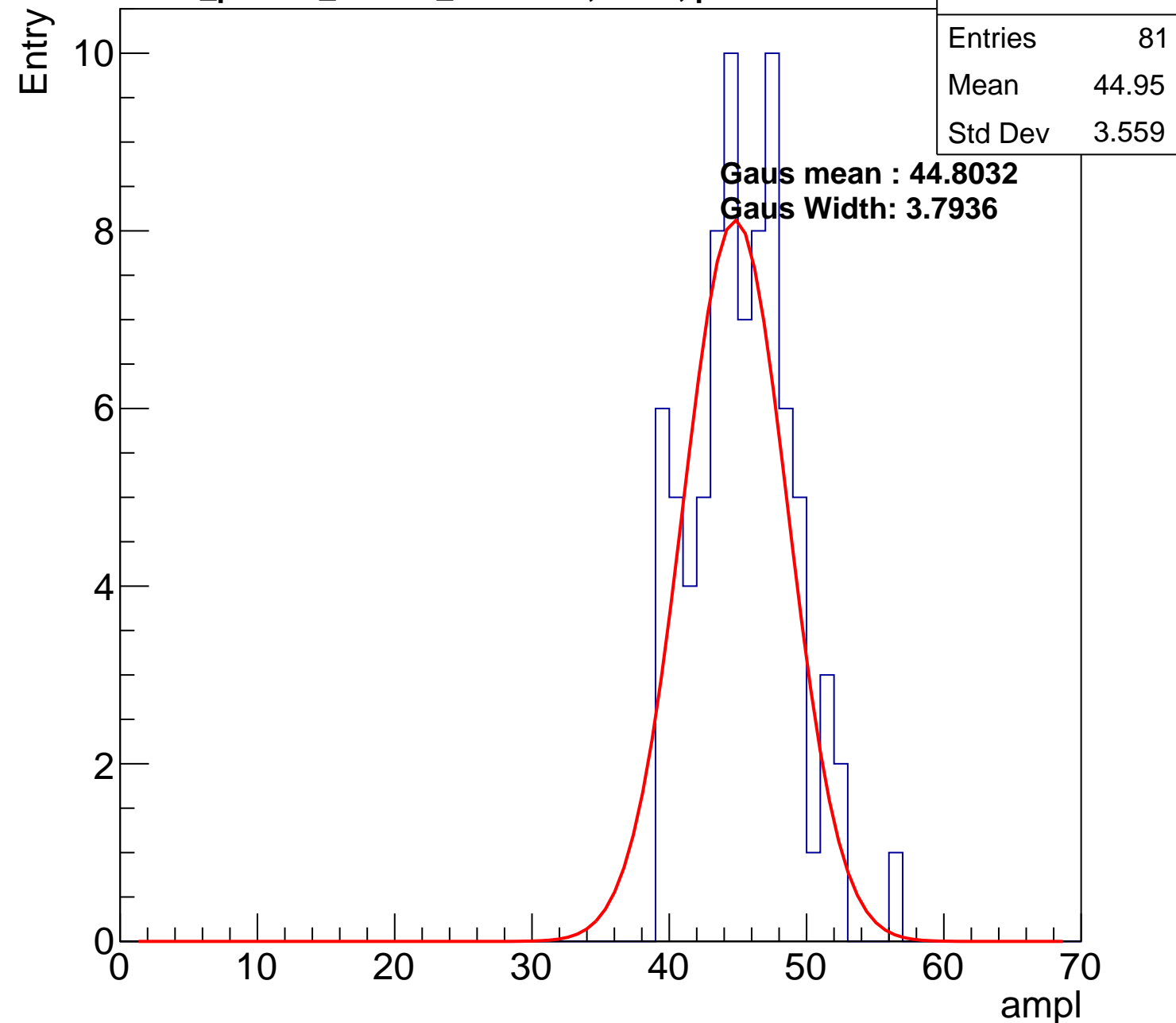
**Gaus Width: 3.7936**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

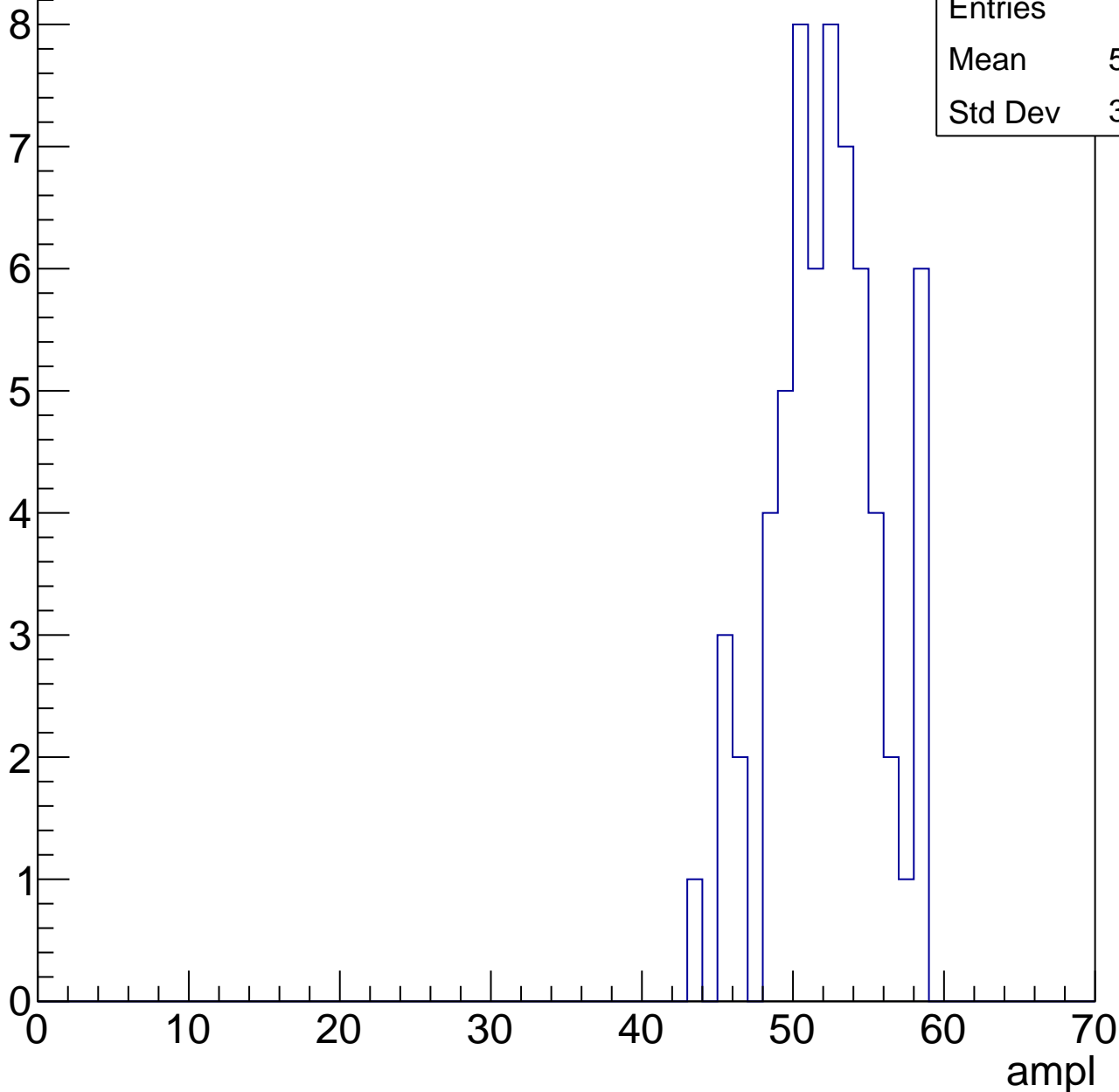


# B0L002S, U8-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

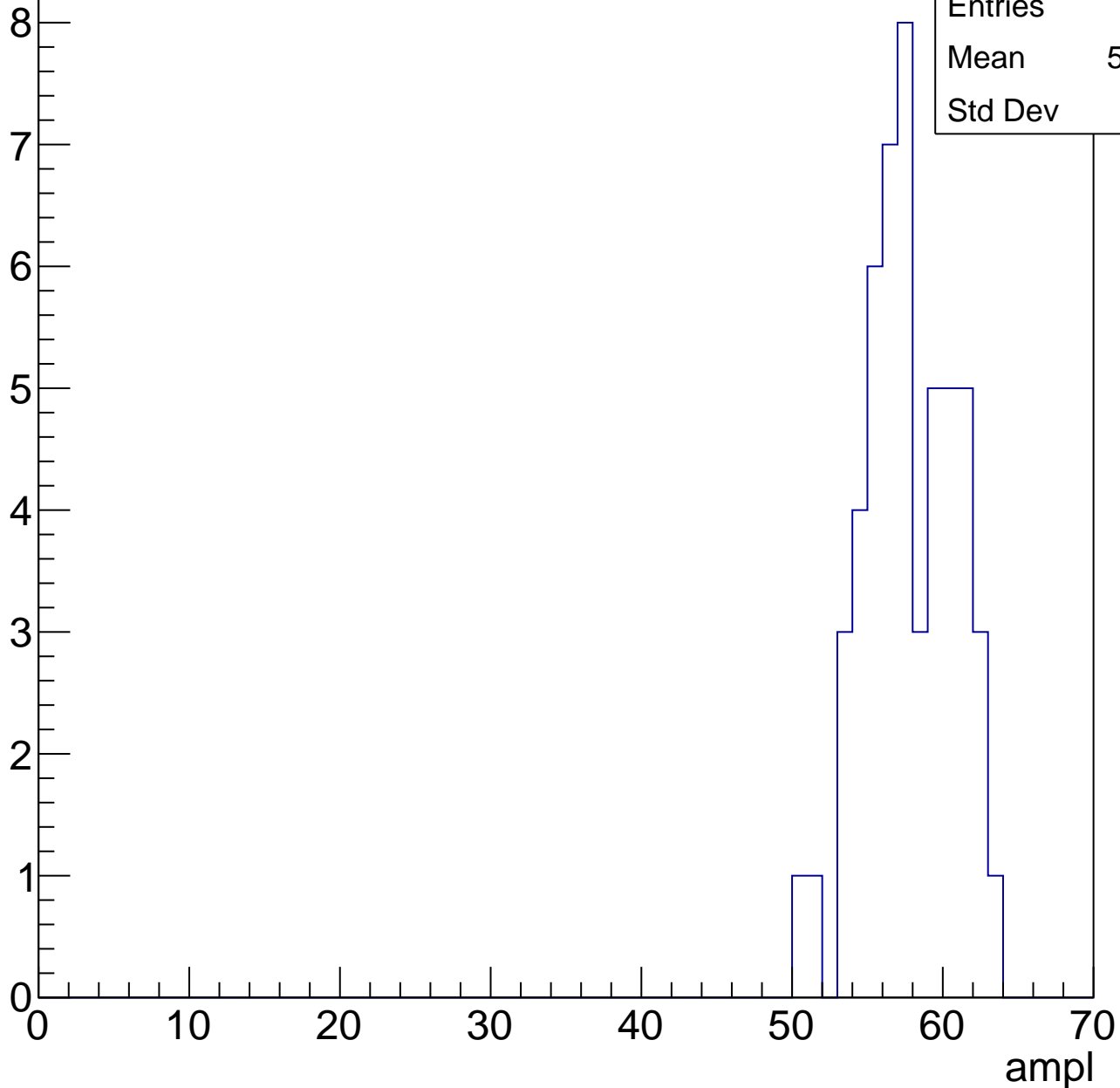
Entries	63
Mean	51.76
Std Dev	3.526



# B0L002S, U8-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

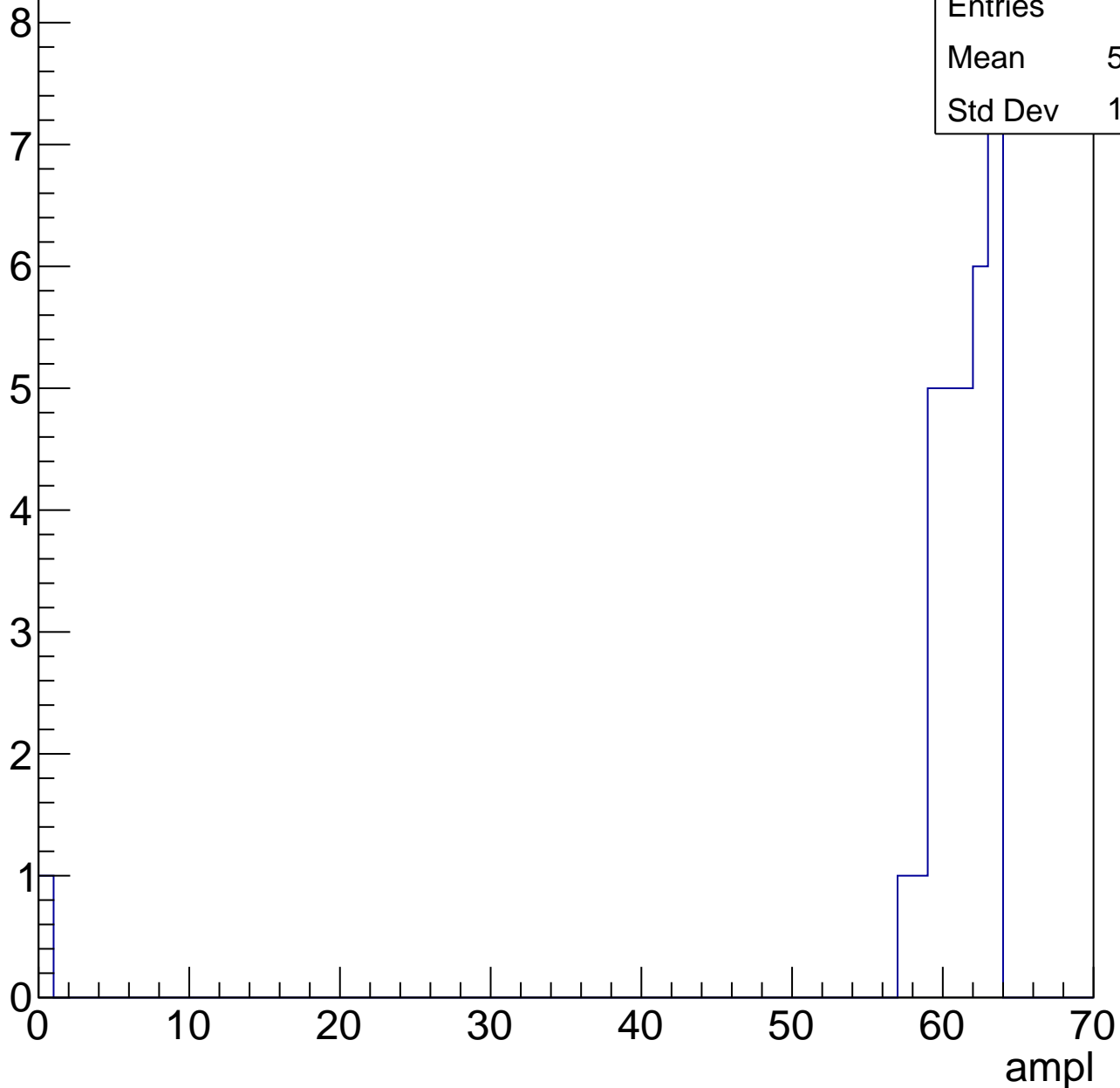


# B0L002S, U8-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

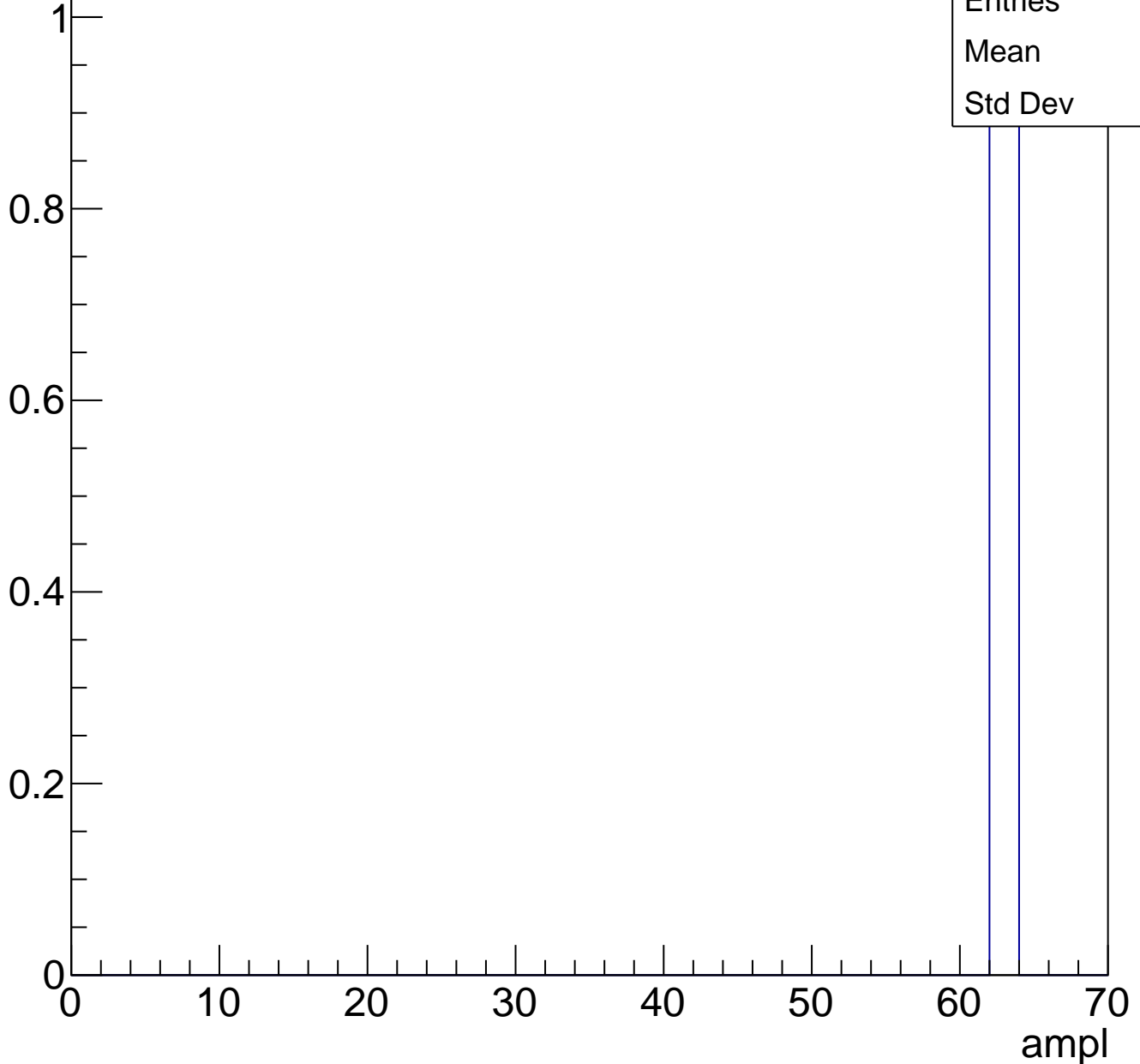
Entries	32
Mean	59.09
Std Dev	10.74



# B0L002S, U8-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

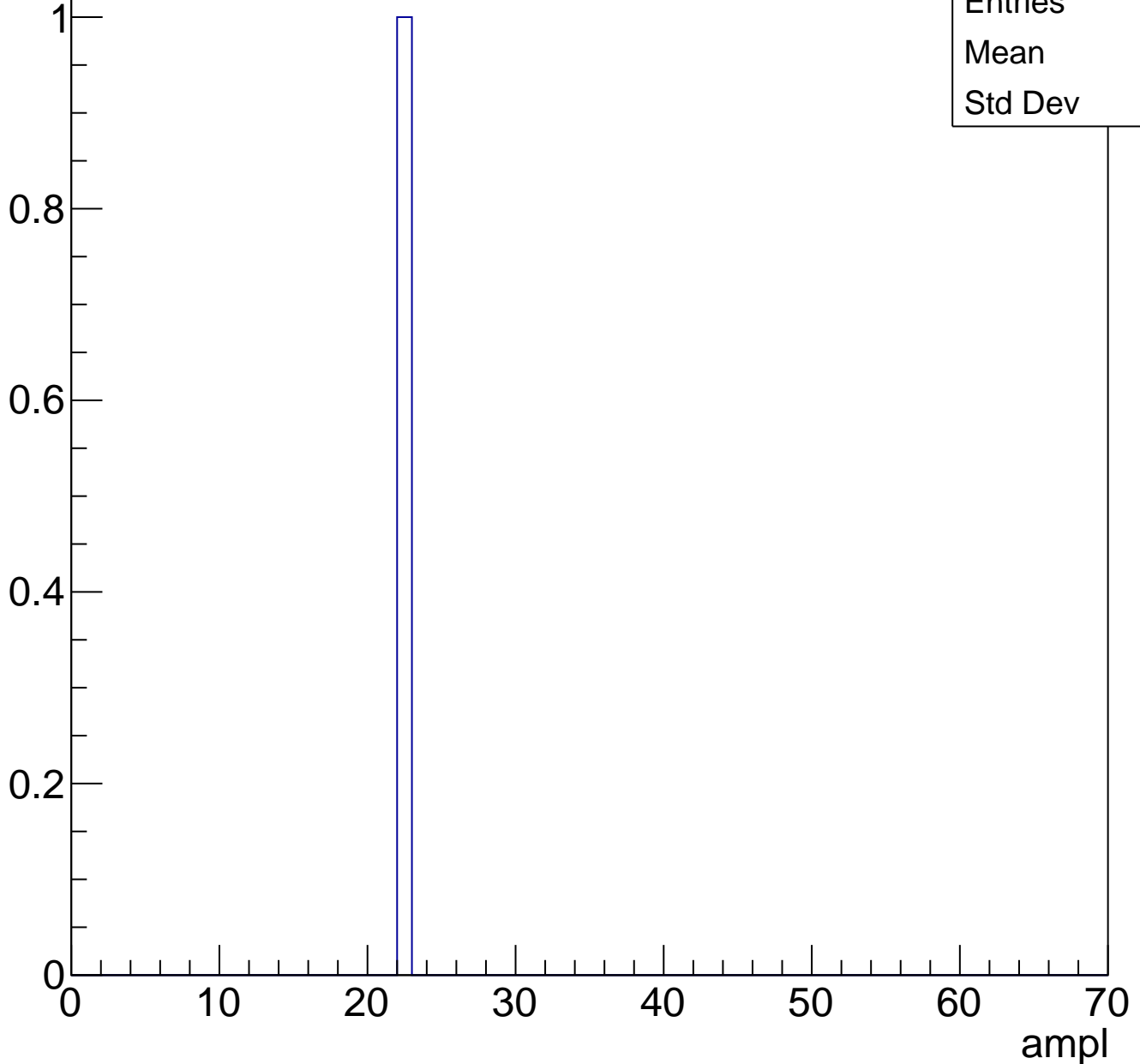




# B0L002S, U8-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L002S, U8-ch1, adc0

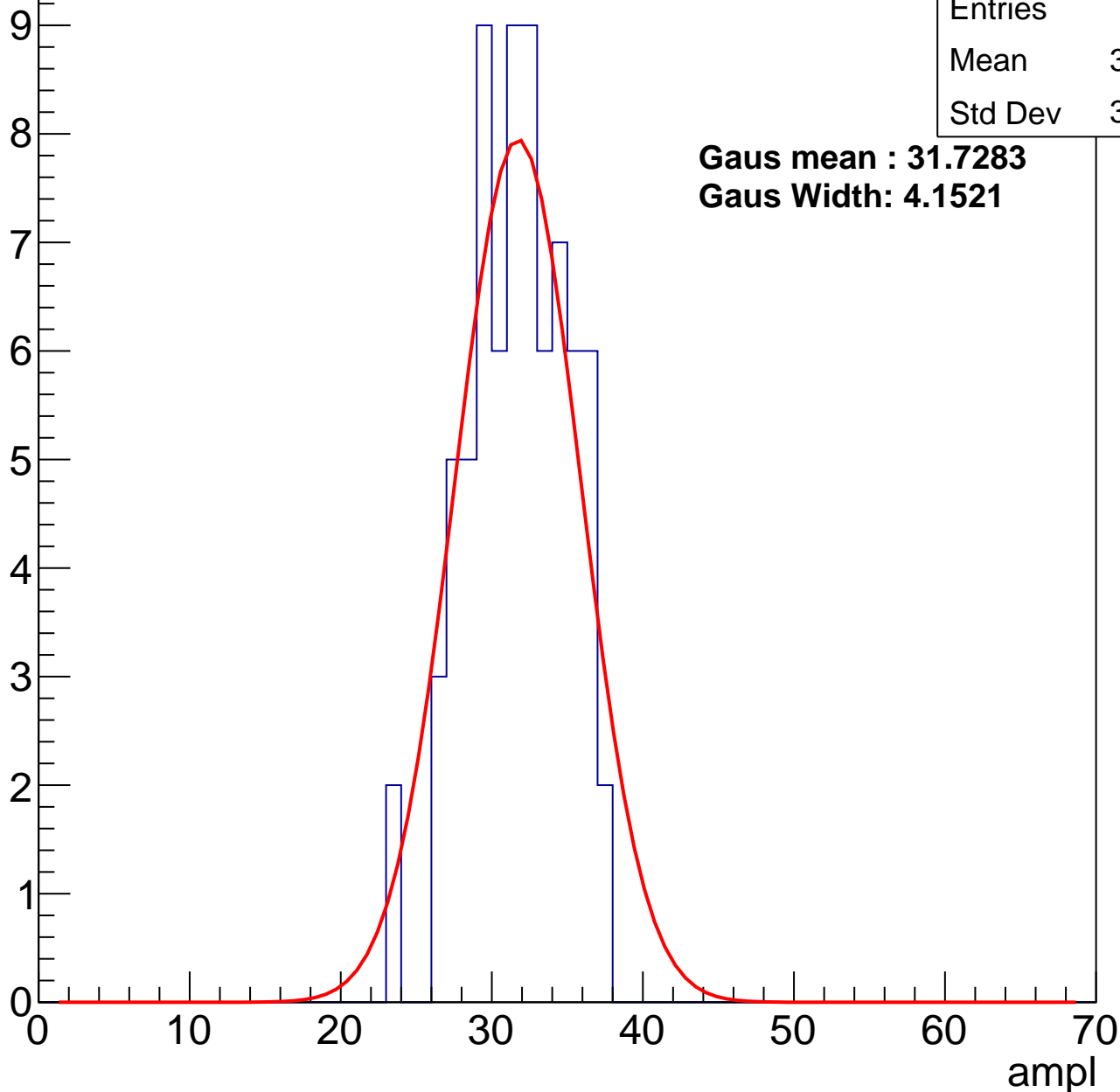
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.24
Std Dev	3.224

**Gaus mean : 31.7283**

**Gaus Width: 4.1521**



# B0L002S, U8-ch1, adc1

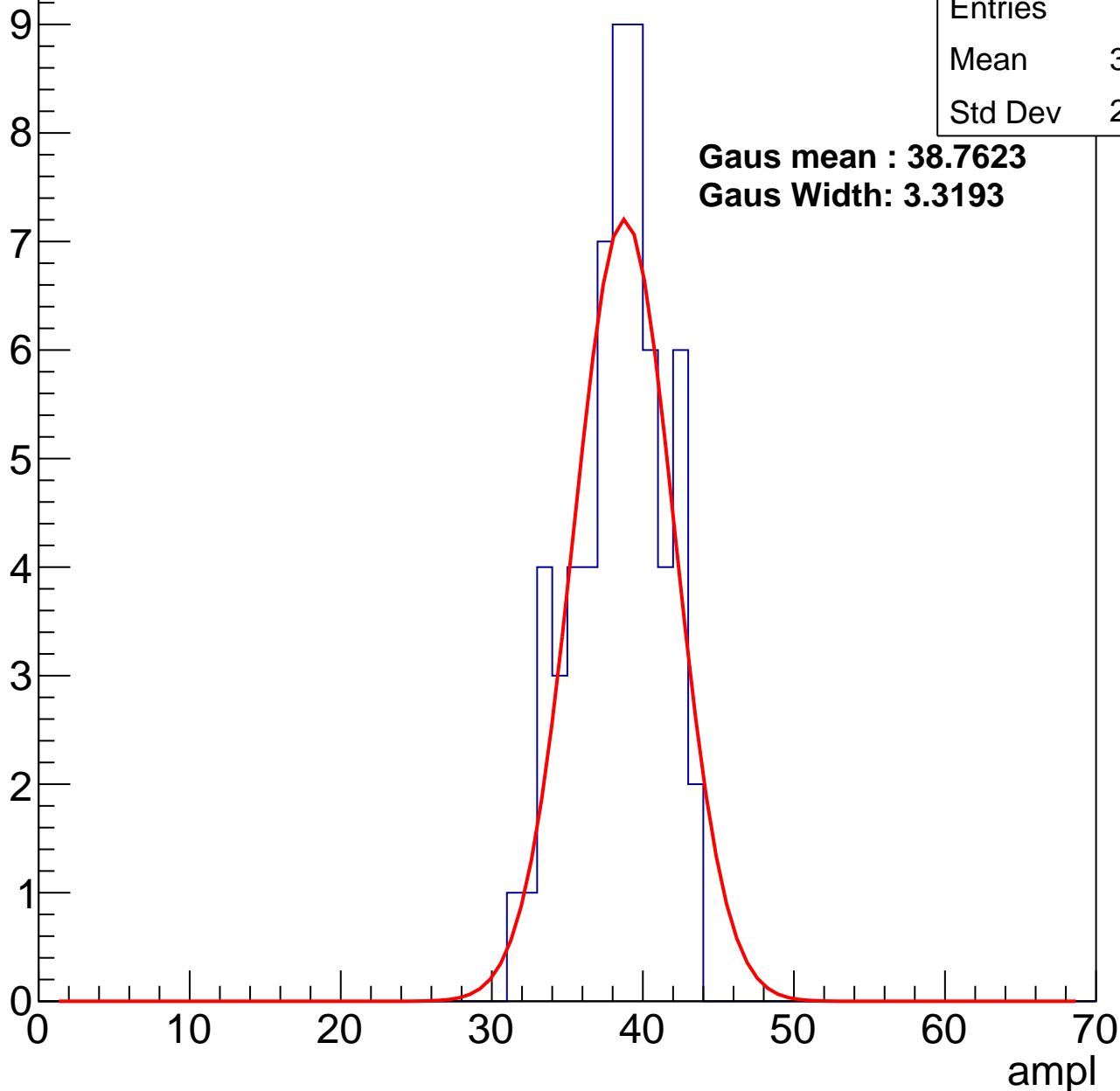
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.92
Std Dev	2.906

**Gaus mean : 38.7623**

**Gaus Width: 3.3193**



# B0L002S, U8-ch1, adc2

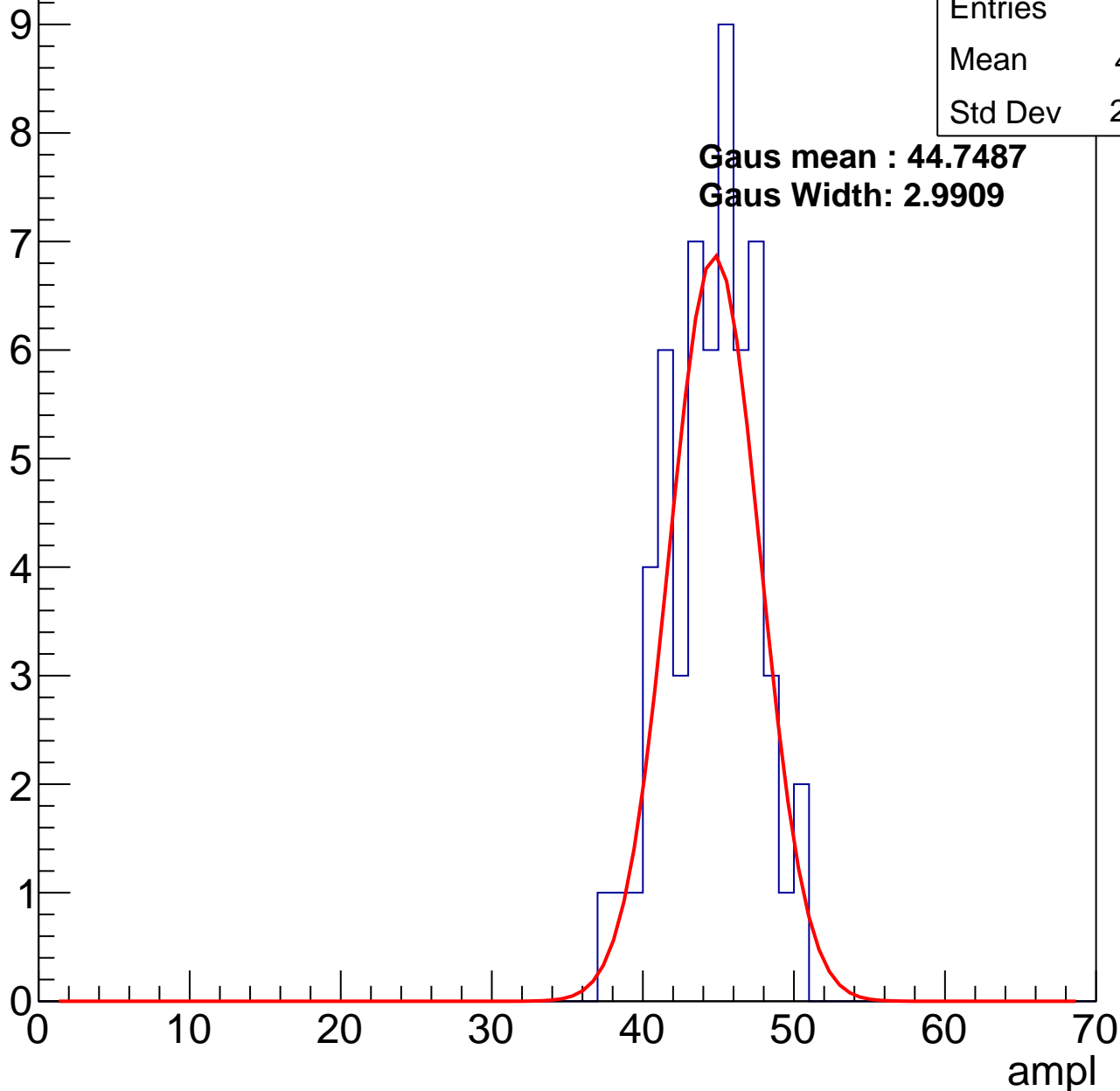
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.11
Std Dev	2.924

**Gaus mean : 44.7487**

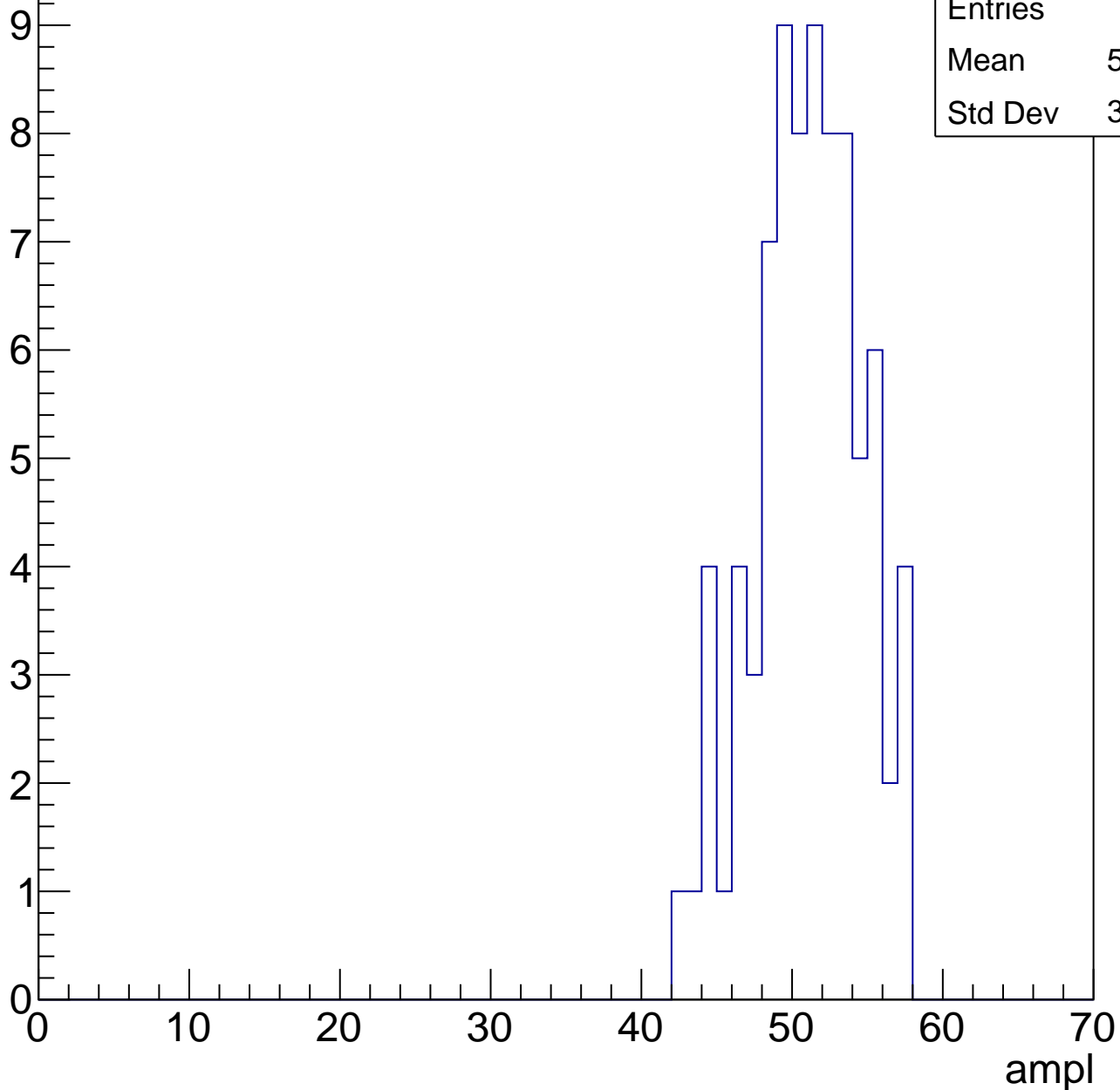
**Gaus Width: 2.9909**



# B0L002S, U8-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

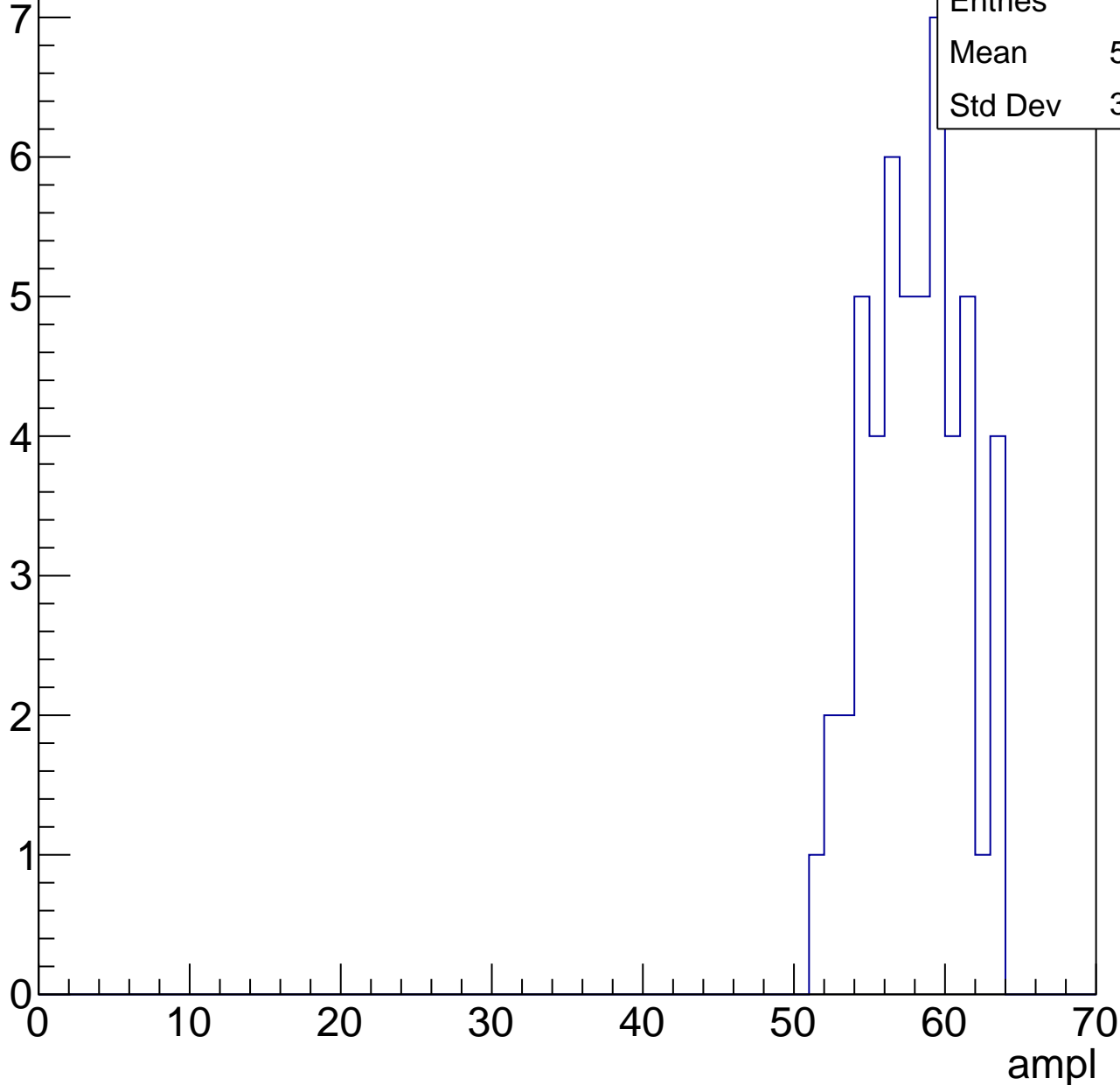


# B0L002S, U8-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.53
Std Dev	3.096

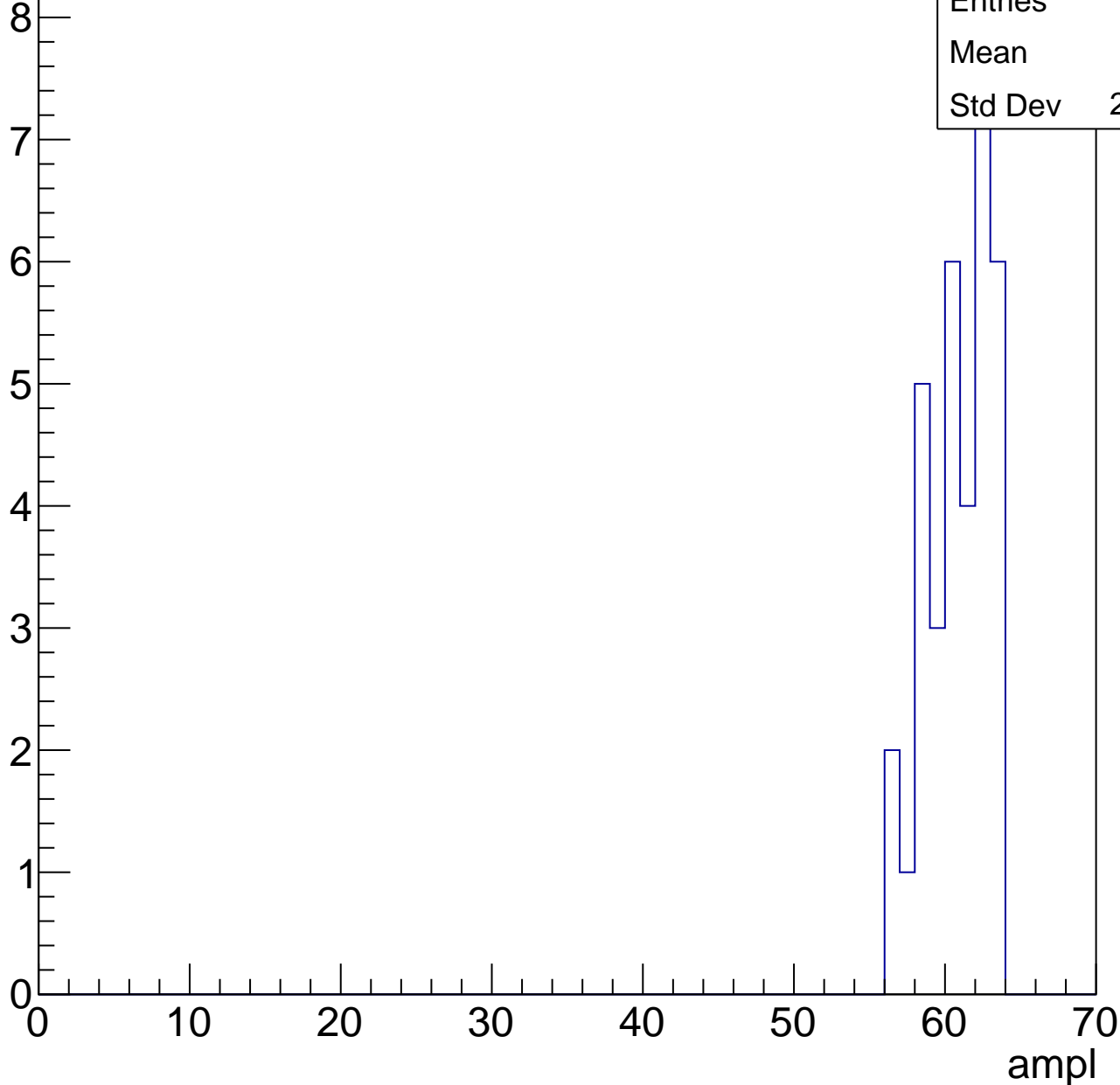


# B0L002S, U8-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.4
Std Dev	2.059



# B0L002S, U8-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

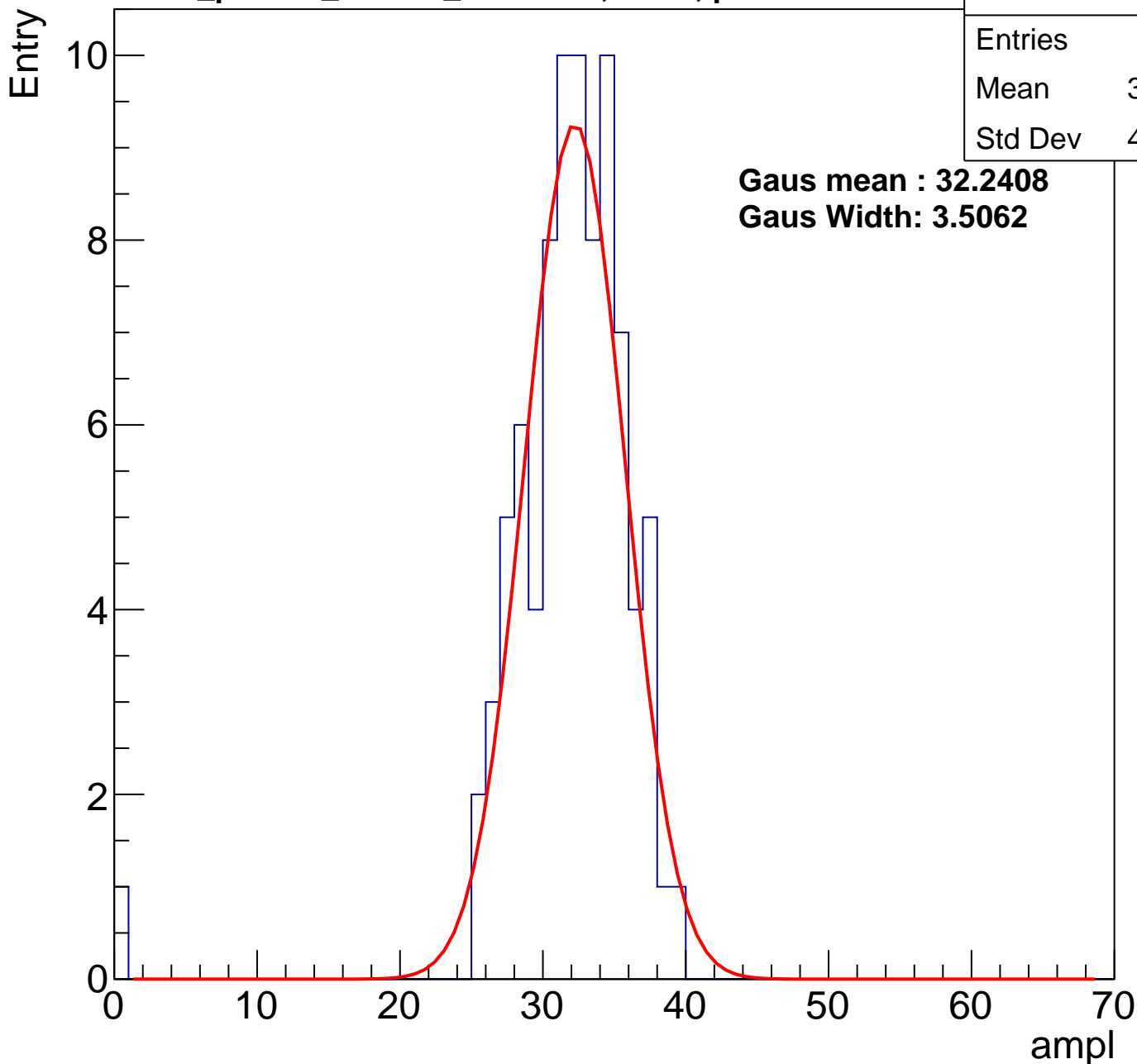
# B0L002S, U8-ch2, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	31.44
Std Dev	4.712

**Gaus mean : 32.2408**

**Gaus Width: 3.5062**



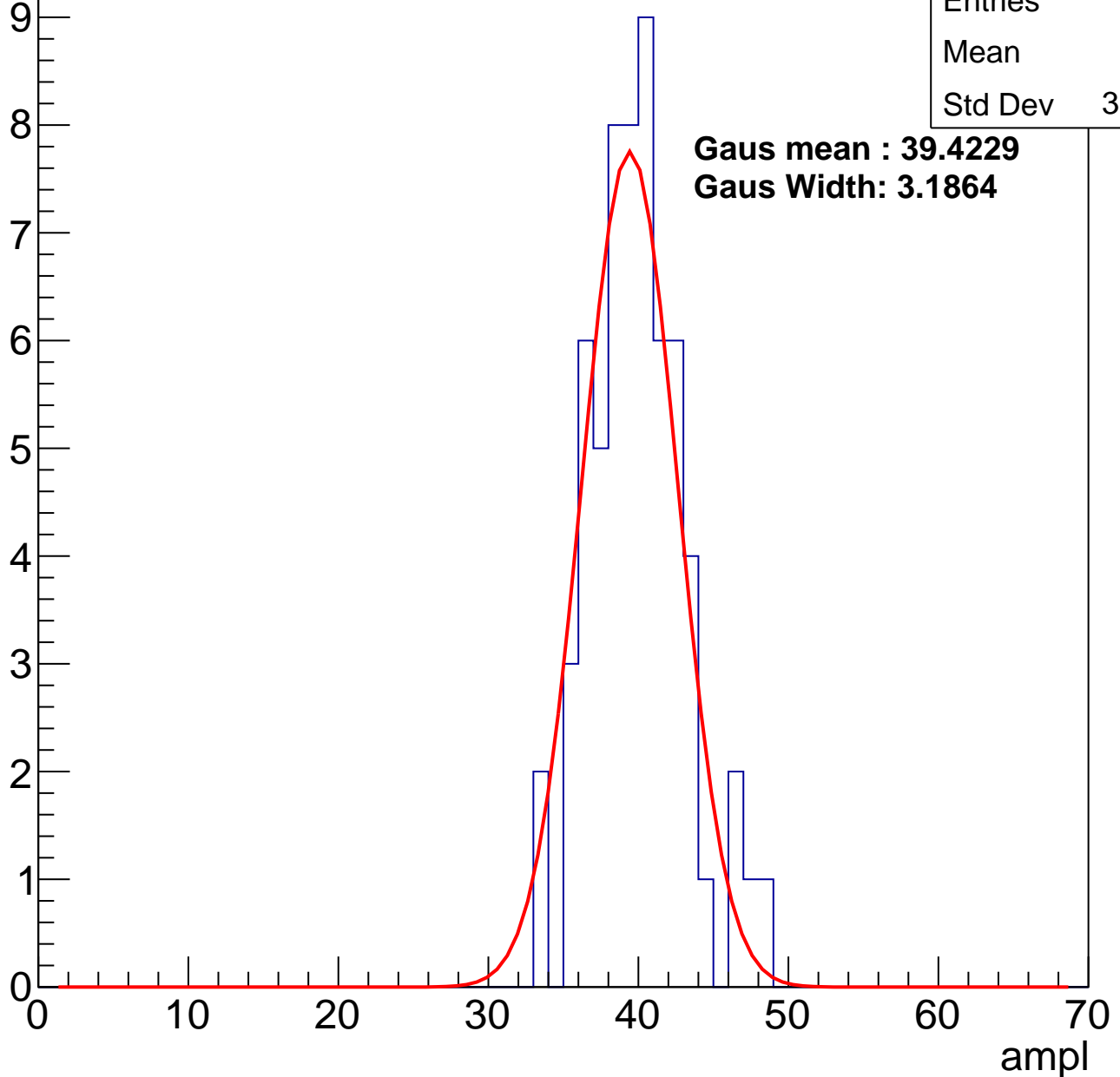
# B0L002S, U8-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

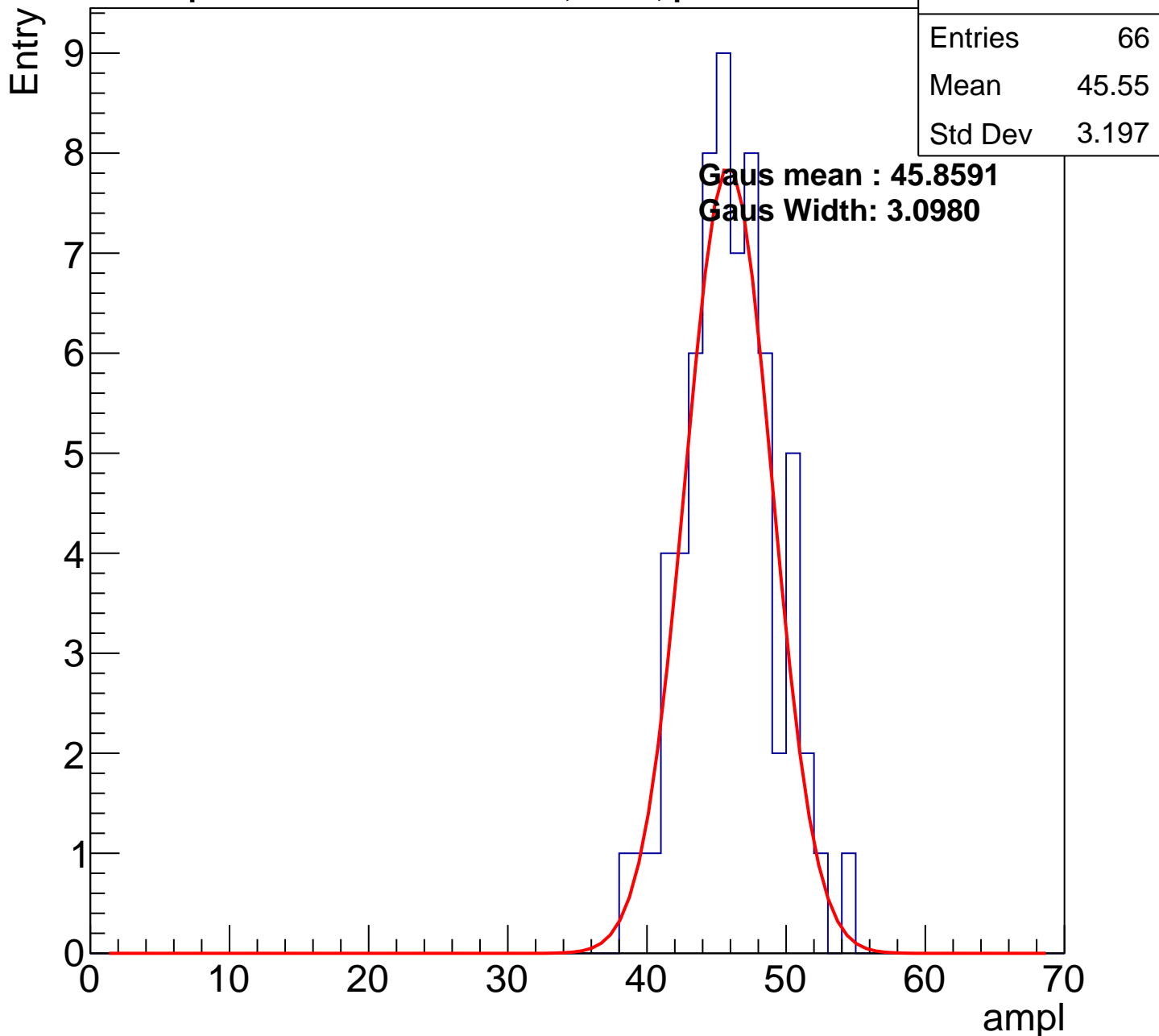
Entries	62
Mean	39.5
Std Dev	3.125

**Gaus mean : 39.4229**  
**Gaus Width: 3.1864**



# B0L002S, U8-ch2, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

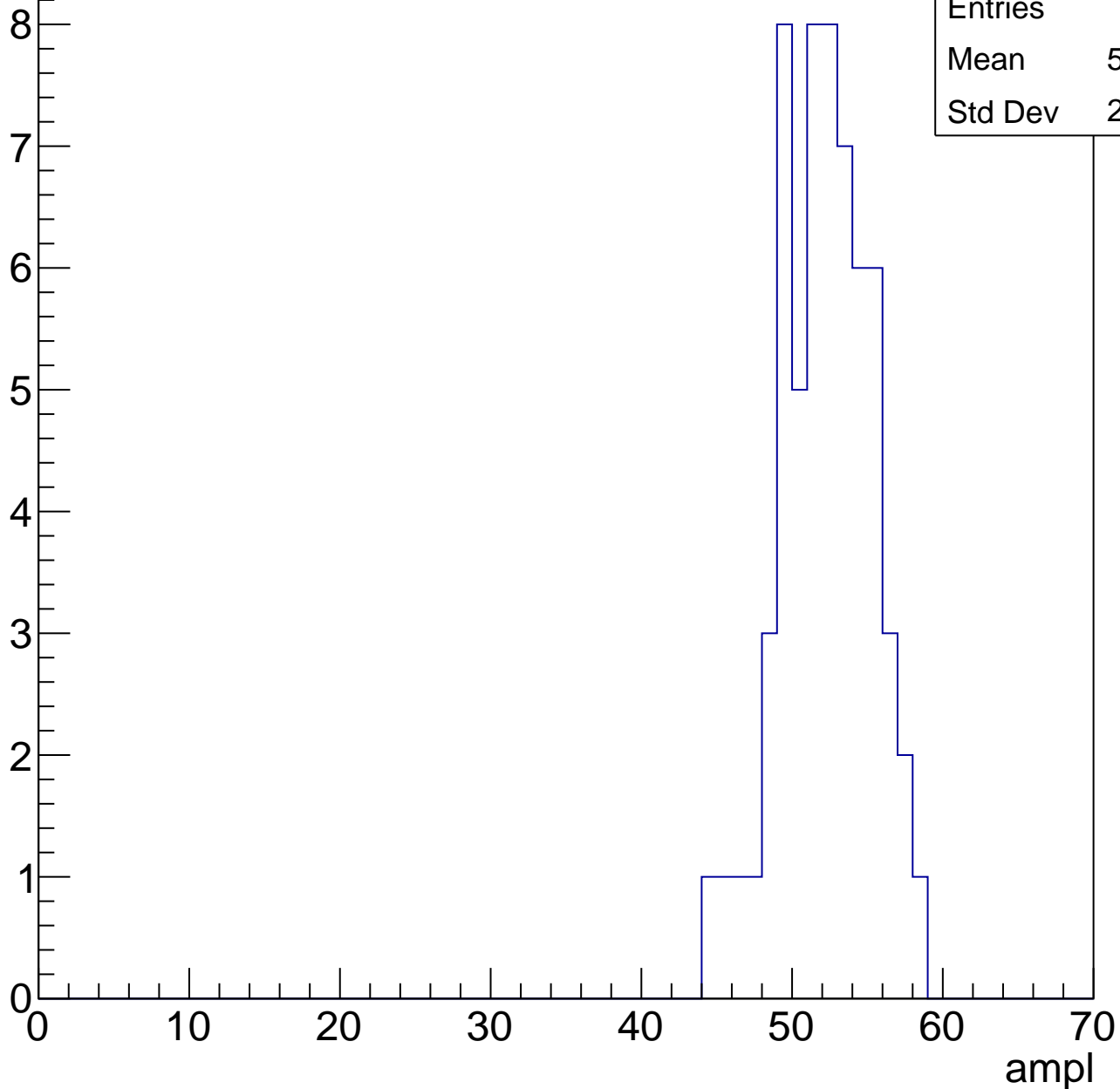


# B0L002S, U8-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

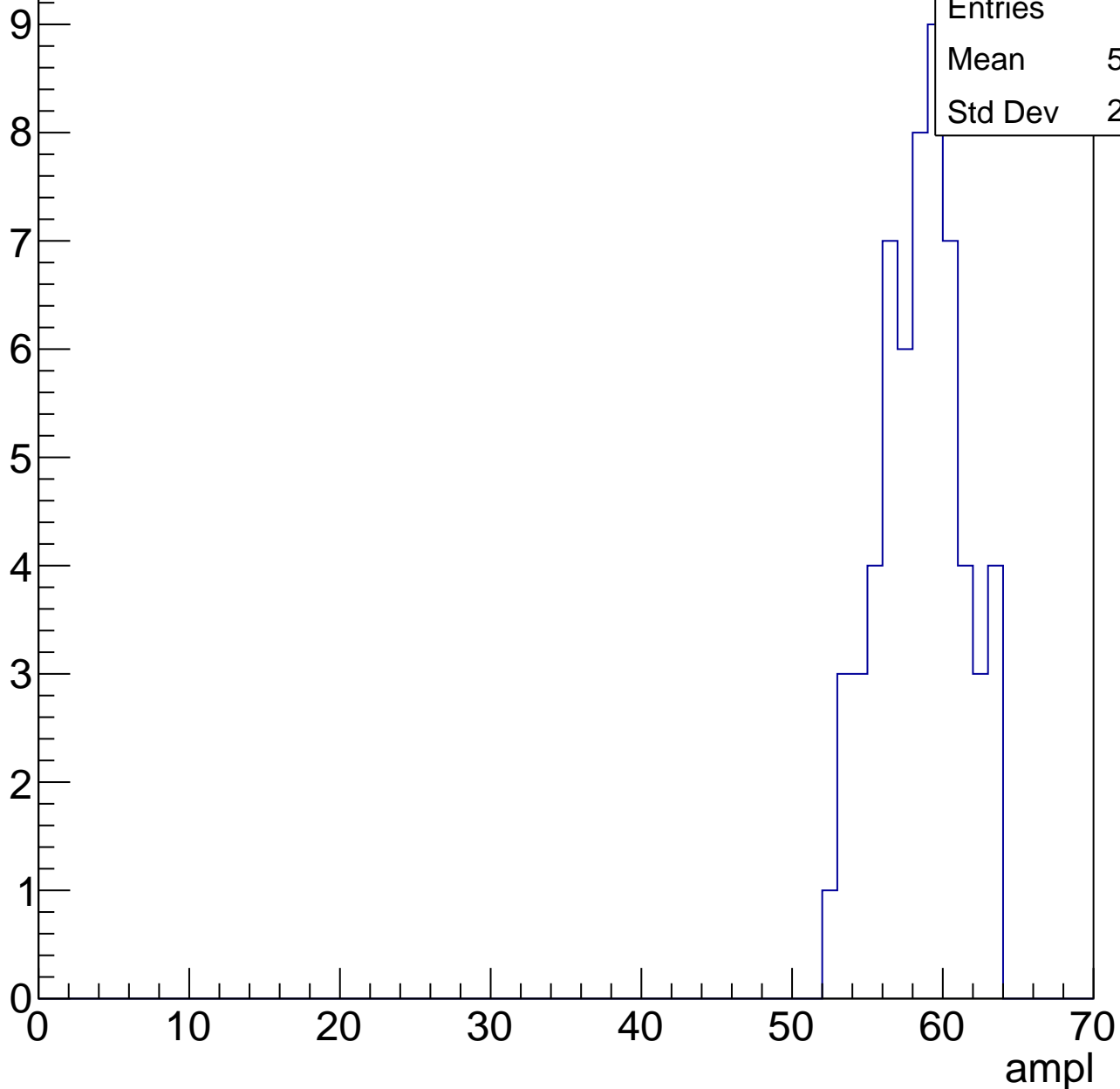
Entries	61
Mean	51.75
Std Dev	2.968



# B0L002S, U8-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



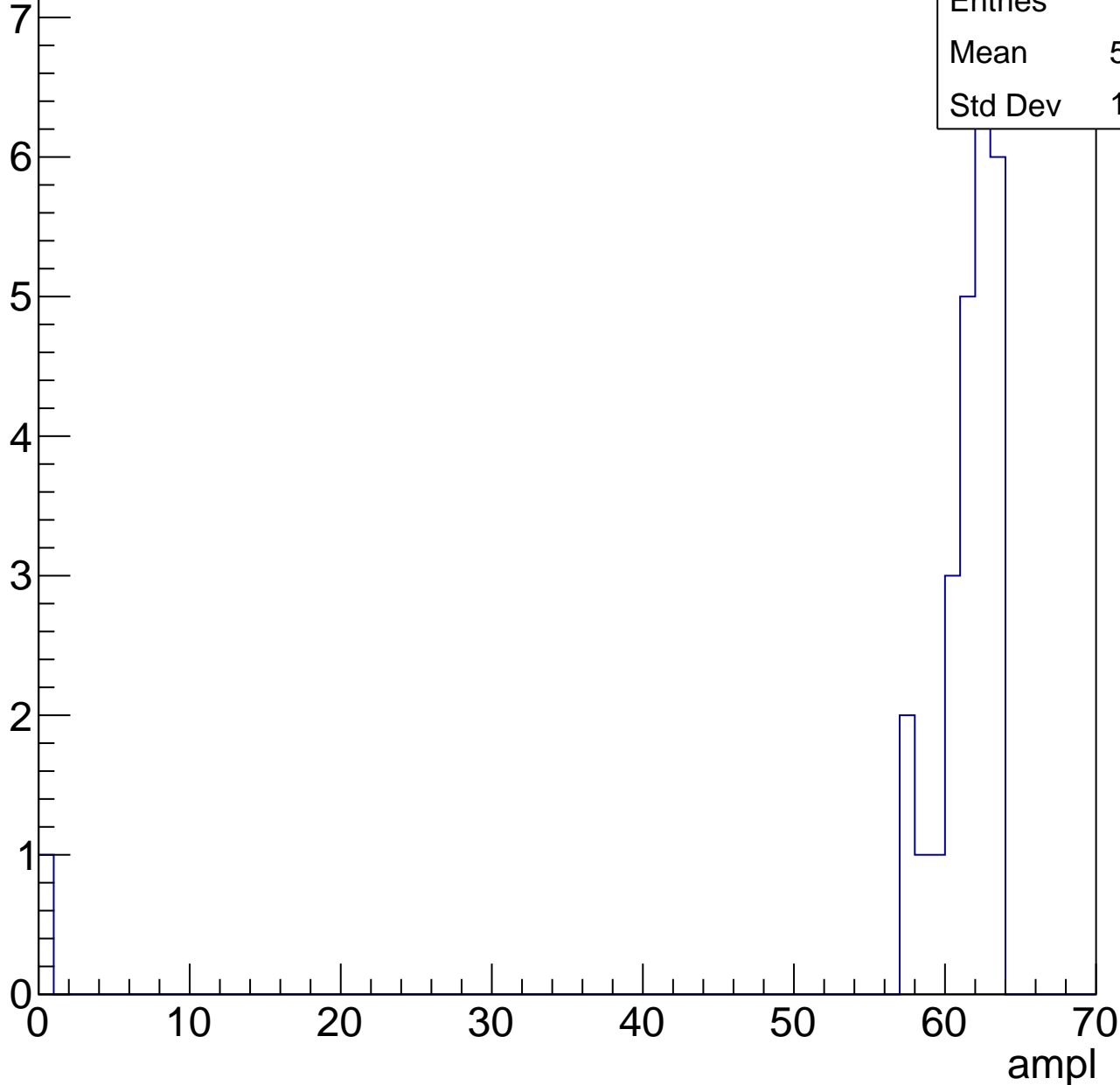
Entries	59
Mean	58.03
Std Dev	2.762

# B0L002S, U8-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

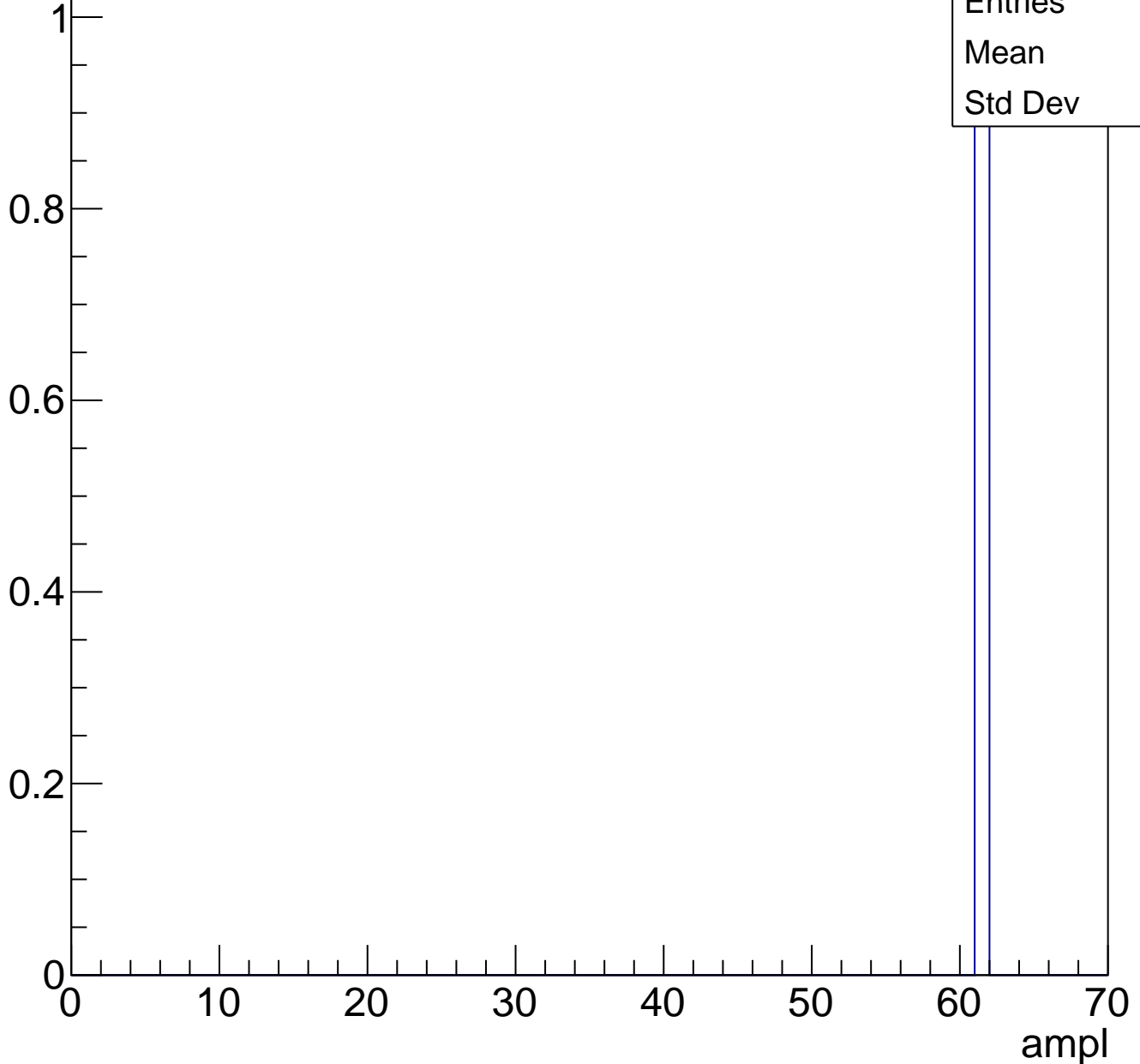
Entries	26
Mean	58.77
Std Dev	11.88



# B0L002S, U8-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch3, adc0

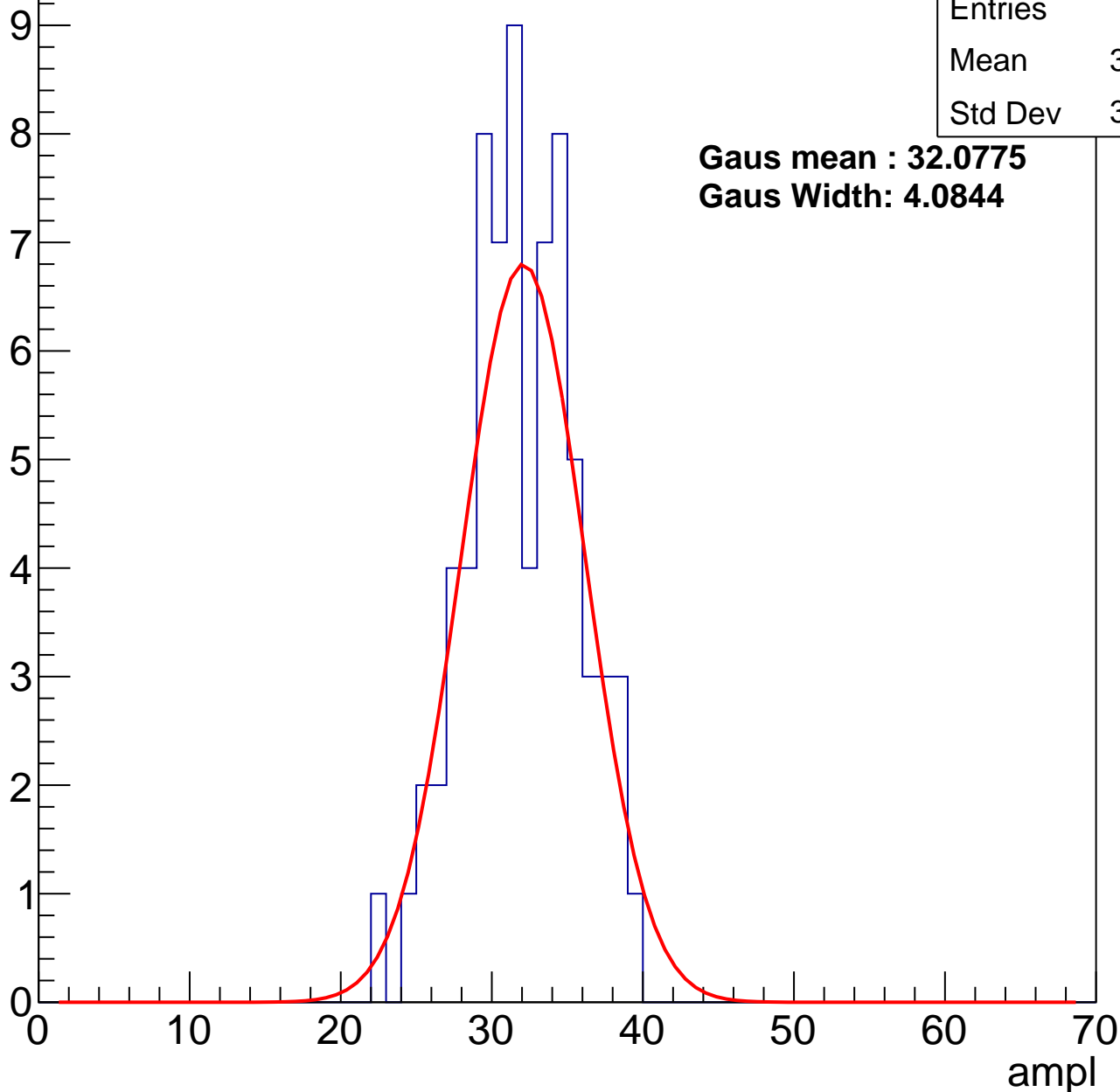
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	31.49
Std Dev	3.636

**Gaus mean : 32.0775**

**Gaus Width: 4.0844**



# B0L002S, U8-ch3, adc1

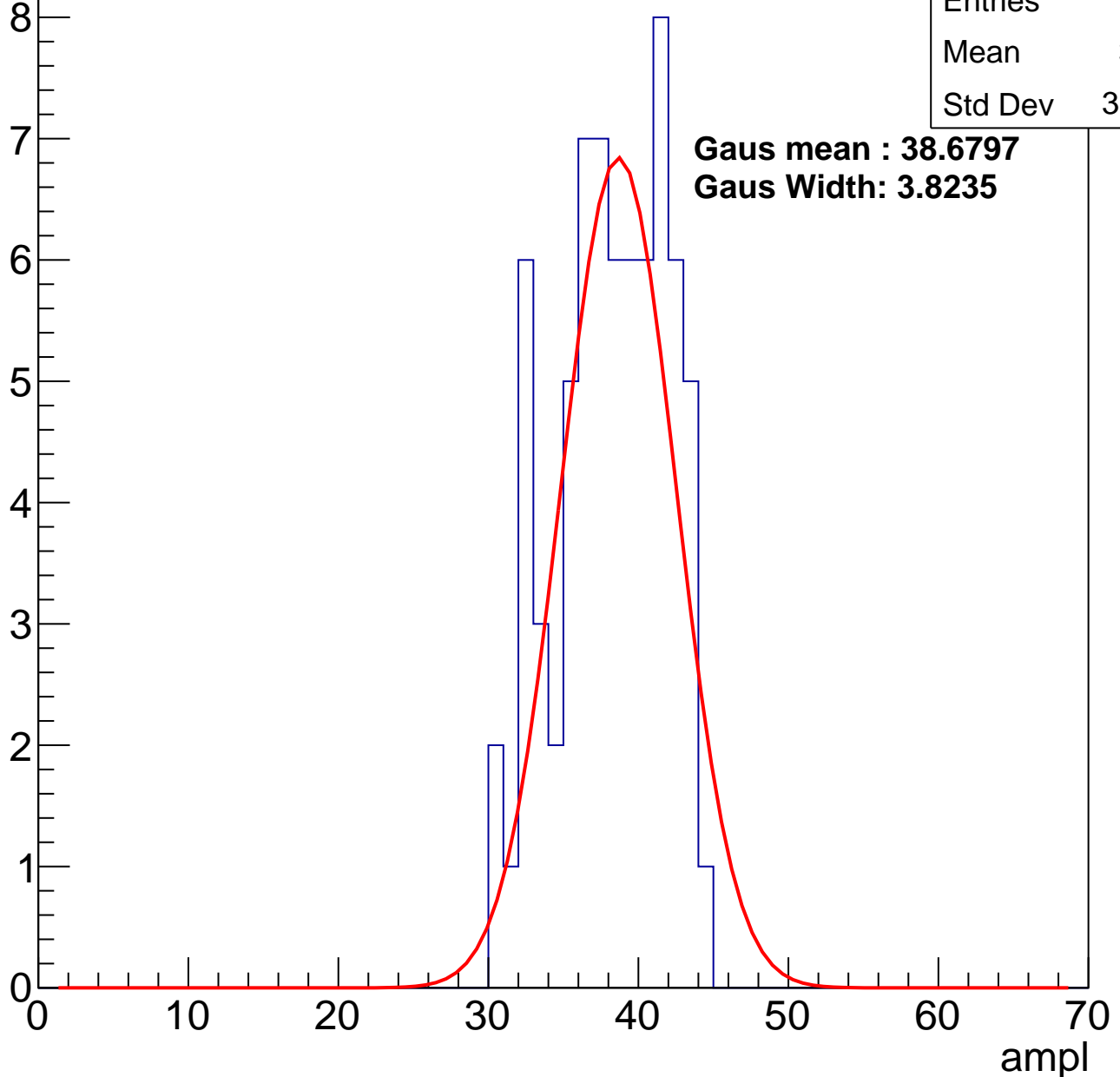
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.7
Std Dev	3.625

**Gaus mean : 38.6797**

**Gaus Width: 3.8235**



# B0L002S, U8-ch3, adc2

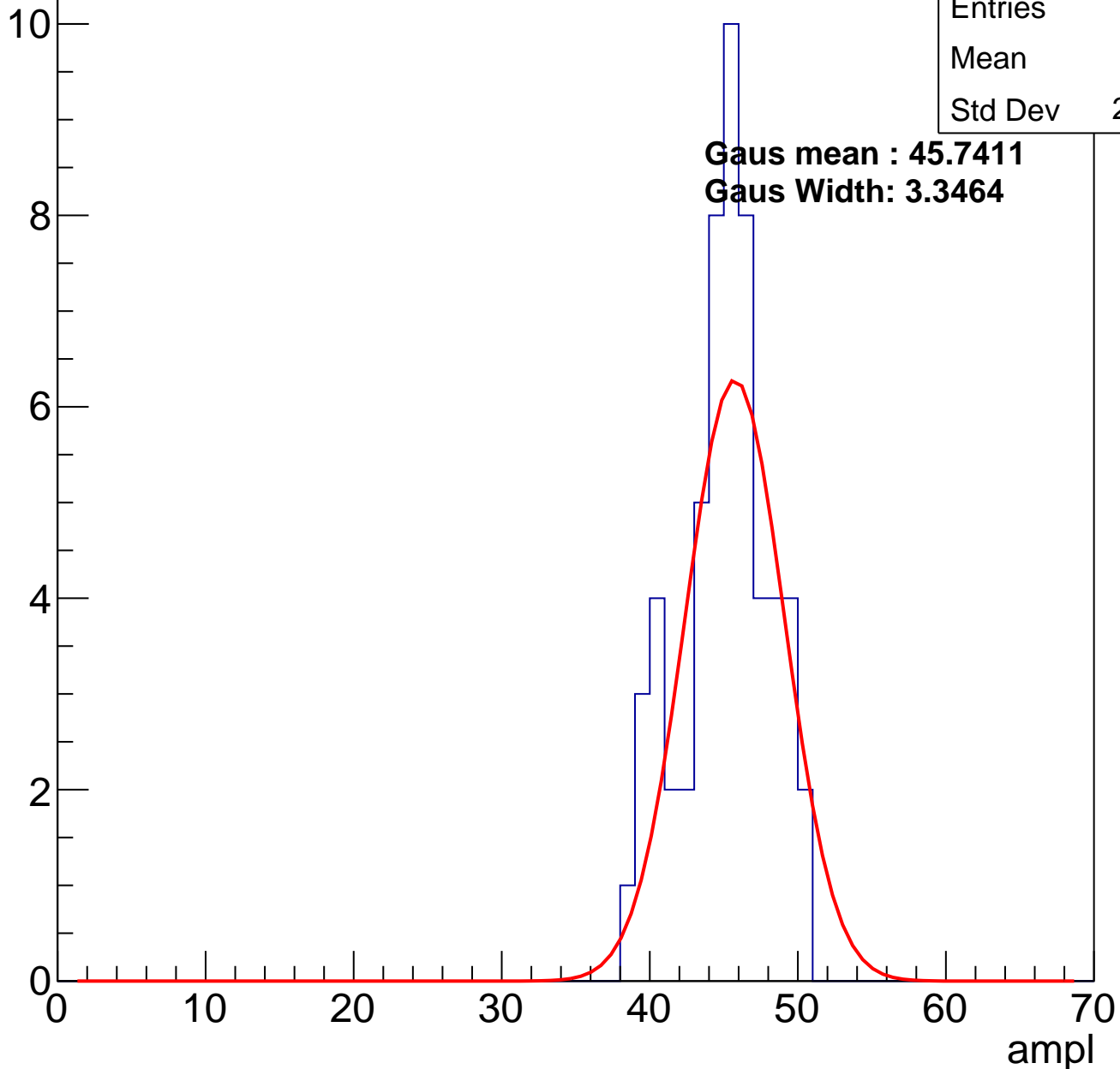
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	57
Mean	44.6
Std Dev	2.961

**Gaus mean : 45.7411**

**Gaus Width: 3.3464**

Entry



# B0L002S, U8-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

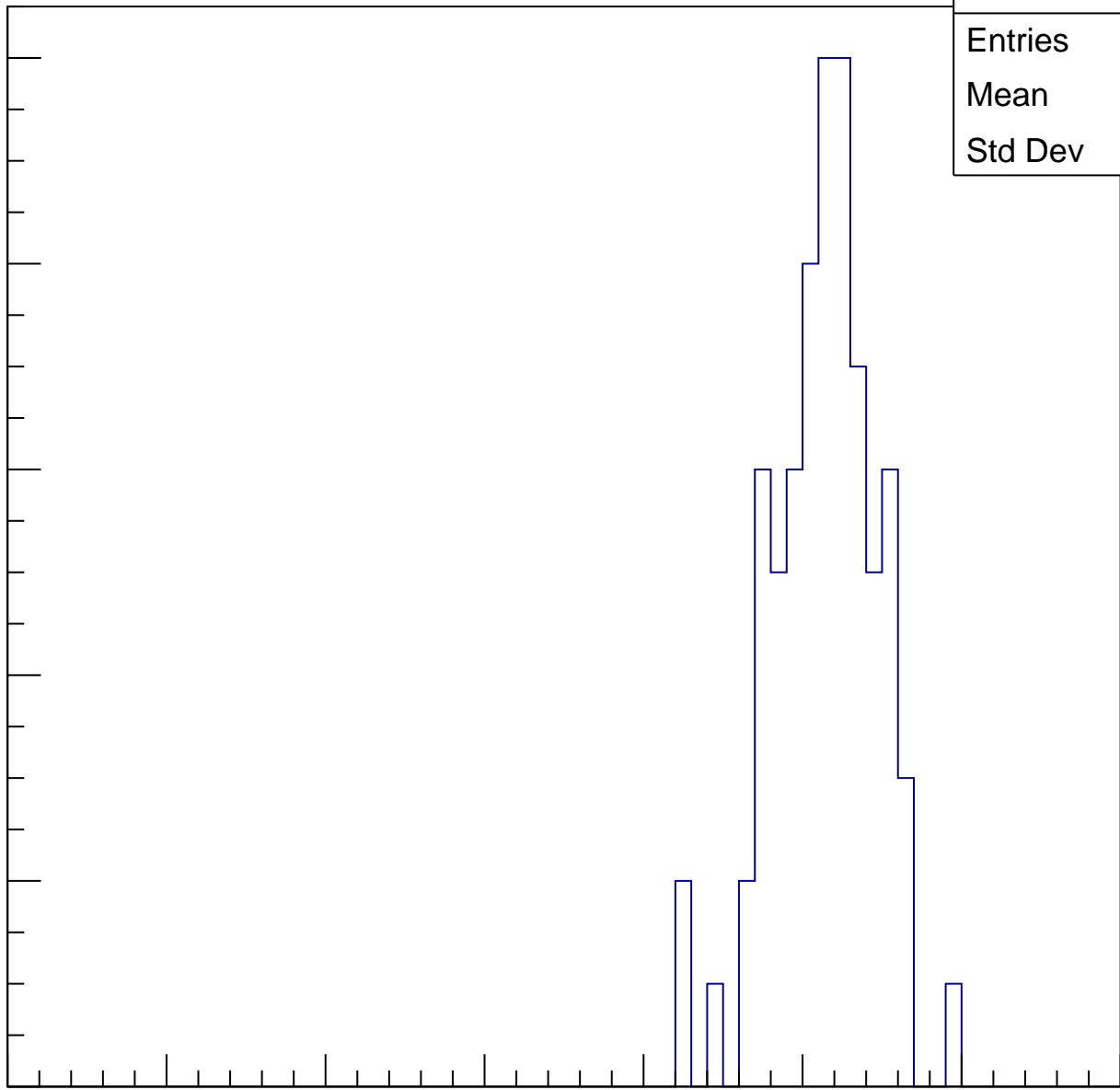
Entries	72
Mean	50.89
Std Dev	3.23

Entry

10  
8  
6  
4  
2  
0

ampl

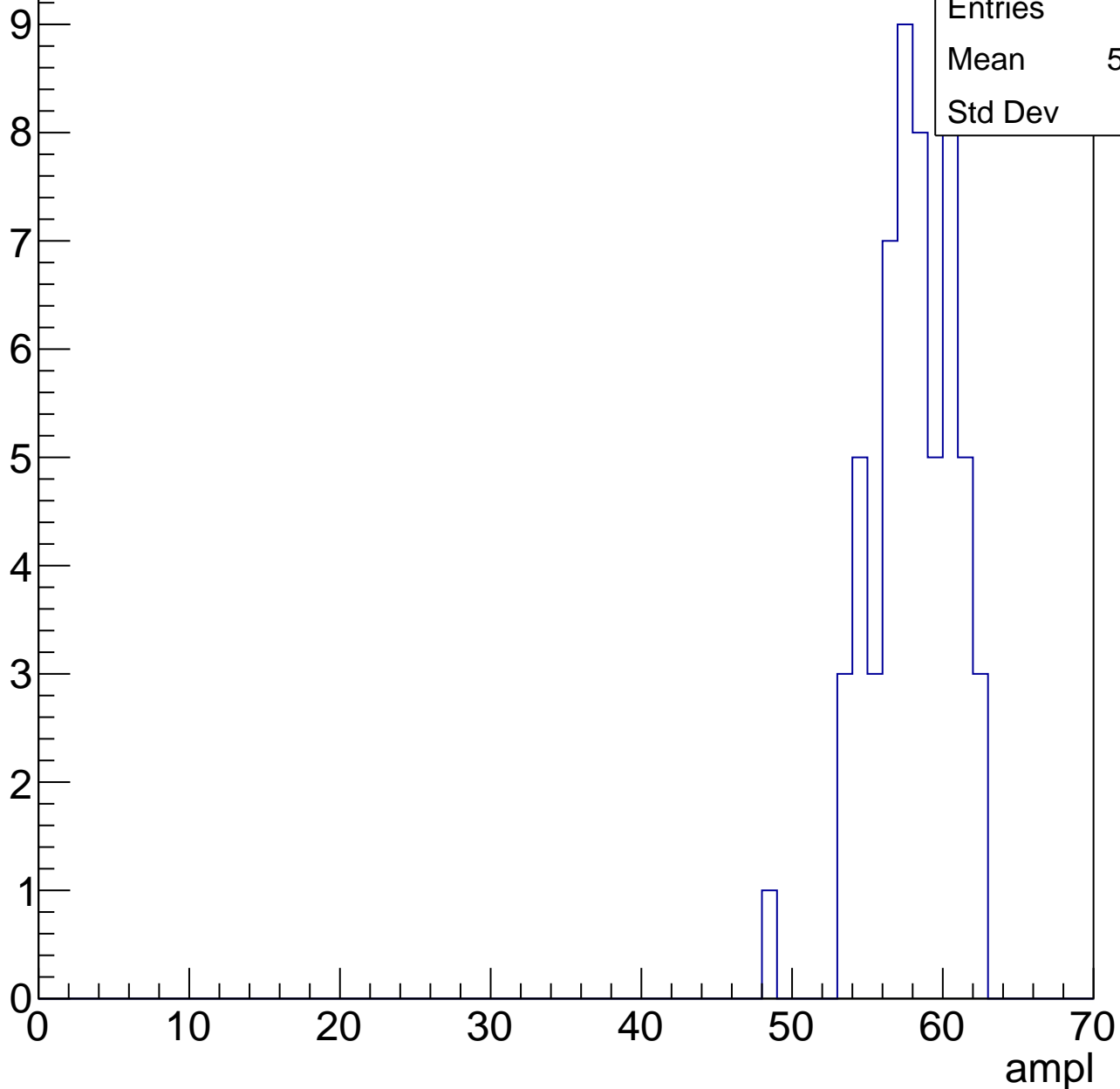
0 10 20 30 40 50 60 70



# B0L002S, U8-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	57
Mean	57.49
Std Dev	2.76

# B0L002S, U8-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

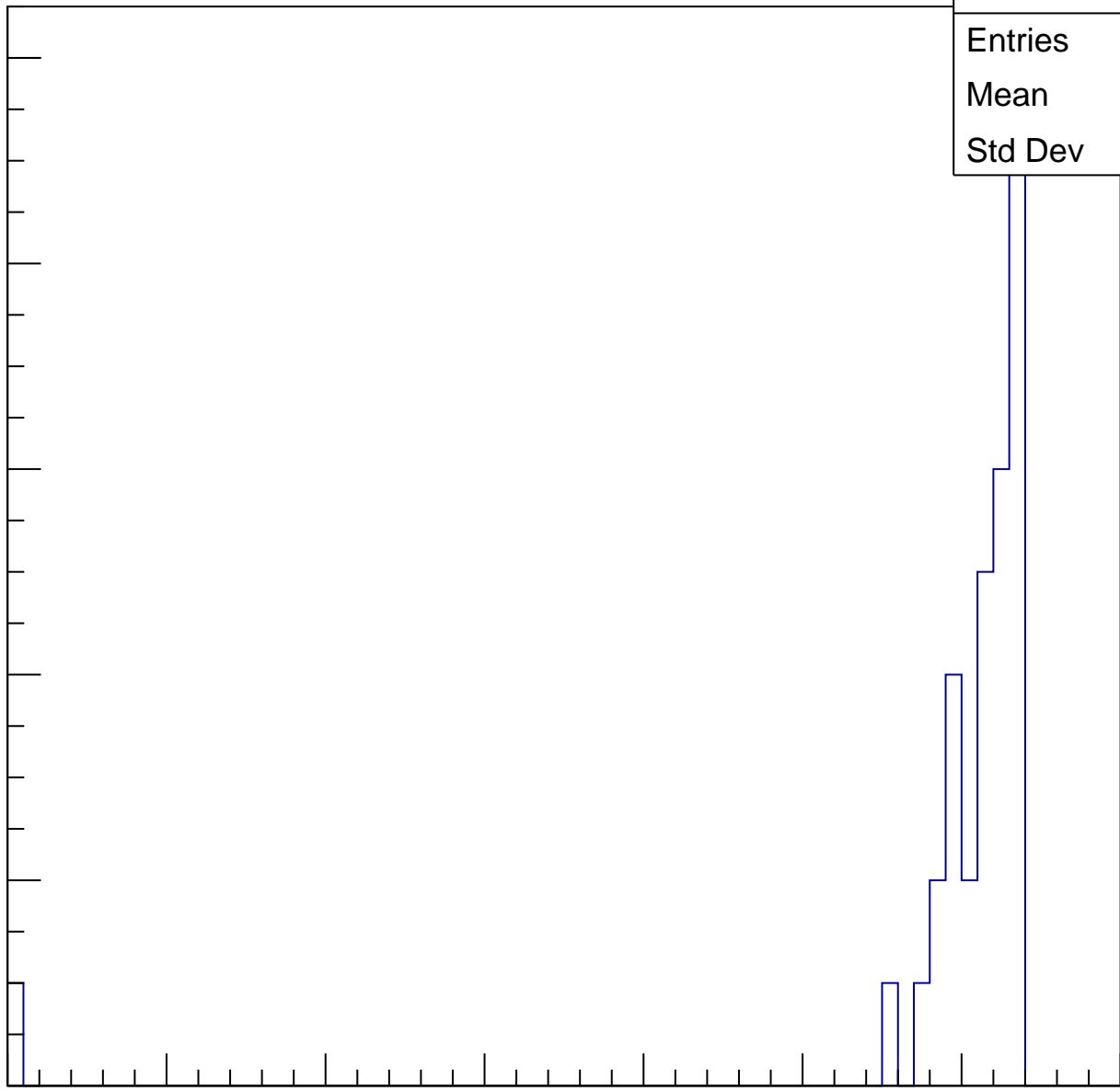
Entries	32
Mean	59.09
Std Dev	10.81

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

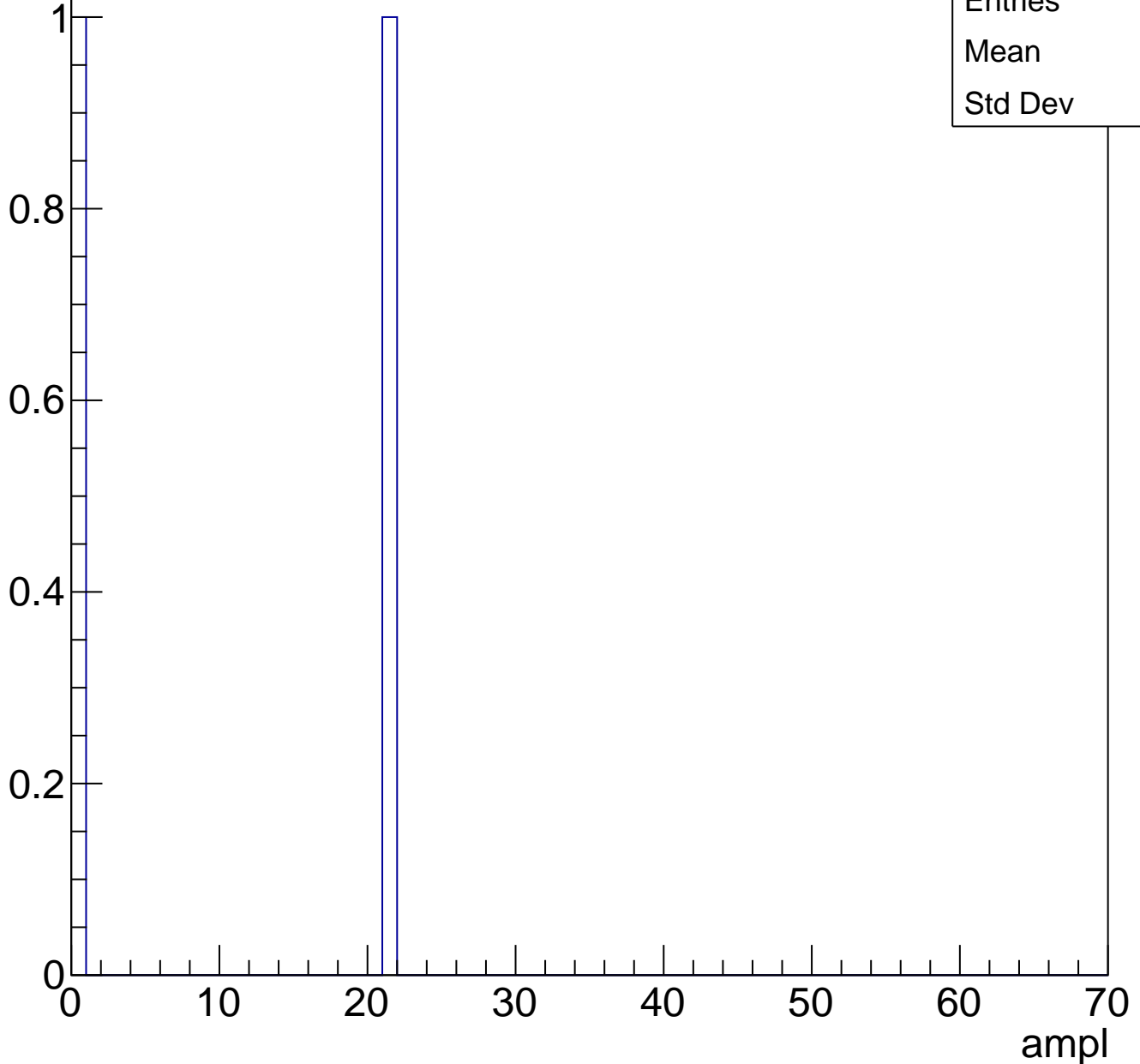




# B0L002S, U8-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch4, adc0

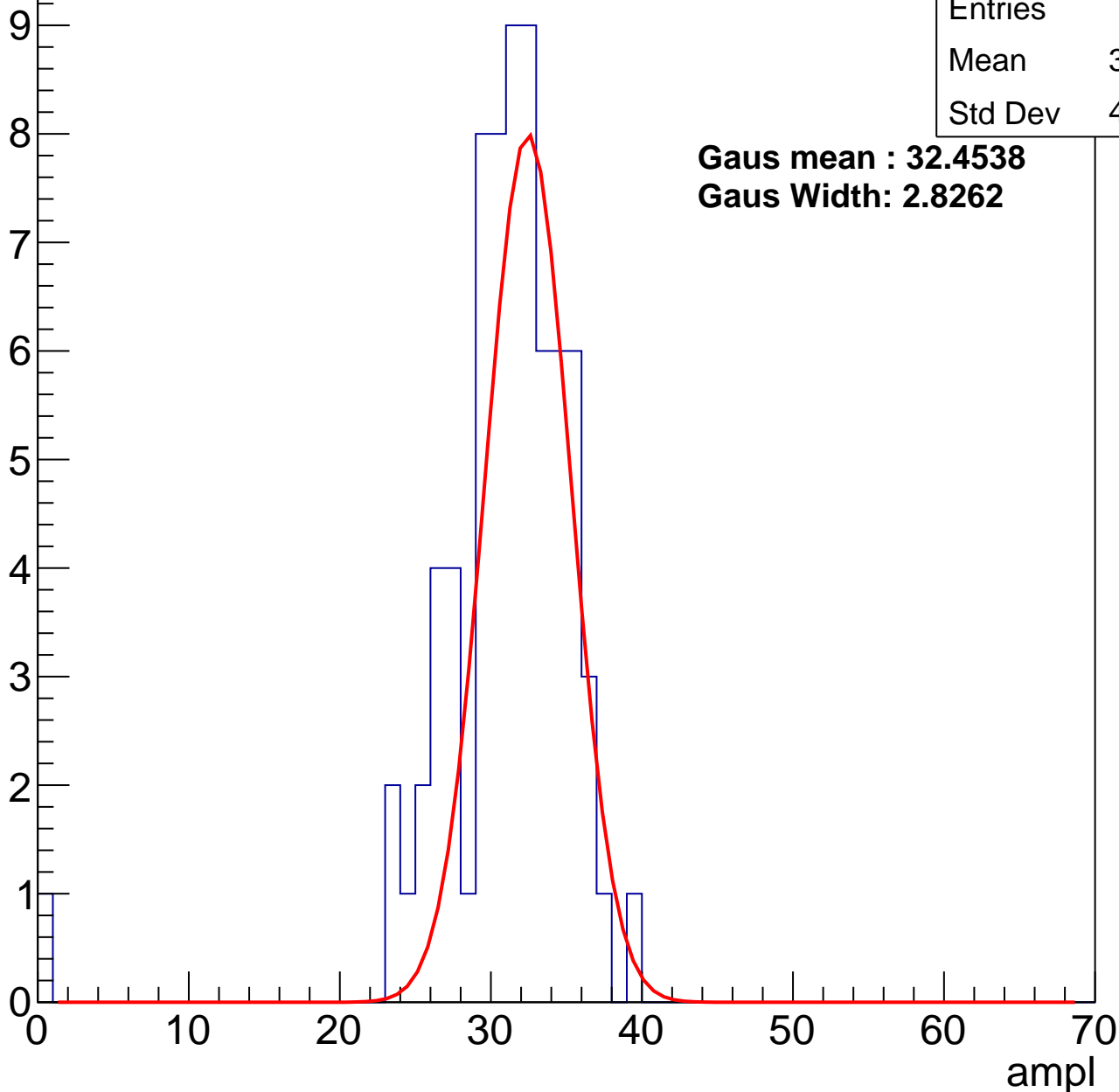
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.49
Std Dev	4.947

**Gaus mean : 32.4538**

**Gaus Width: 2.8262**



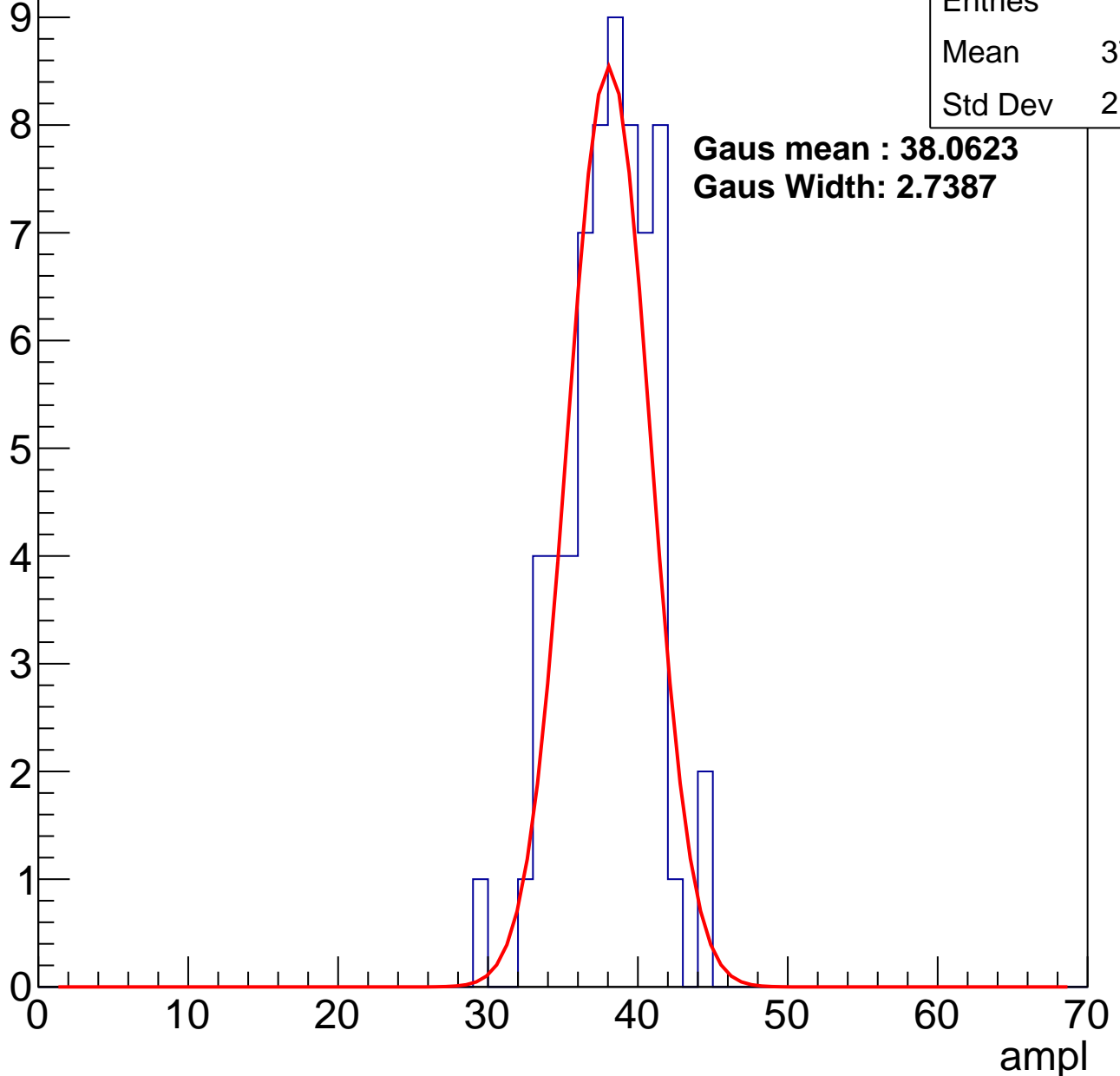
# B0L002S, U8-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.64
Std Dev	2.907

**Gaus mean : 38.0623**  
**Gaus Width: 2.7387**



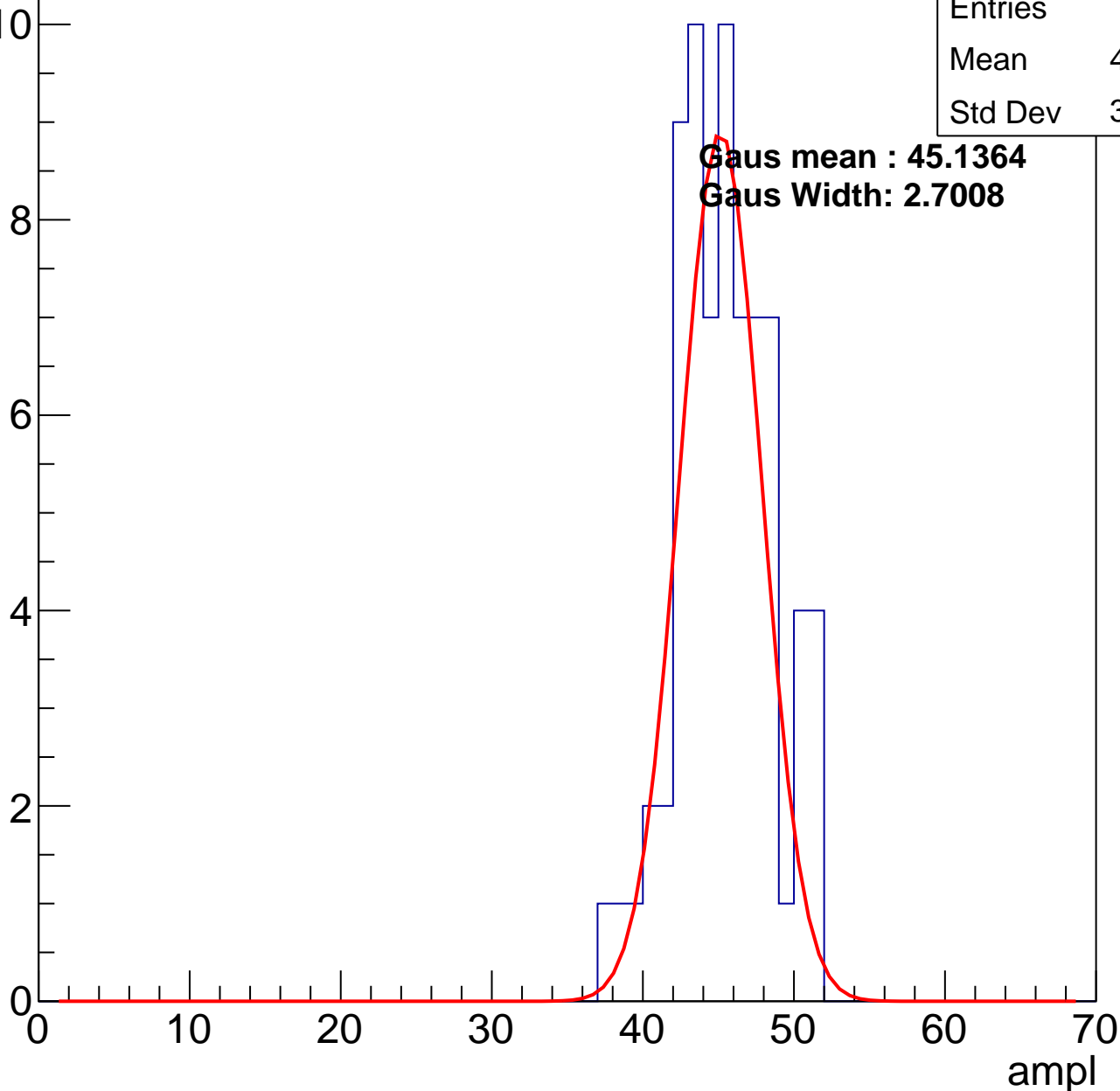
# B0L002S, U8-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	44.96
Std Dev	3.134

**Gaus mean : 45.1364**  
**Gaus Width: 2.7008**

Entry

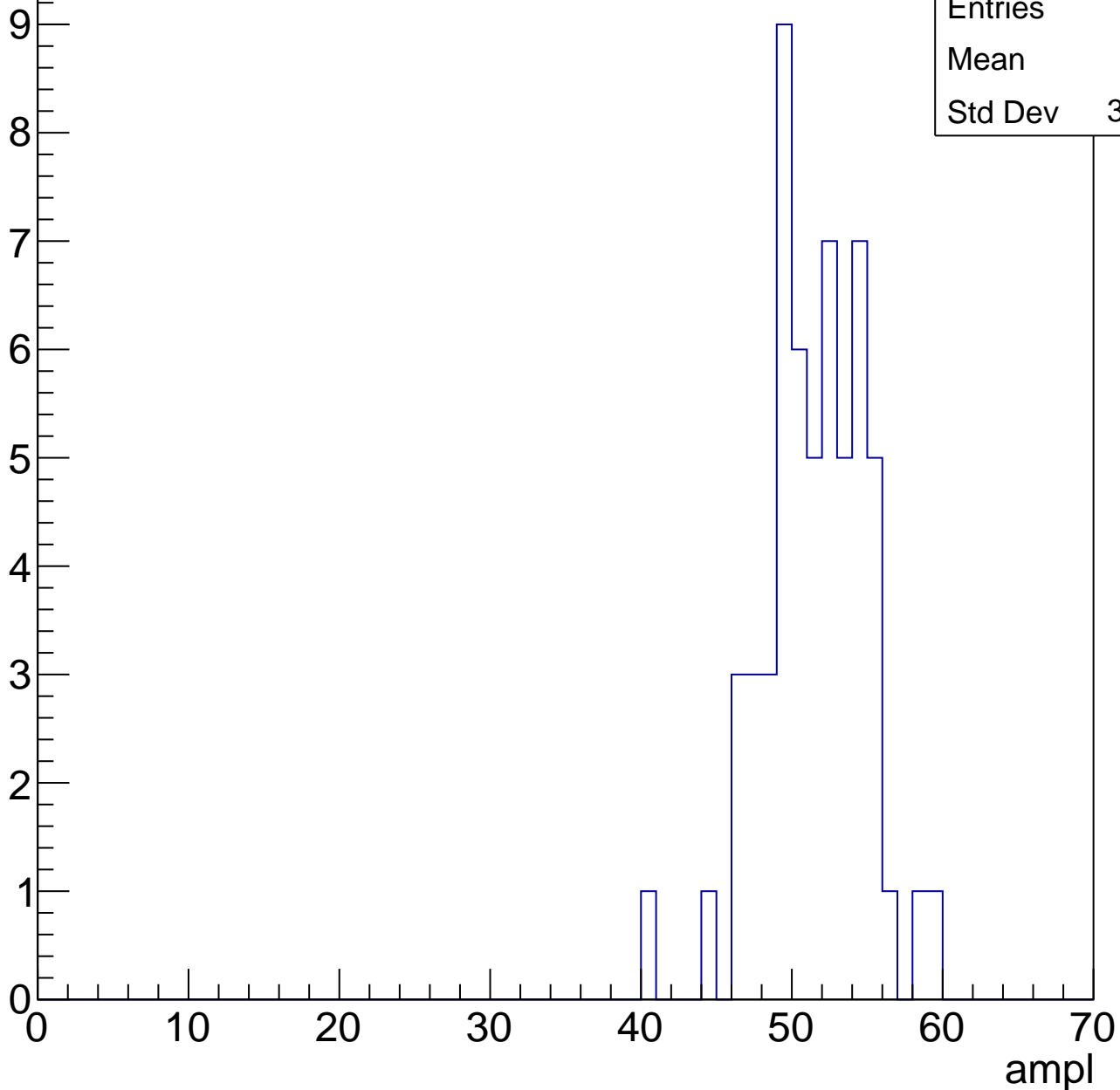


# B0L002S, U8-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

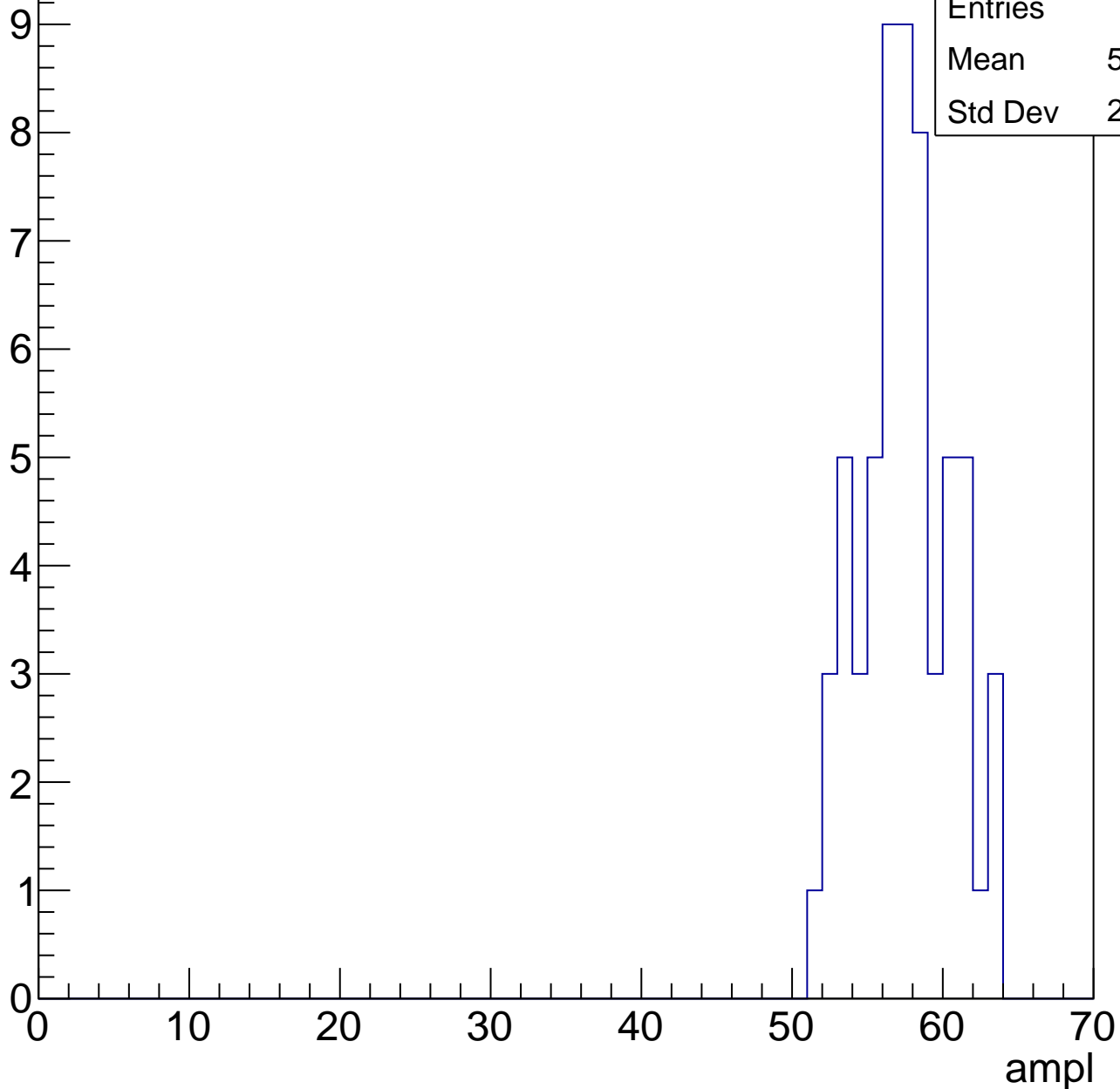
Entries	58
Mean	51
Std Dev	3.399



# B0L002S, U8-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

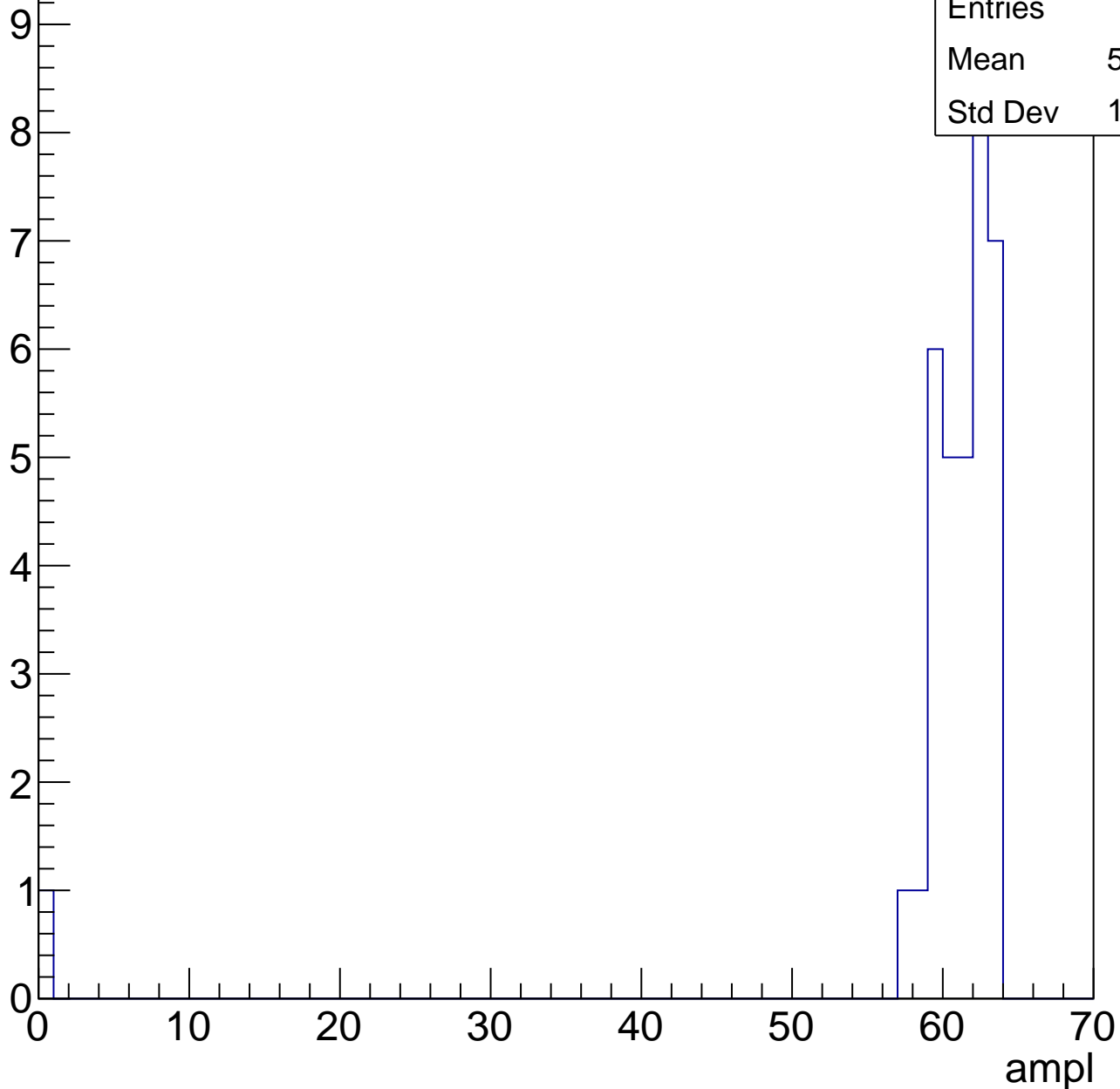


# B0L002S, U8-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.23
Std Dev	10.28



# B0L002S, U8-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl

# B0L002S, U8-ch5, adc0

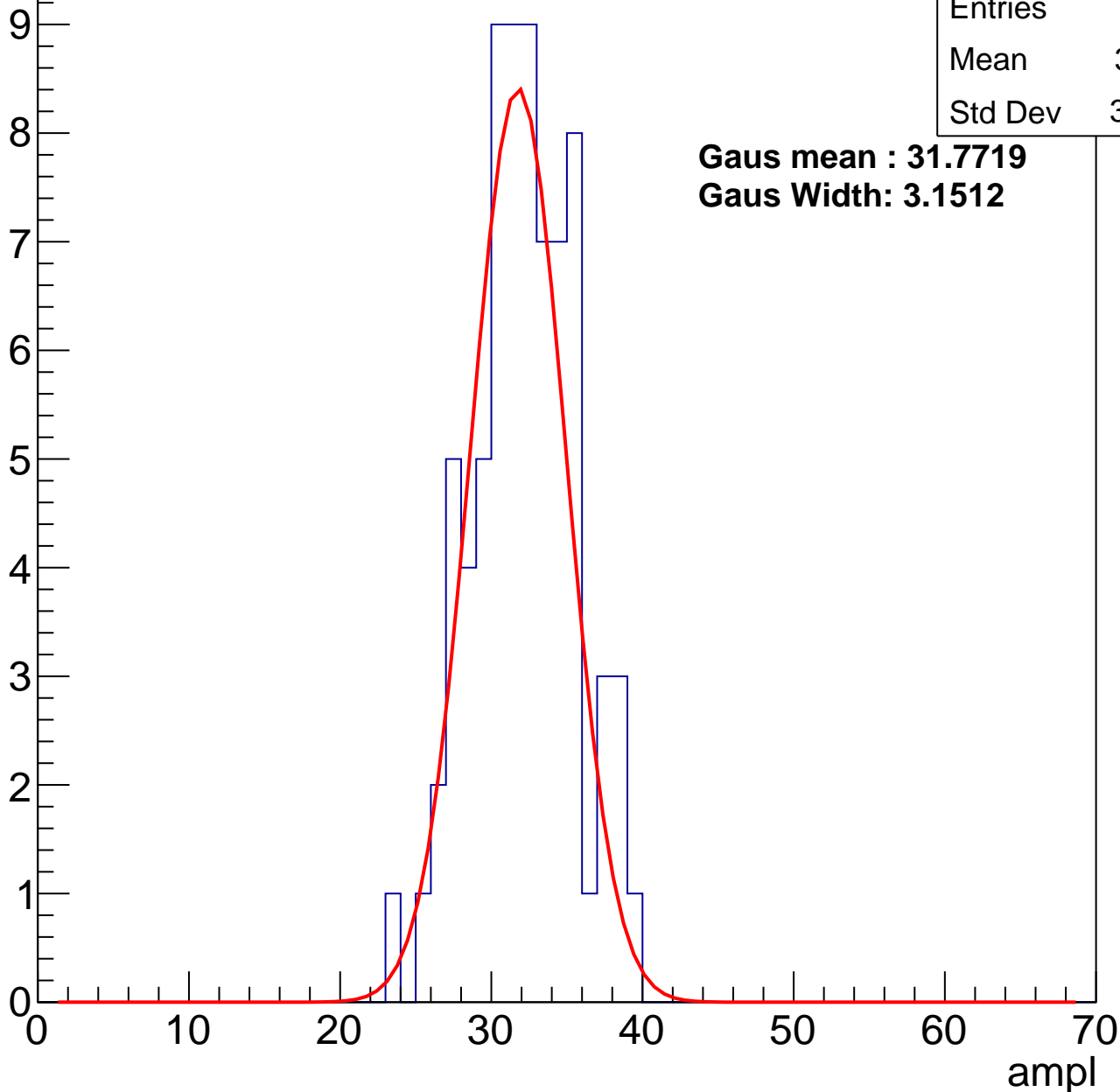
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.71
Std Dev	3.326

**Gaus mean : 31.7719**

**Gaus Width: 3.1512**



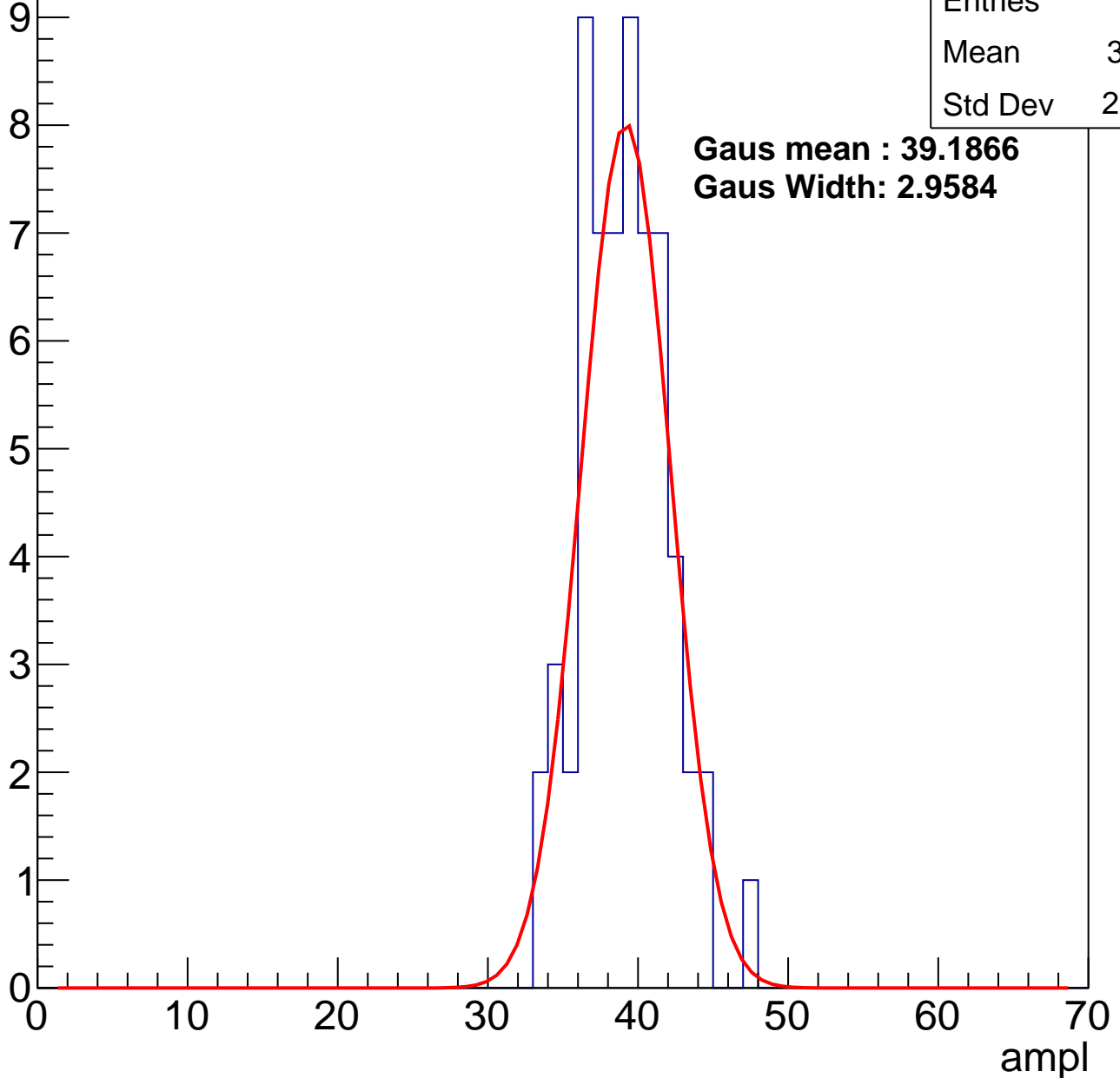
# B0L002S, U8-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.61
Std Dev	2.847

**Gaus mean : 39.1866**  
**Gaus Width: 2.9584**



# B0L002S, U8-ch5, adc2

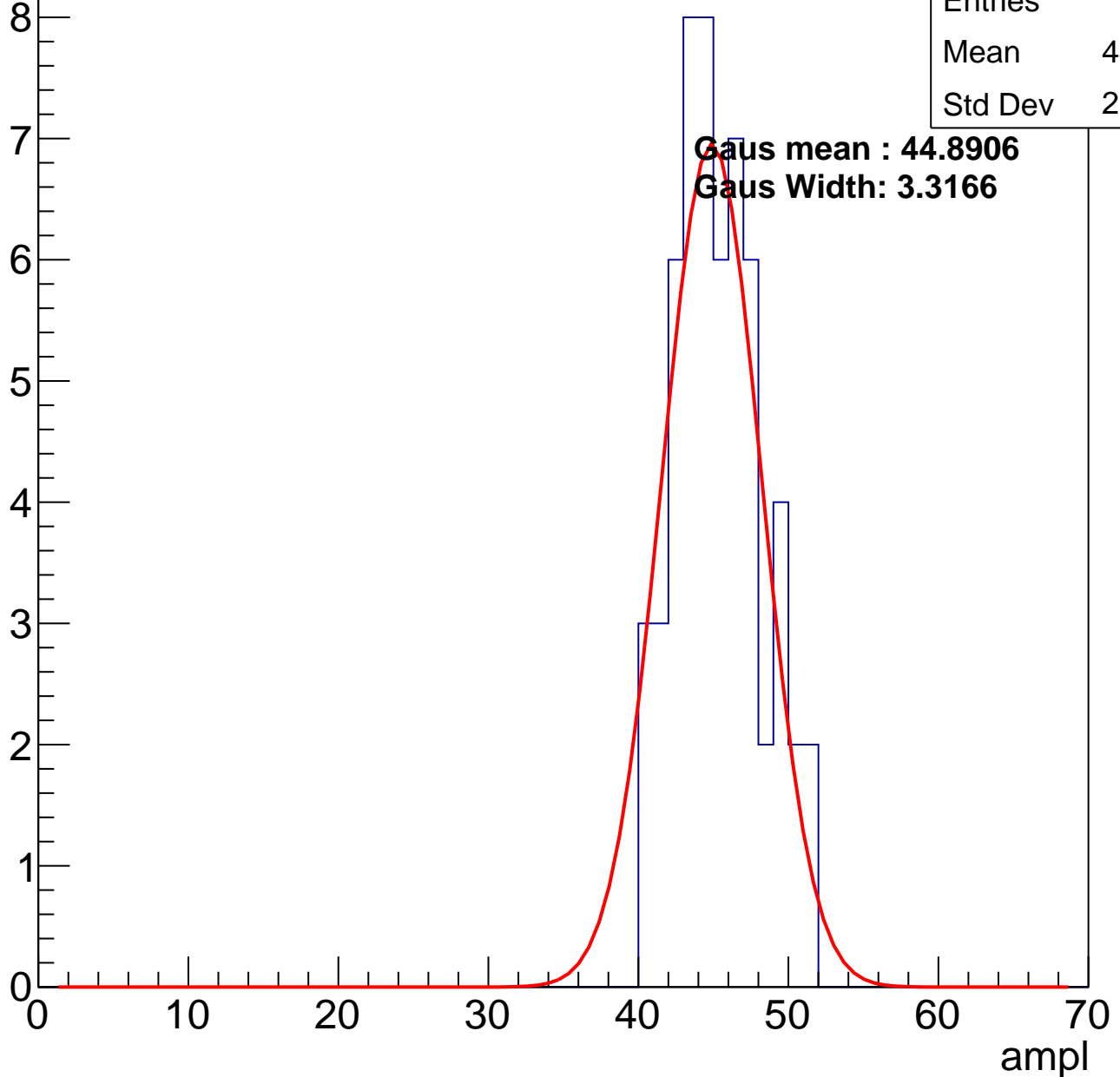
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.89
Std Dev	2.814

**Gaus mean : 44.8906**

**Gaus Width: 3.3166**

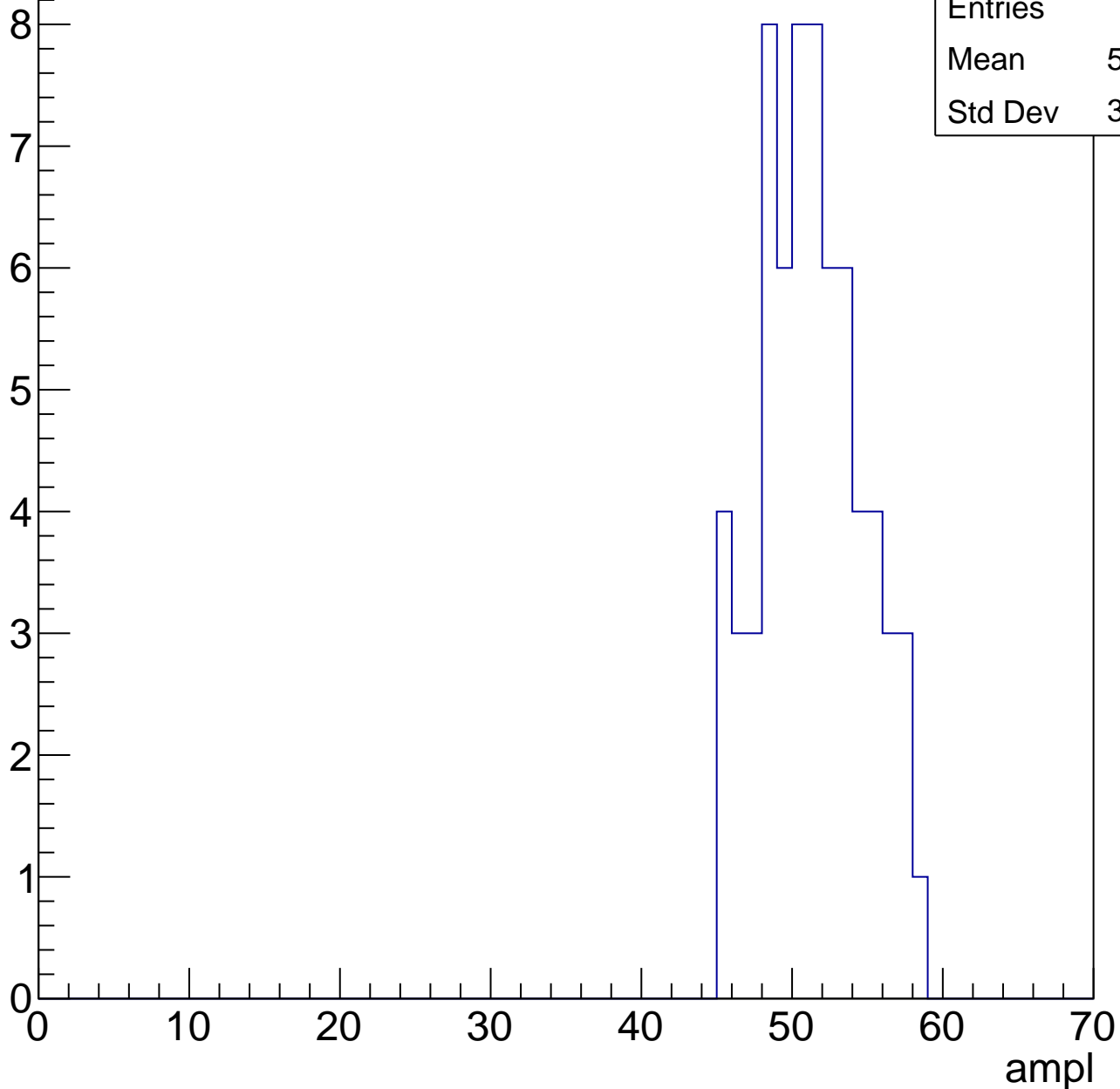


# B0L002S, U8-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.87
Std Dev	3.305



# B0L002S, U8-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	57.56
Std Dev	2.97

Entry

10

8

6

4

2

0

0

10

20

30

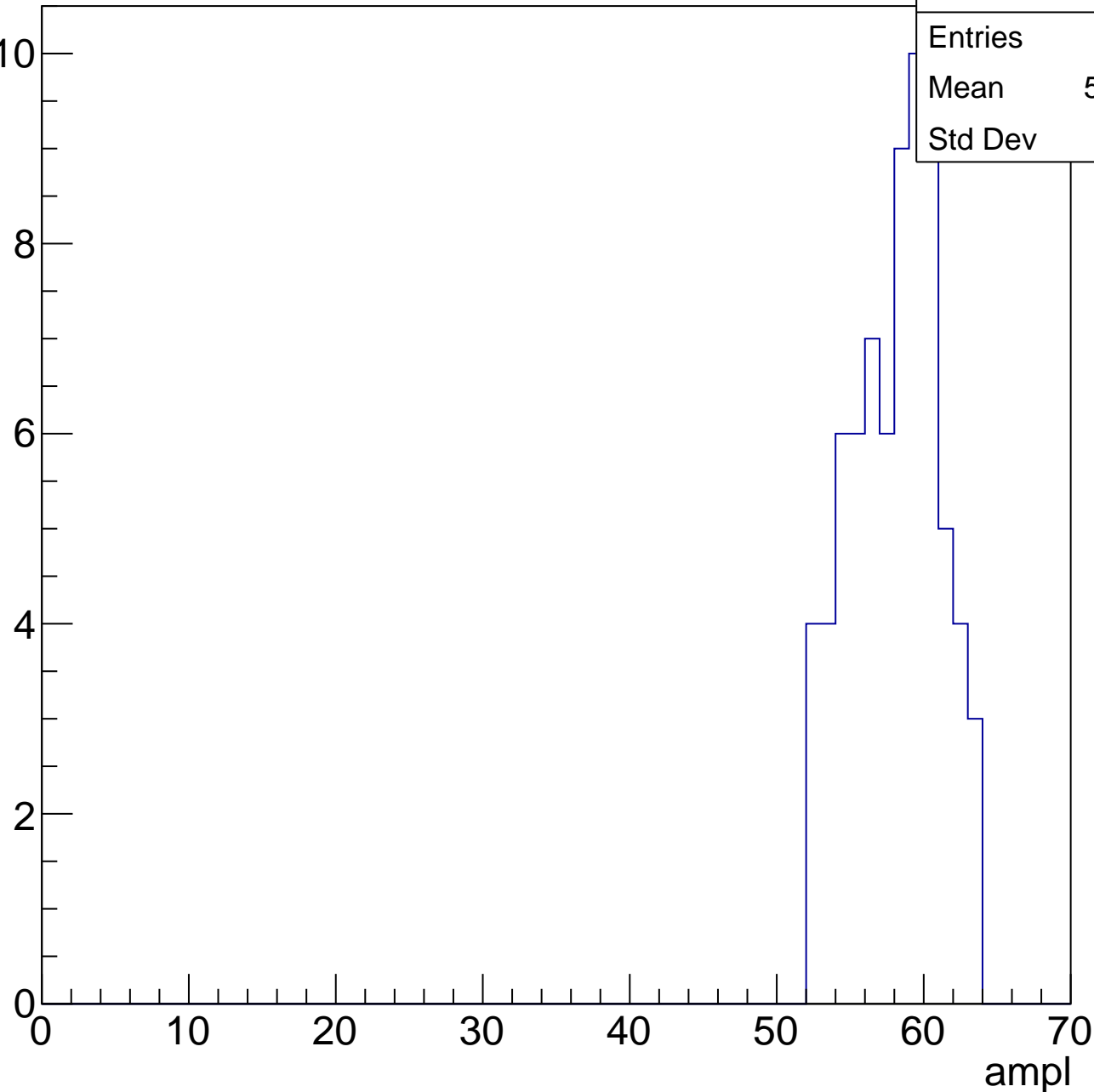
40

50

60

70

ampl

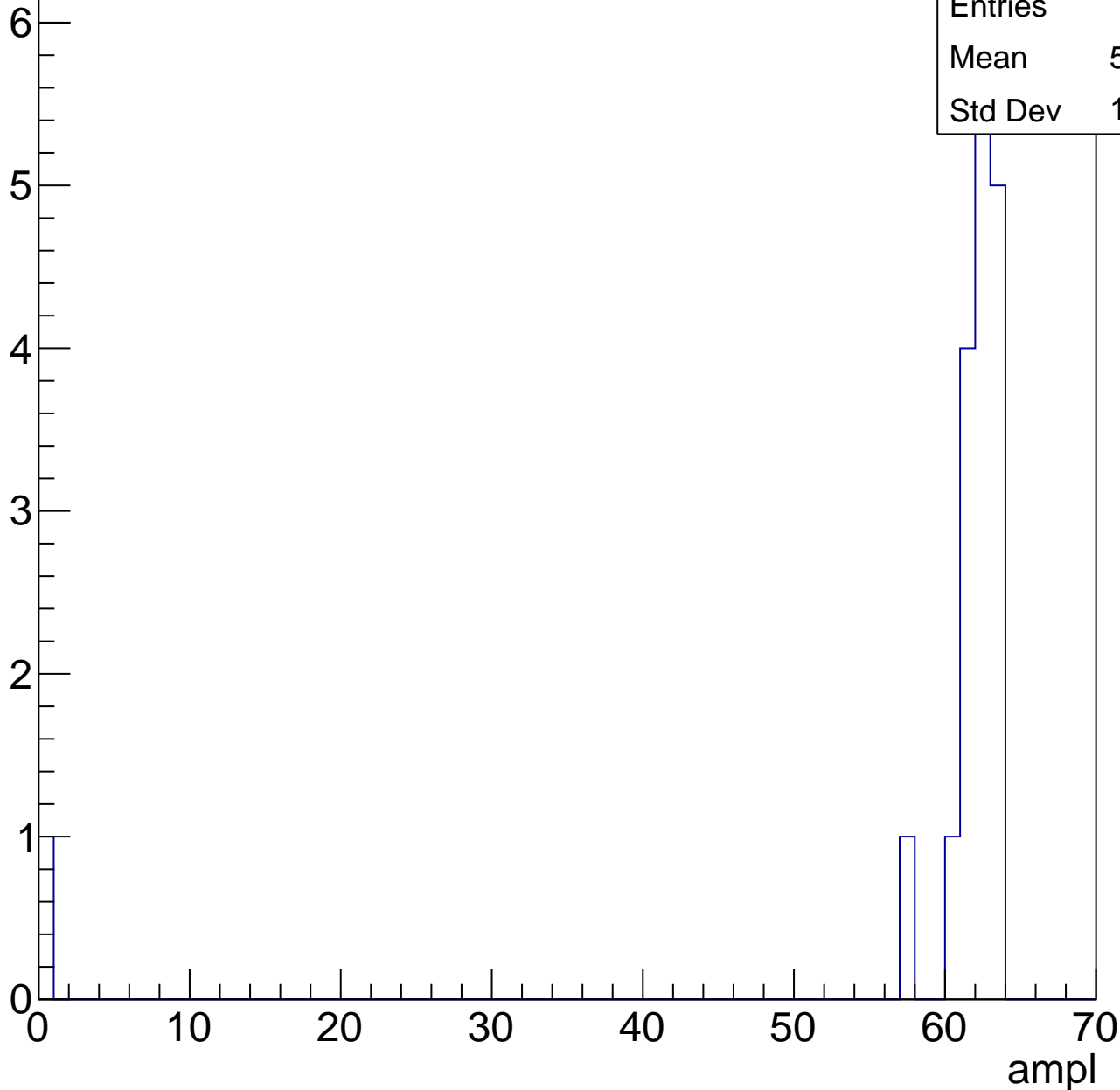


# B0L002S, U8-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	18
Mean	58.22
Std Dev	14.19



# B0L002S, U8-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B0L002S, U8-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch6, adc0

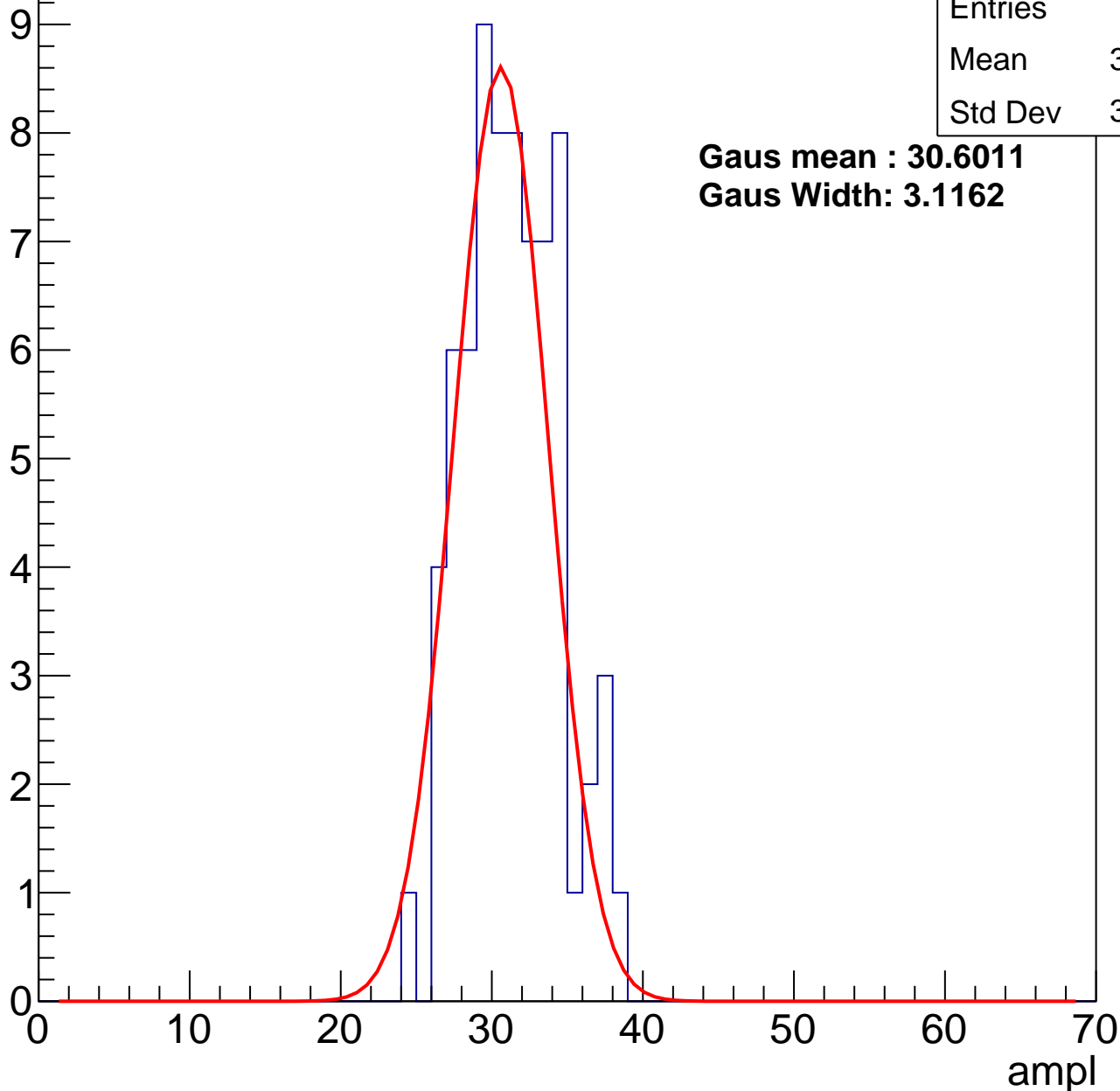
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.85
Std Dev	3.075

**Gaus mean : 30.6011**

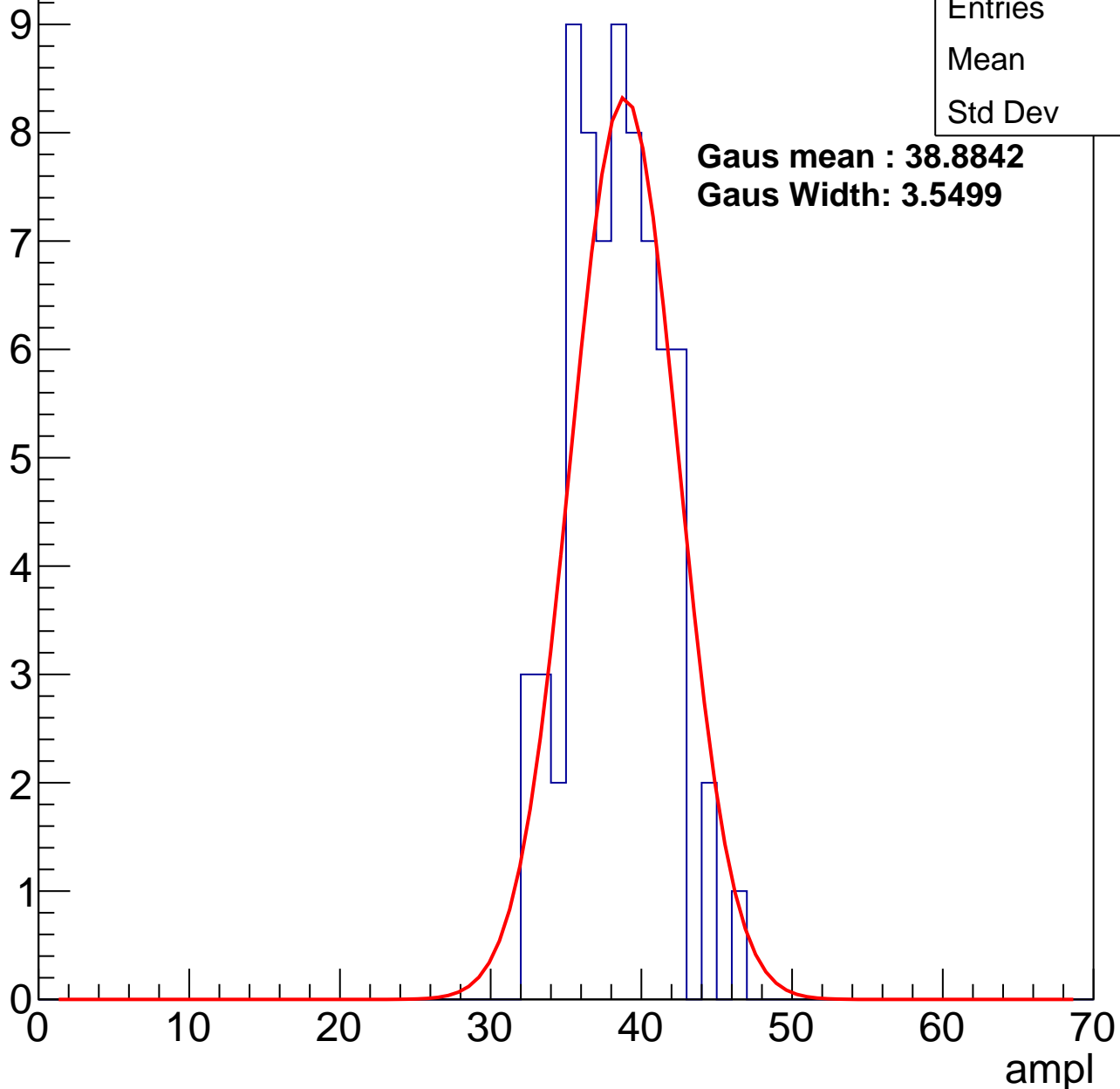
**Gaus Width: 3.1162**



# B0L002S, U8-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	71
Mean	37.9
Std Dev	3.04

# B0L002S, U8-ch6, adc2

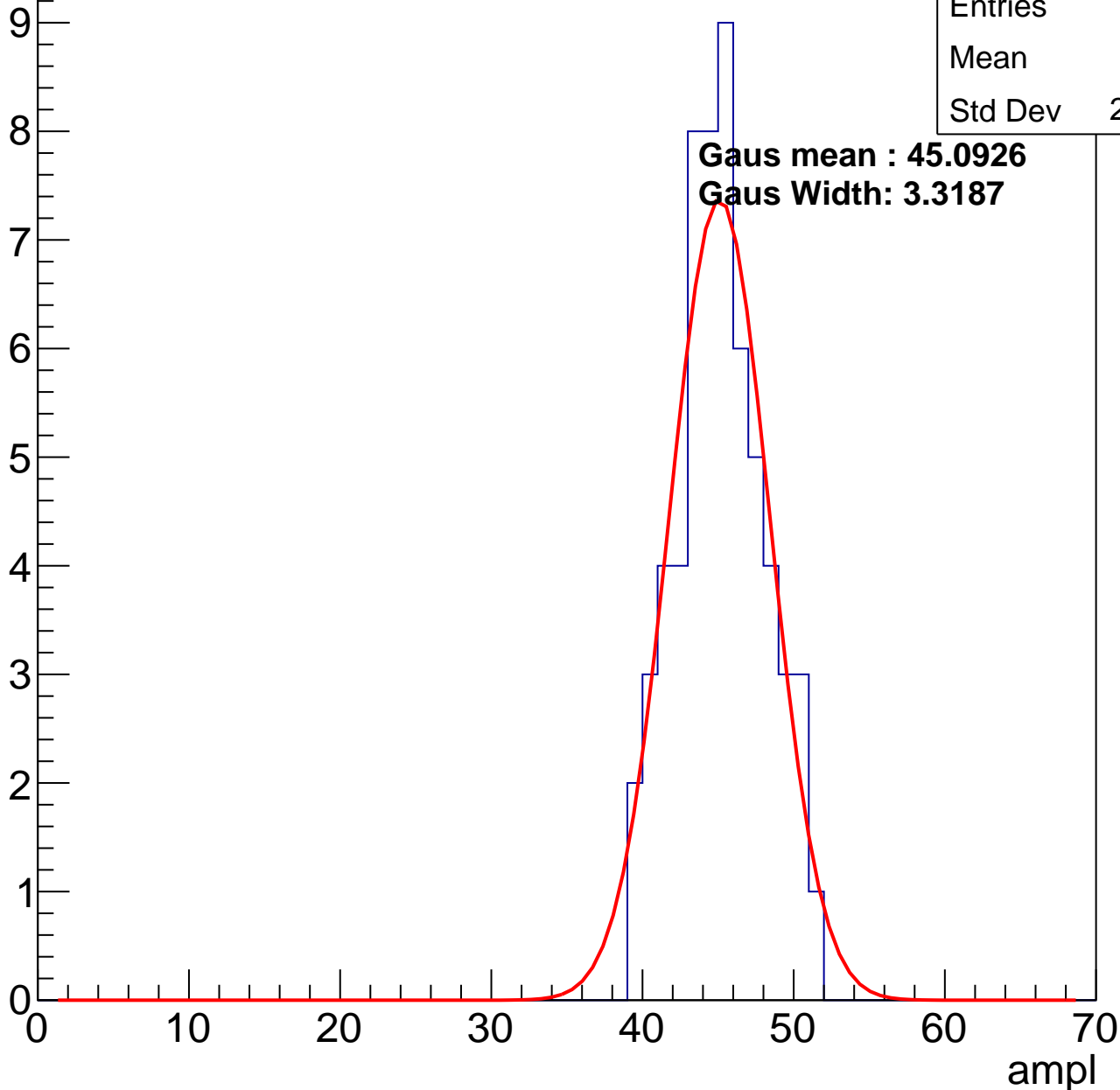
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.7
Std Dev	2.894

**Gaus mean : 45.0926**

**Gaus Width: 3.3187**

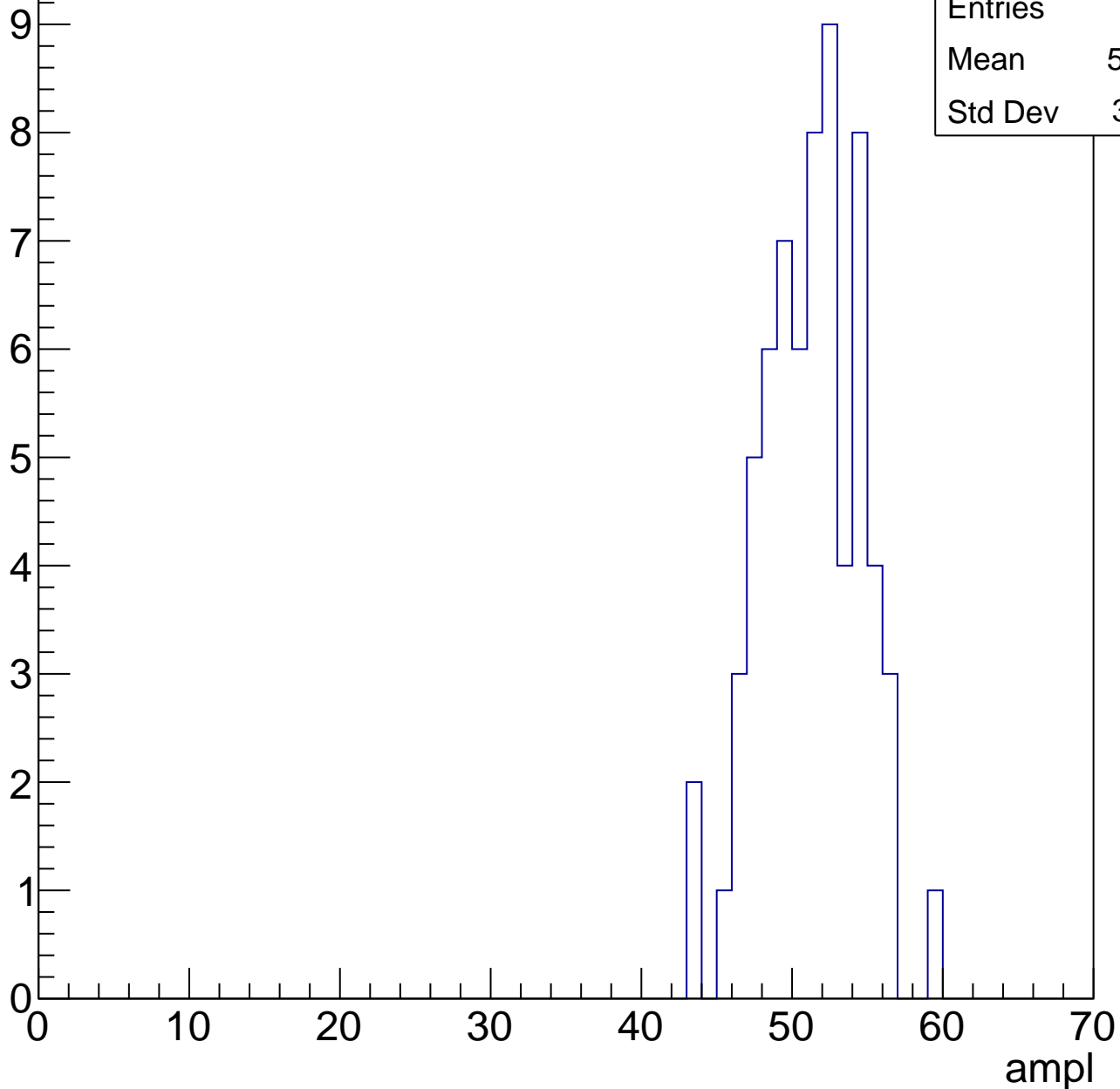


# B0L002S, U8-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.78
Std Dev	3.241



# B0L002S, U8-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	62
Mean	57.02
Std Dev	2.756

Entry

10

8

6

4

2

0

0

10

20

30

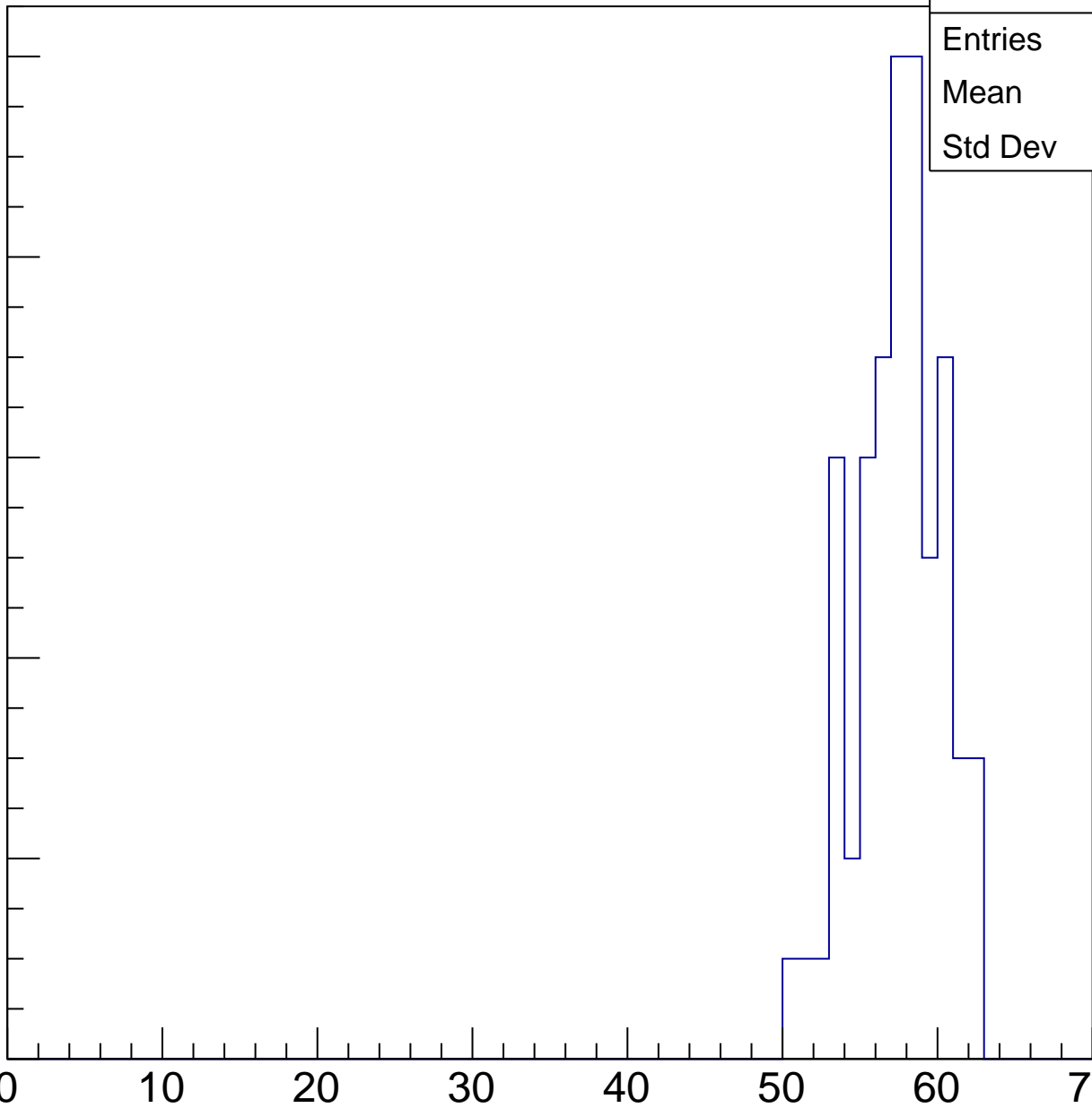
40

50

60

70

ampl

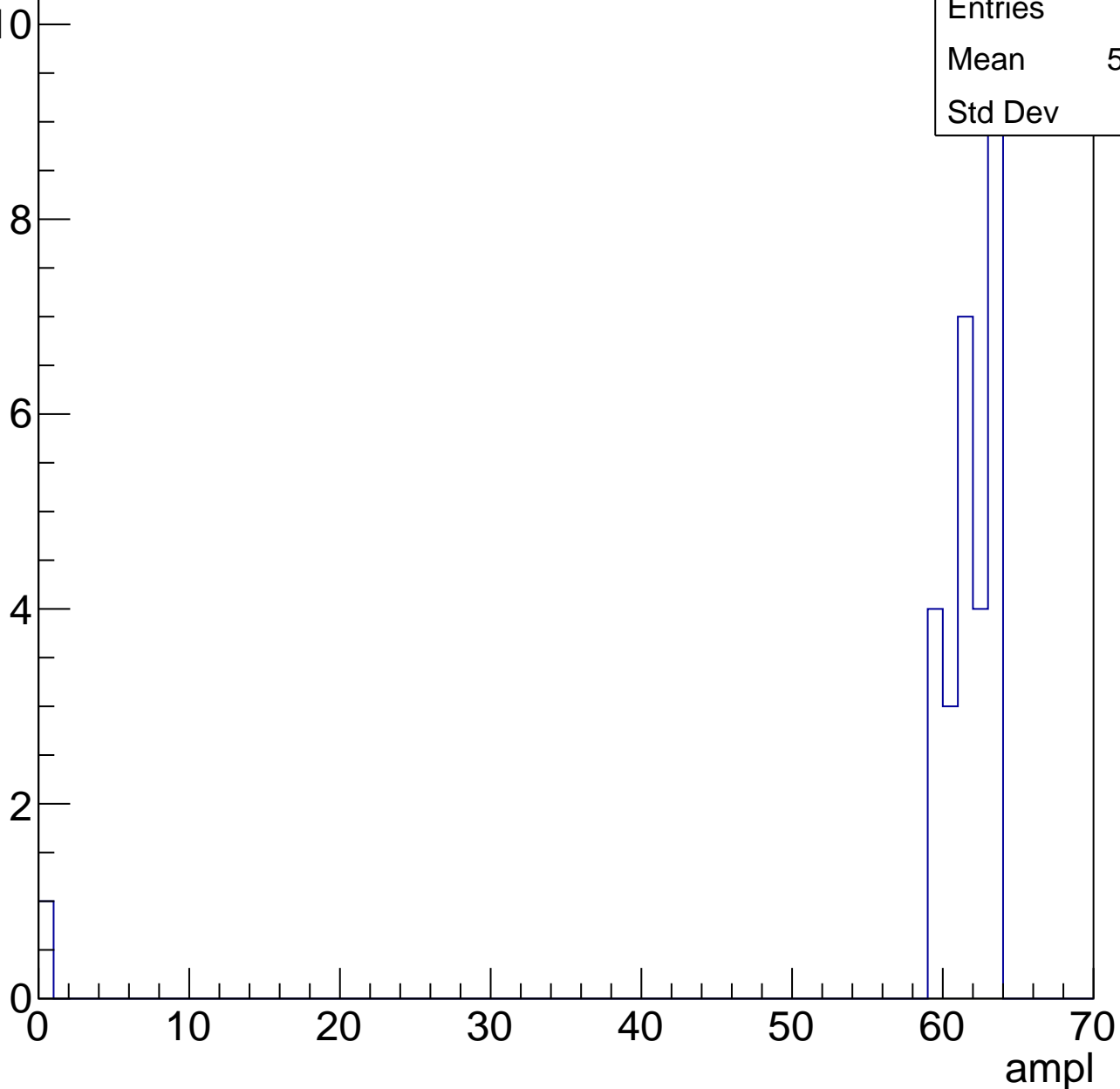


# B0L002S, U8-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.34
Std Dev	11.3



# B0L002S, U8-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch7, adc0

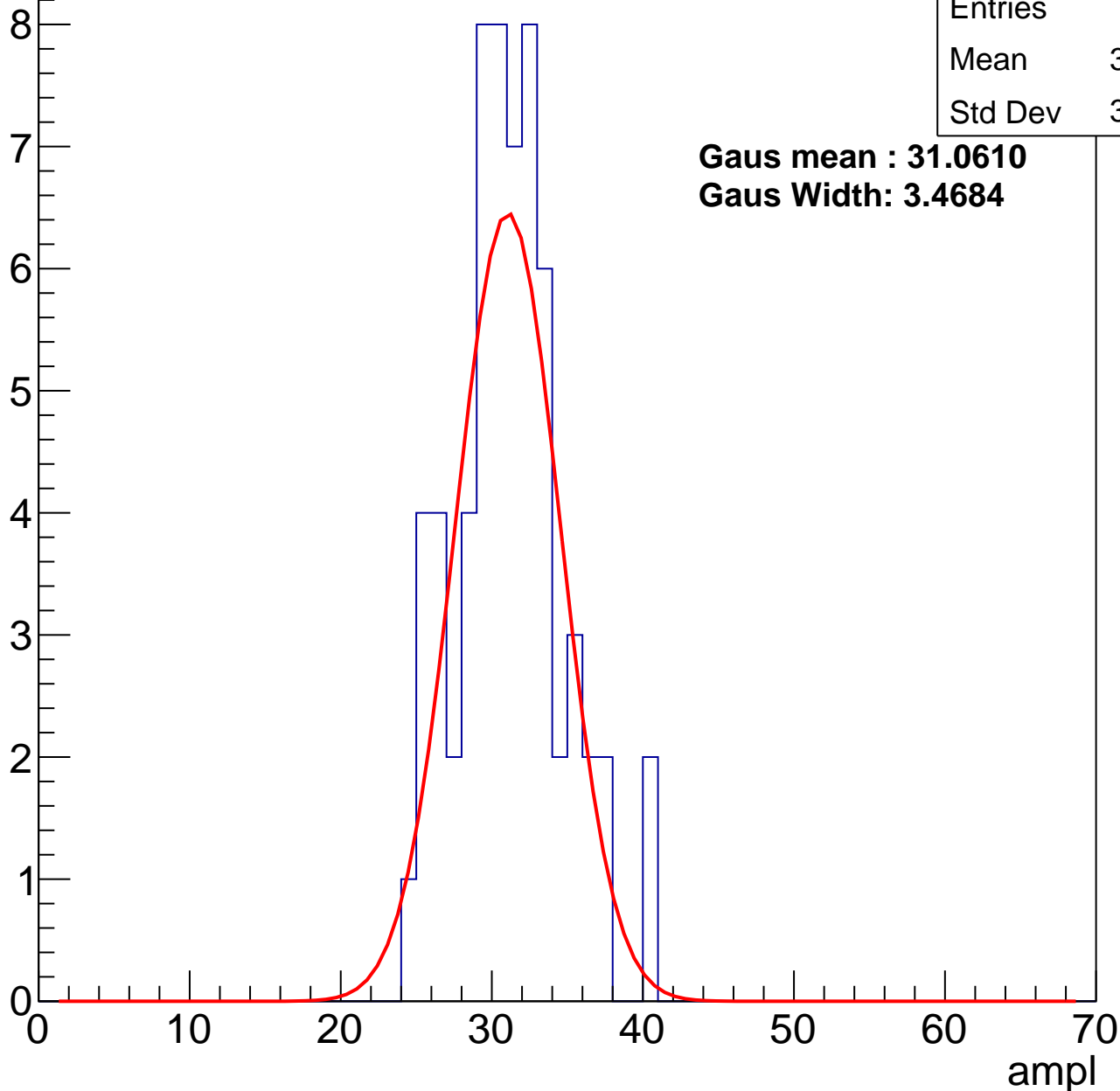
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.73
Std Dev	3.515

**Gaus mean : 31.0610**

**Gaus Width: 3.4684**



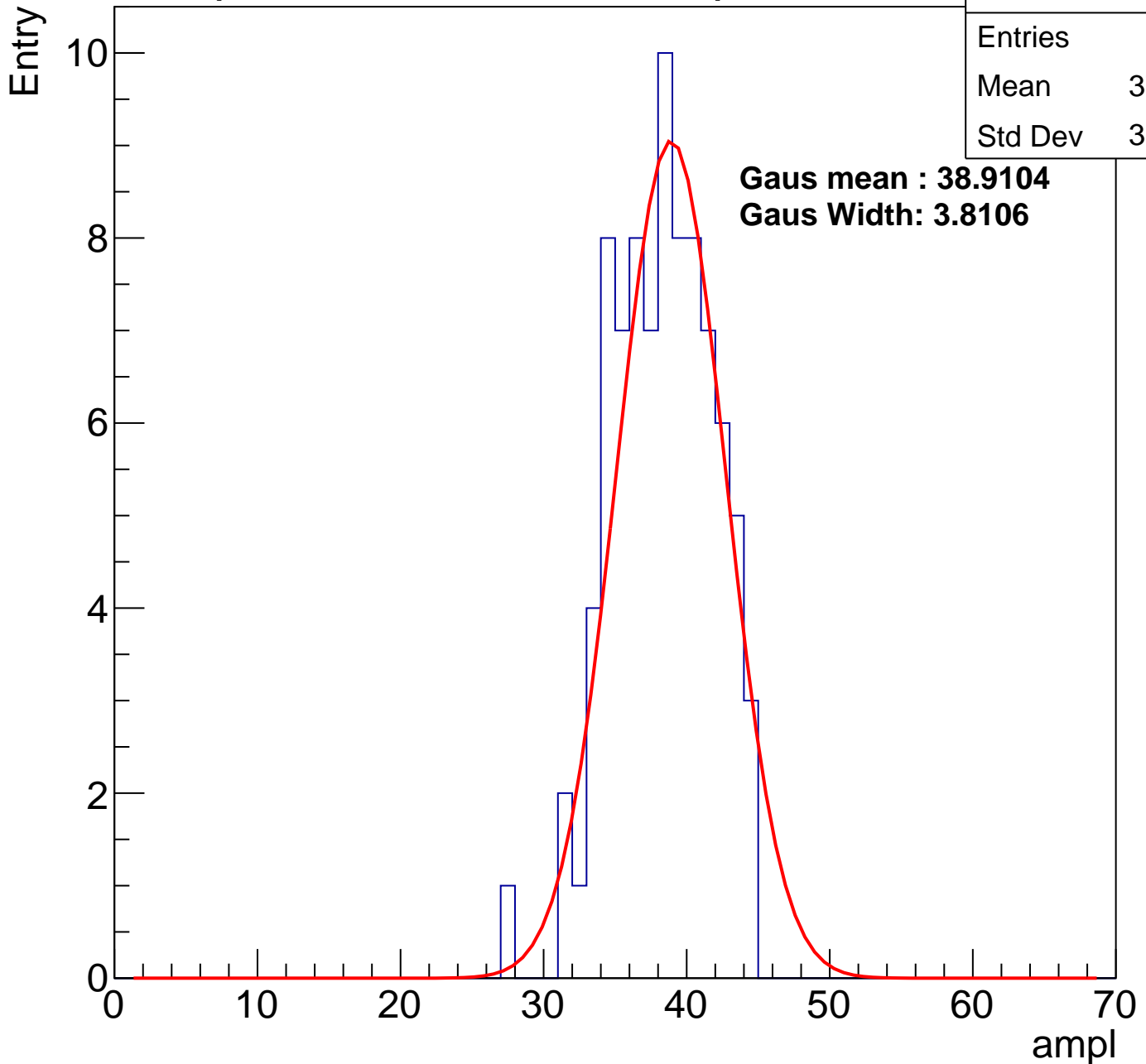
# B0L002S, U8-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	37.82
Std Dev	3.454

**Gaus mean : 38.9104**

**Gaus Width: 3.8106**



# B0L002S, U8-ch7, adc2

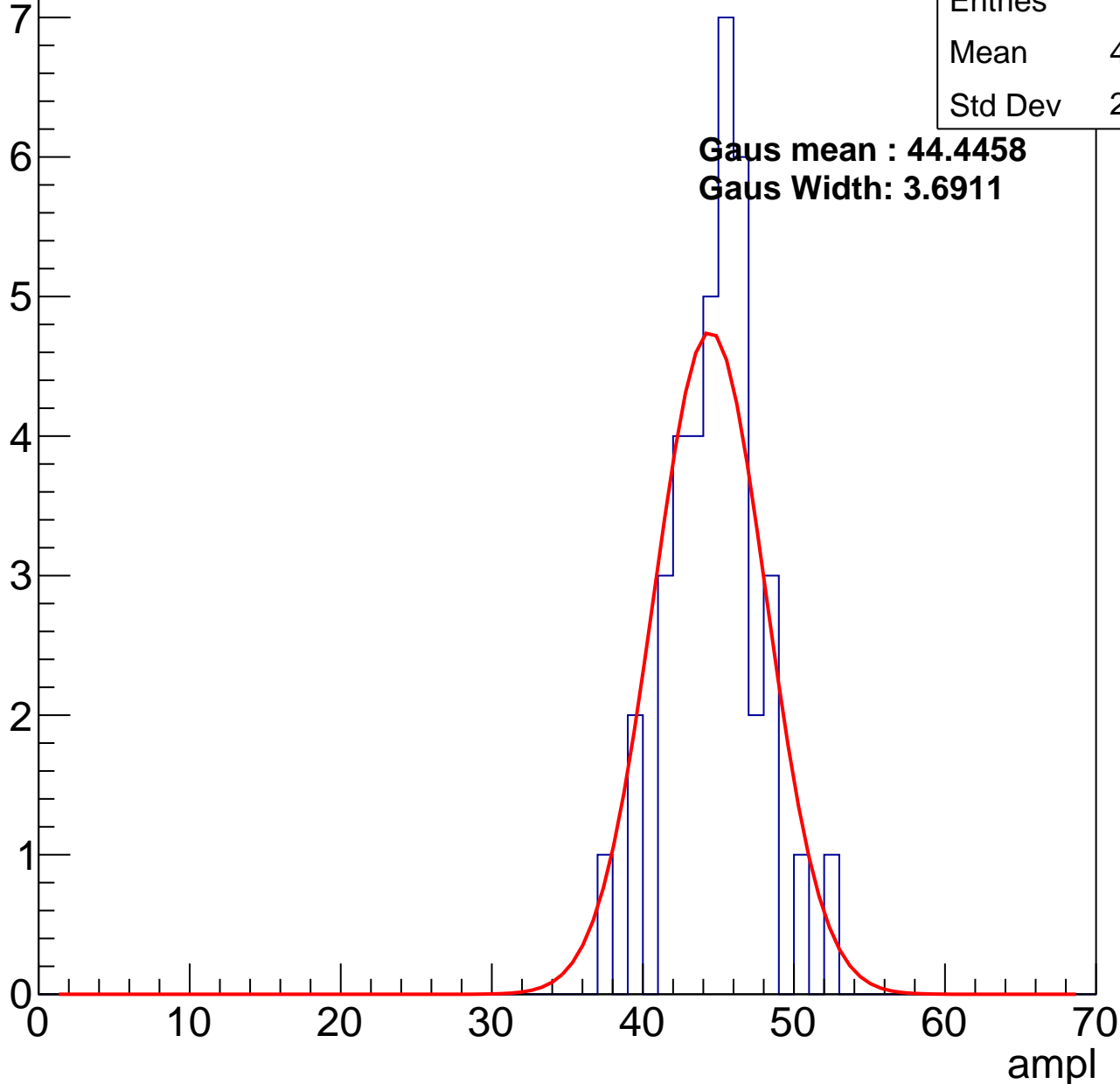
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	44.33
Std Dev	2.947

**Gaus mean : 44.4458**

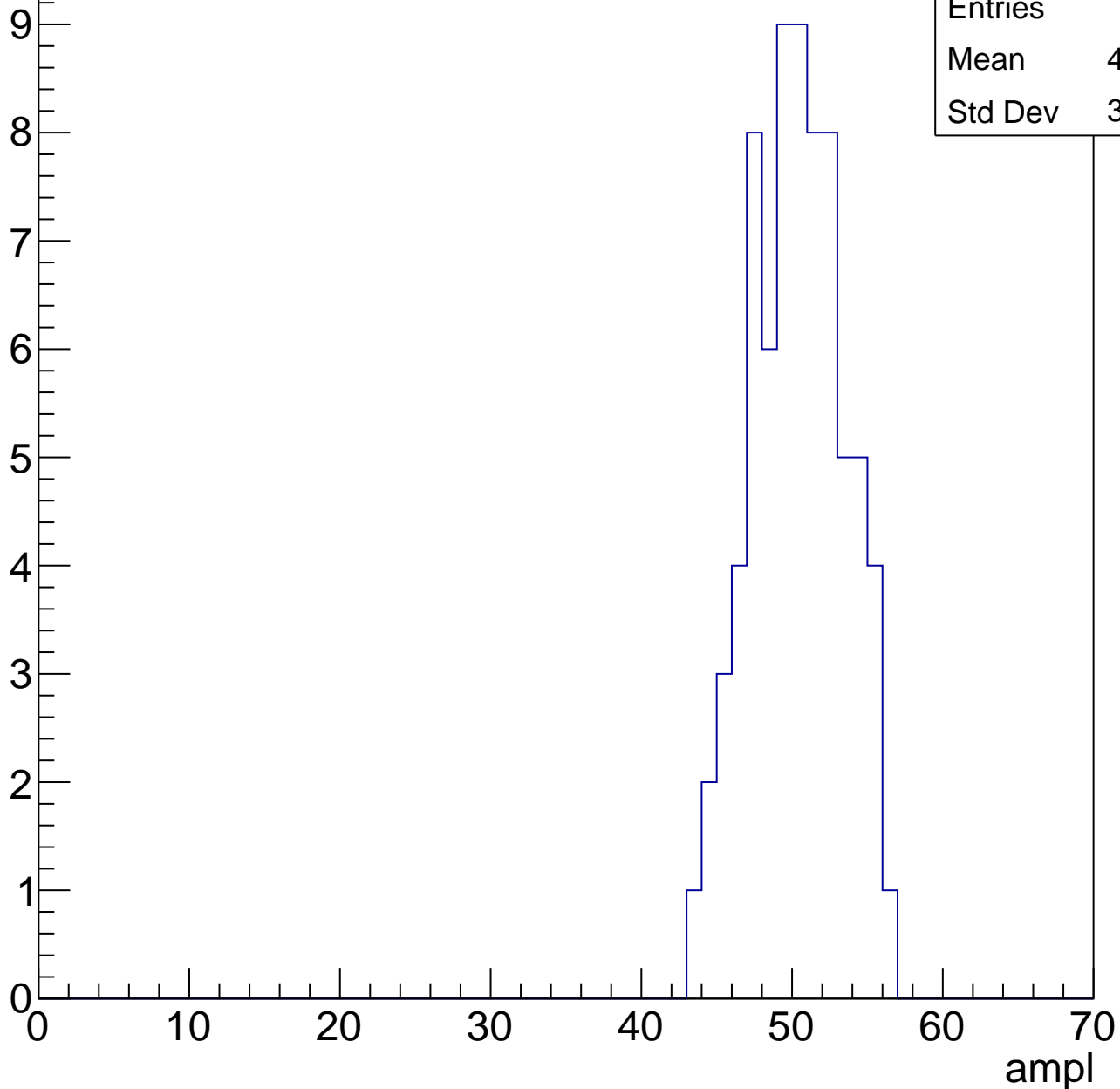
**Gaus Width: 3.6911**



# B0L002S, U8-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



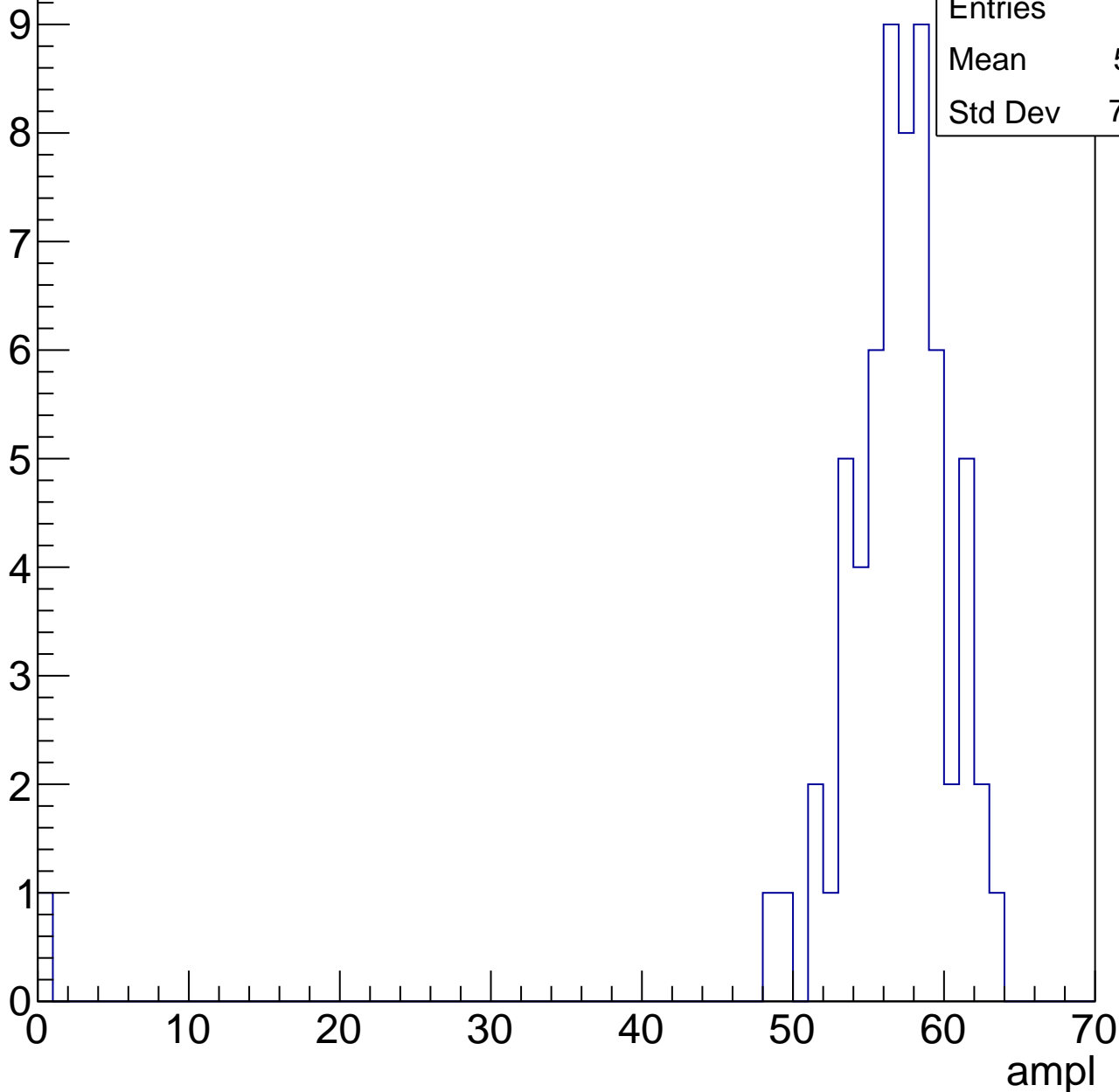
Entries	73
Mean	49.86
Std Dev	3.017

# B0L002S, U8-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	55.71
Std Dev	7.718



# B0L002S, U8-ch7, adc5

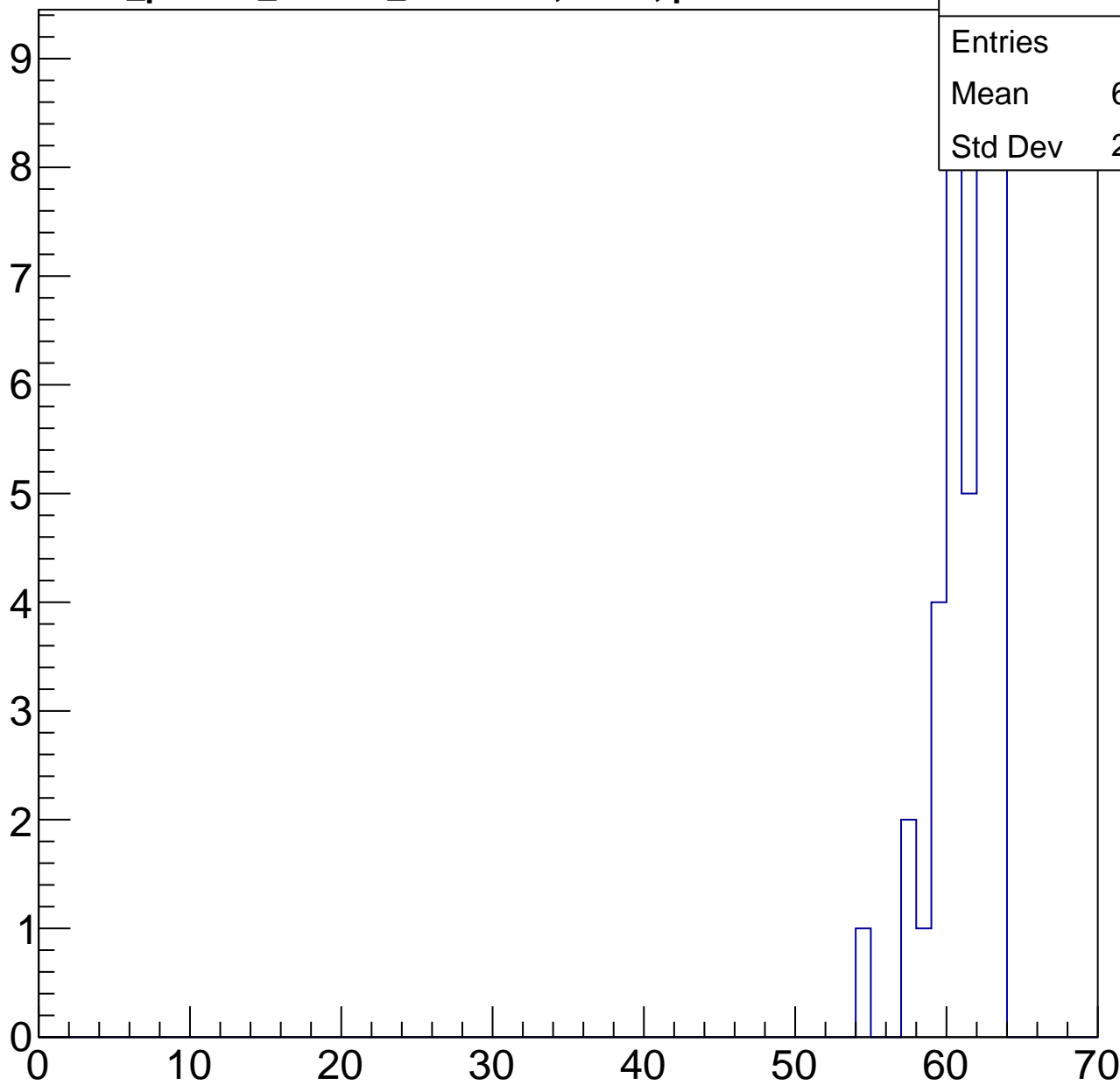
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	60.79
Std Dev	2.028

ampl



# B0L002S, U8-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch8, adc0

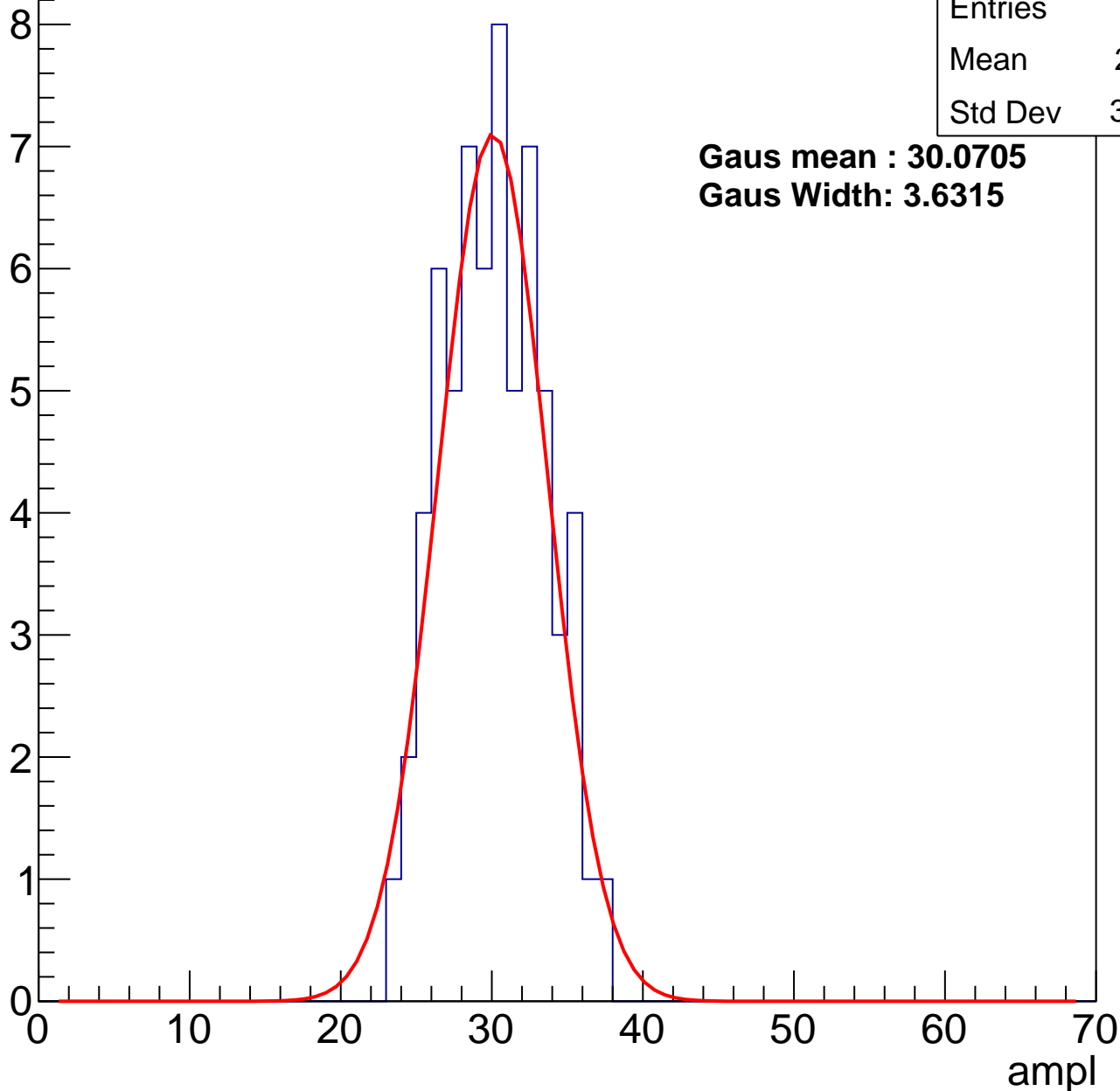
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.71
Std Dev	3.285

**Gaus mean : 30.0705**

**Gaus Width: 3.6315**

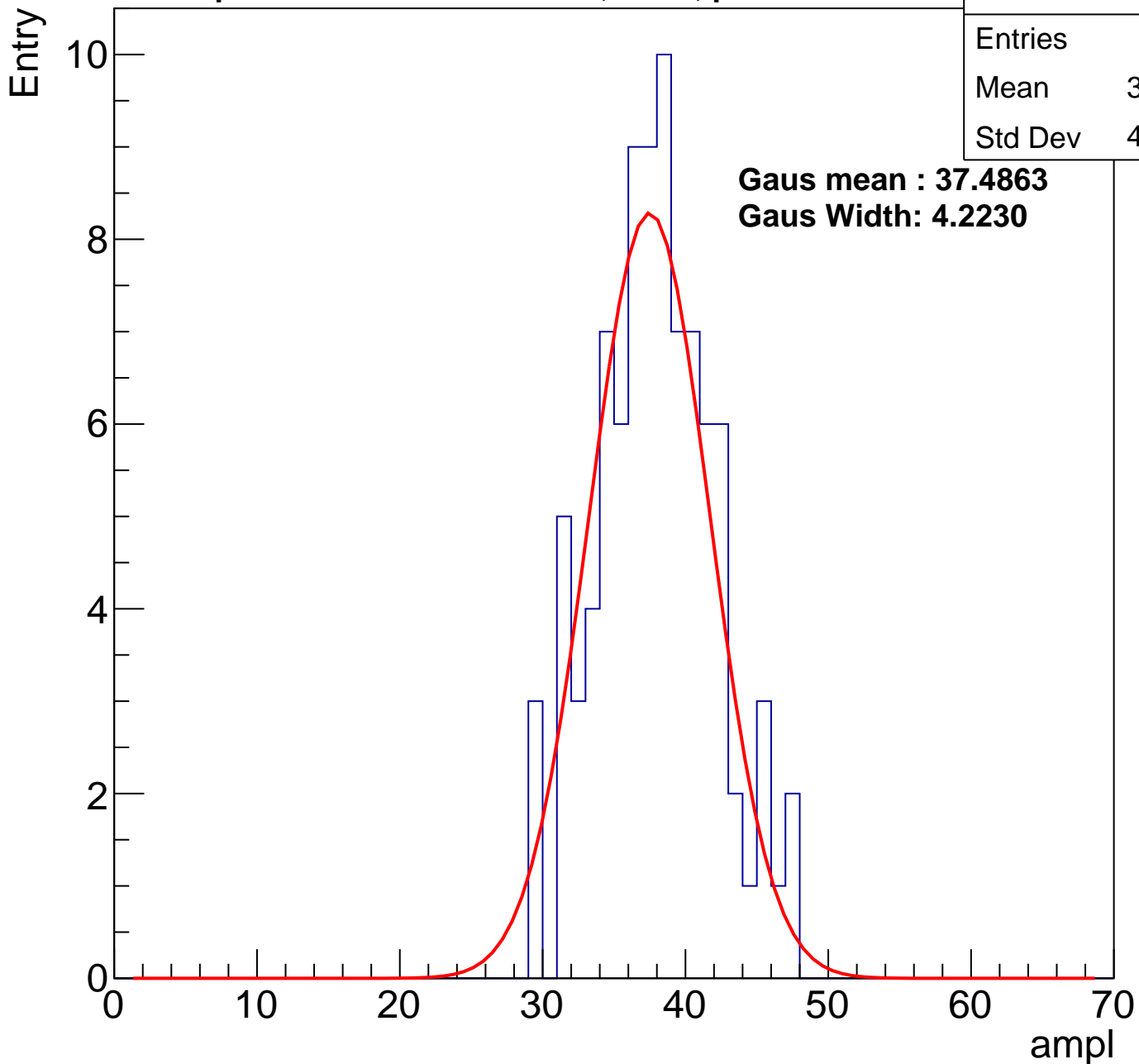


# B0L002S, U8-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	91
Mean	37.48
Std Dev	4.096

**Gaus mean : 37.4863**  
**Gaus Width: 4.2230**



# B0L002S, U8-ch8, adc2

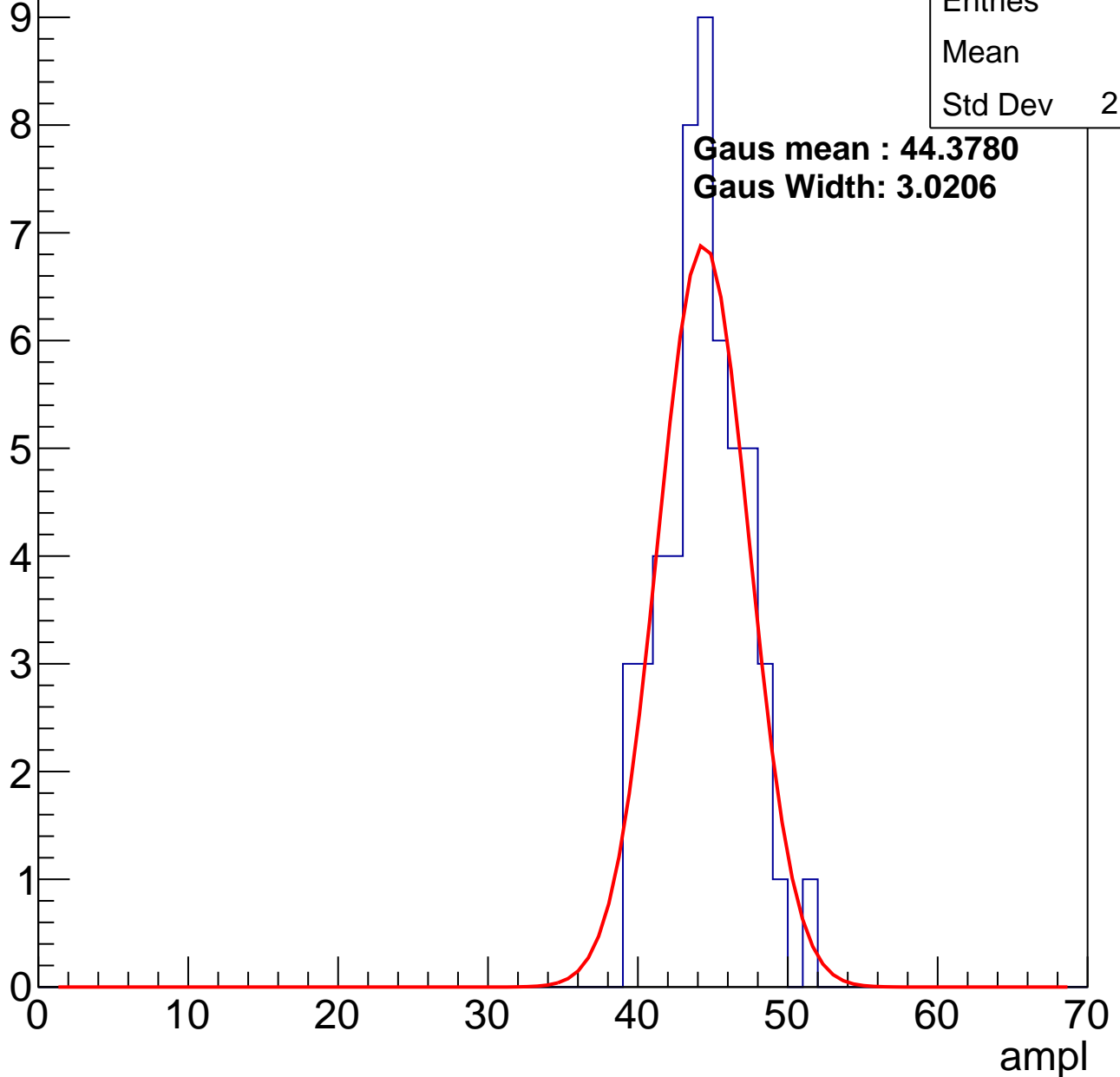
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44
Std Dev	2.689

**Gaus mean : 44.3780**

**Gaus Width: 3.0206**

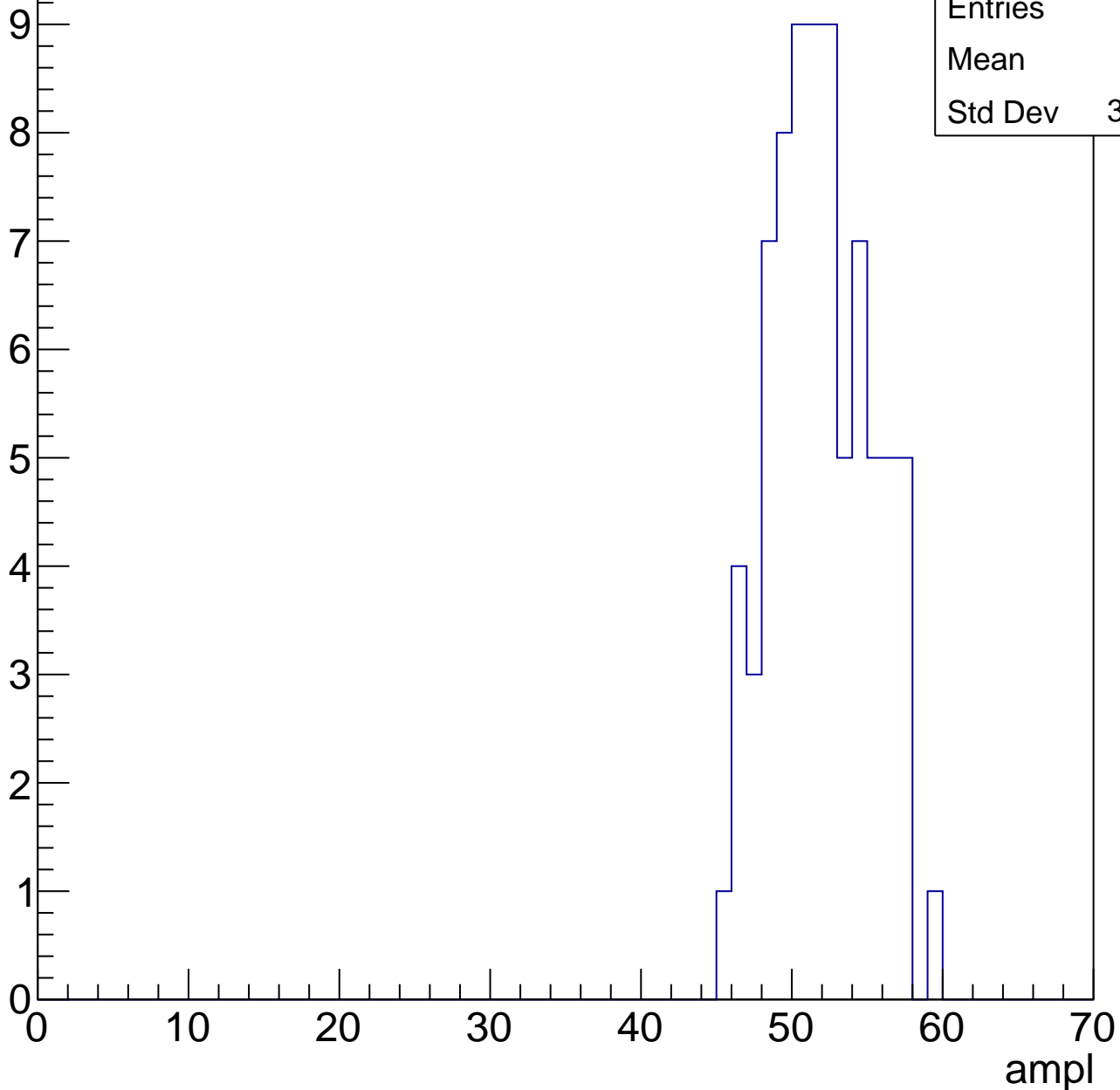


# B0L002S, U8-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

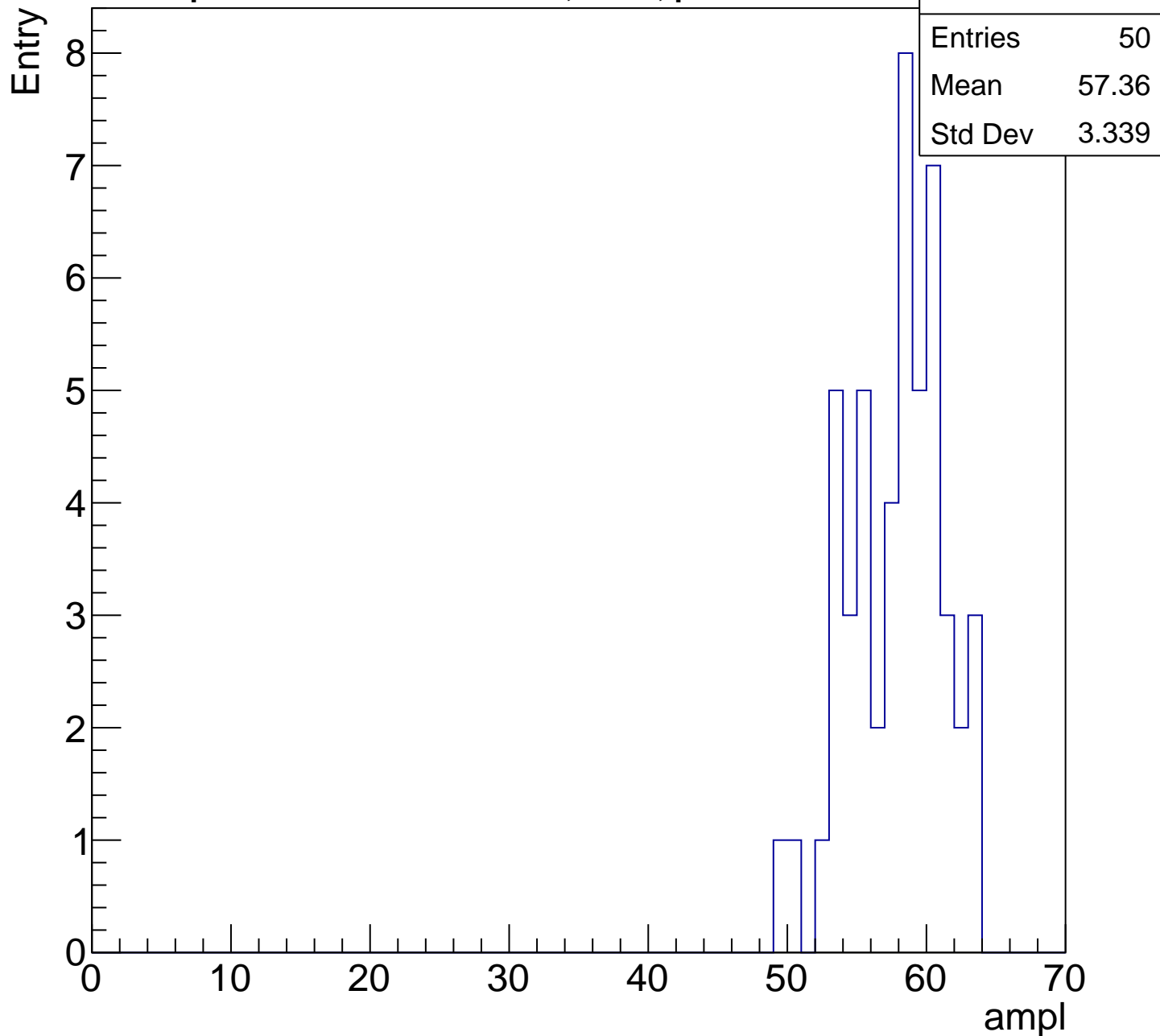
Entry

Entries	78
Mean	51.5
Std Dev	3.222



# B0L002S, U8-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

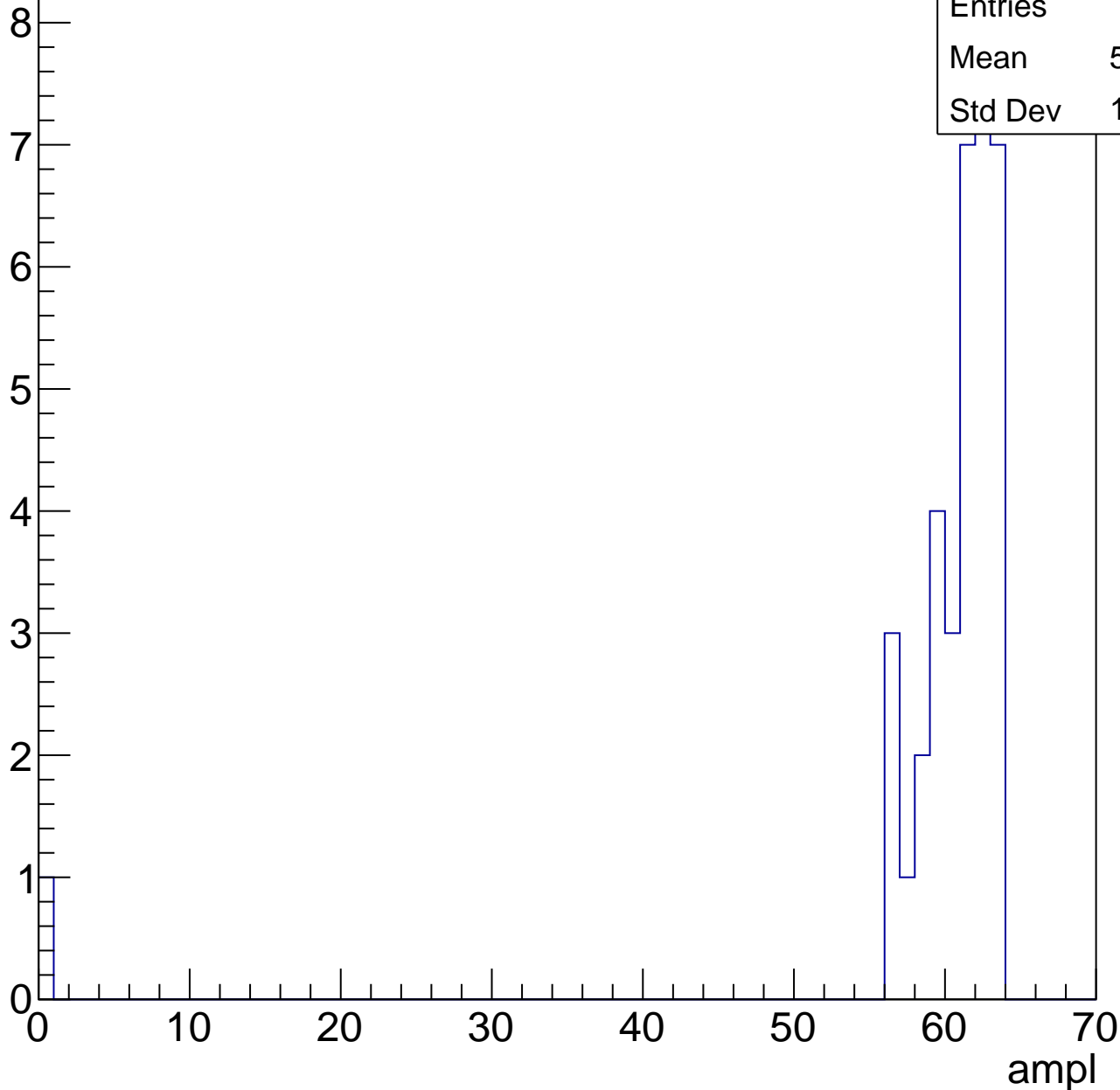


# B0L002S, U8-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.92
Std Dev	10.18



# B0L002S, U8-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



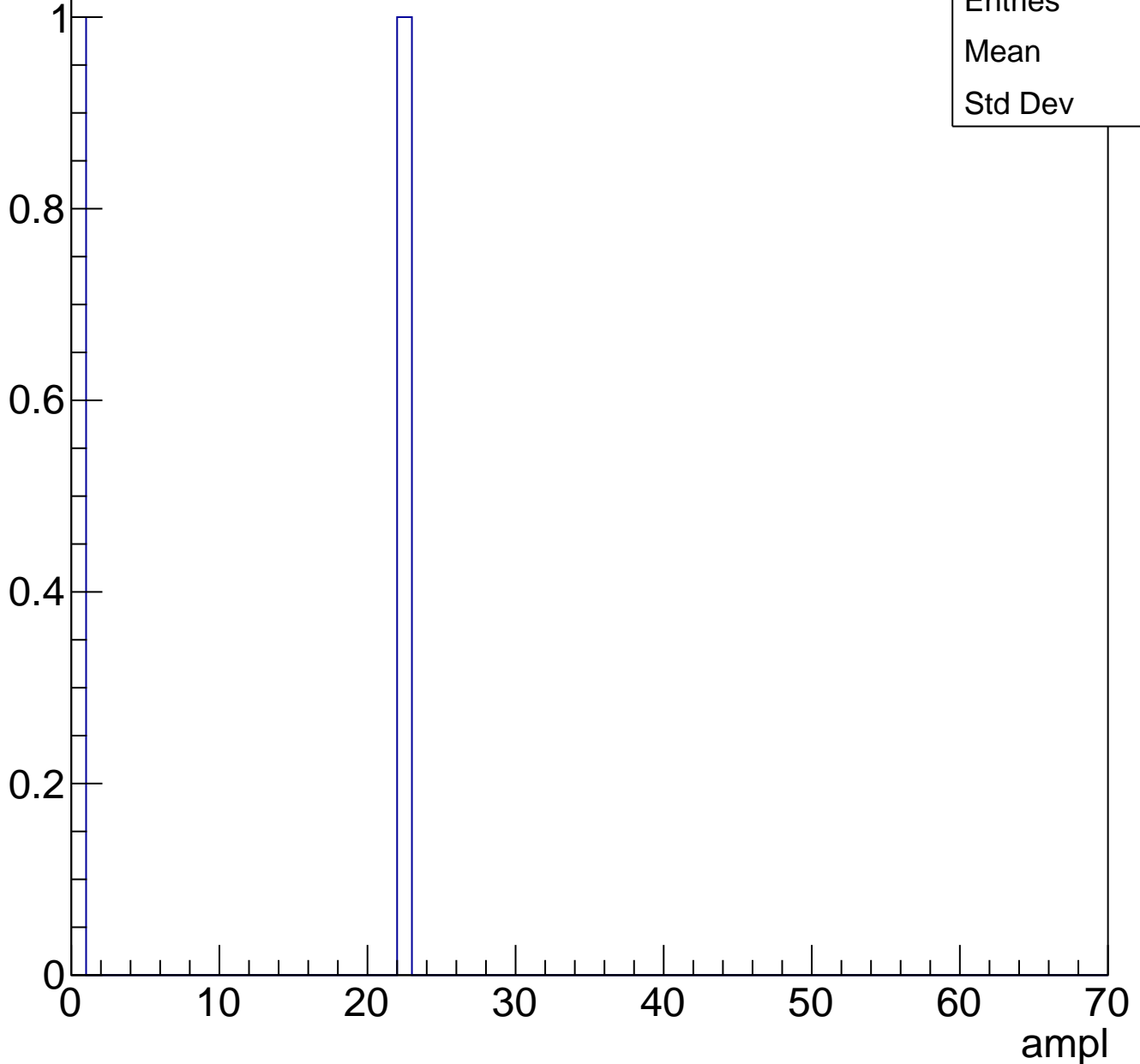
Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U8-ch9, adc0

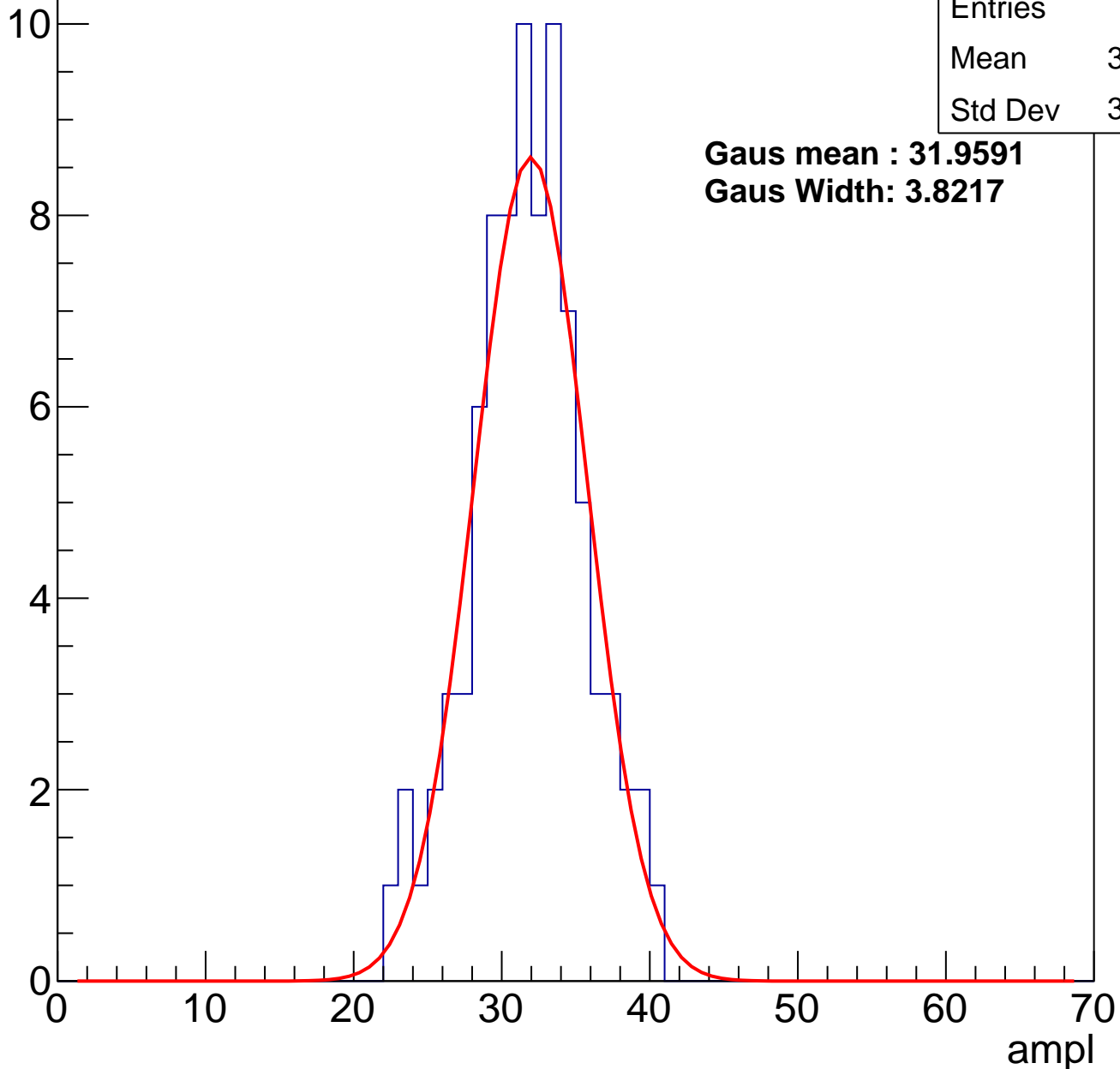
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	31.33
Std Dev	3.787

**Gaus mean : 31.9591**

**Gaus Width: 3.8217**

Entry



# B0L002S, U8-ch9, adc1

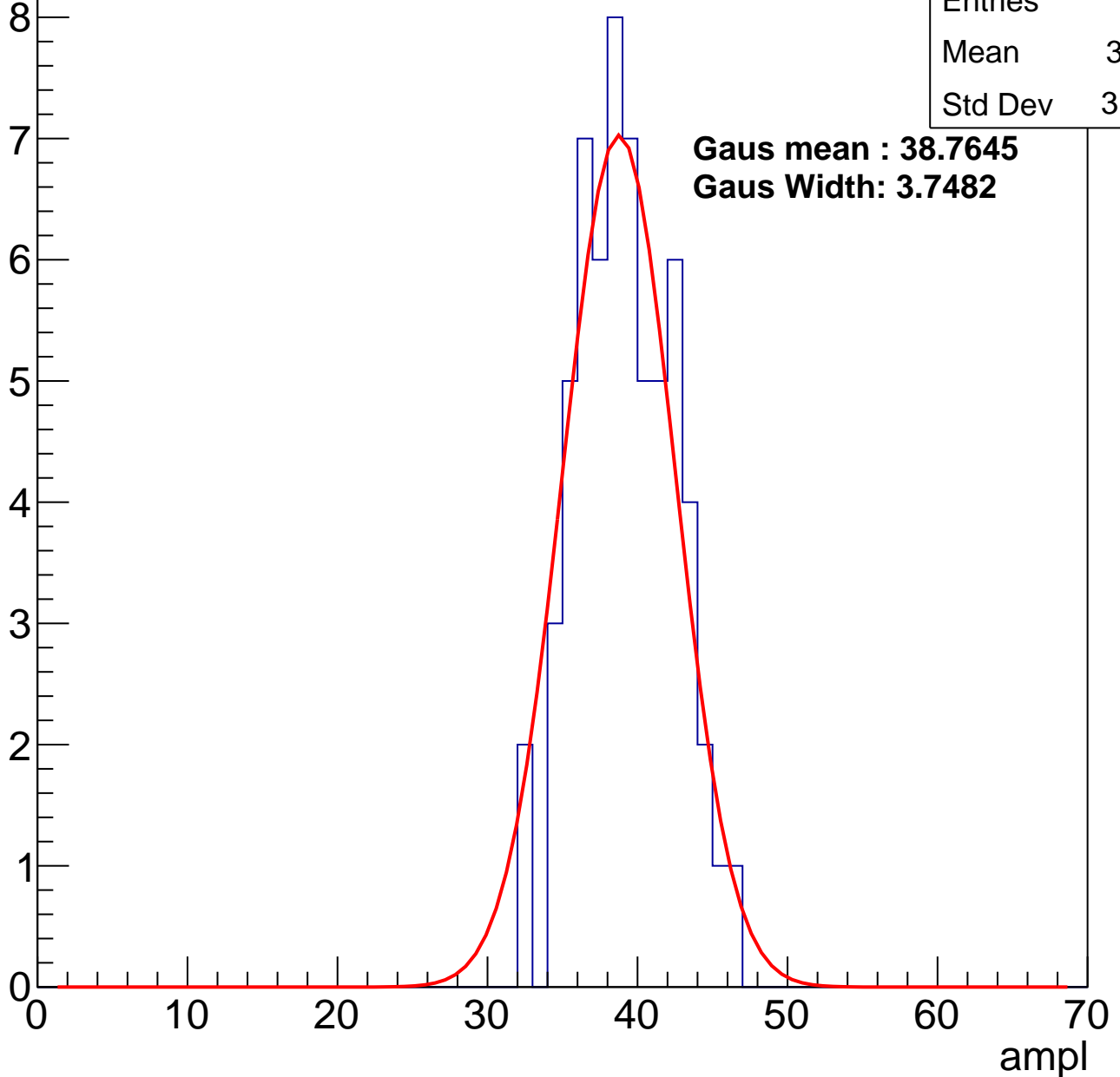
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.71
Std Dev	3.164

**Gaus mean : 38.7645**

**Gaus Width: 3.7482**



# B0L002S, U8-ch9, adc2

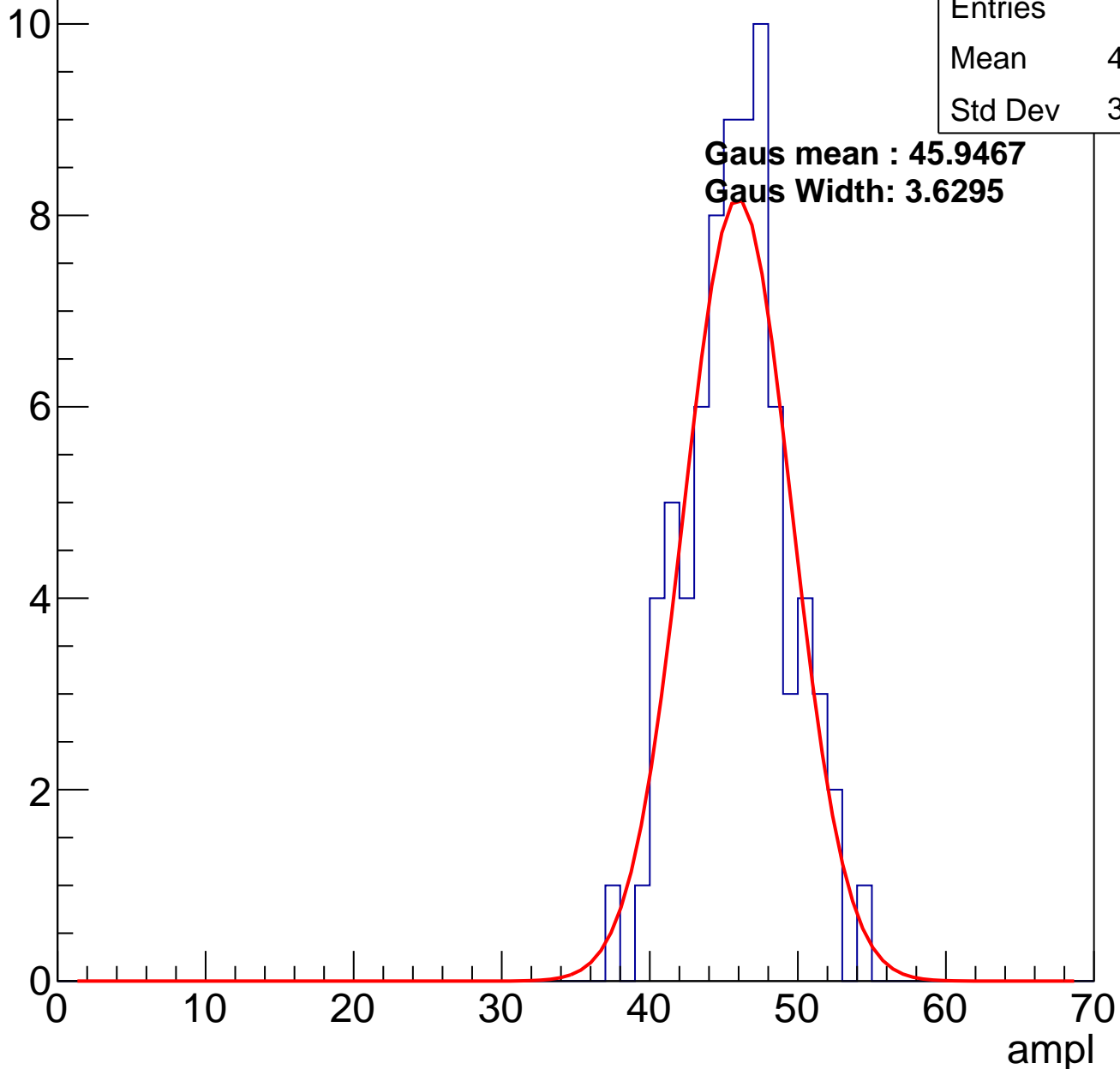
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	76
Mean	45.45
Std Dev	3.385

**Gaus mean : 45.9467**

**Gaus Width: 3.6295**

Entry

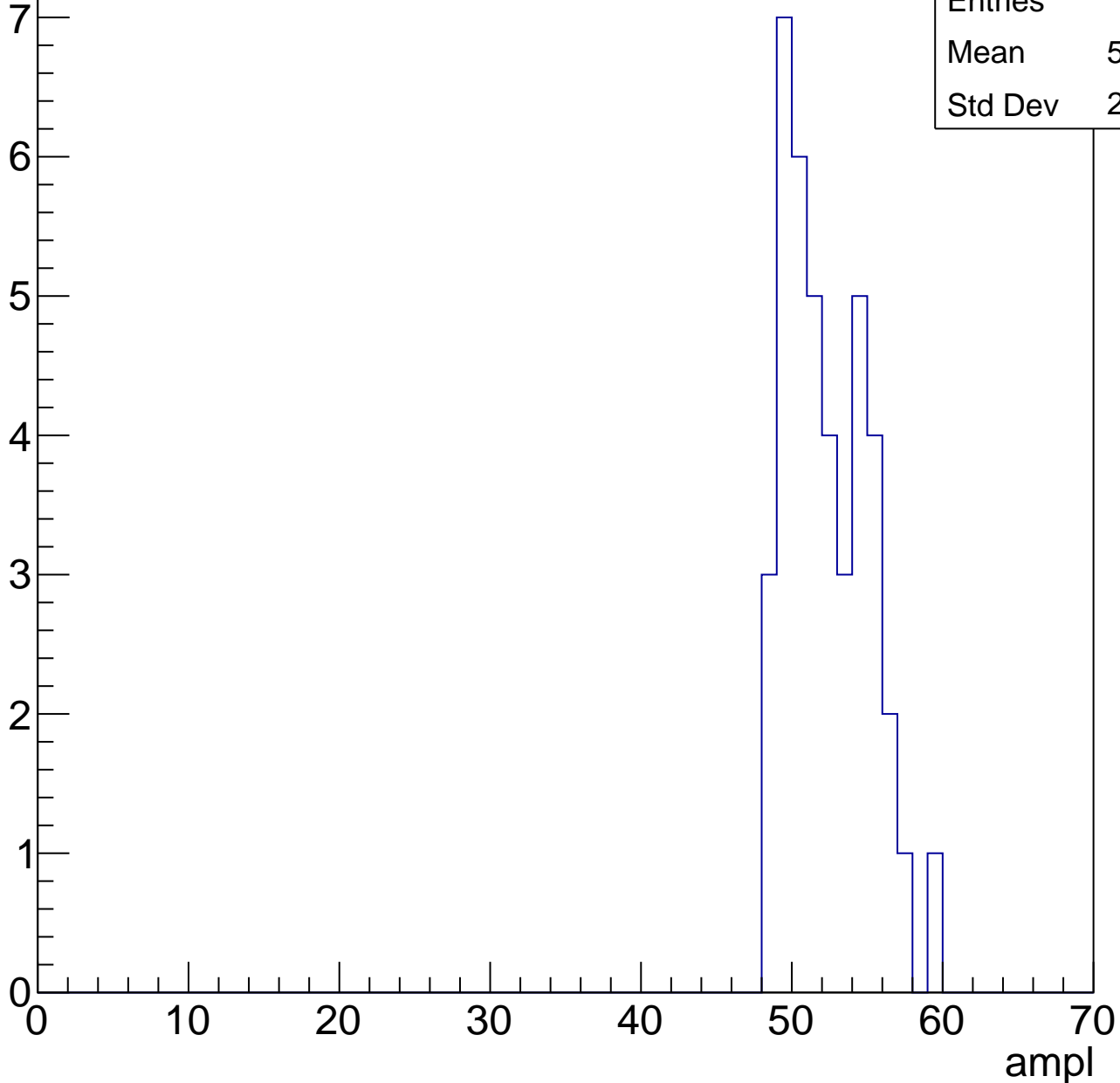


# B0L002S, U8-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	41
Mean	51.88
Std Dev	2.725

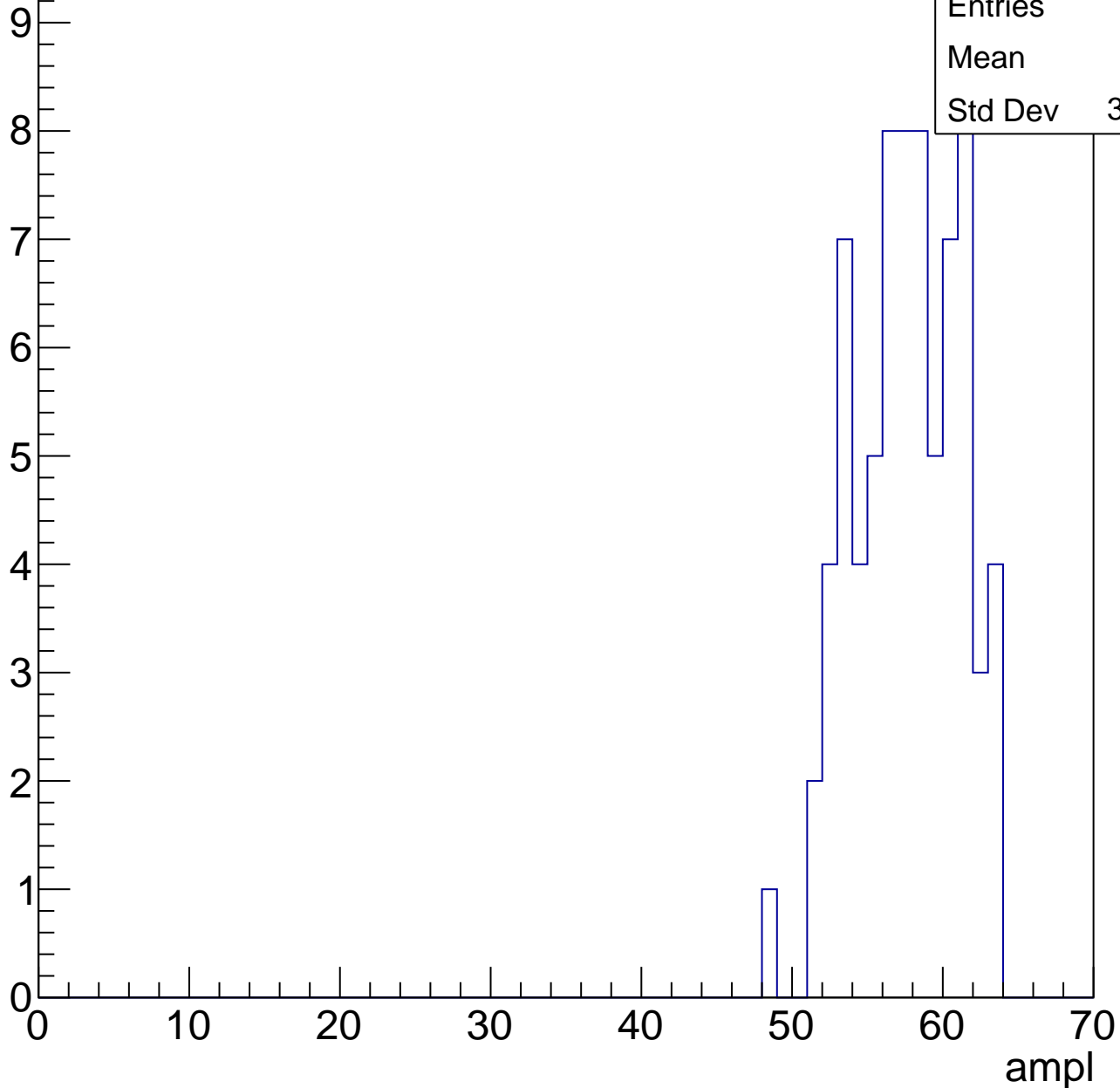


# B0L002S, U8-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	57.2
Std Dev	3.425

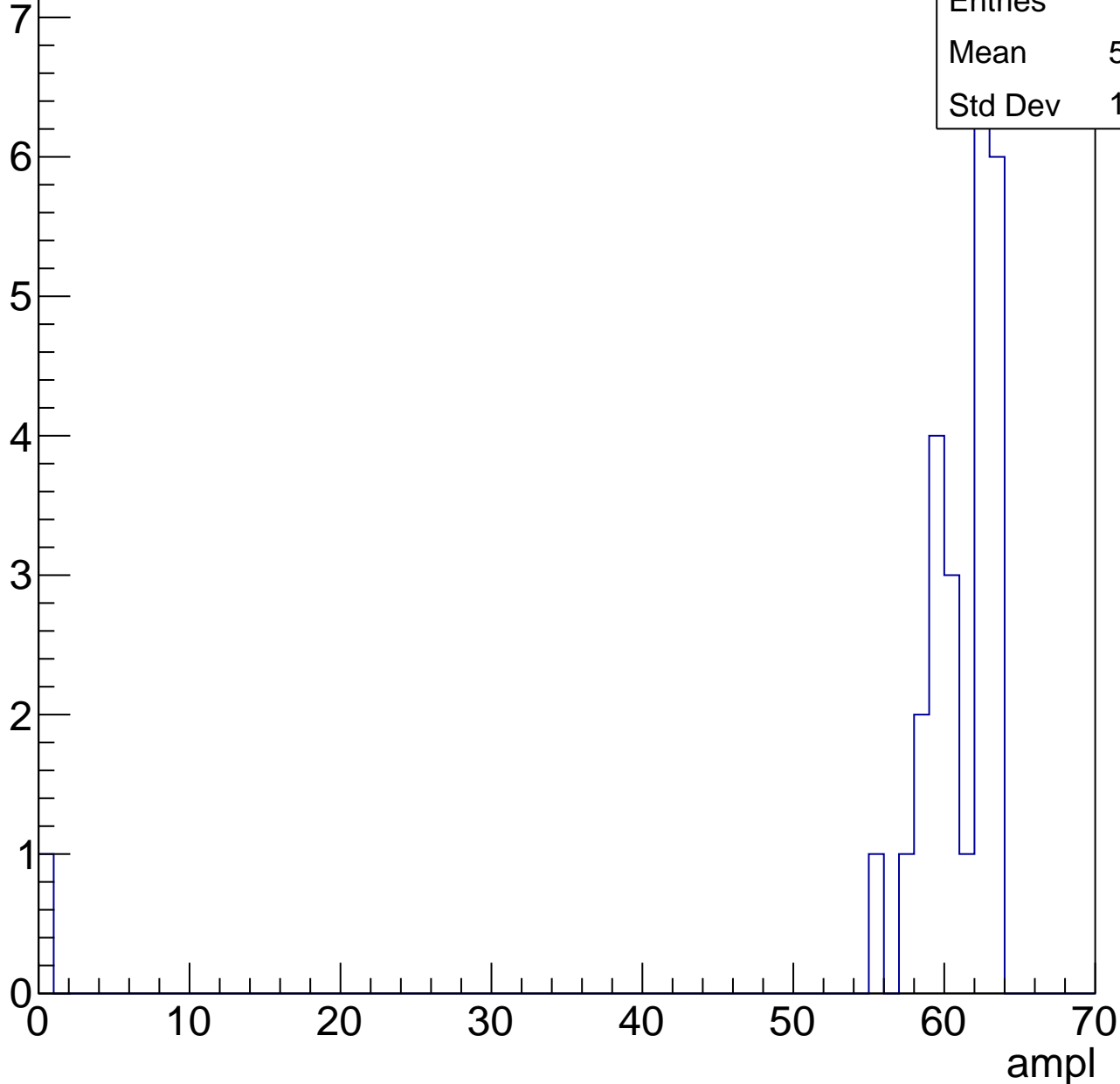


# B0L002S, U8-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.35
Std Dev	11.86



# B0L002S, U8-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch10, adc0

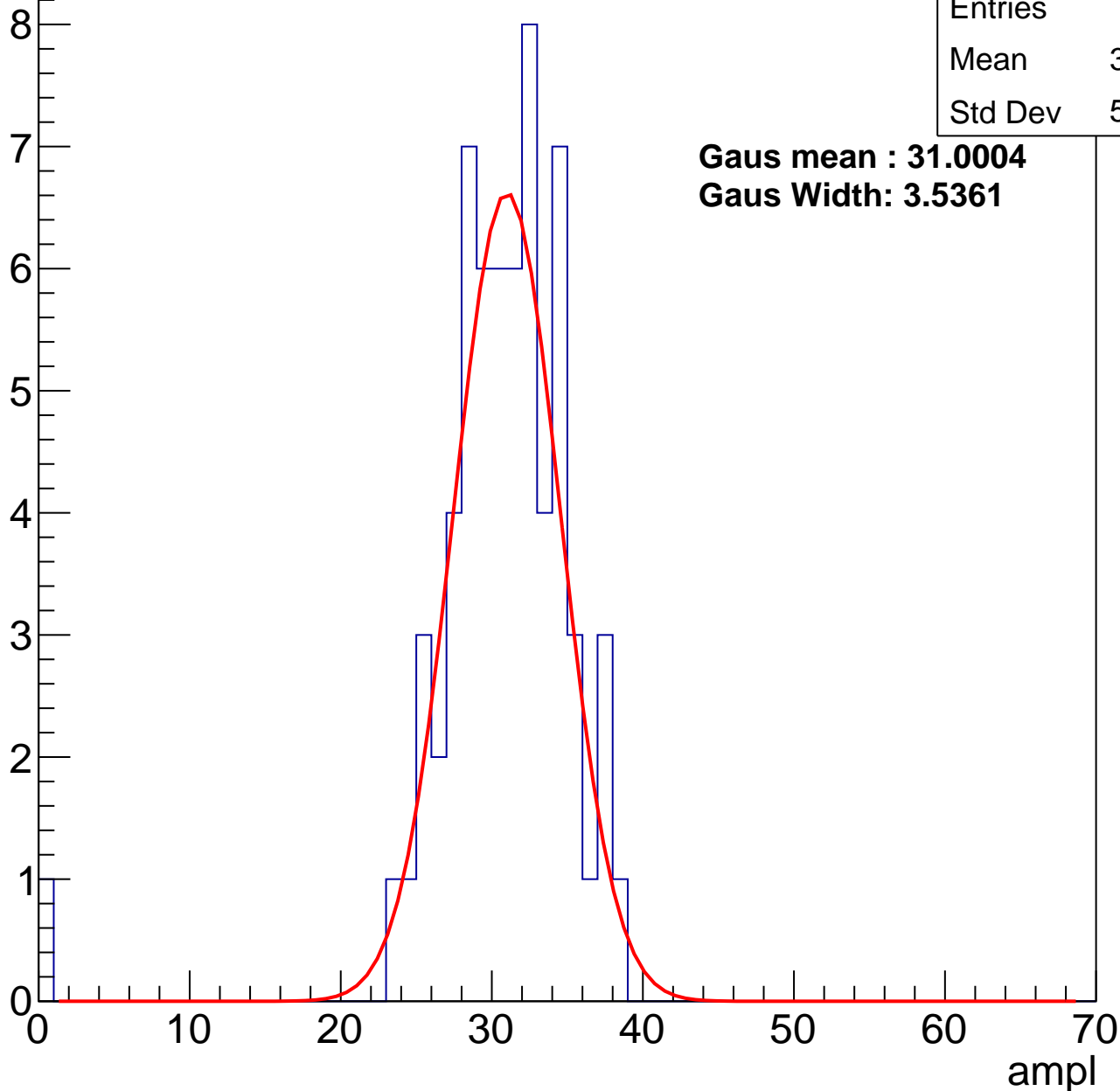
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.22
Std Dev	5.104

**Gaus mean : 31.0004**

**Gaus Width: 3.5361**



# B0L002S, U8-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	92
Mean	38.68
Std Dev	3.796

**Gaus mean : 38.7578**

**Gaus Width: 3.6784**

Entry

10  
8  
6  
4  
2  
0

0

10

20

30

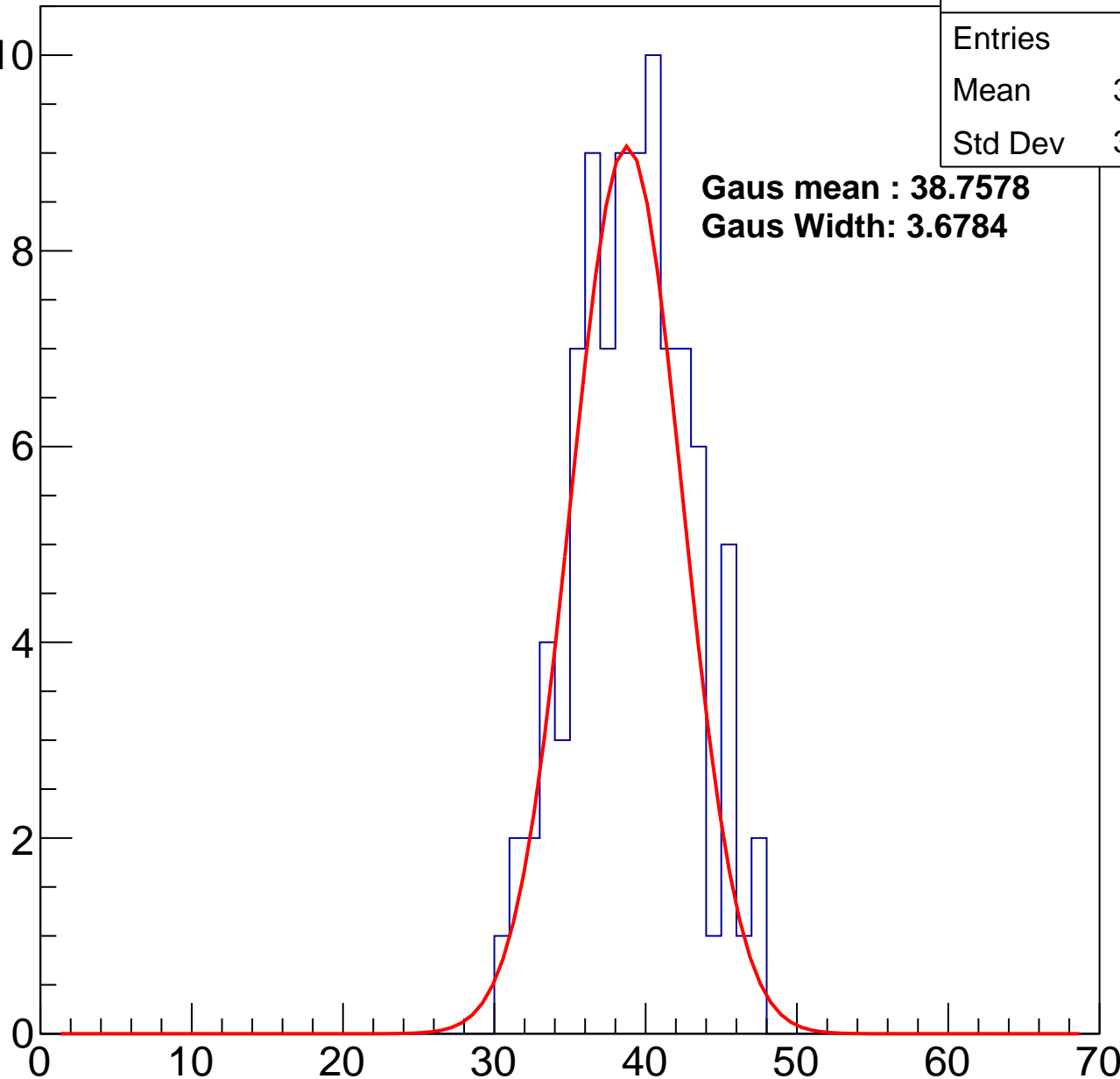
40

50

60

70

ampl



# B0L002S, U8-ch10, adc2

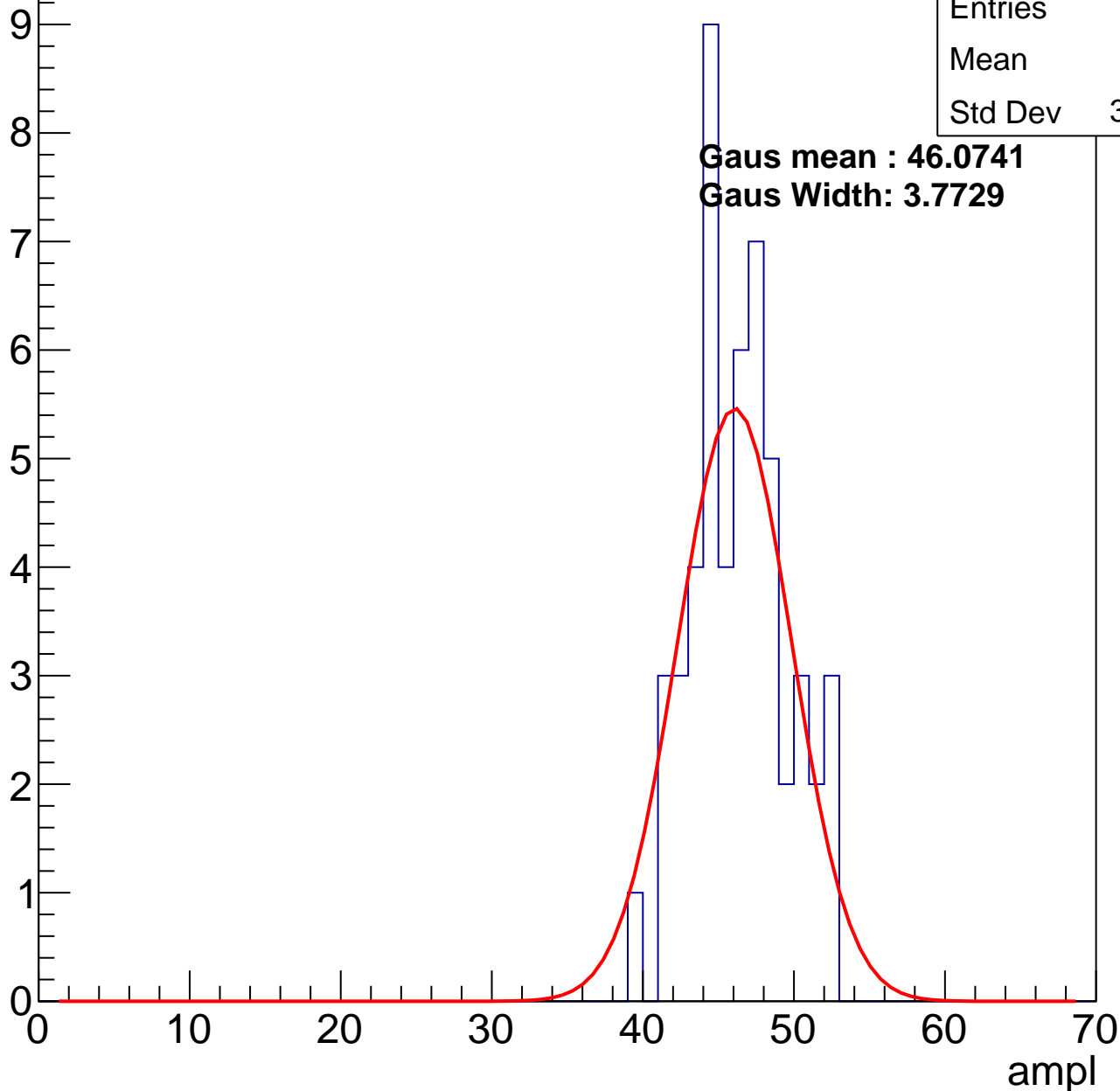
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	45.9
Std Dev	3.096

**Gaus mean : 46.0741**

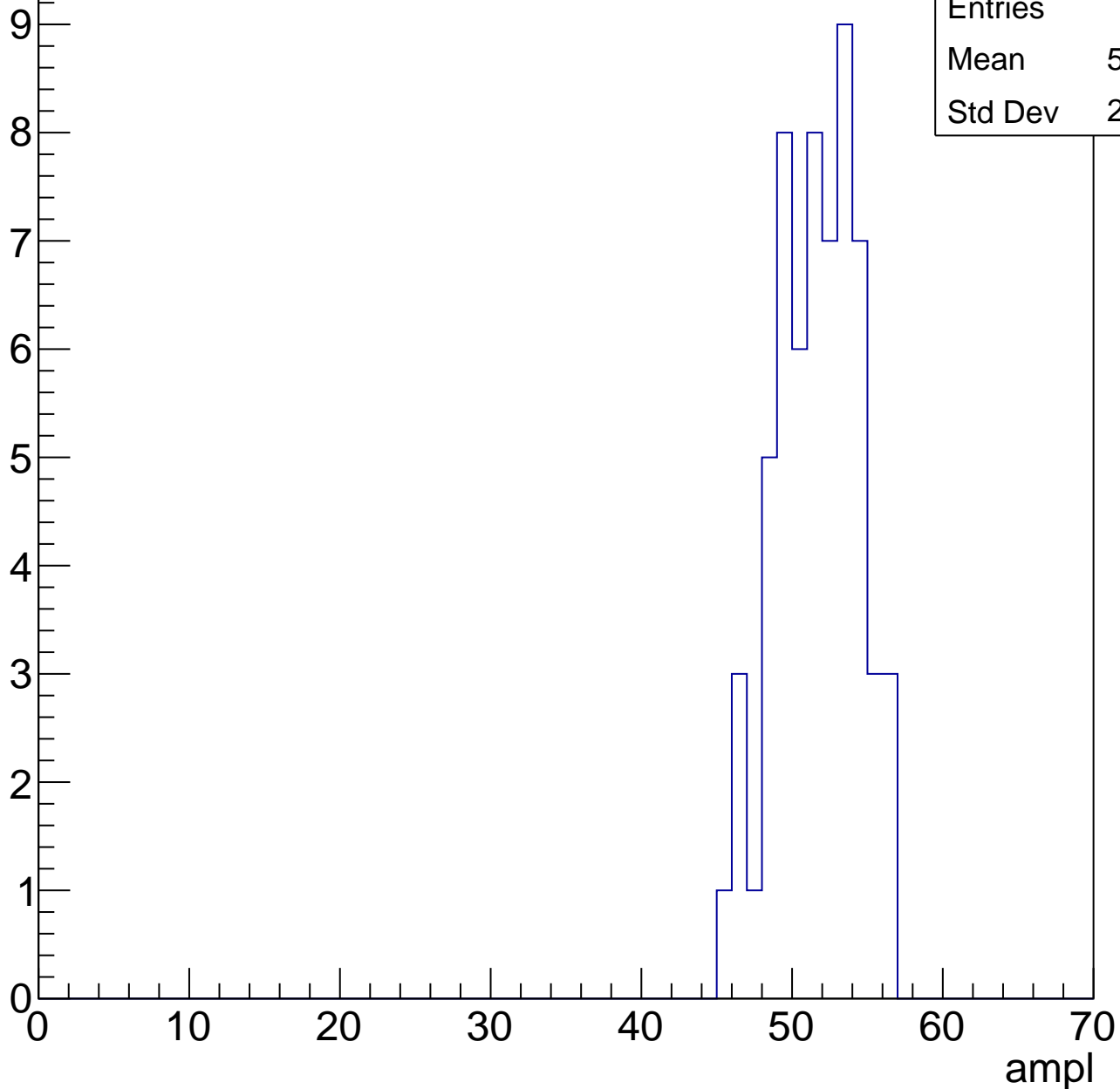
**Gaus Width: 3.7729**



# B0L002S, U8-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



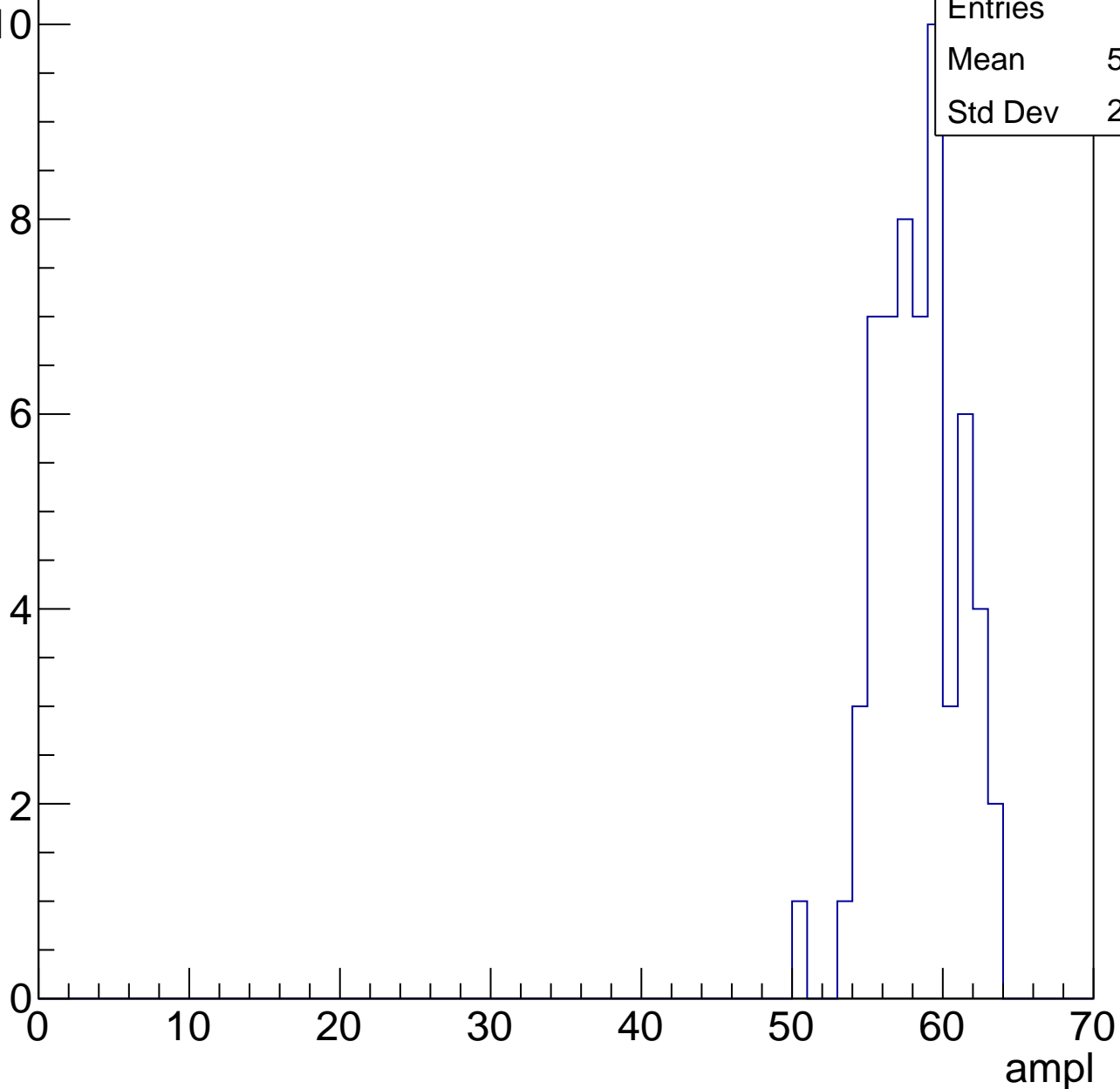
Entries	61
Mean	51.18
Std Dev	2.677

# B0L002S, U8-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

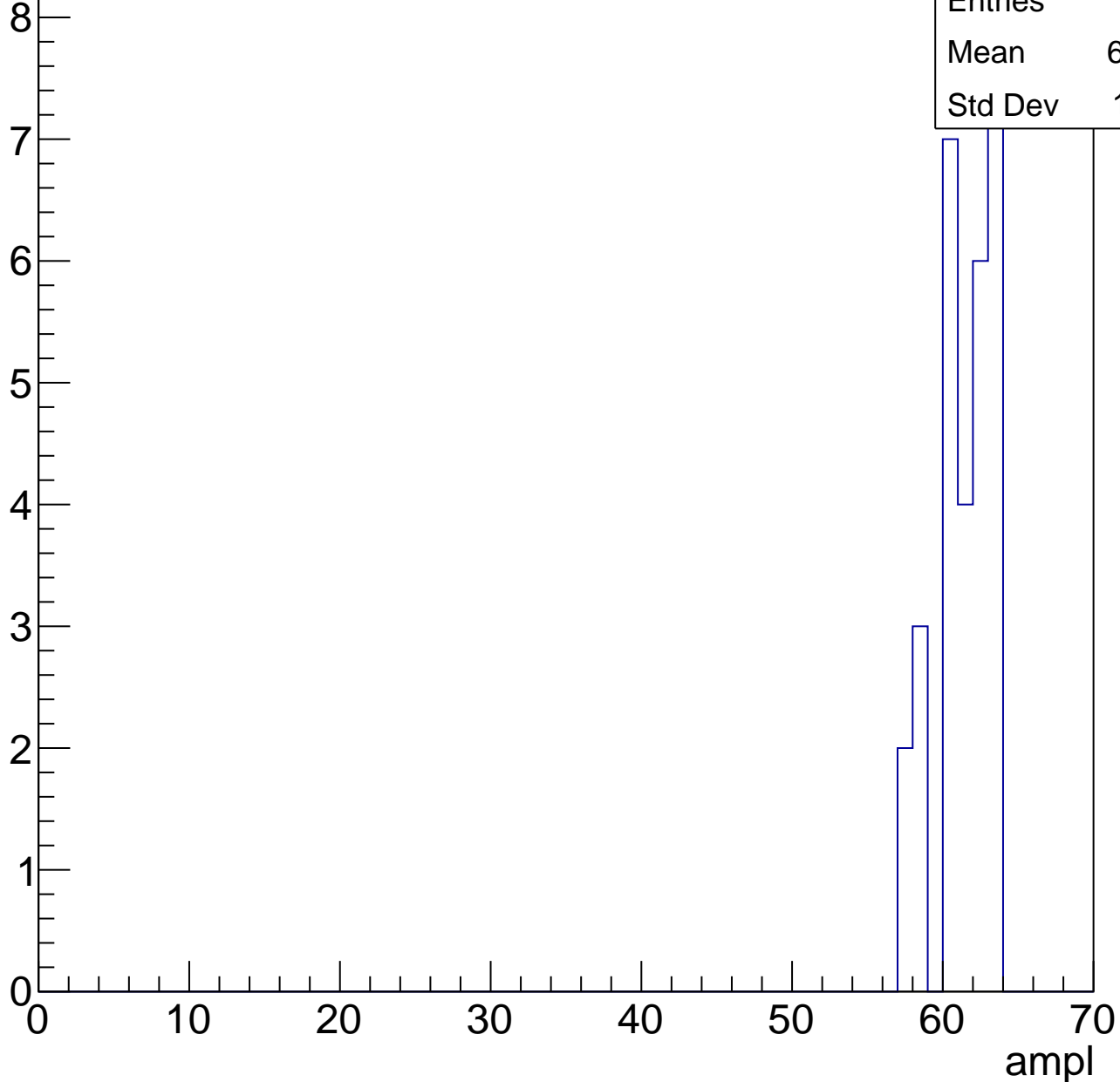
Entries	59
Mean	57.86
Std Dev	2.684



# B0L002S, U8-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch11, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	69
Mean	31
Std Dev	3.608

**Gaus mean : 31.5316**

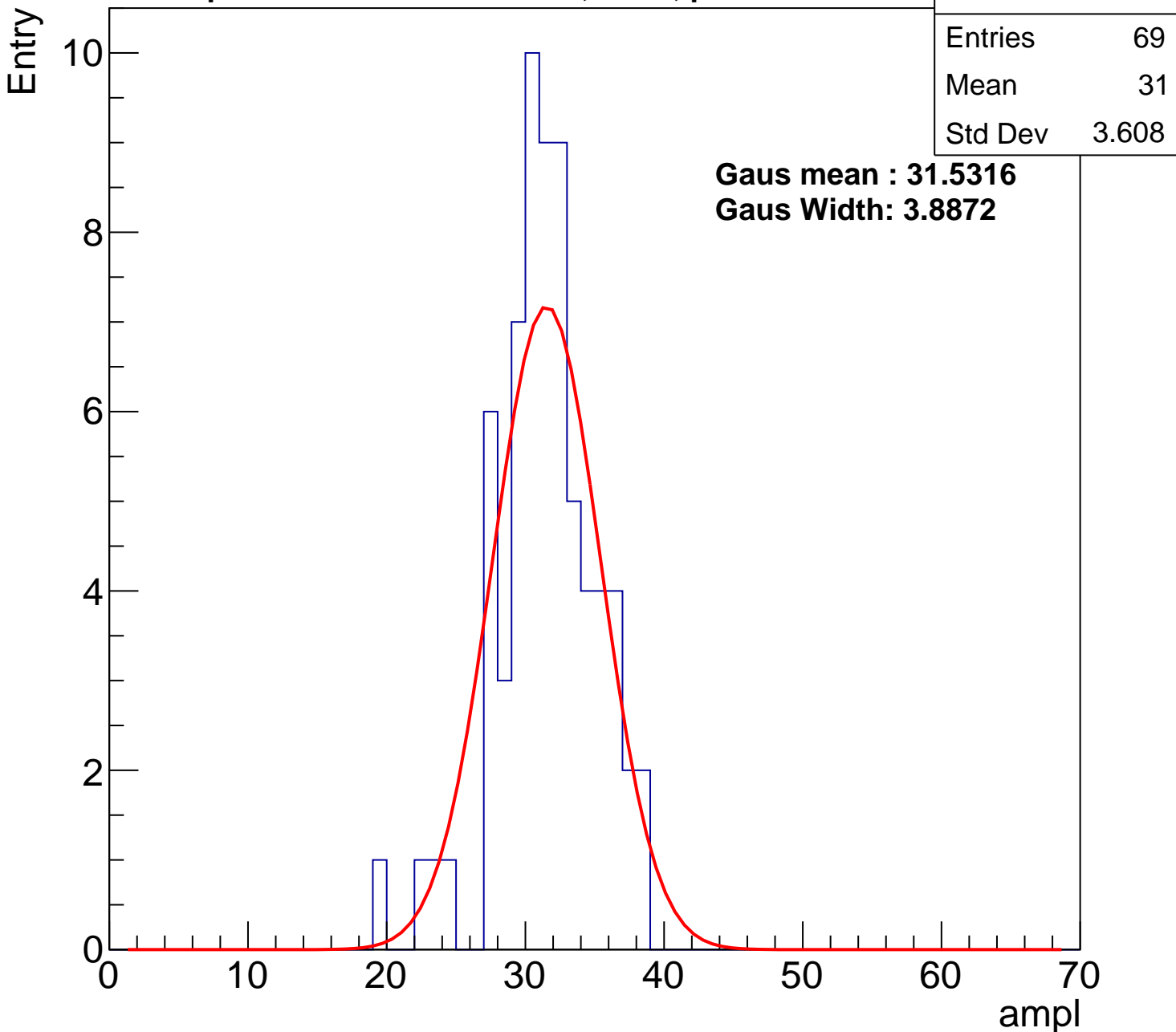
**Gaus Width: 3.8872**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch11, adc1

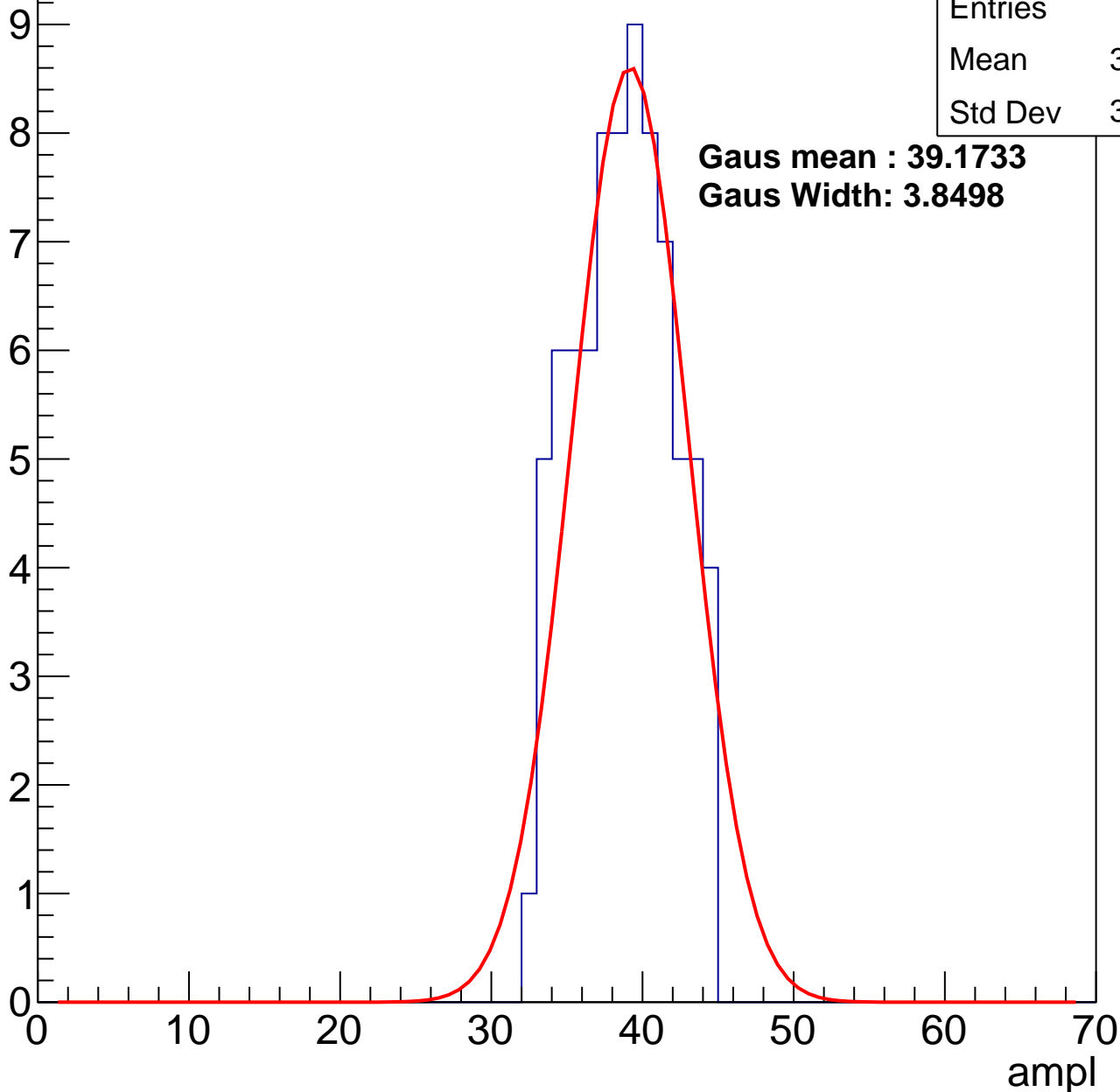
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	38.28
Std Dev	3.182

**Gaus mean : 39.1733**

**Gaus Width: 3.8498**



# B0L002S, U8-ch11, adc2

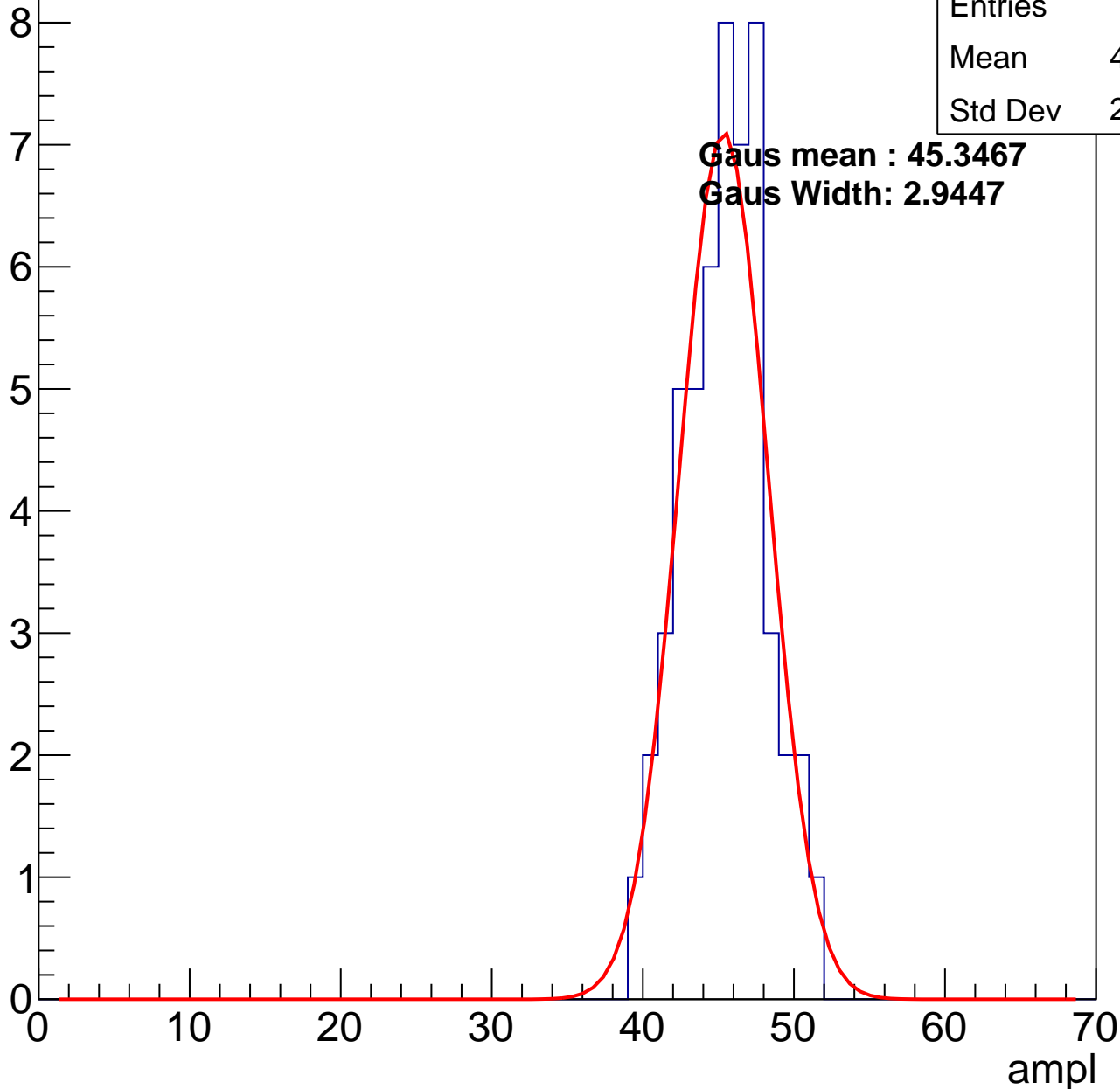
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.94
Std Dev	2.709

**Gaus mean : 45.3467**

**Gaus Width: 2.9447**

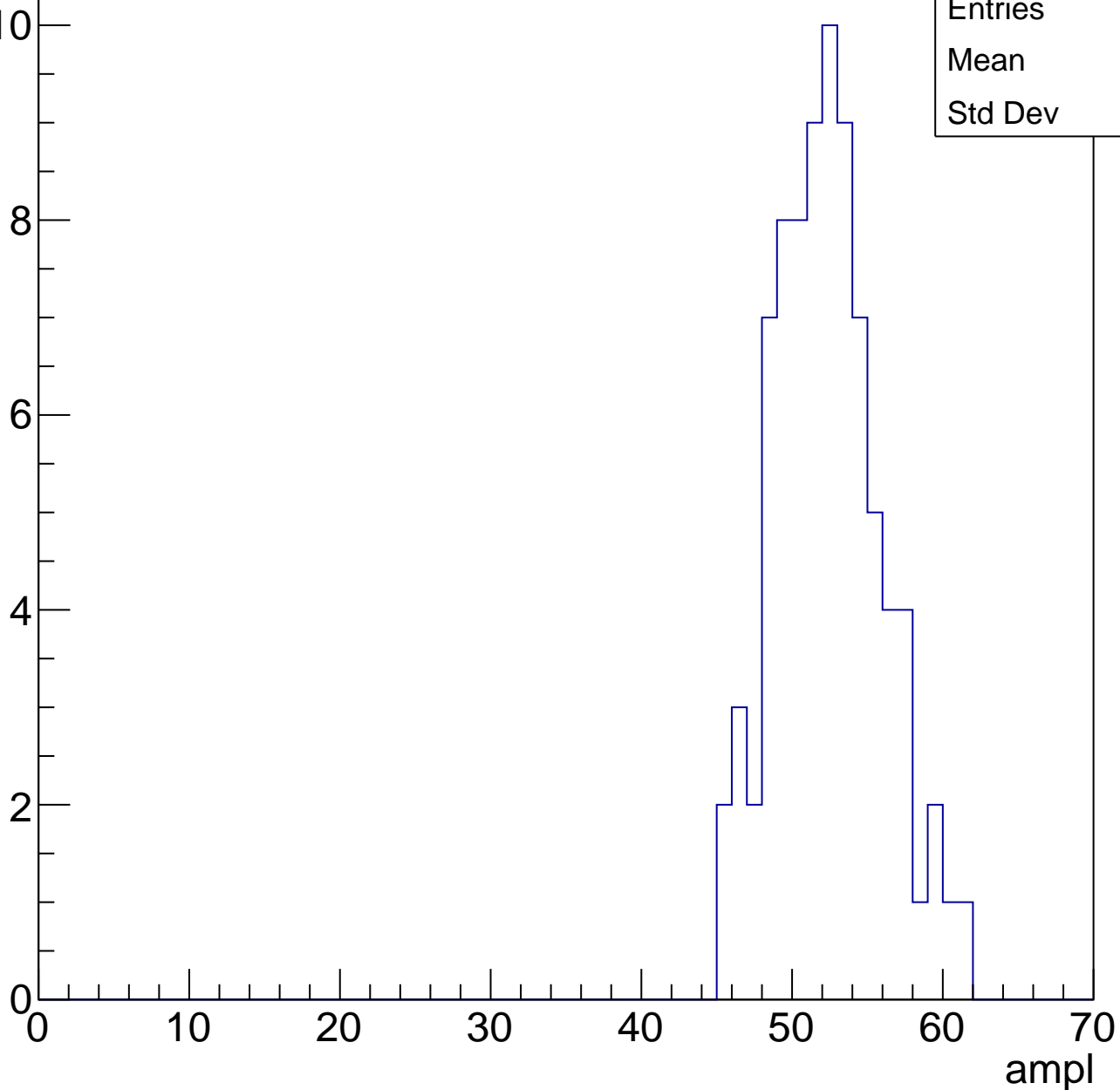


# B0L002S, U8-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	83
Mean	51.9
Std Dev	3.48

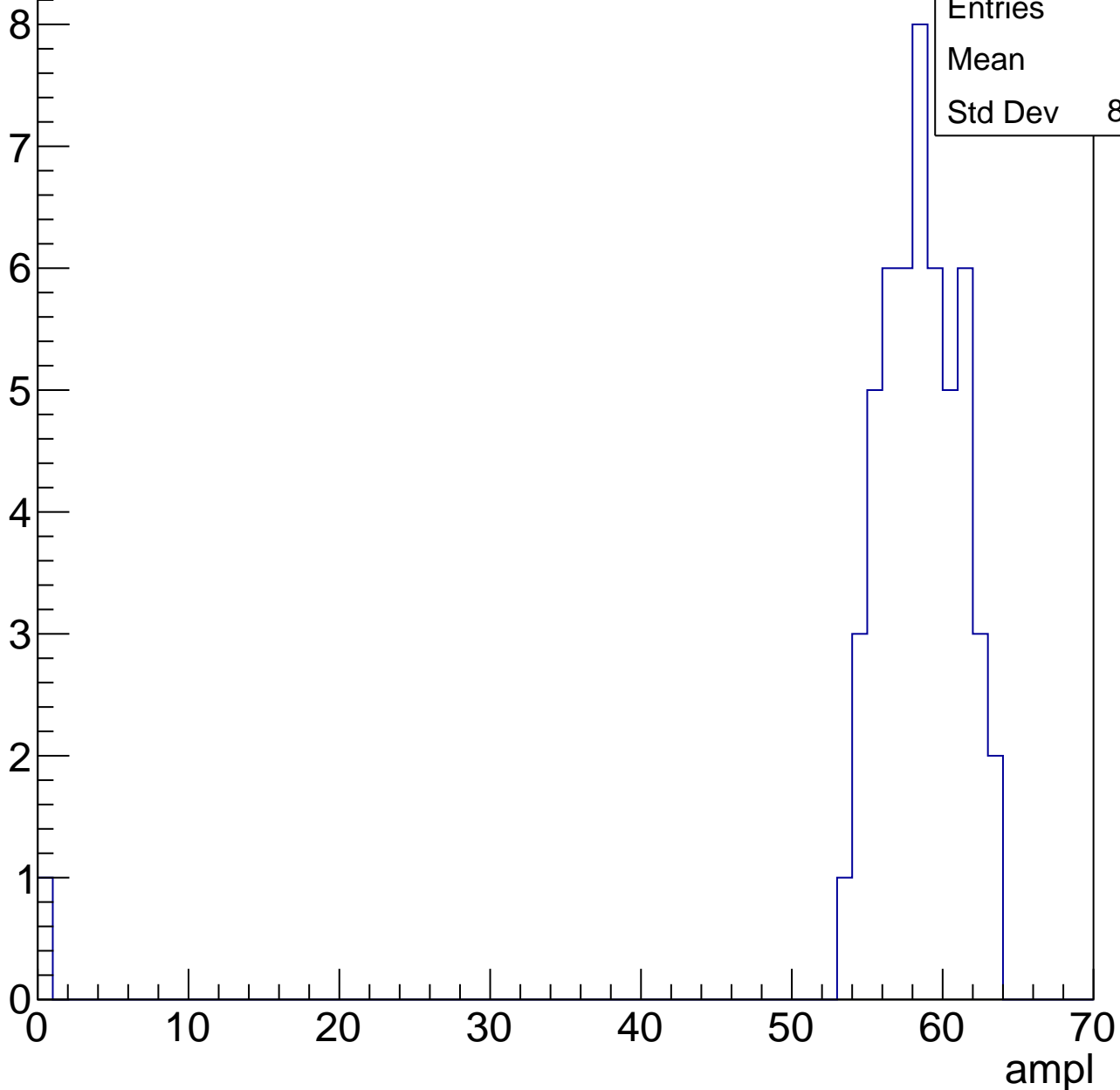


# B0L002S, U8-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	57
Std Dev	8.364

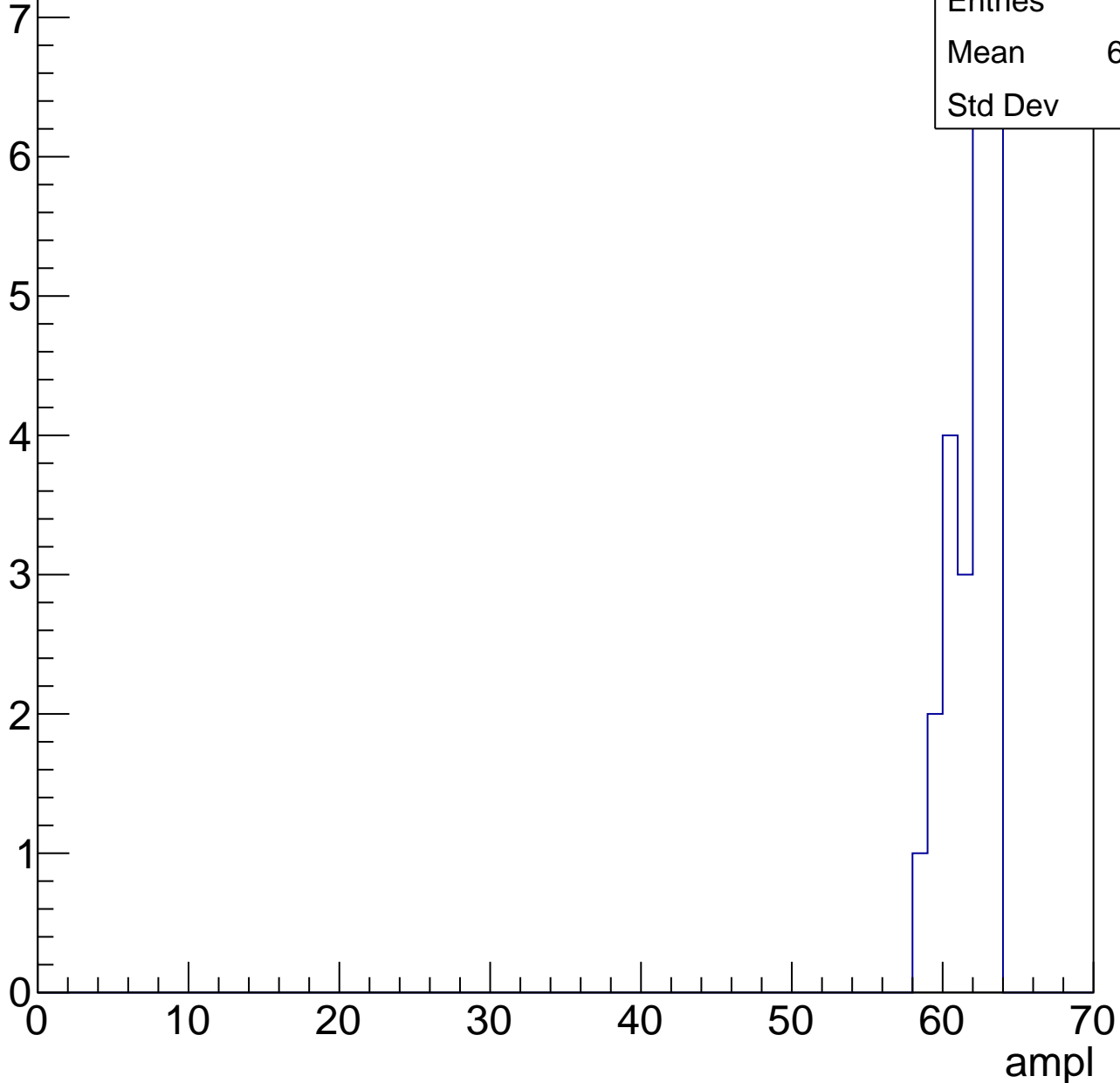


# B0L002S, U8-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.42
Std Dev	1.47



# B0L002S, U8-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch12, adc0

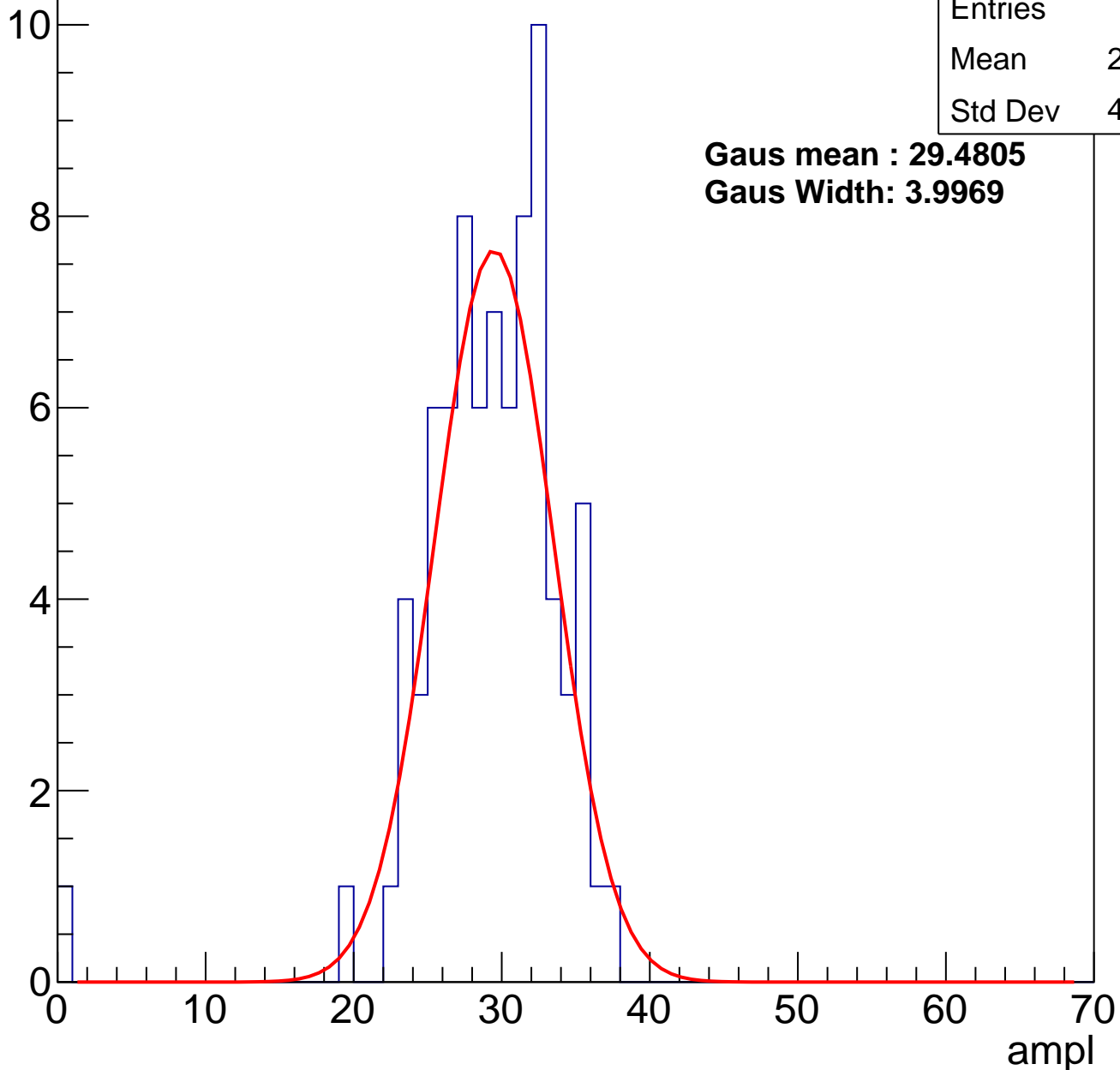
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	81
Mean	28.74
Std Dev	4.908

**Gaus mean : 29.4805**

**Gaus Width: 3.9969**

Entry



# B0L002S, U8-ch12, adc1

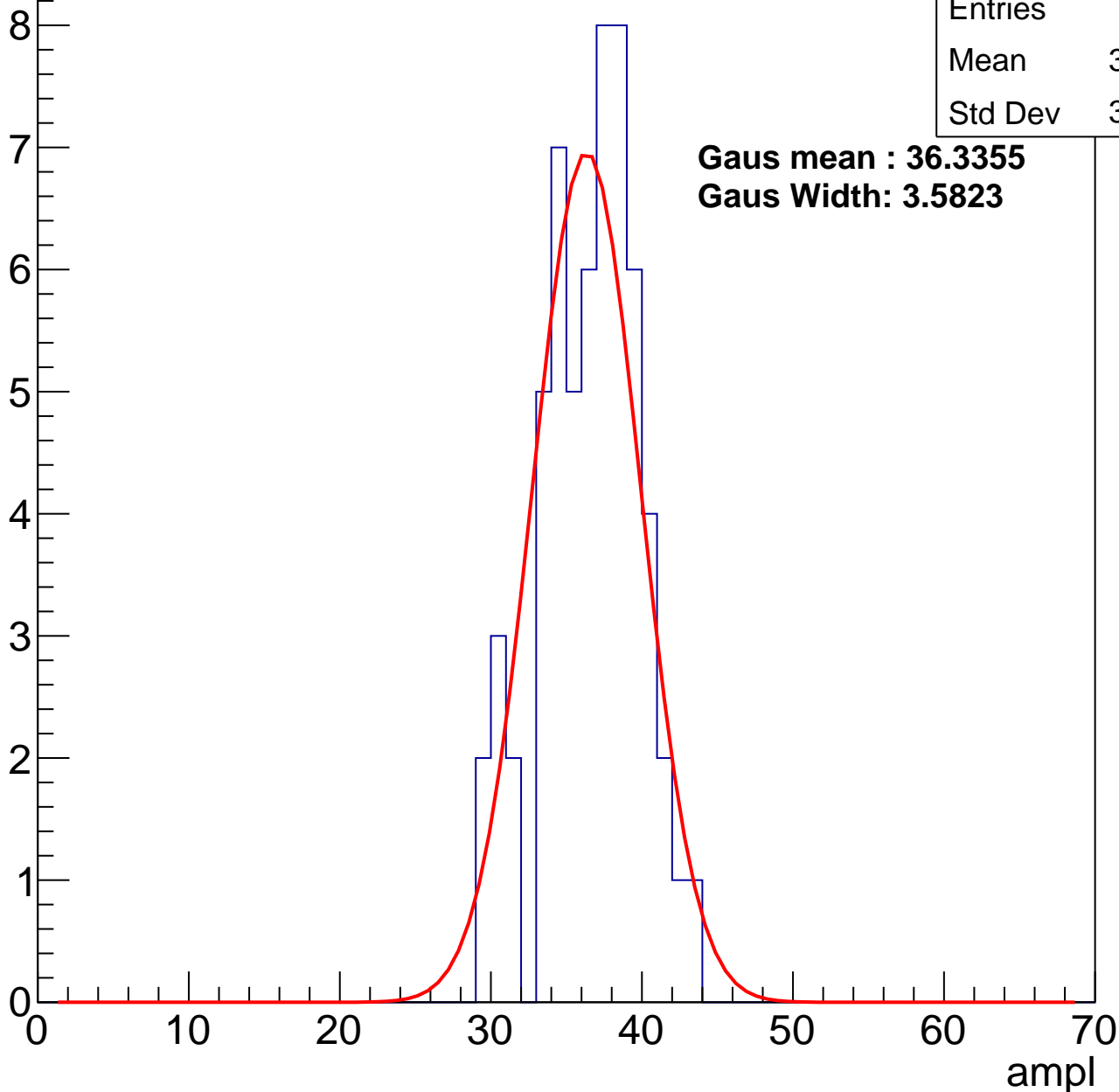
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.08
Std Dev	3.232

**Gaus mean : 36.3355**

**Gaus Width: 3.5823**



# B0L002S, U8-ch12, adc2

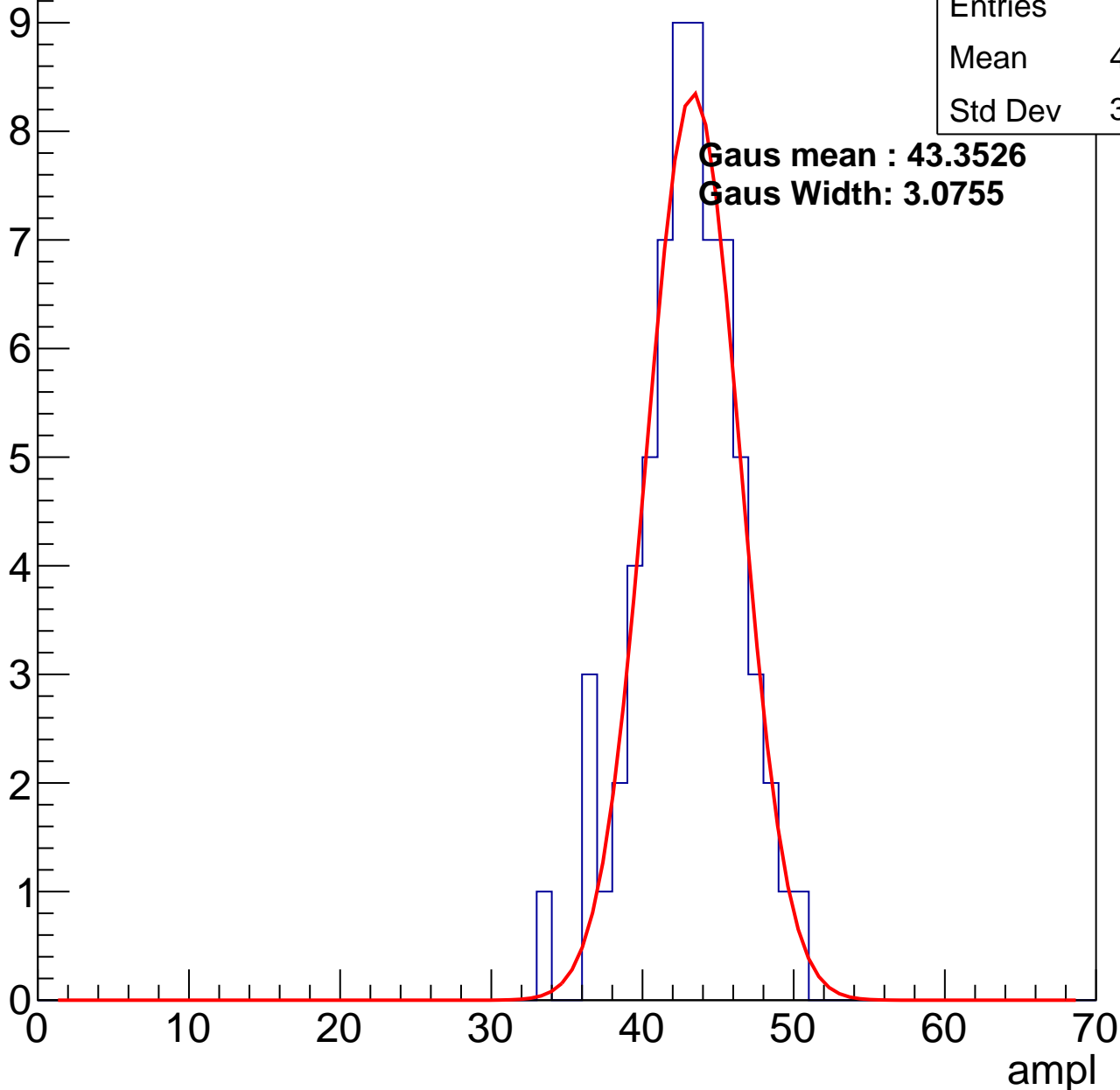
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.55
Std Dev	3.298

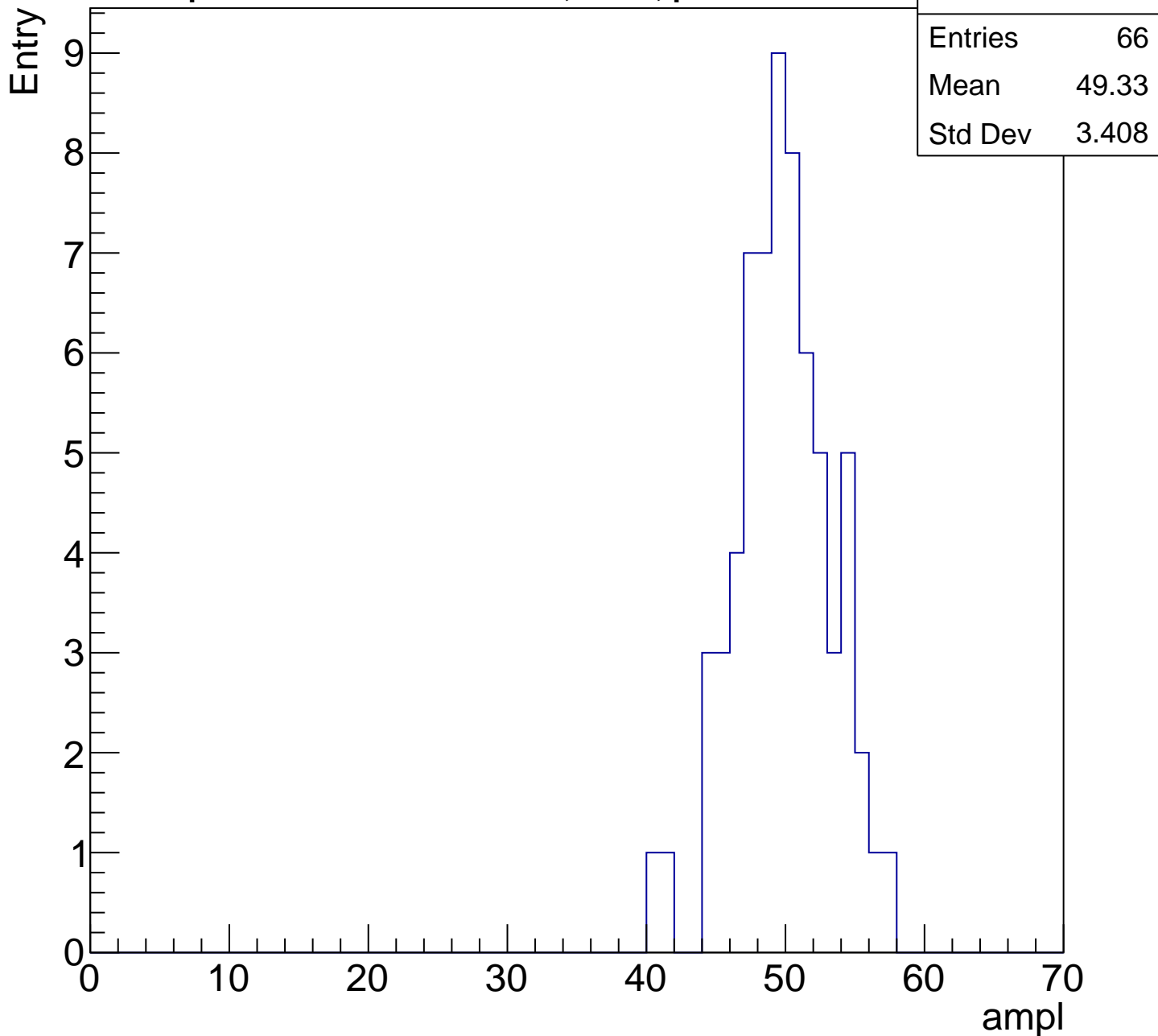
**Gaus mean : 43.3526**

**Gaus Width: 3.0755**



# B0L002S, U8-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

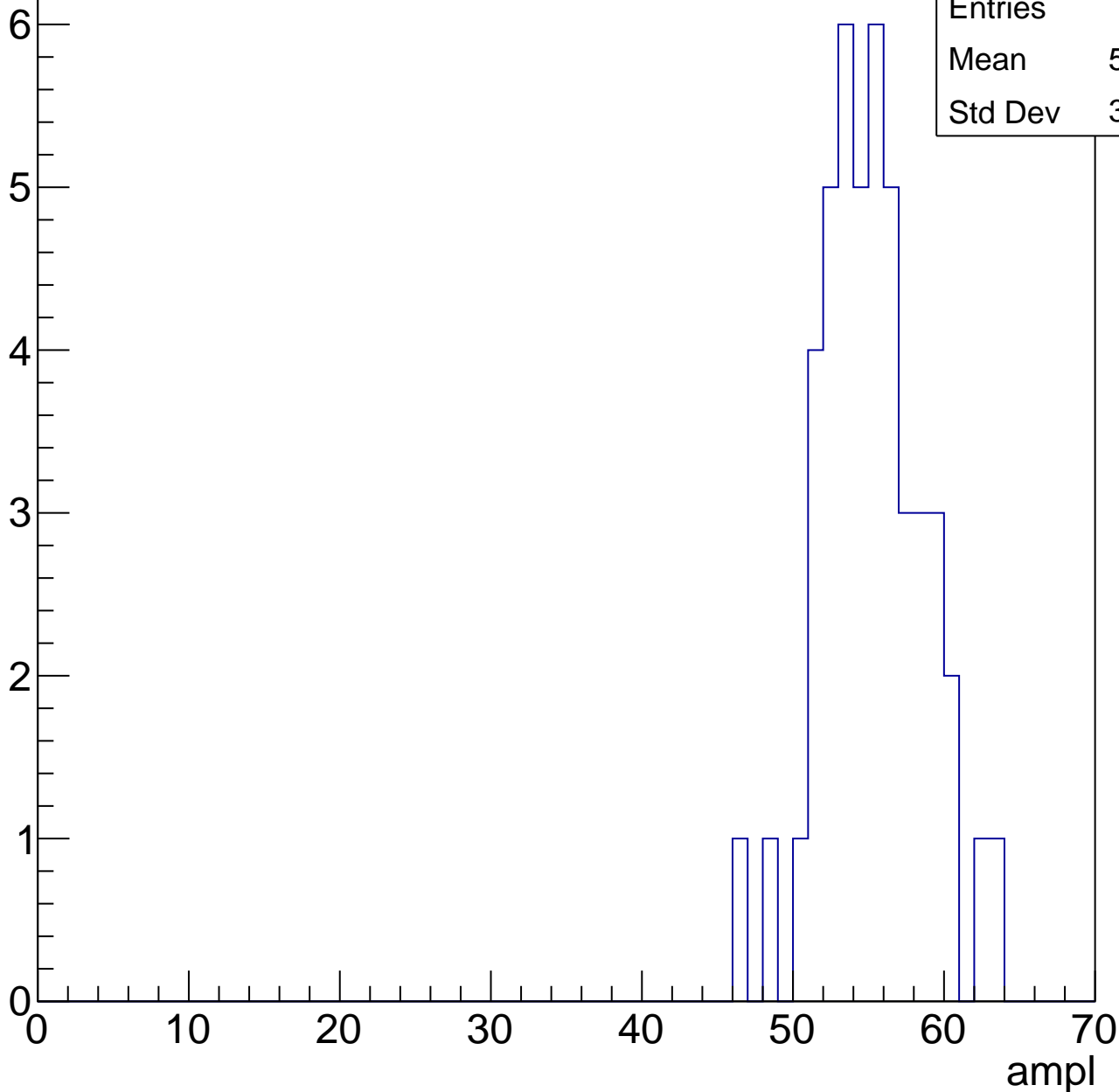


# B0L002S, U8-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	54.74
Std Dev	3.405



# B0L002S, U8-ch12, adc5

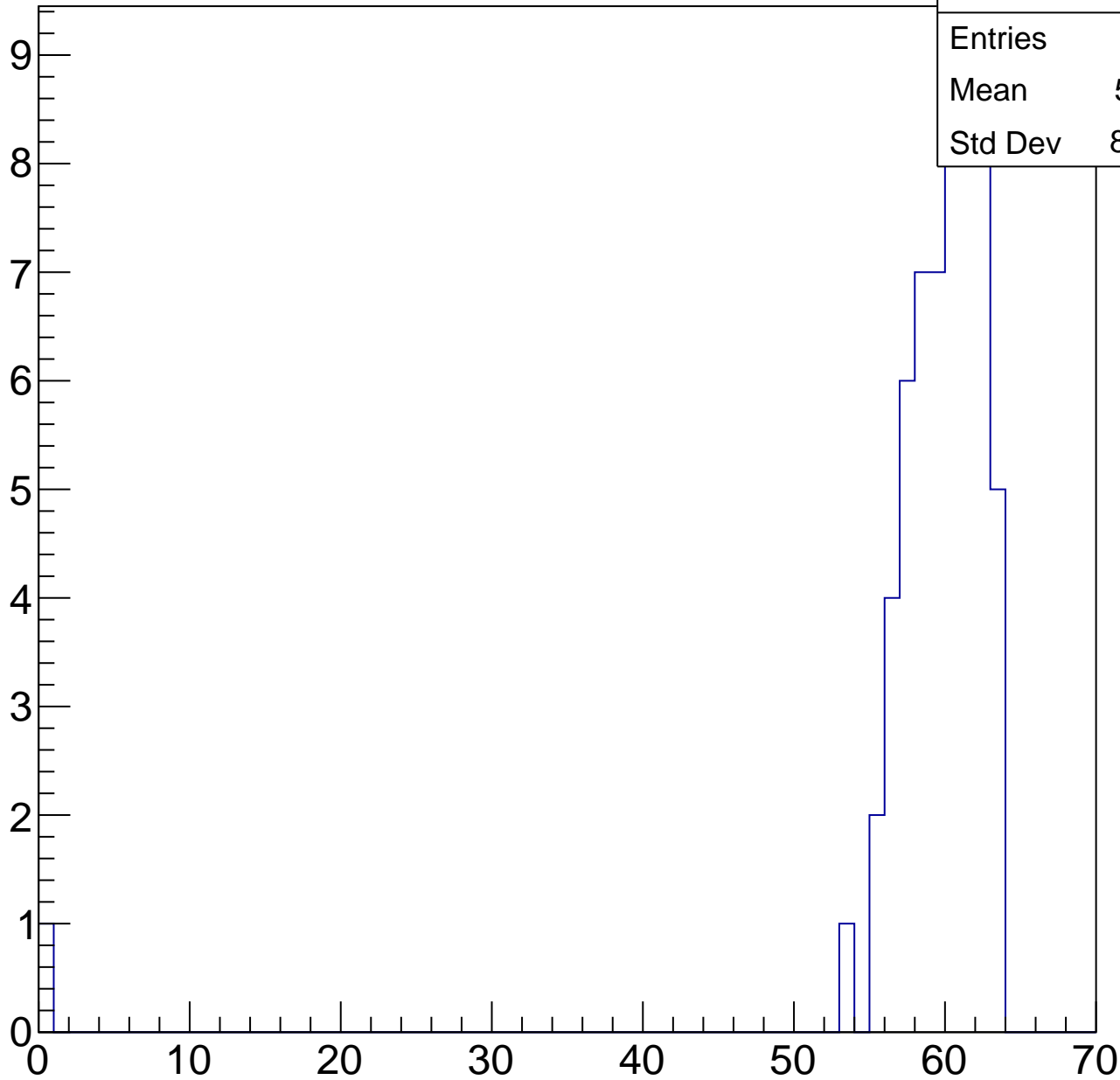
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.41
Std Dev	8.086

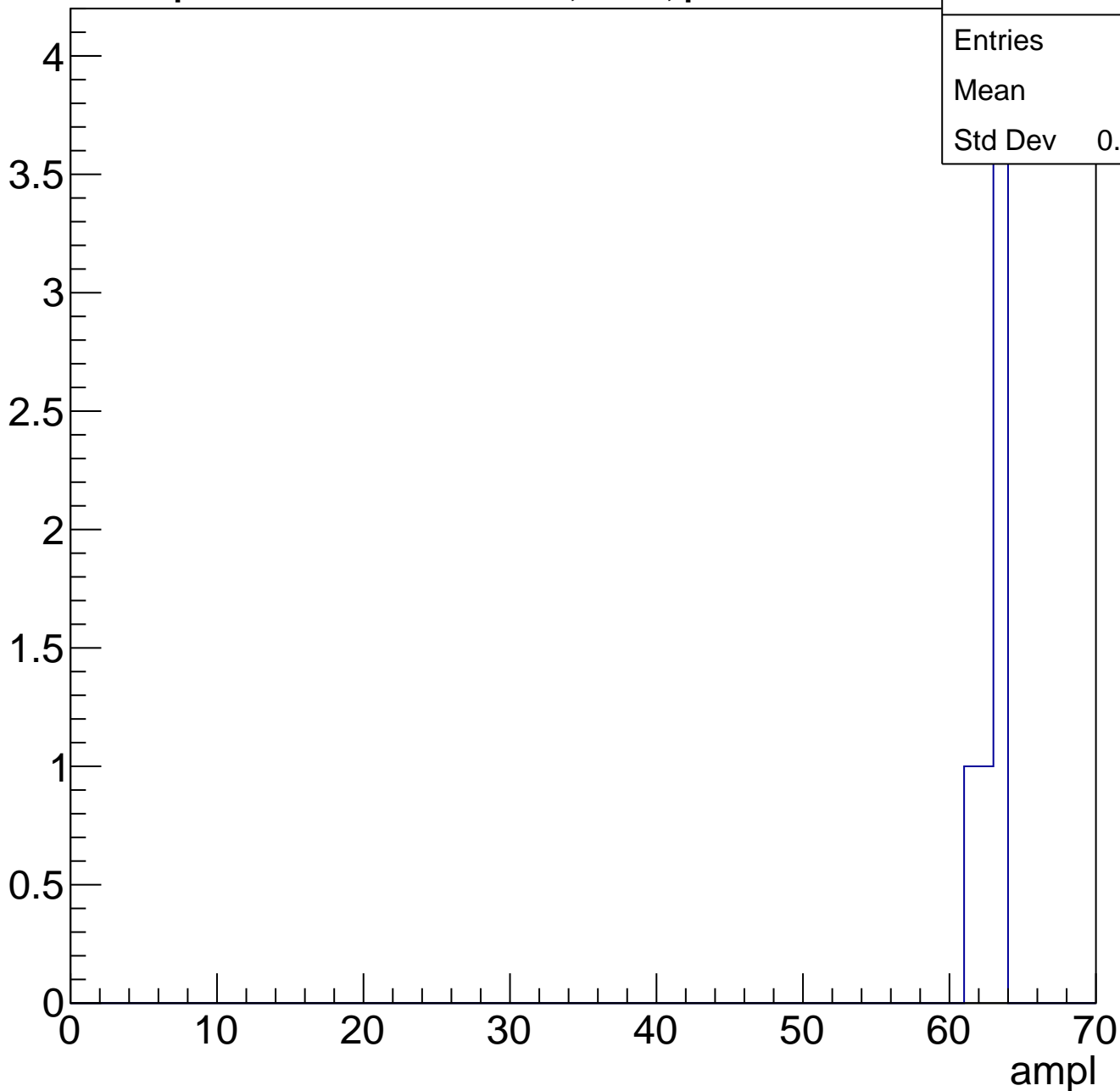
ampl



# B0L002S, U8-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch13, adc0

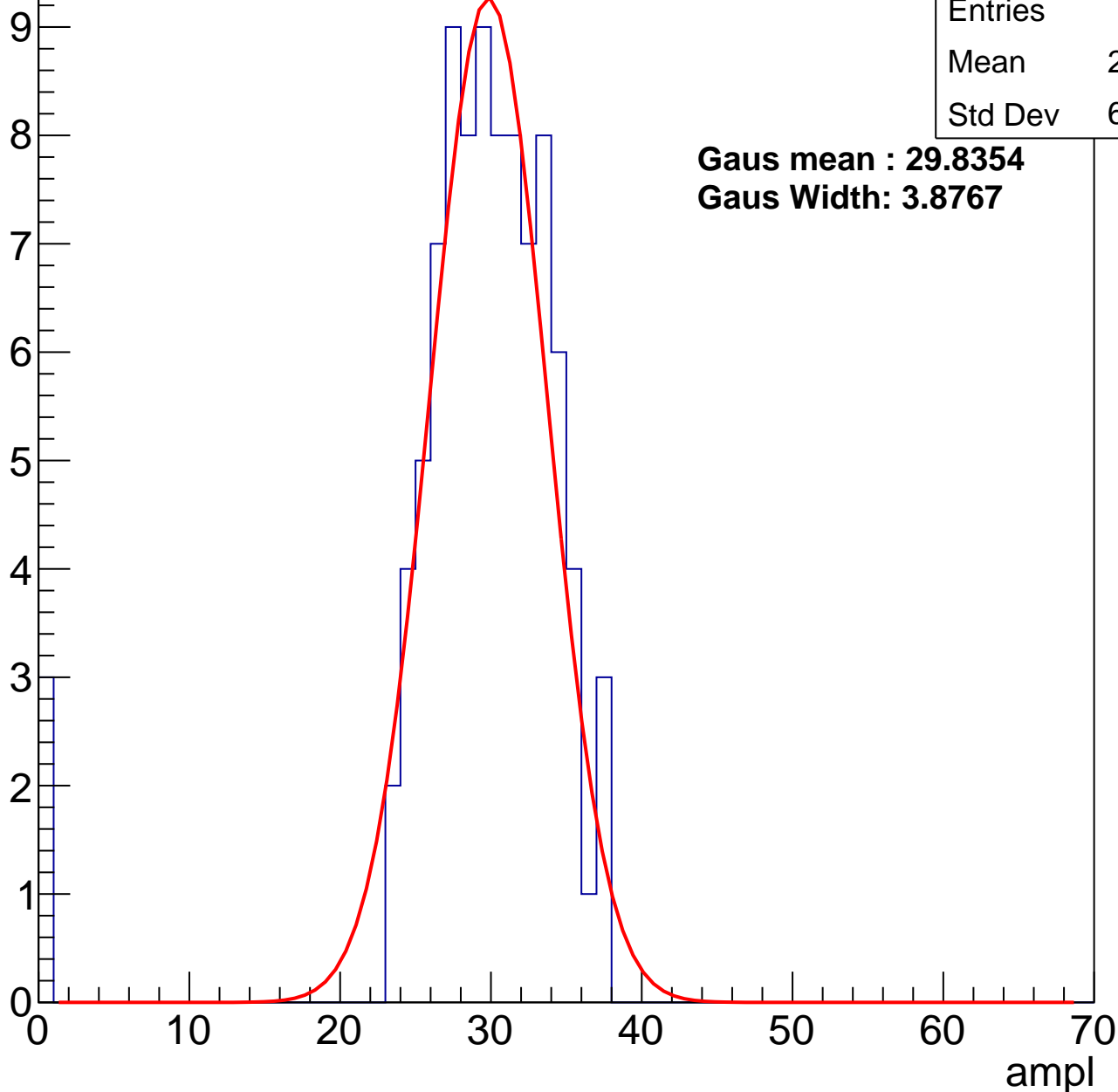
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	92
Mean	28.74
Std Dev	6.292

**Gaus mean : 29.8354**

**Gaus Width: 3.8767**



# B0L002S, U8-ch13, adc1

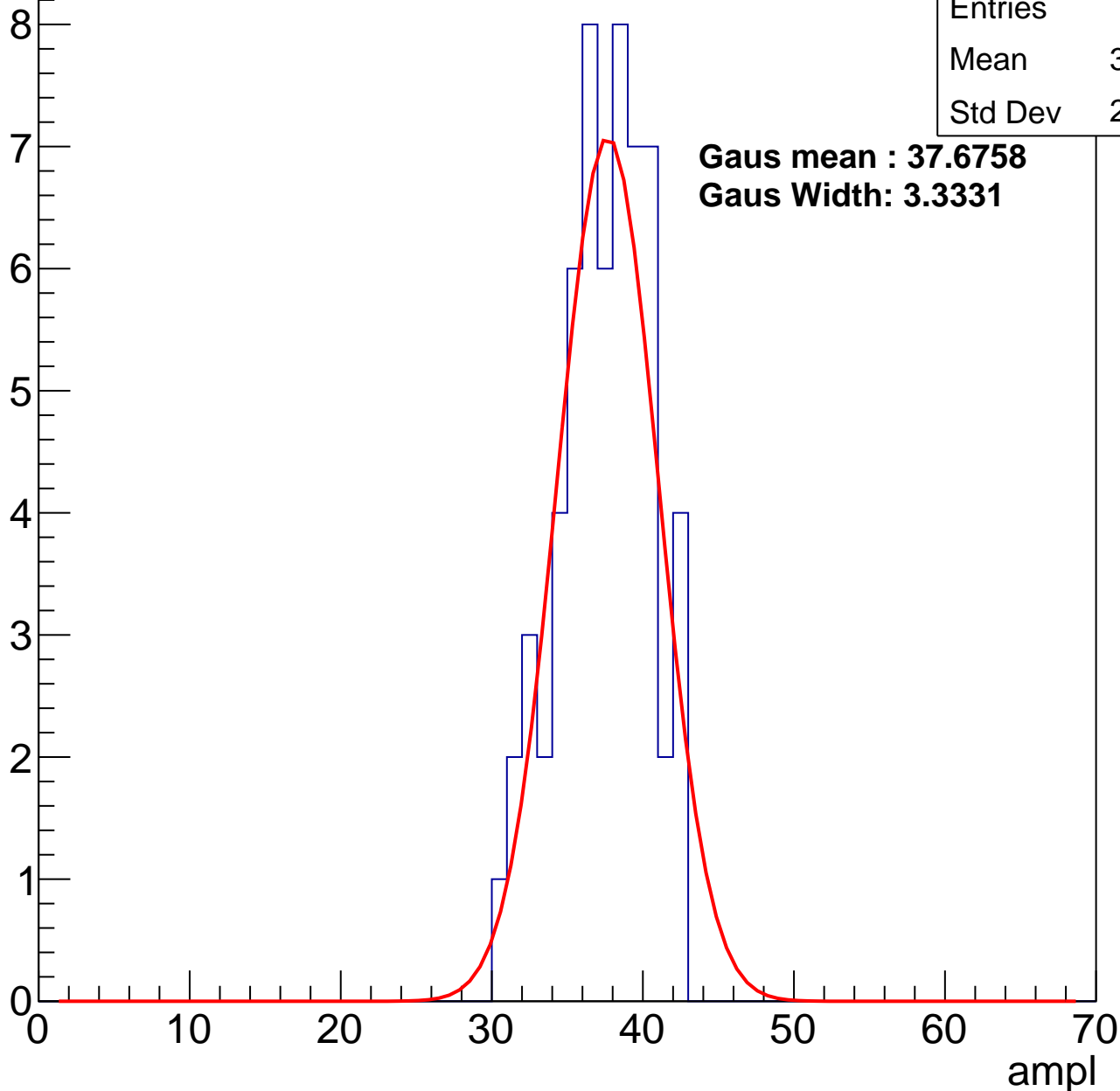
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.95
Std Dev	2.963

**Gaus mean : 37.6758**

**Gaus Width: 3.3331**



# B0L002S, U8-ch13, adc2

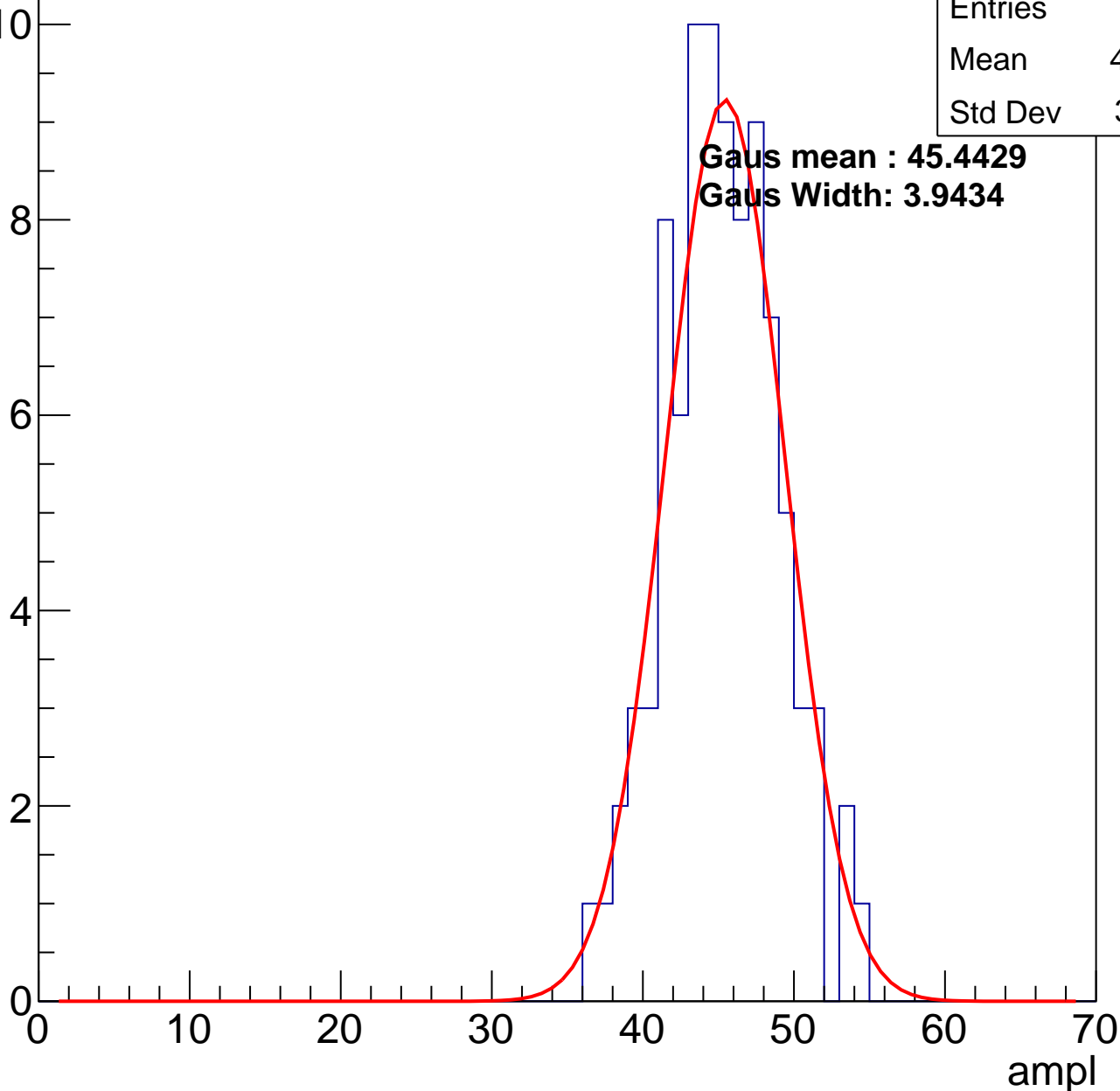
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	91
Mean	44.79
Std Dev	3.681

**Gaus mean : 45.4429**

**Gaus Width: 3.9434**

Entry

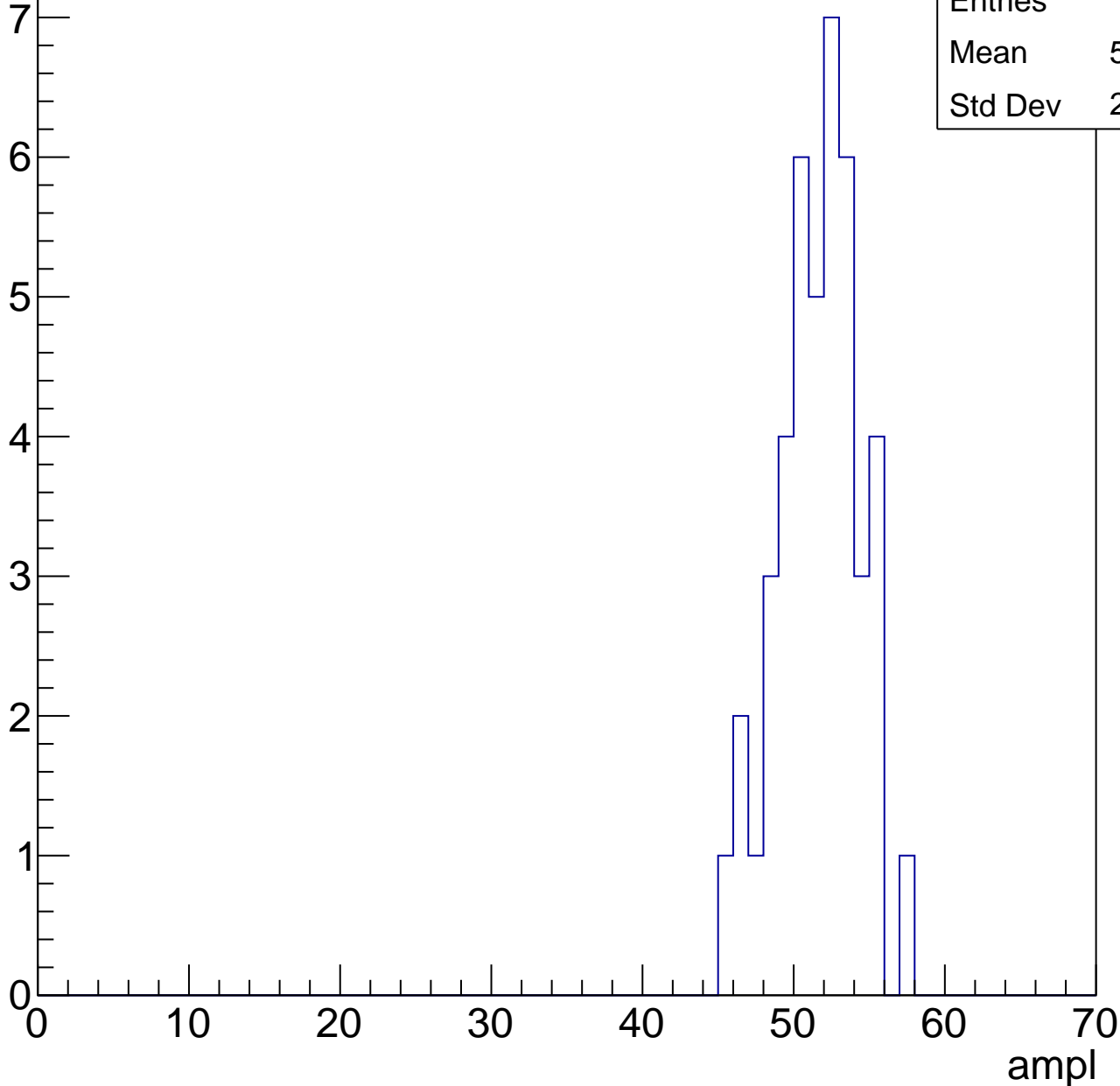


# B0L002S, U8-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	51.16
Std Dev	2.676

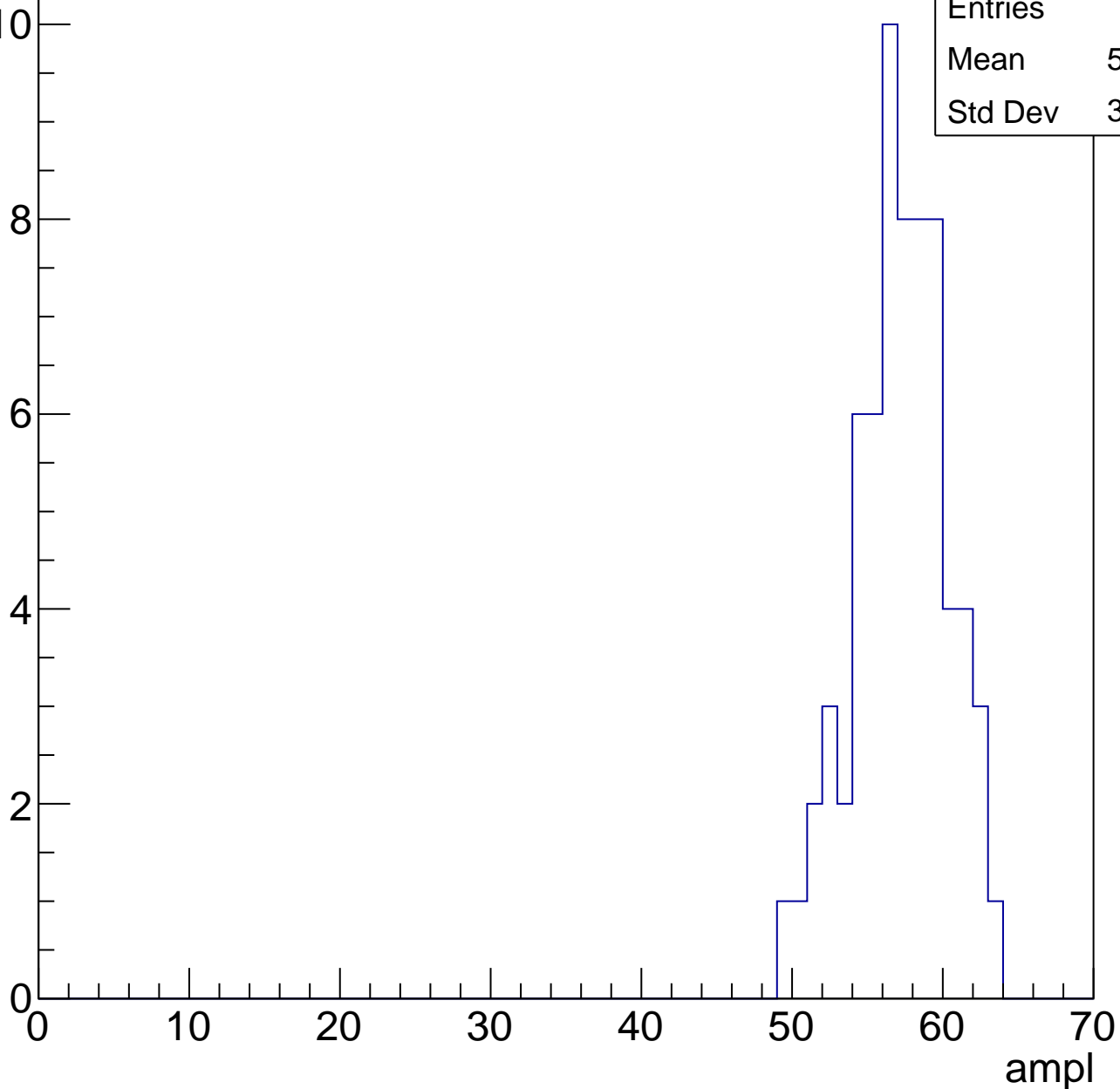


# B0L002S, U8-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.75
Std Dev	3.058



# B0L002S, U8-ch13, adc5

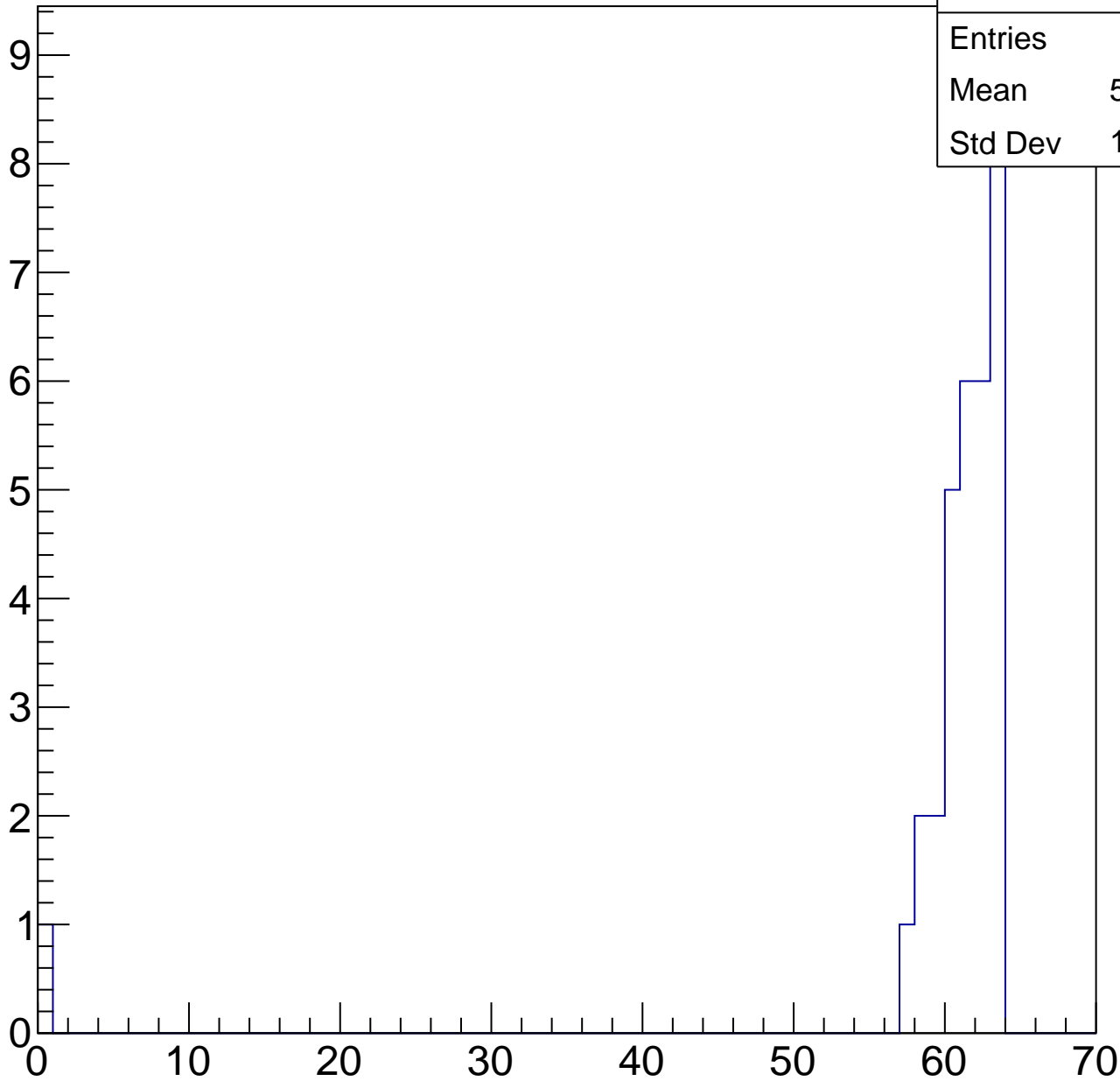
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	59.25
Std Dev	10.77

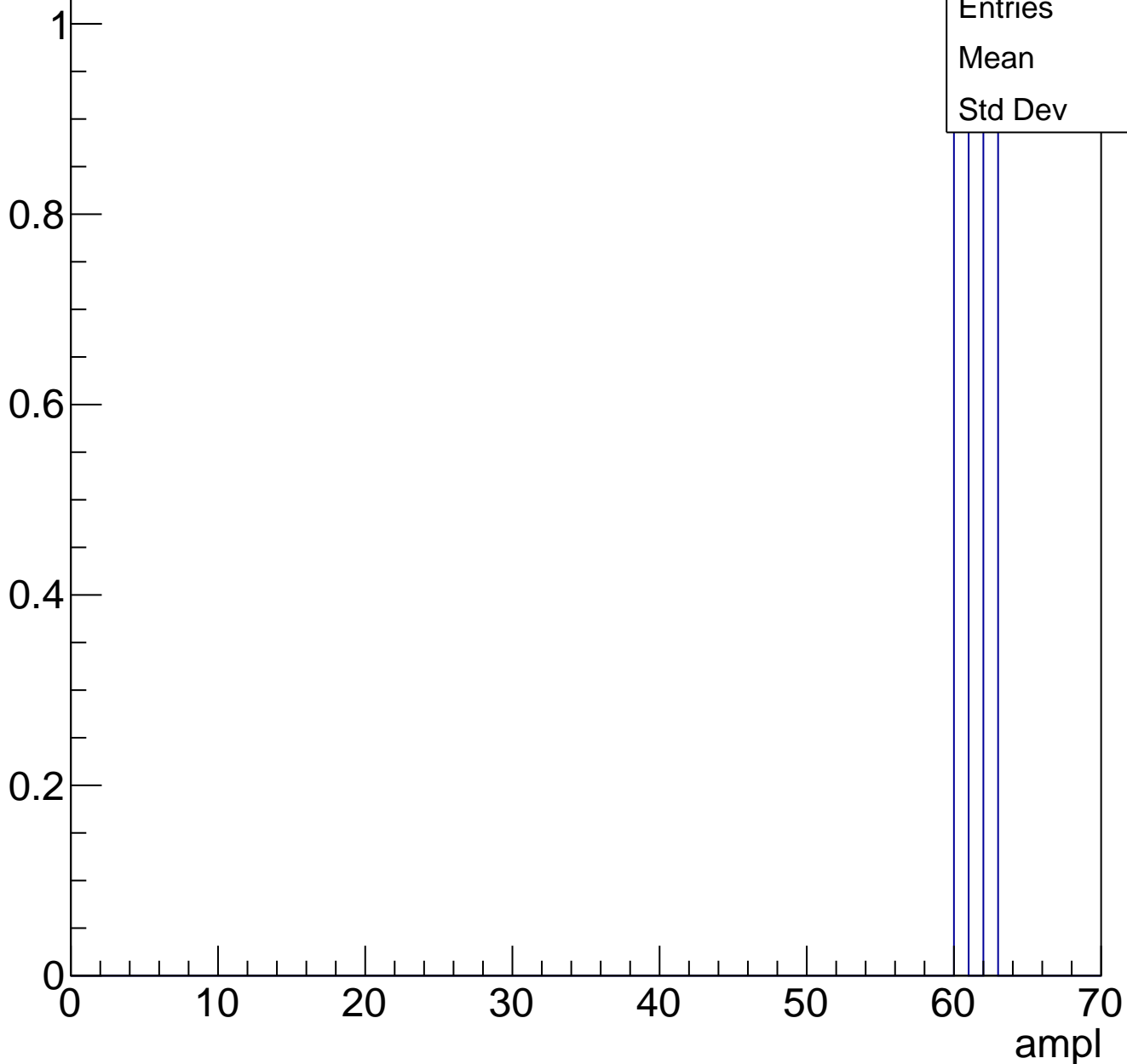
ampl



# B0L002S, U8-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

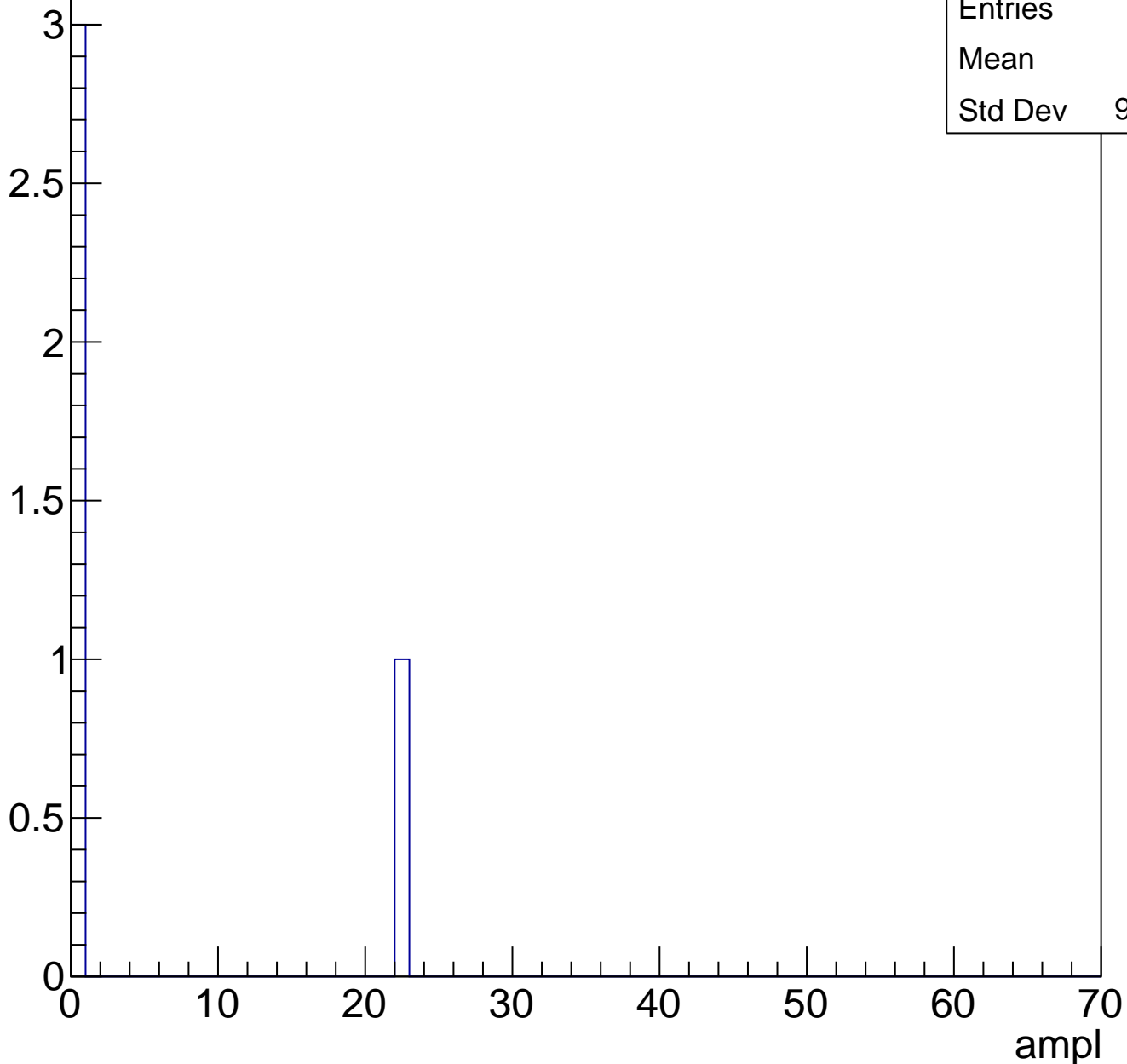




# B0L002S, U8-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	5.5
Std Dev	9.526

# B0L002S, U8-ch14, adc0

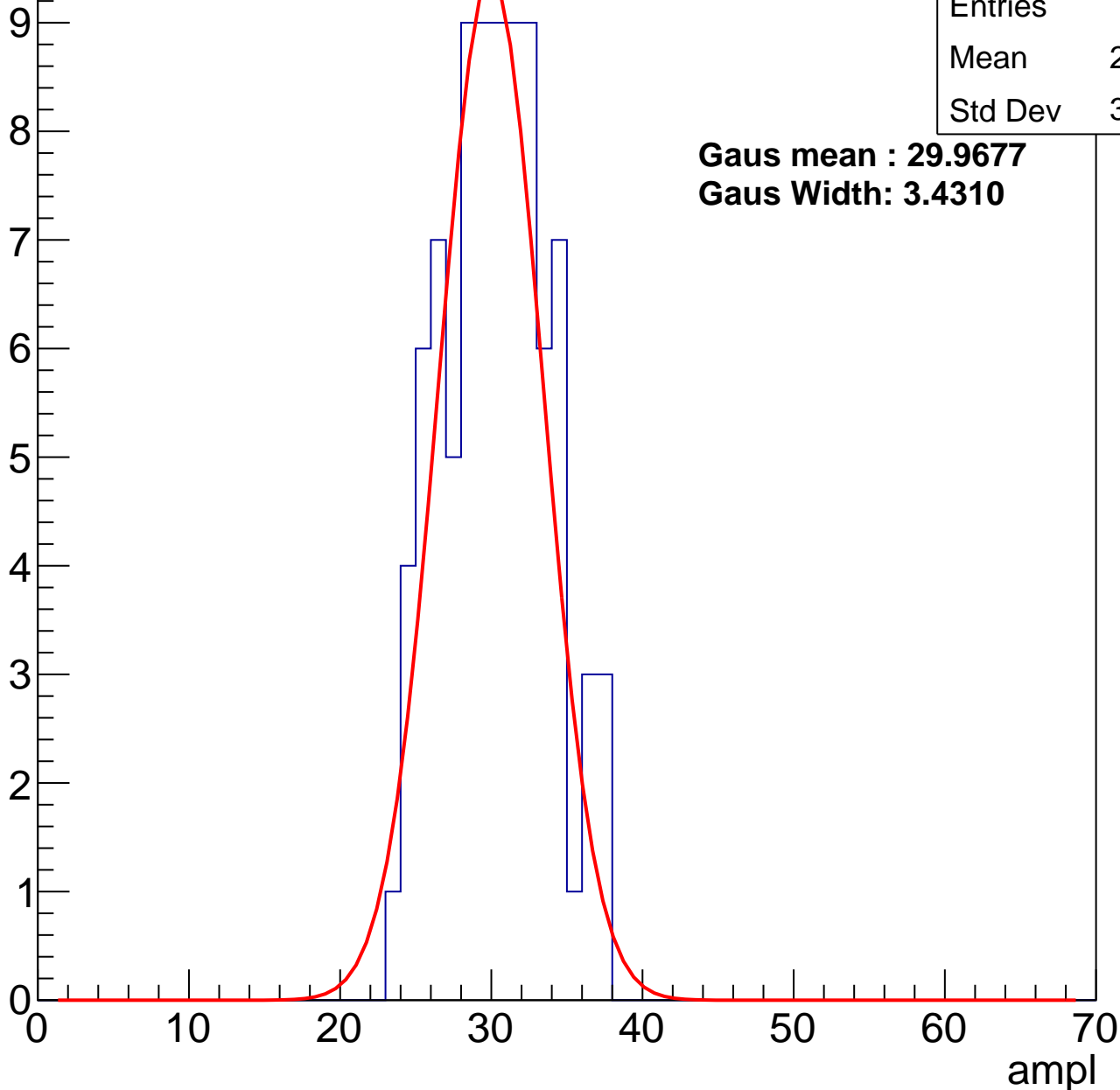
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	88
Mean	29.84
Std Dev	3.427

**Gaus mean : 29.9677**

**Gaus Width: 3.4310**



# B0L002S, U8-ch14, adc1

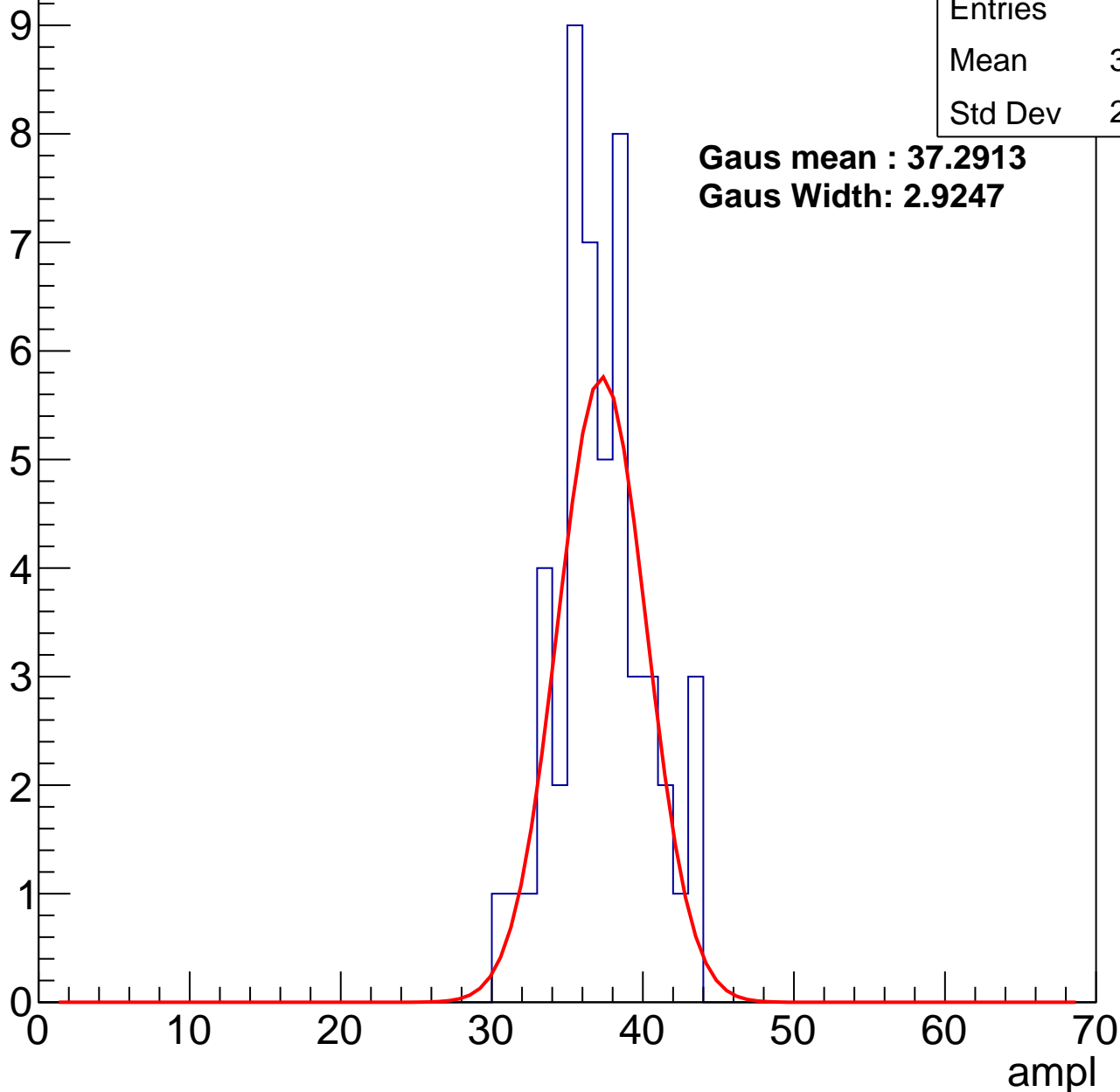
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	36.78
Std Dev	2.982

**Gaus mean : 37.2913**

**Gaus Width: 2.9247**



# B0L002S, U8-ch14, adc2

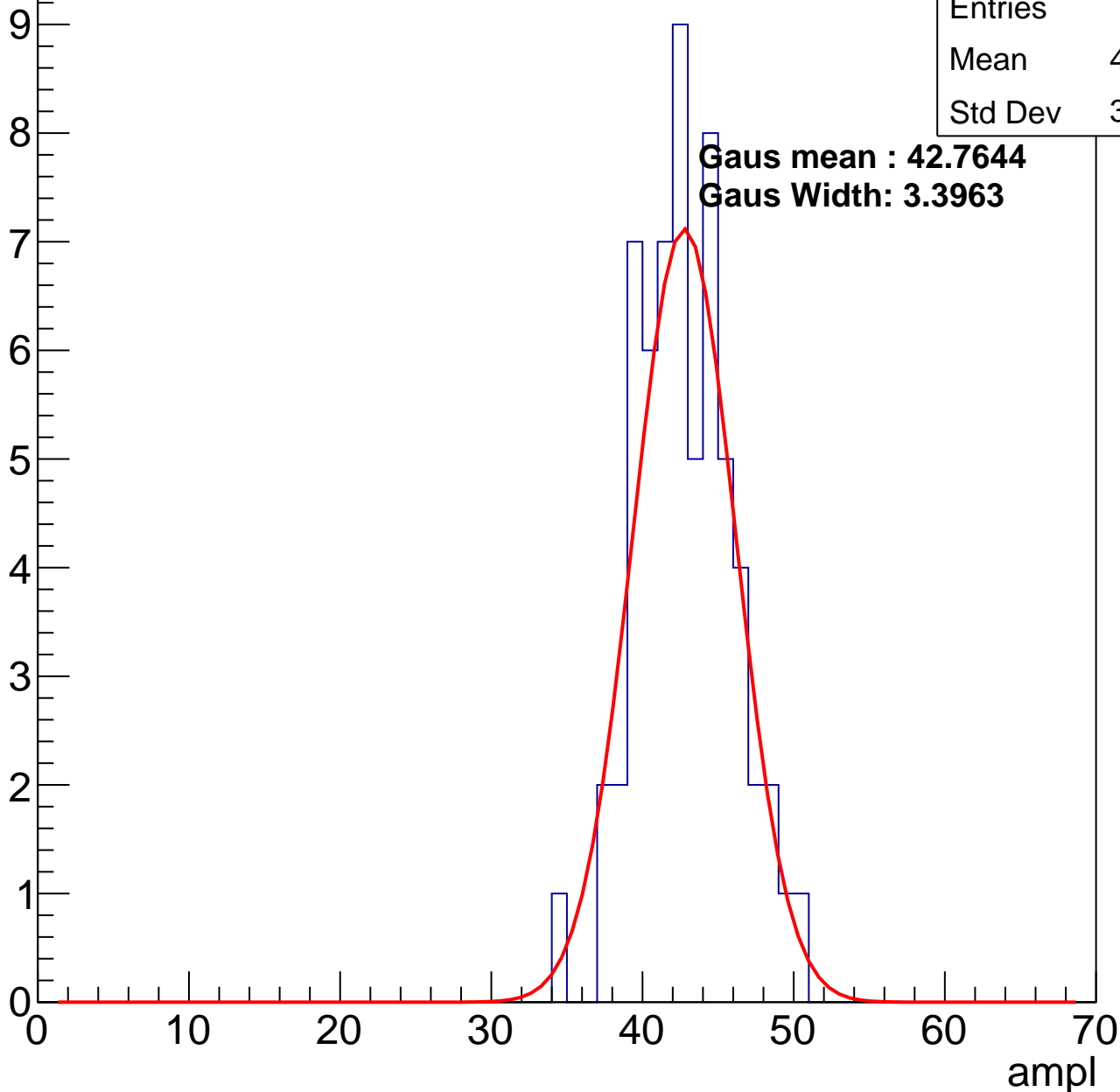
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.37
Std Dev	3.143

**Gaus mean : 42.7644**

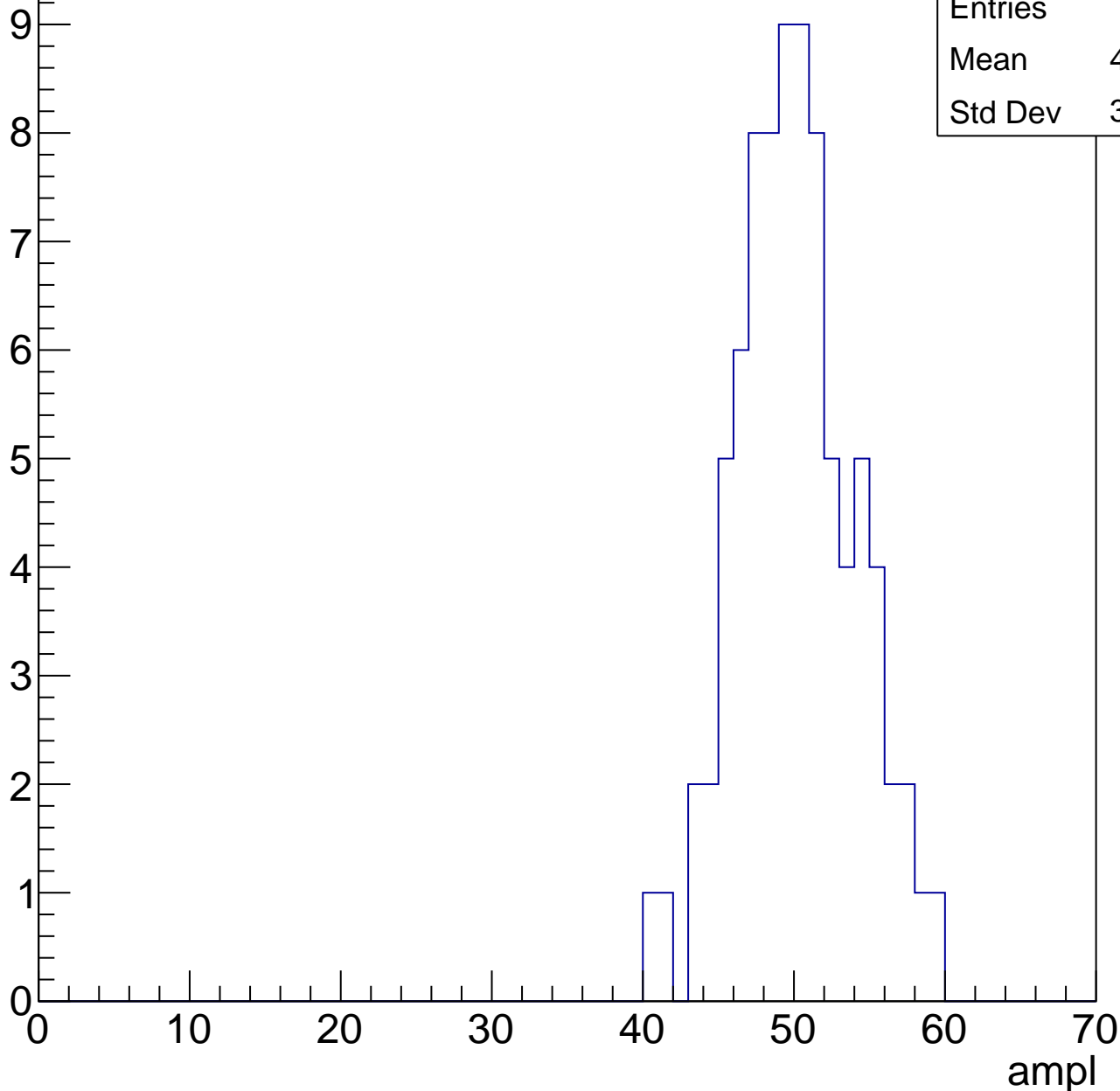
**Gaus Width: 3.3963**



# B0L002S, U8-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

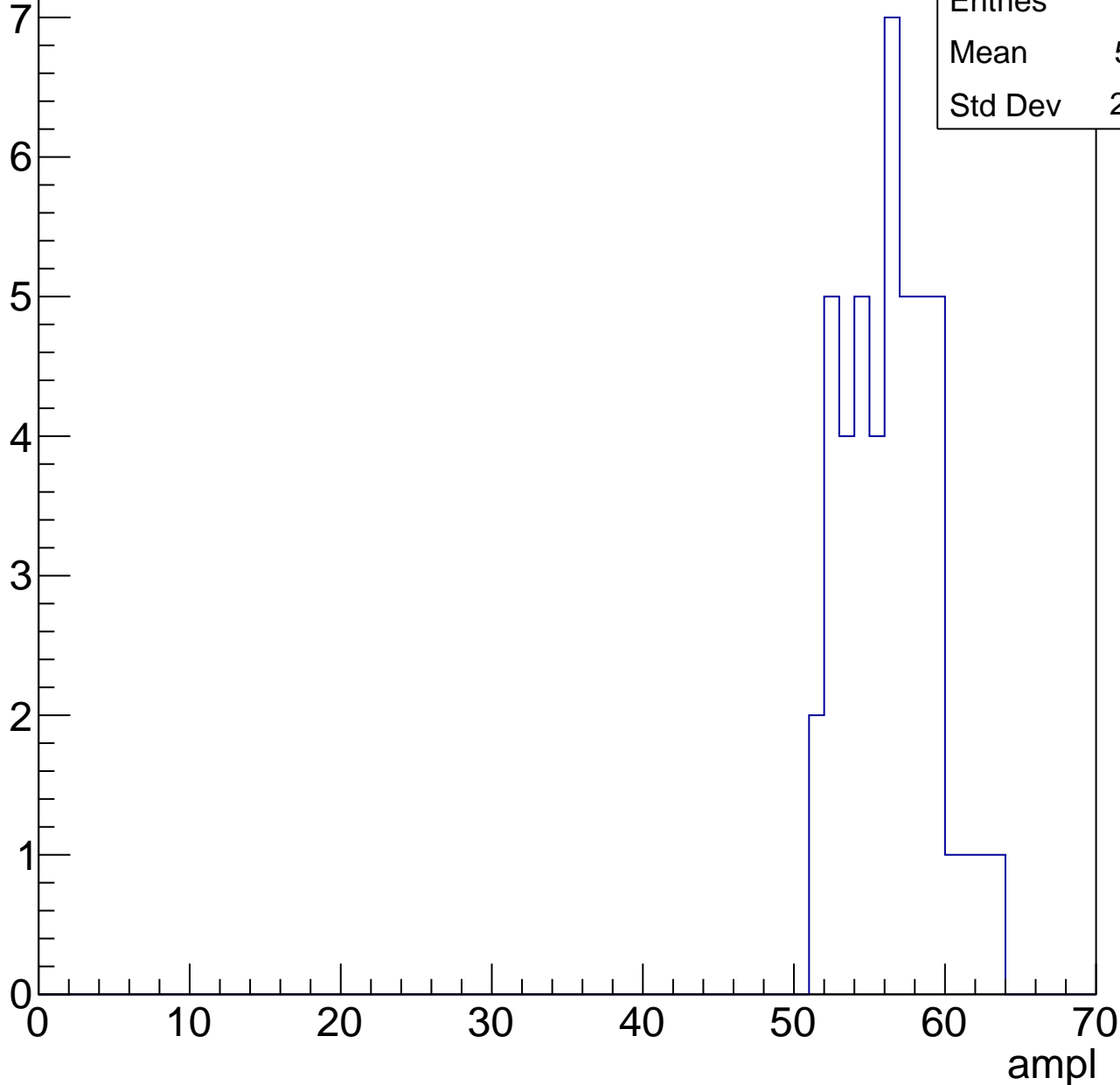
Entry



# B0L002S, U8-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.82
Std Dev	8.692

ampl

0

10

20

30

40

50

60

70

# B0L002S, U8-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	29.28
Std Dev	3.362

**Gaus mean : 29.9112**

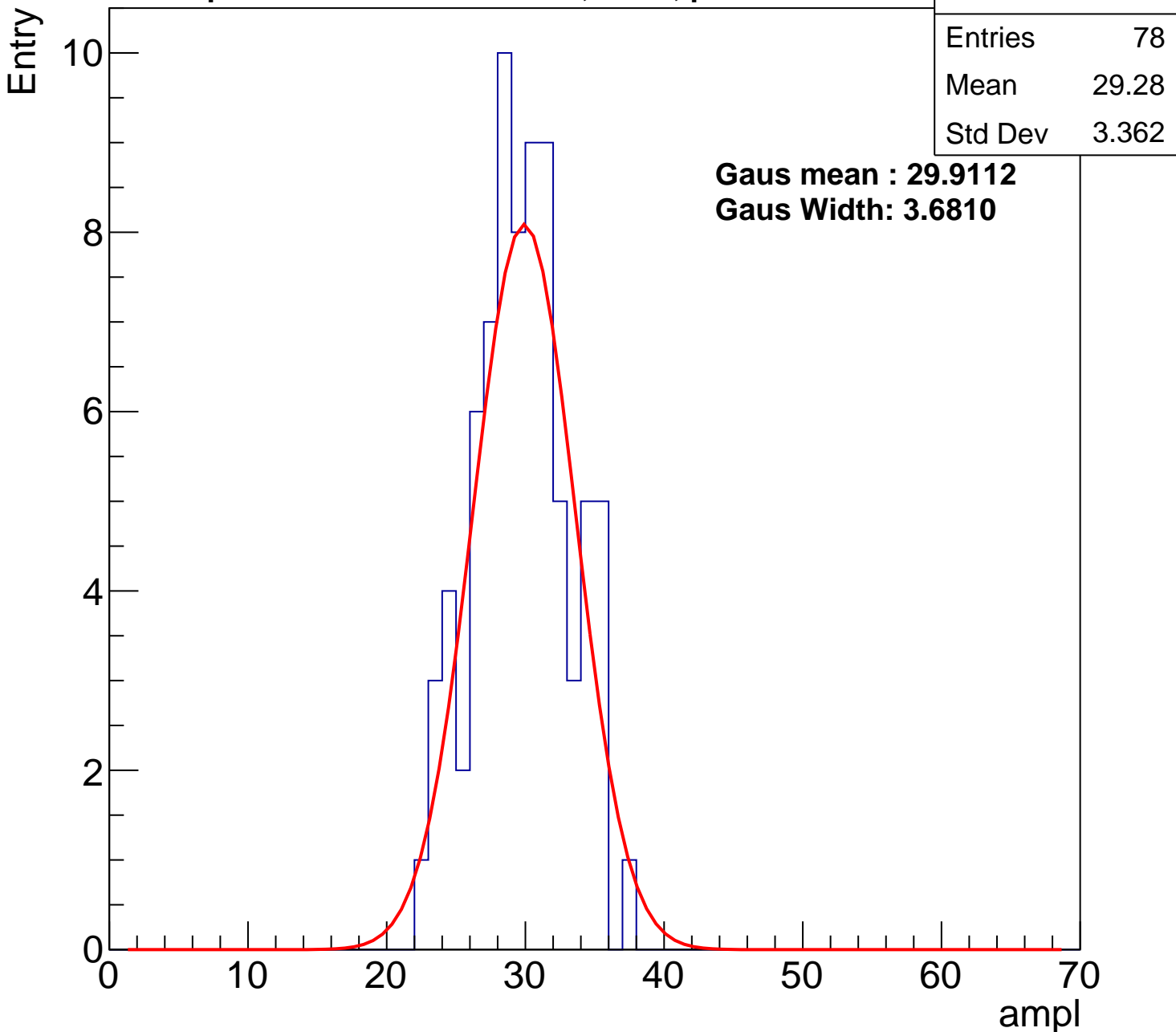
**Gaus Width: 3.6810**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch15, adc1

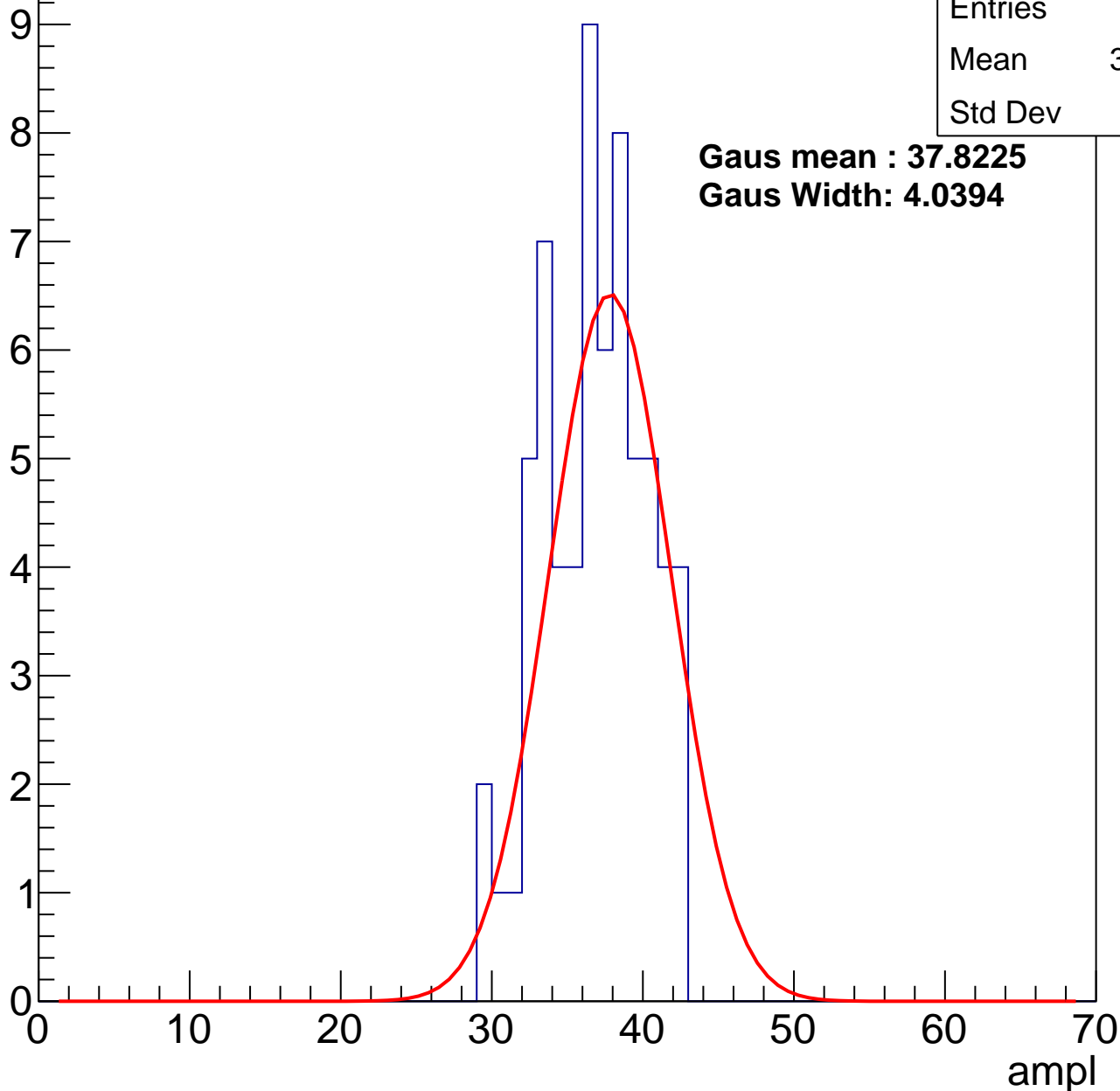
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.35
Std Dev	3.33

**Gaus mean : 37.8225**

**Gaus Width: 4.0394**



# B0L002S, U8-ch15, adc2

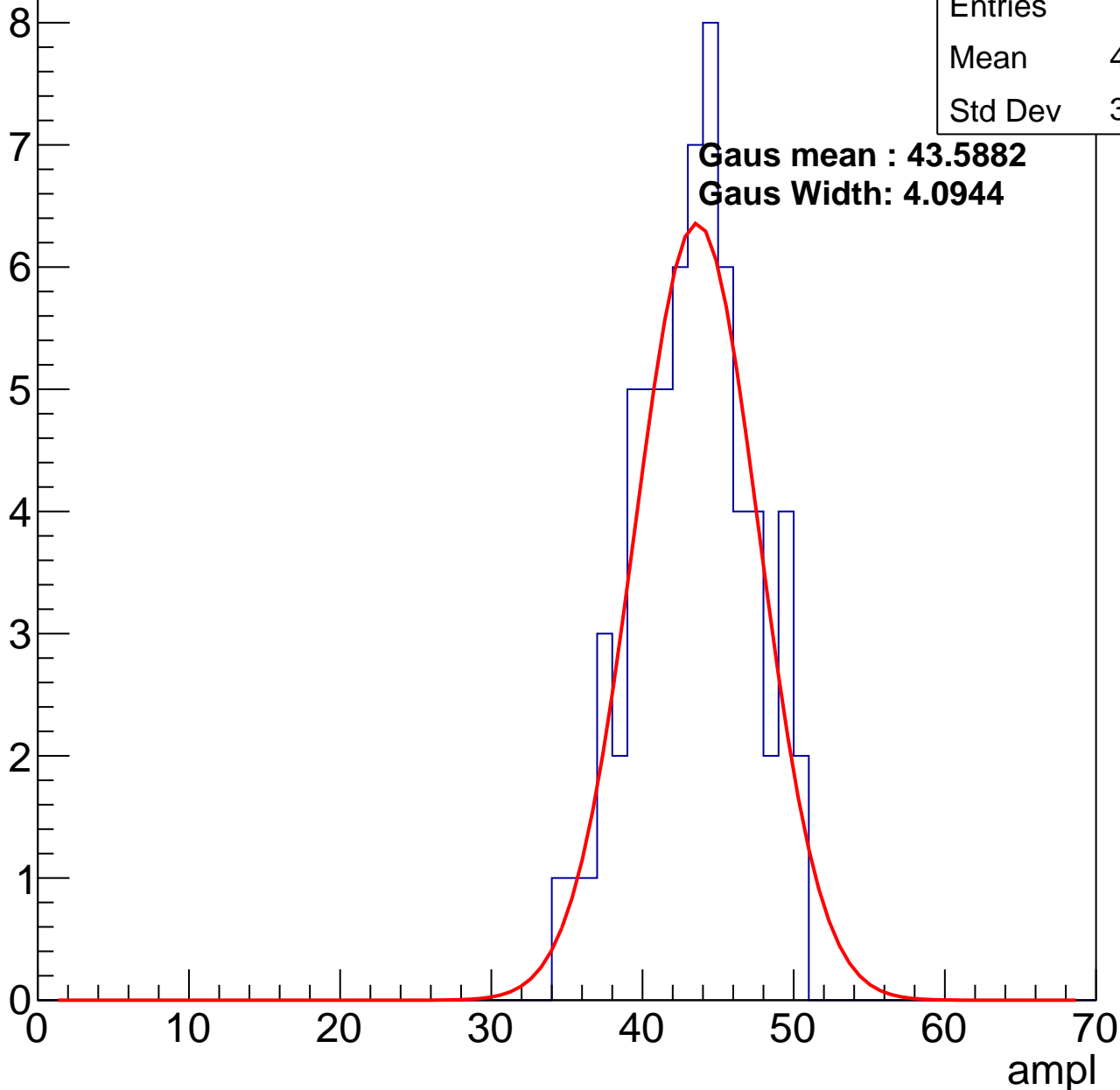
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.89
Std Dev	3.746

**Gaus mean : 43.5882**

**Gaus Width: 4.0944**

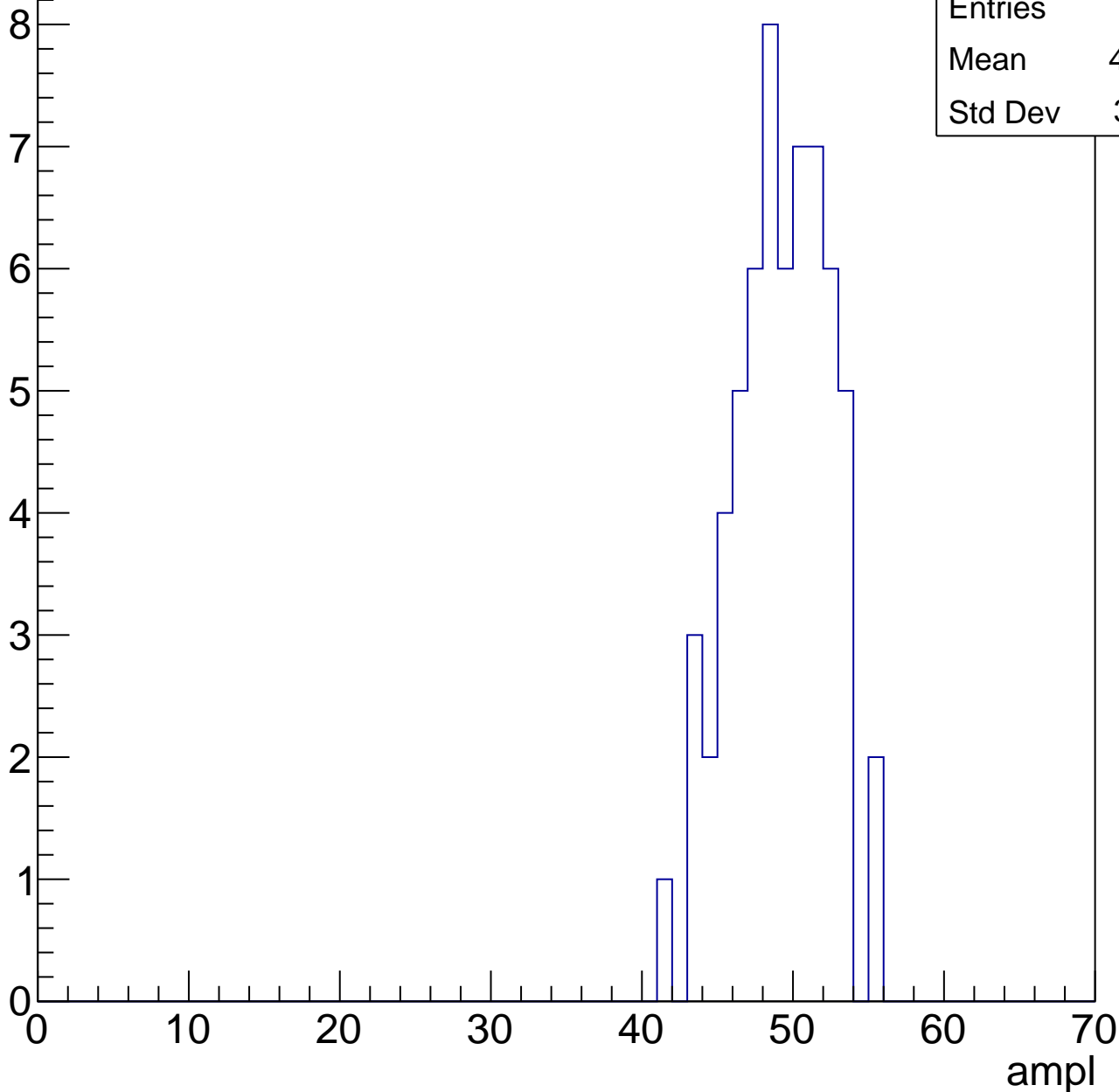


# B0L002S, U8-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	48.74
Std Dev	3.111

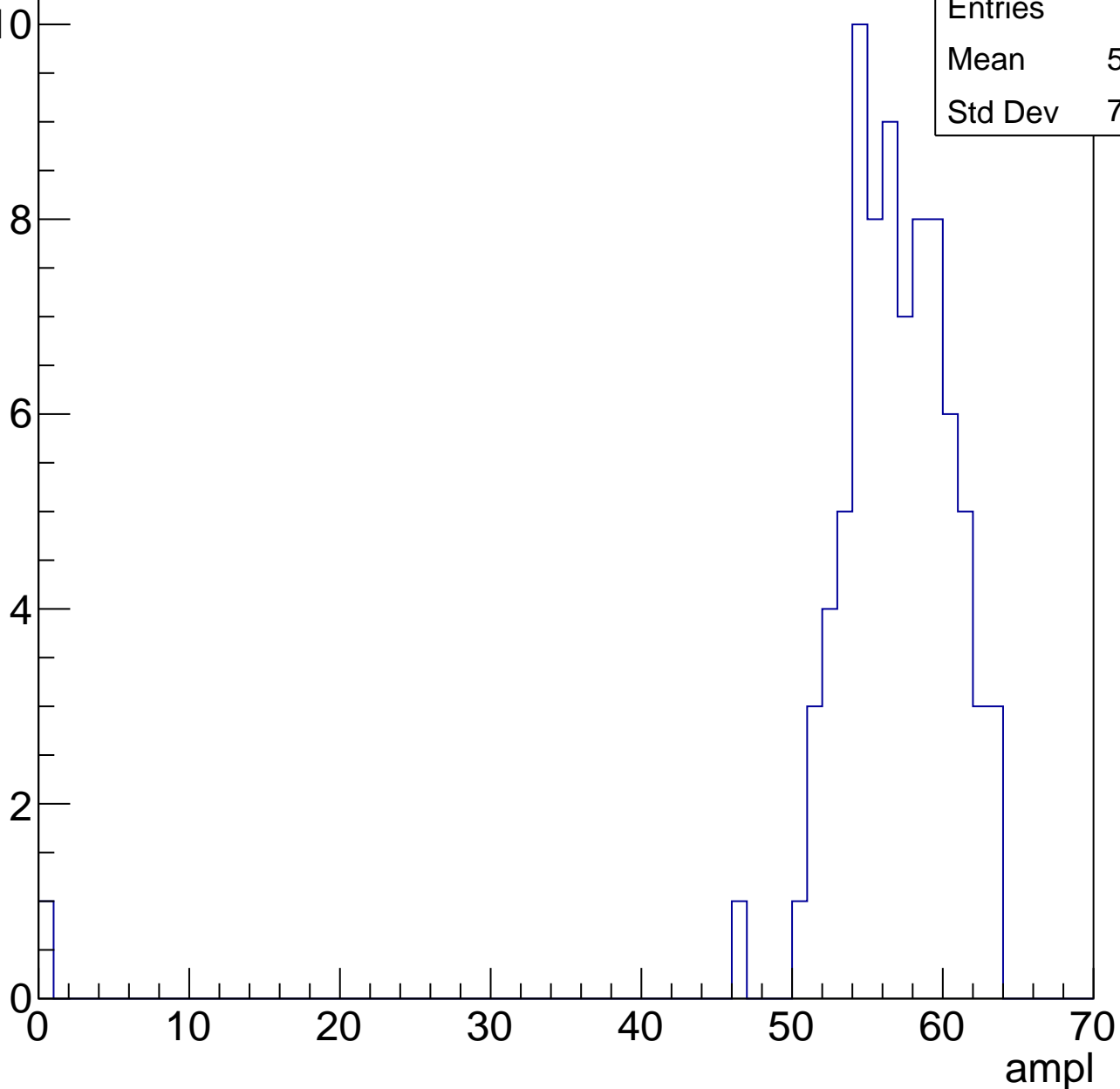


# B0L002S, U8-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	82
Mean	55.87
Std Dev	7.062

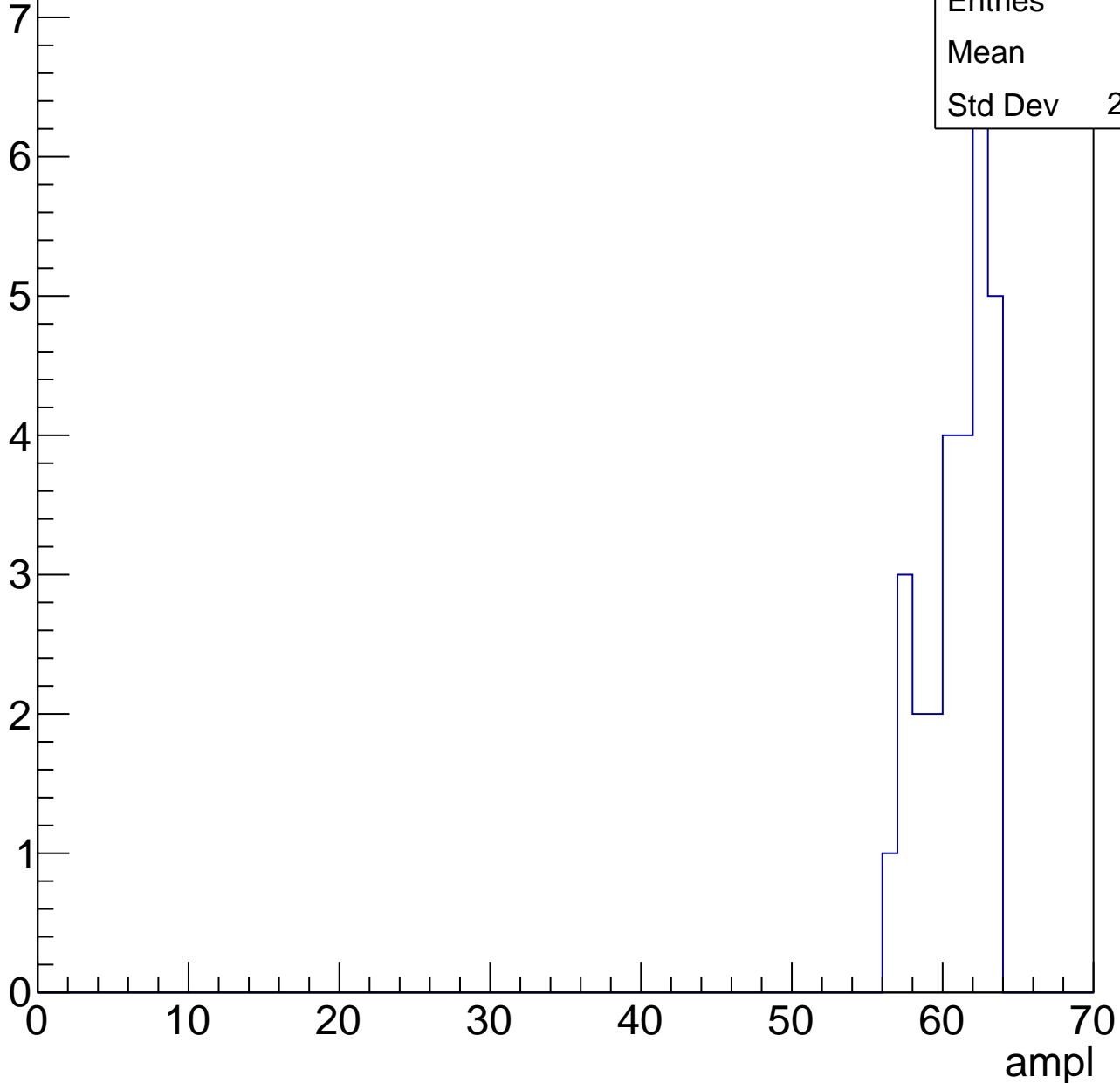


# B0L002S, U8-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	60.5
Std Dev	2.096



# B0L002S, U8-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch16, adc0

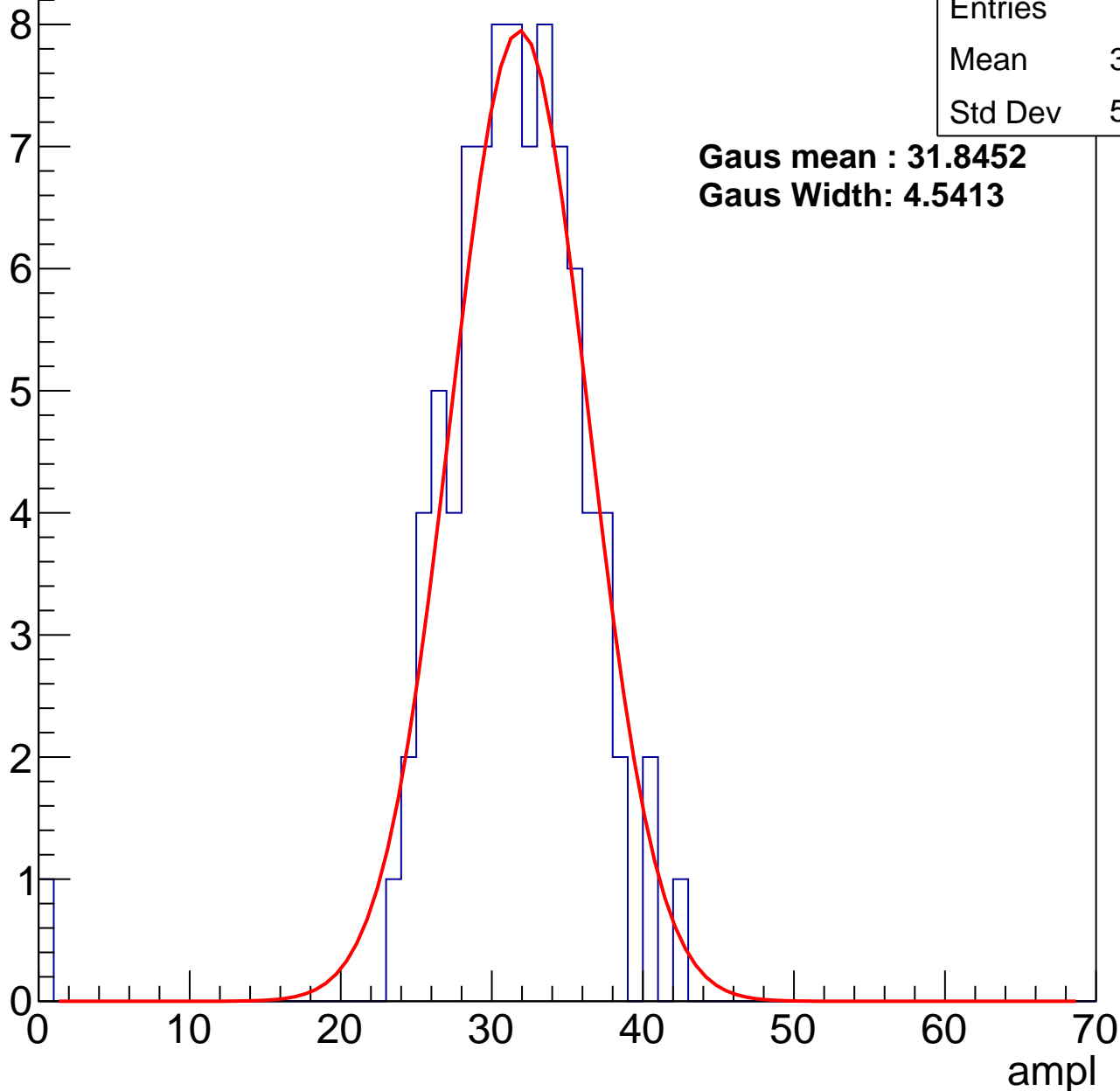
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	88
Mean	30.93
Std Dev	5.198

**Gaus mean : 31.8452**

**Gaus Width: 4.5413**



# B0L002S, U8-ch16, adc1

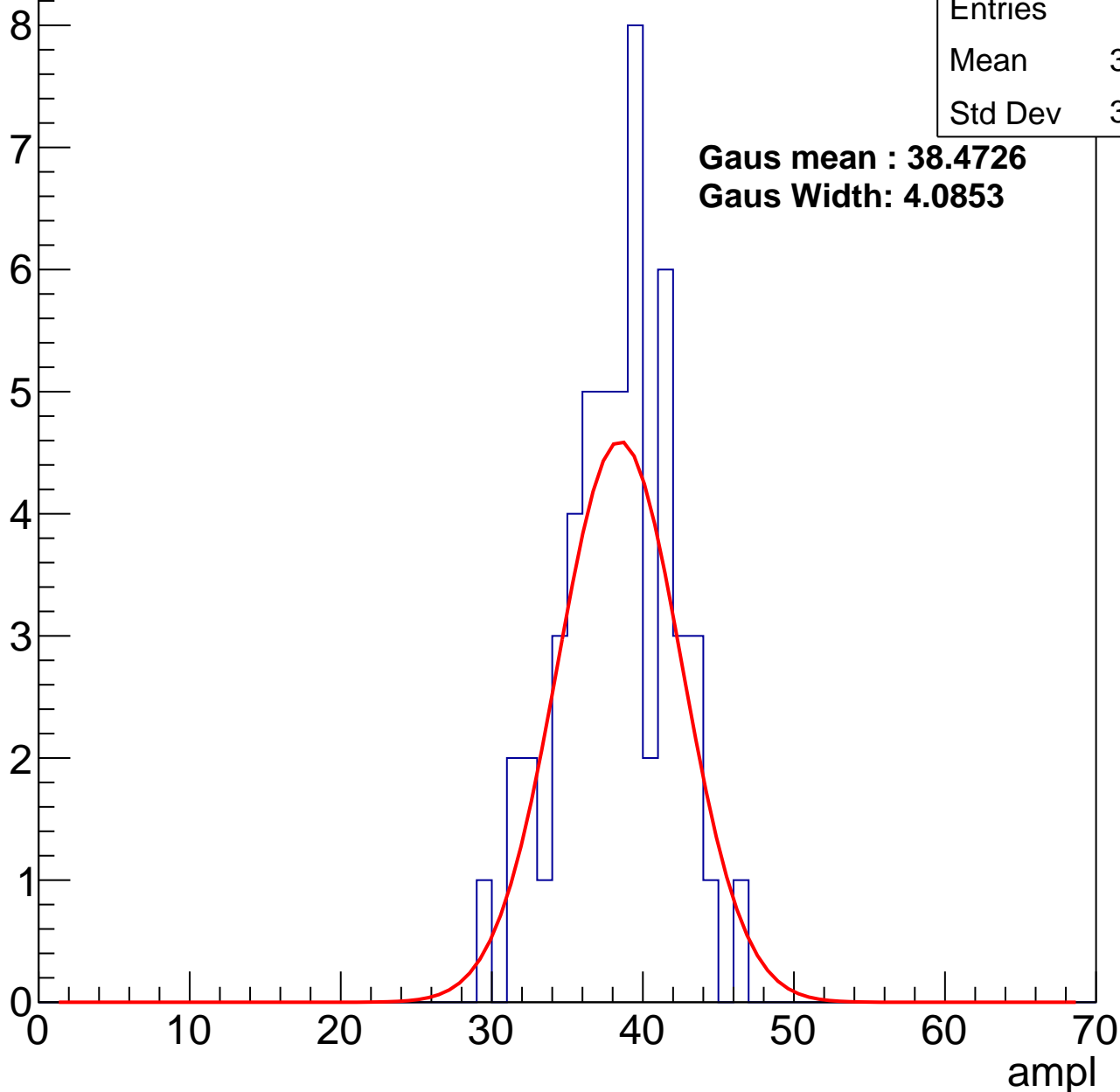
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	37.85
Std Dev	3.608

**Gaus mean : 38.4726**

**Gaus Width: 4.0853**



# B0L002S, U8-ch16, adc2

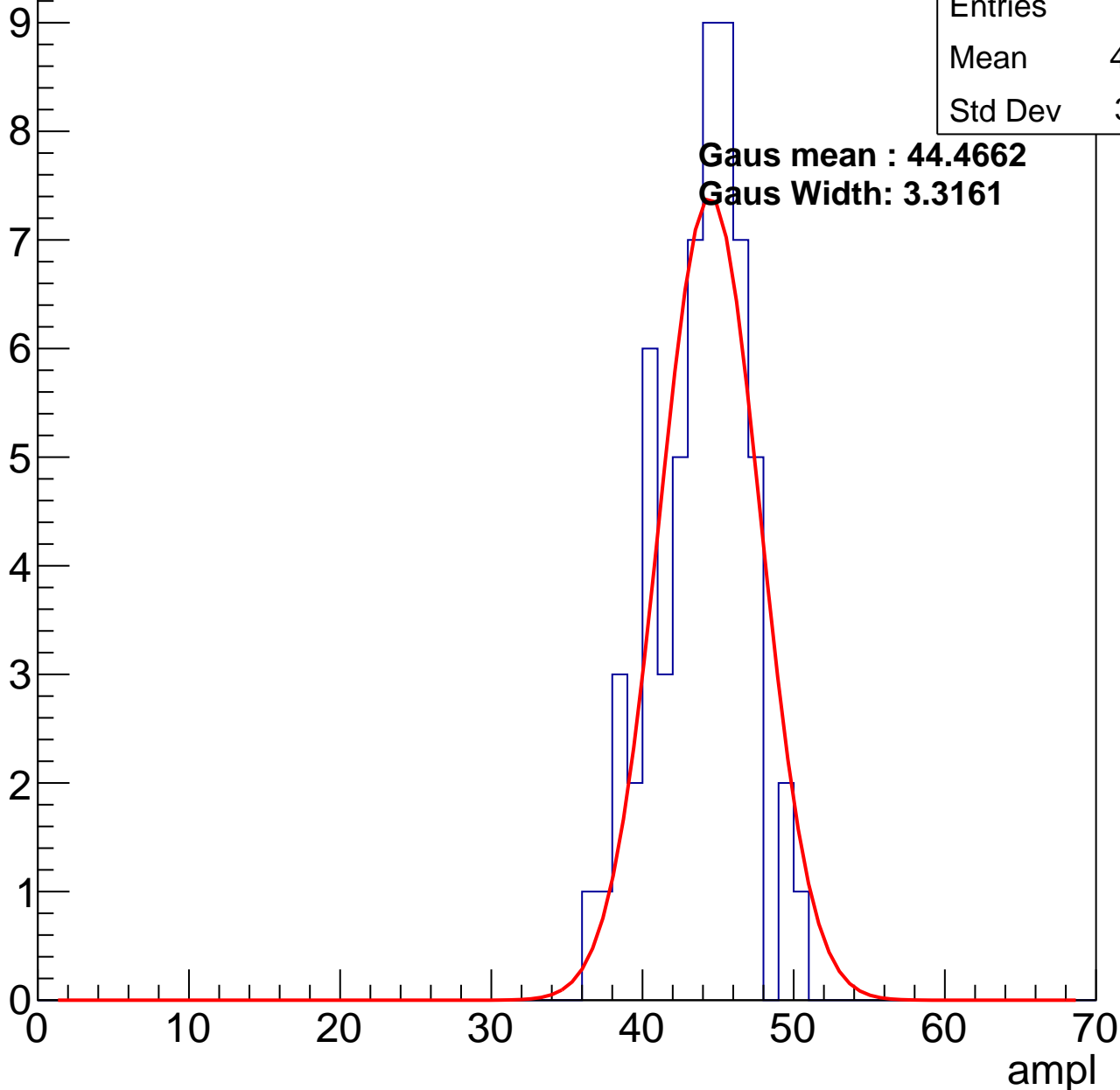
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.36
Std Dev	3.041

**Gaus mean : 44.4662**

**Gaus Width: 3.3161**

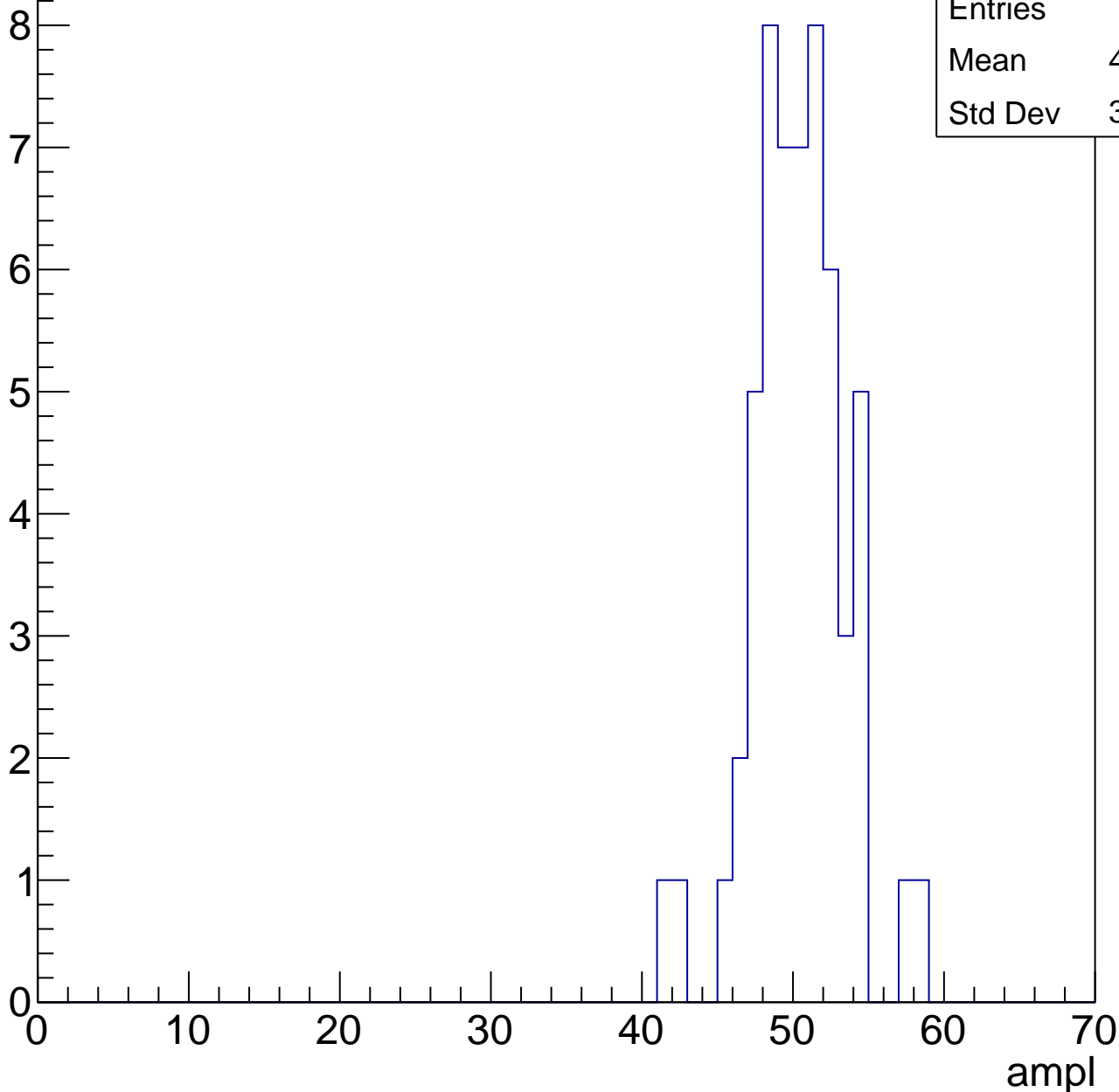


# B0L002S, U8-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

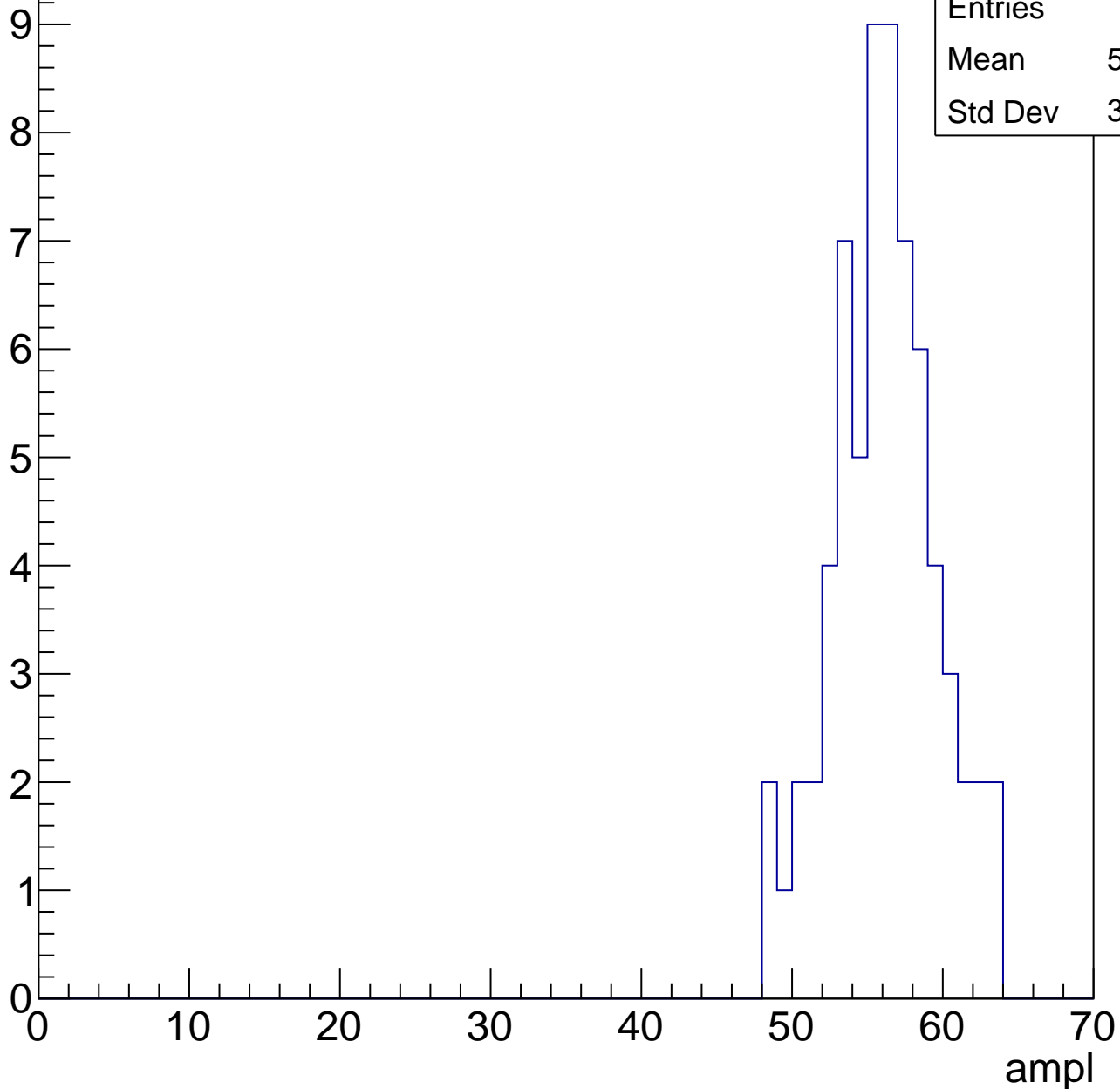
Entries	56
Mean	49.93
Std Dev	3.099



# B0L002S, U8-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



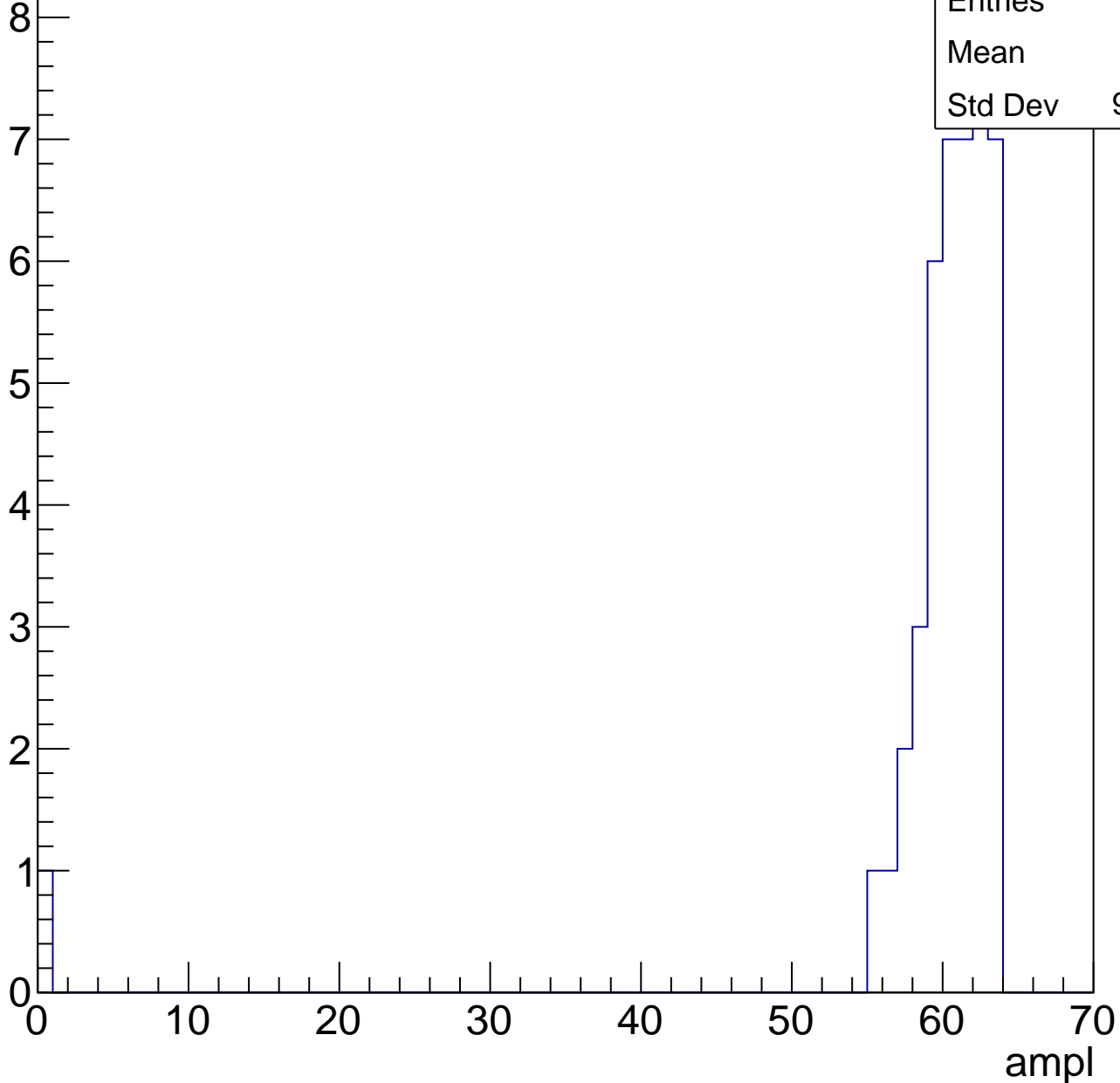
Entries	67
Mean	55.67
Std Dev	3.418

# B0L002S, U8-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59
Std Dev	9.321



# B0L002S, U8-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch17, adc0

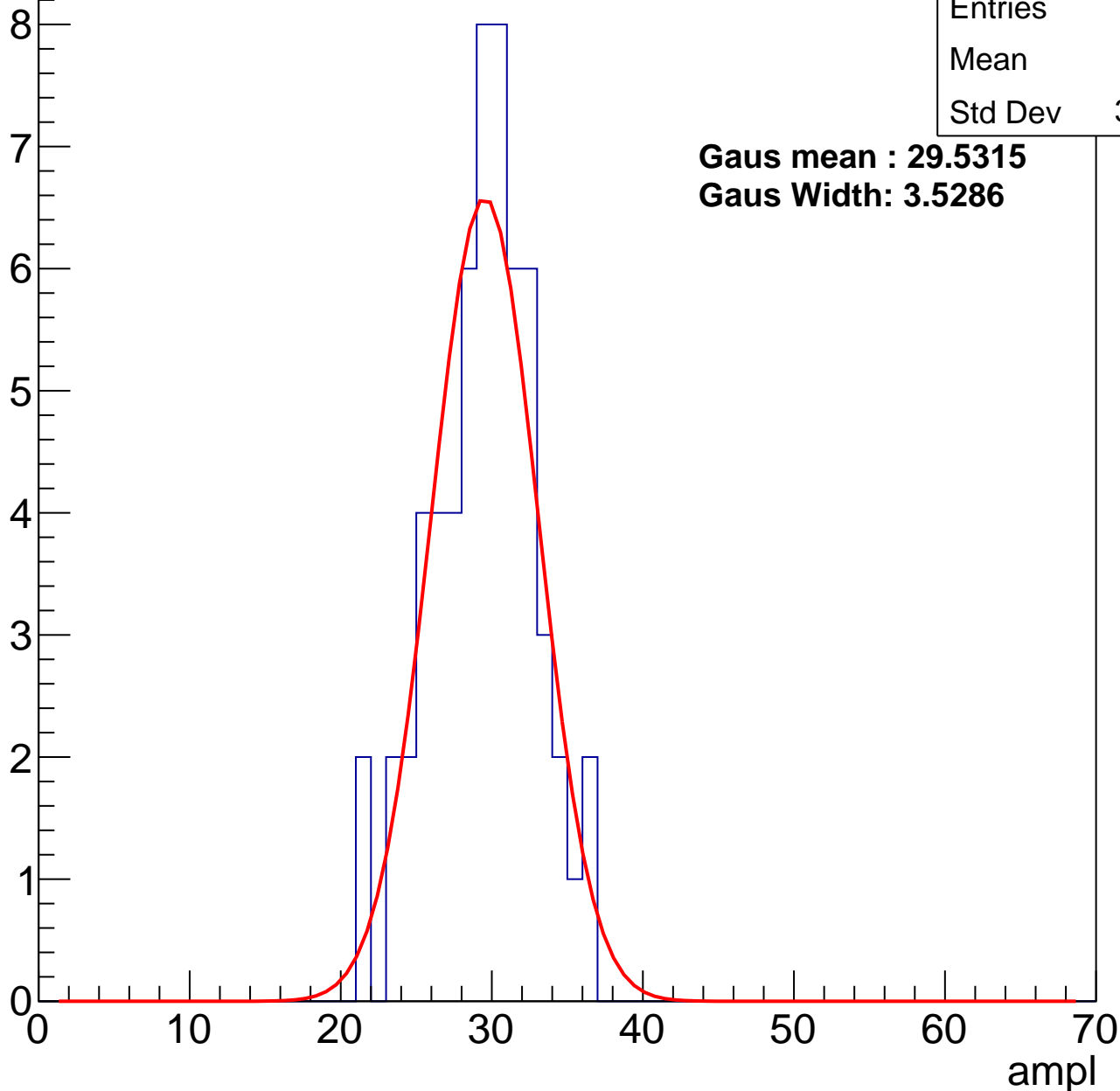
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29
Std Dev	3.391

**Gaus mean : 29.5315**

**Gaus Width: 3.5286**



# B0L002S, U8-ch17, adc1

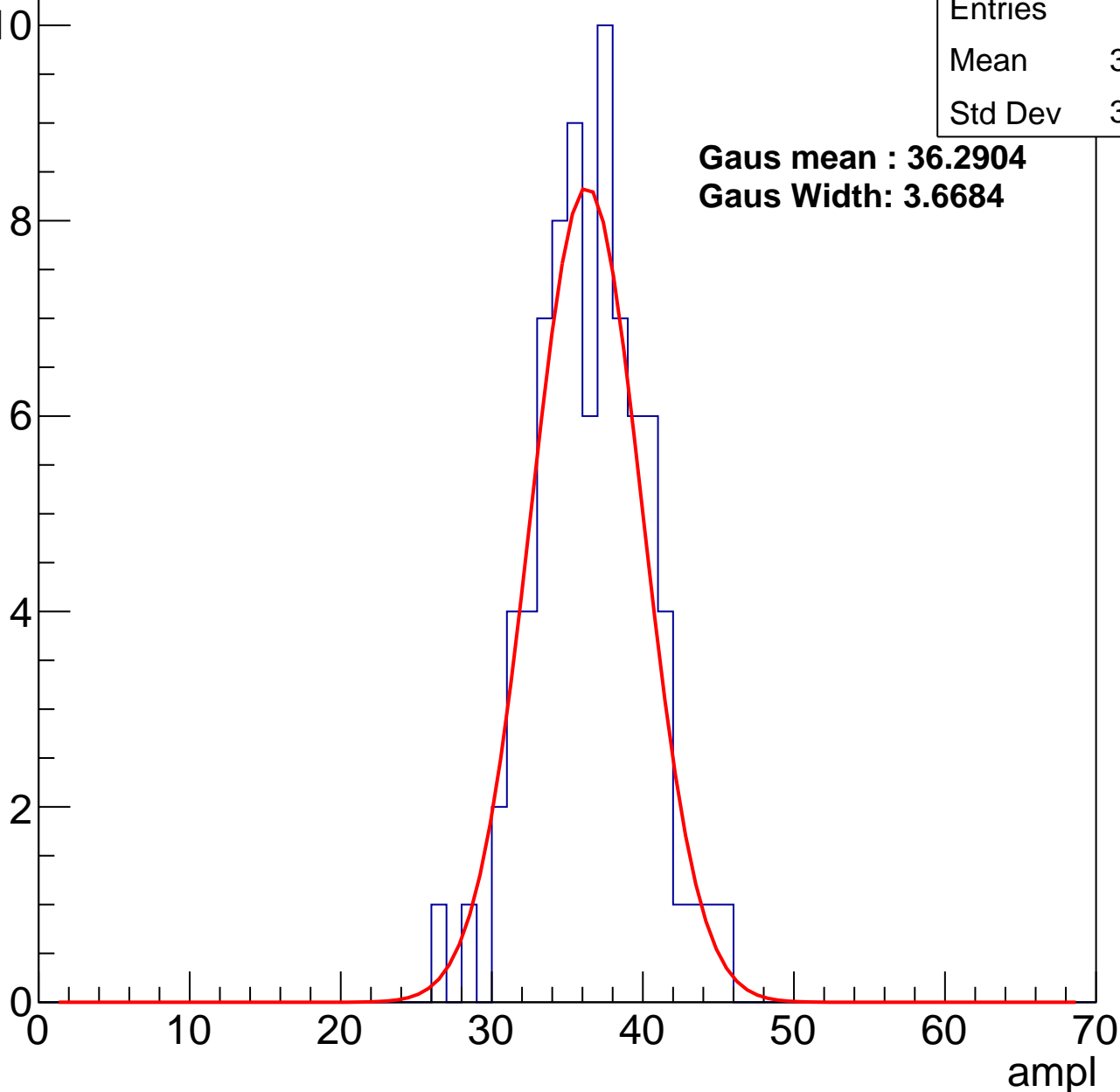
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.05
Std Dev	3.596

**Gaus mean : 36.2904**

**Gaus Width: 3.6684**



# B0L002S, U8-ch17, adc2

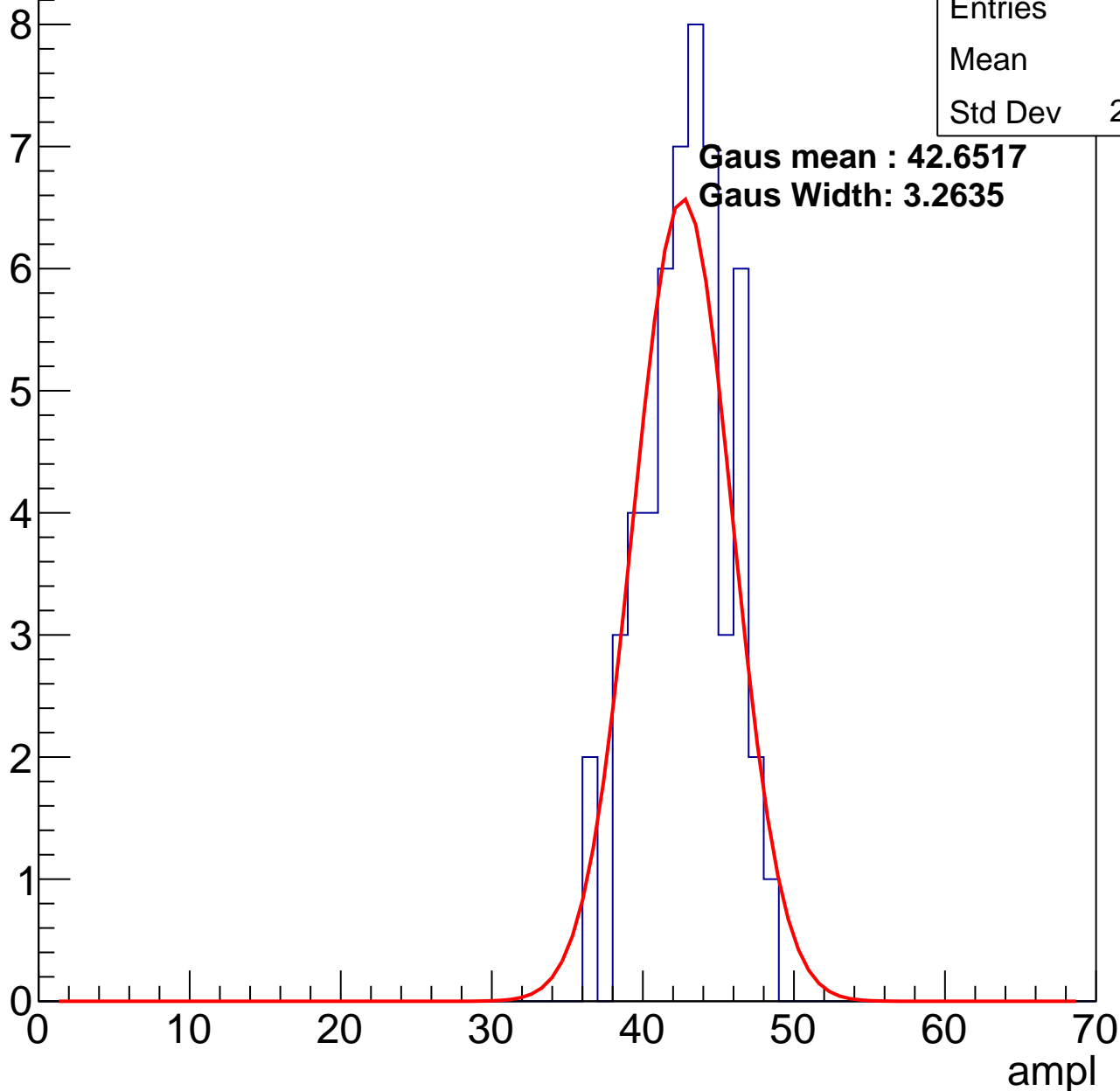
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.4
Std Dev	2.797

**Gaus mean : 42.6517**

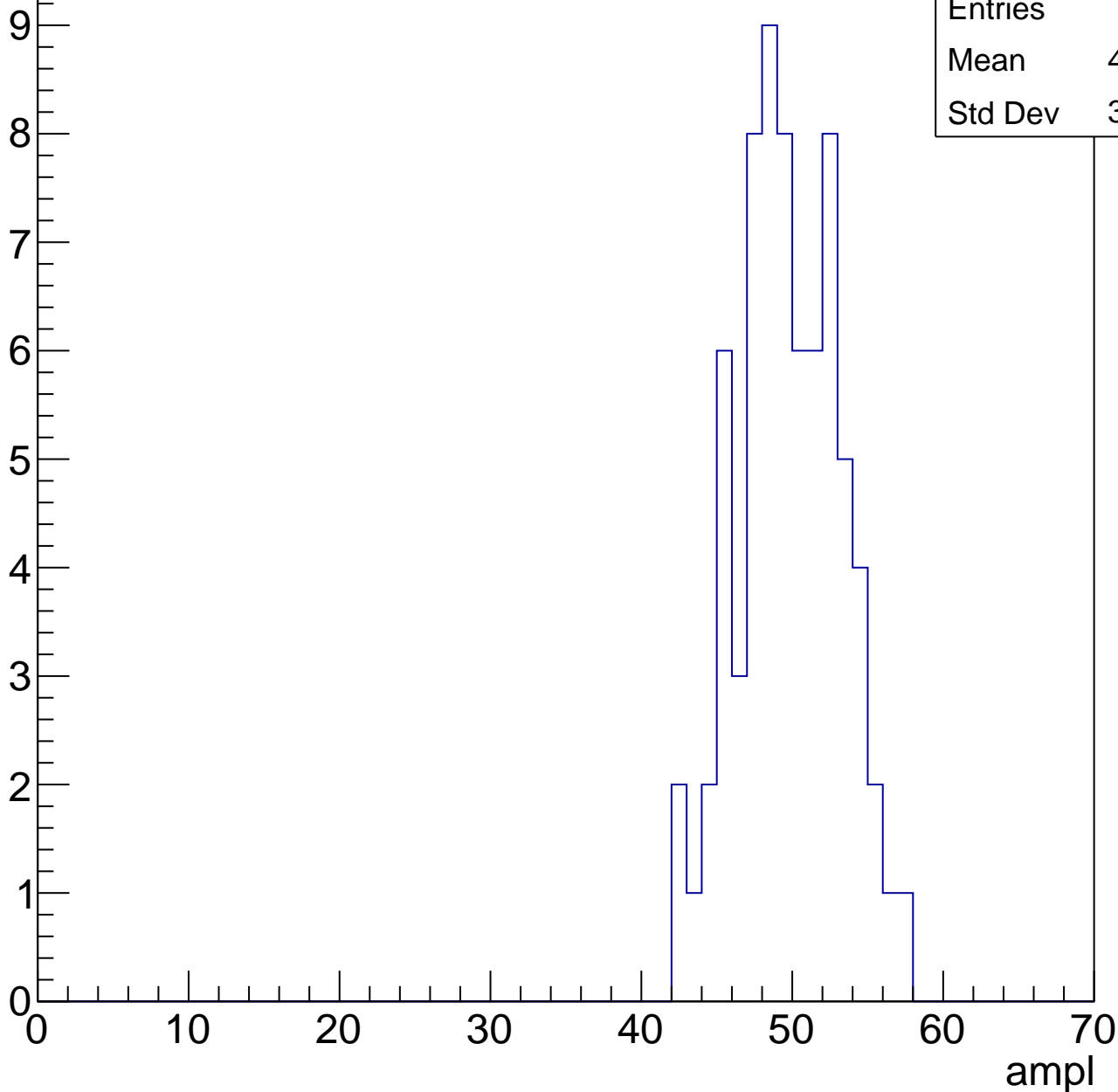
**Gaus Width: 3.2635**



# B0L002S, U8-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

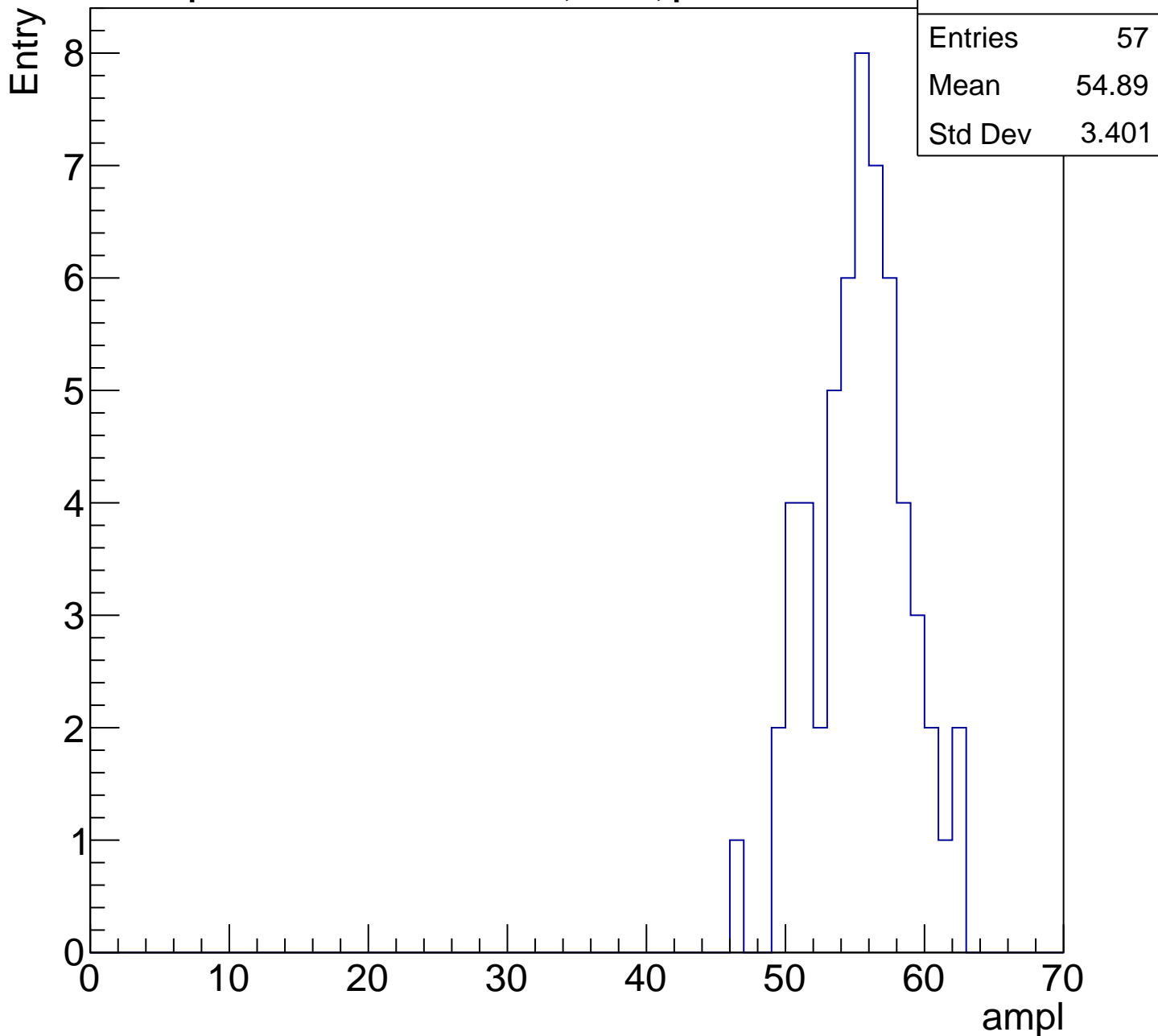
Entry



Entries	72
Mean	49.29
Std Dev	3.352

# B0L002S, U8-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch17, adc5

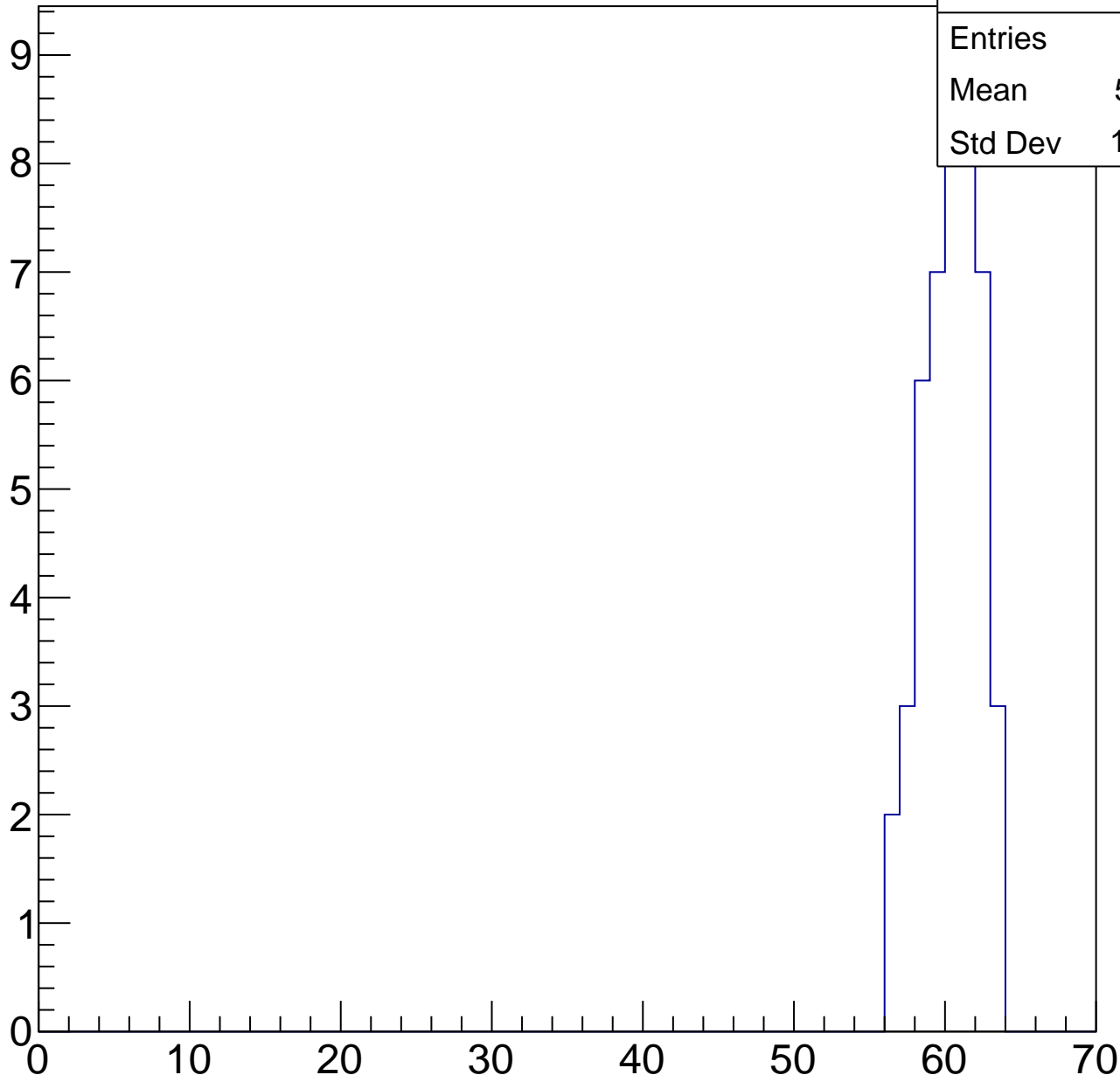
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	59.91
Std Dev	1.848

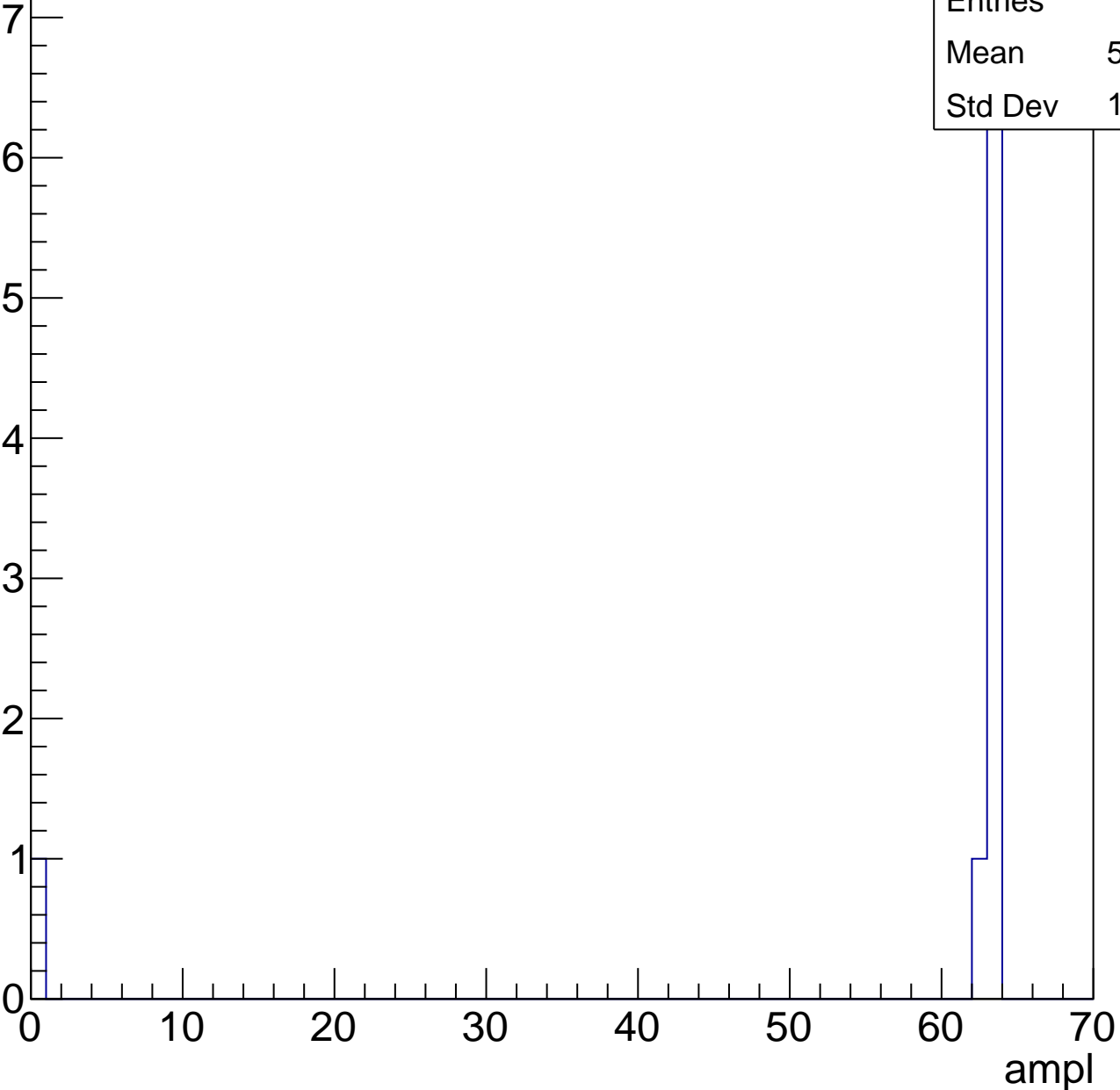
ampl



# B0L002S, U8-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	9
Mean	55.89
Std Dev	19.76



# B0L002S, U8-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch18, adc0

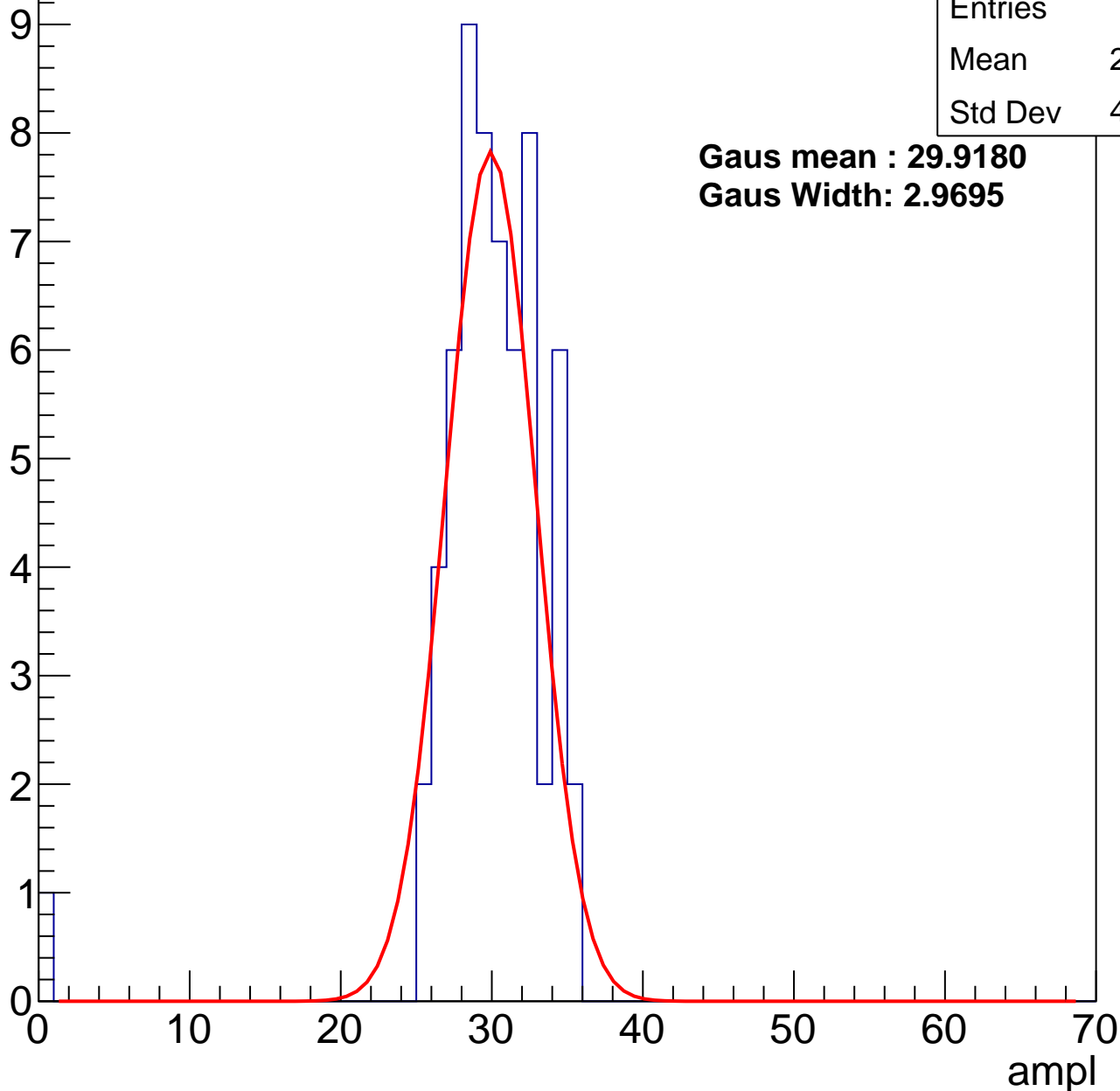
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.38
Std Dev	4.599

**Gaus mean : 29.9180**

**Gaus Width: 2.9695**



# B0L002S, U8-ch18, adc1

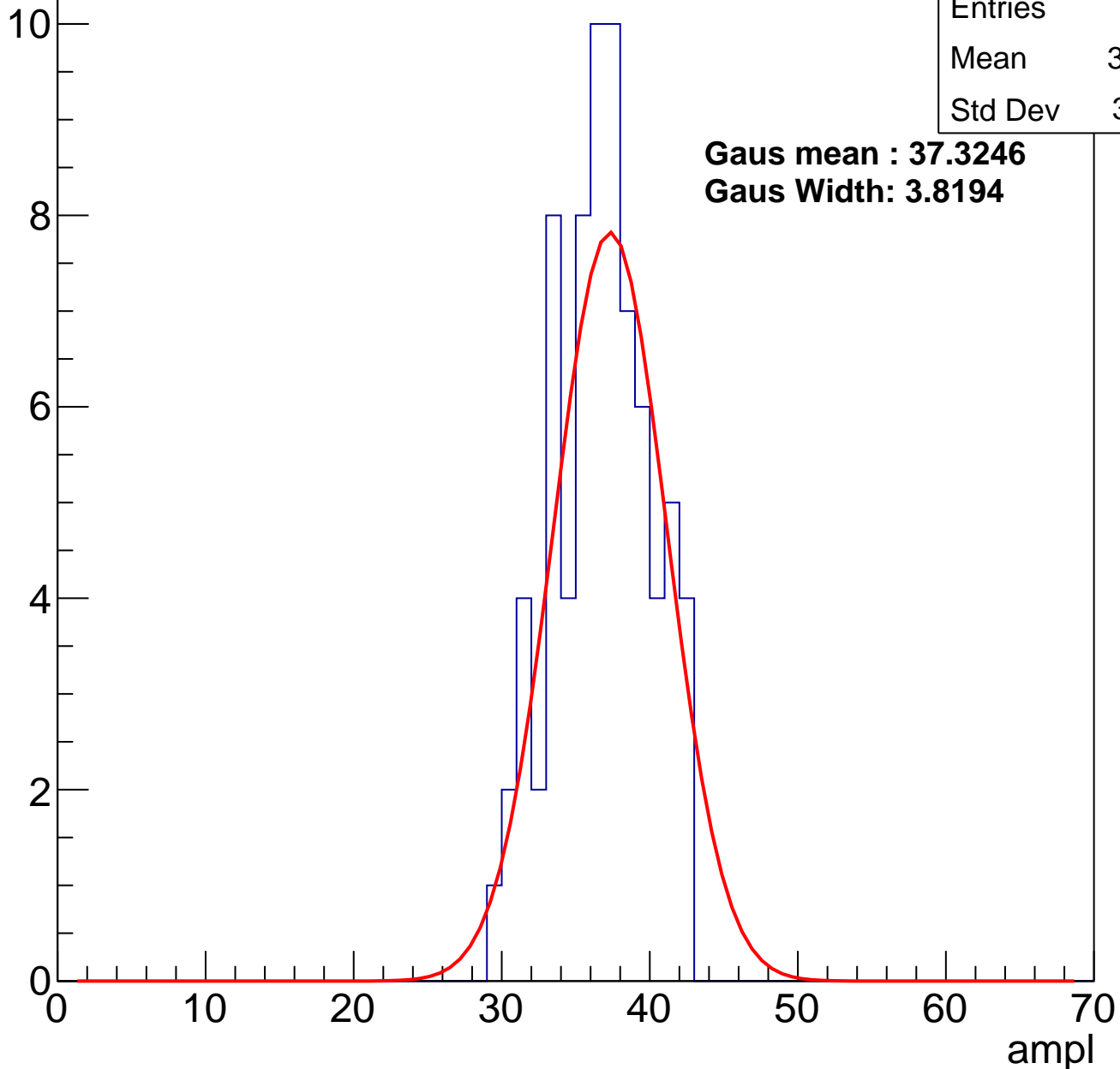
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	36.27
Std Dev	3.201

**Gaus mean : 37.3246**

**Gaus Width: 3.8194**

Entry



# B0L002S, U8-ch18, adc2

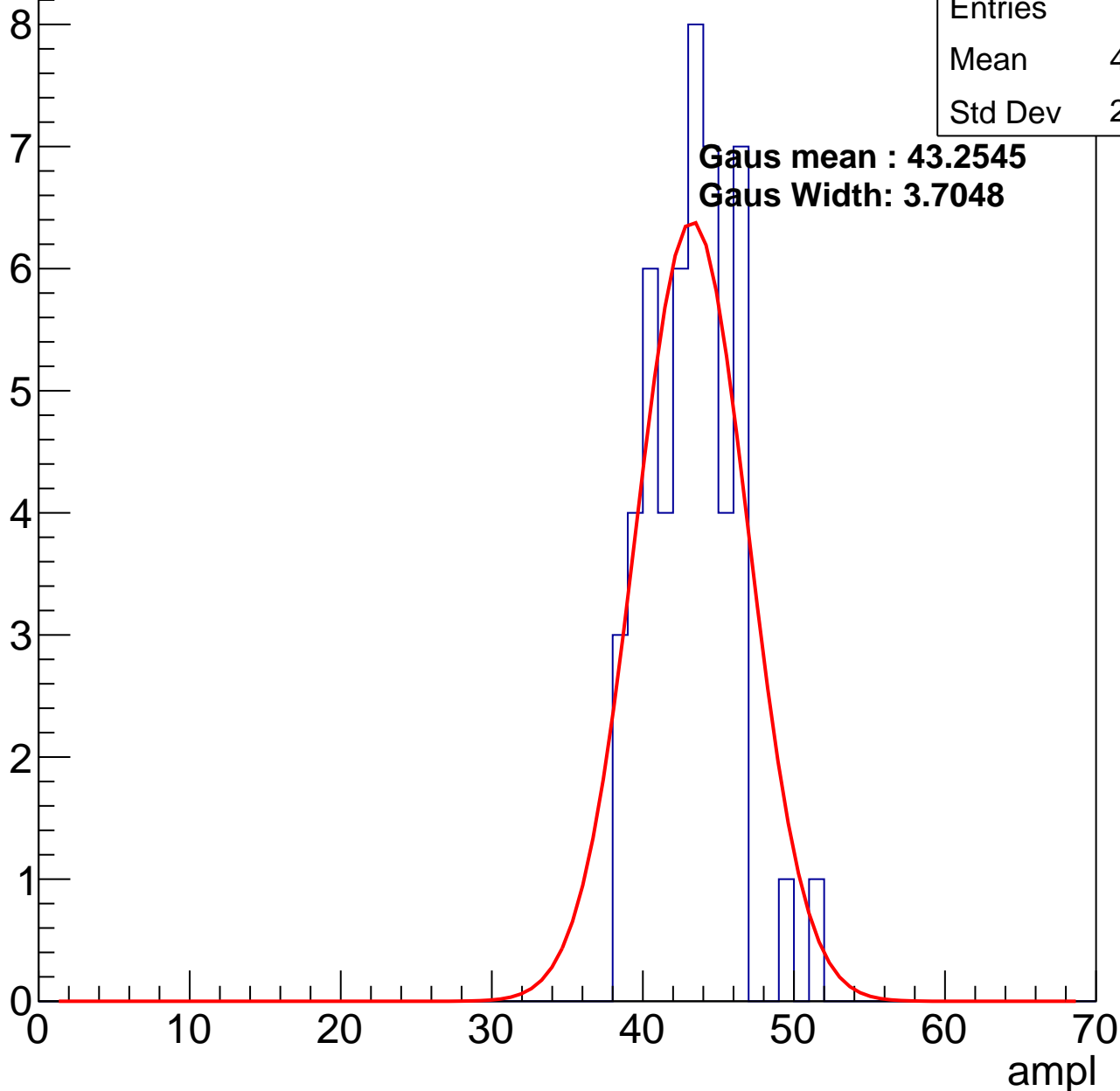
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.75
Std Dev	2.792

**Gaus mean : 43.2545**

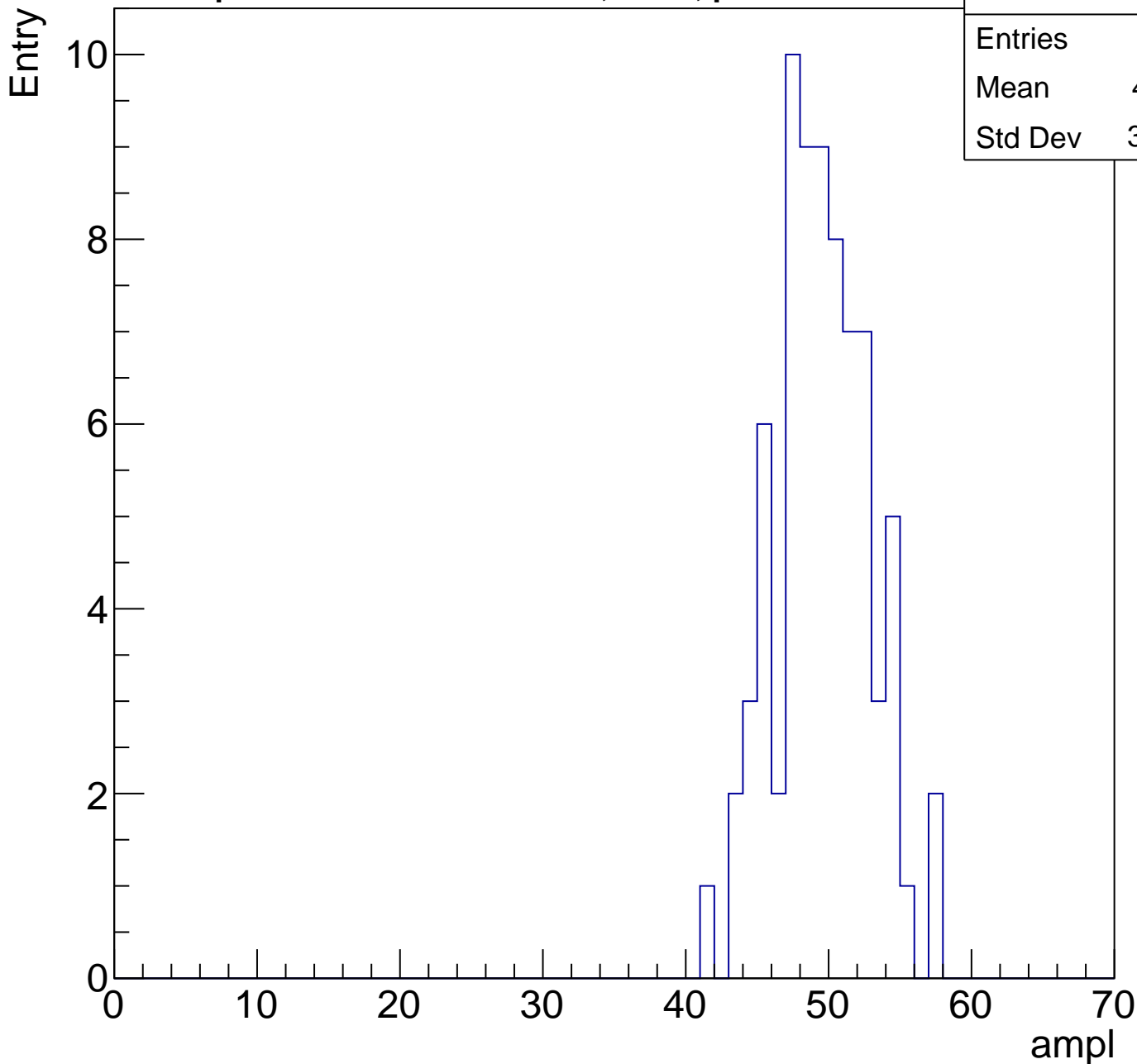
**Gaus Width: 3.7048**



# B0L002S, U8-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

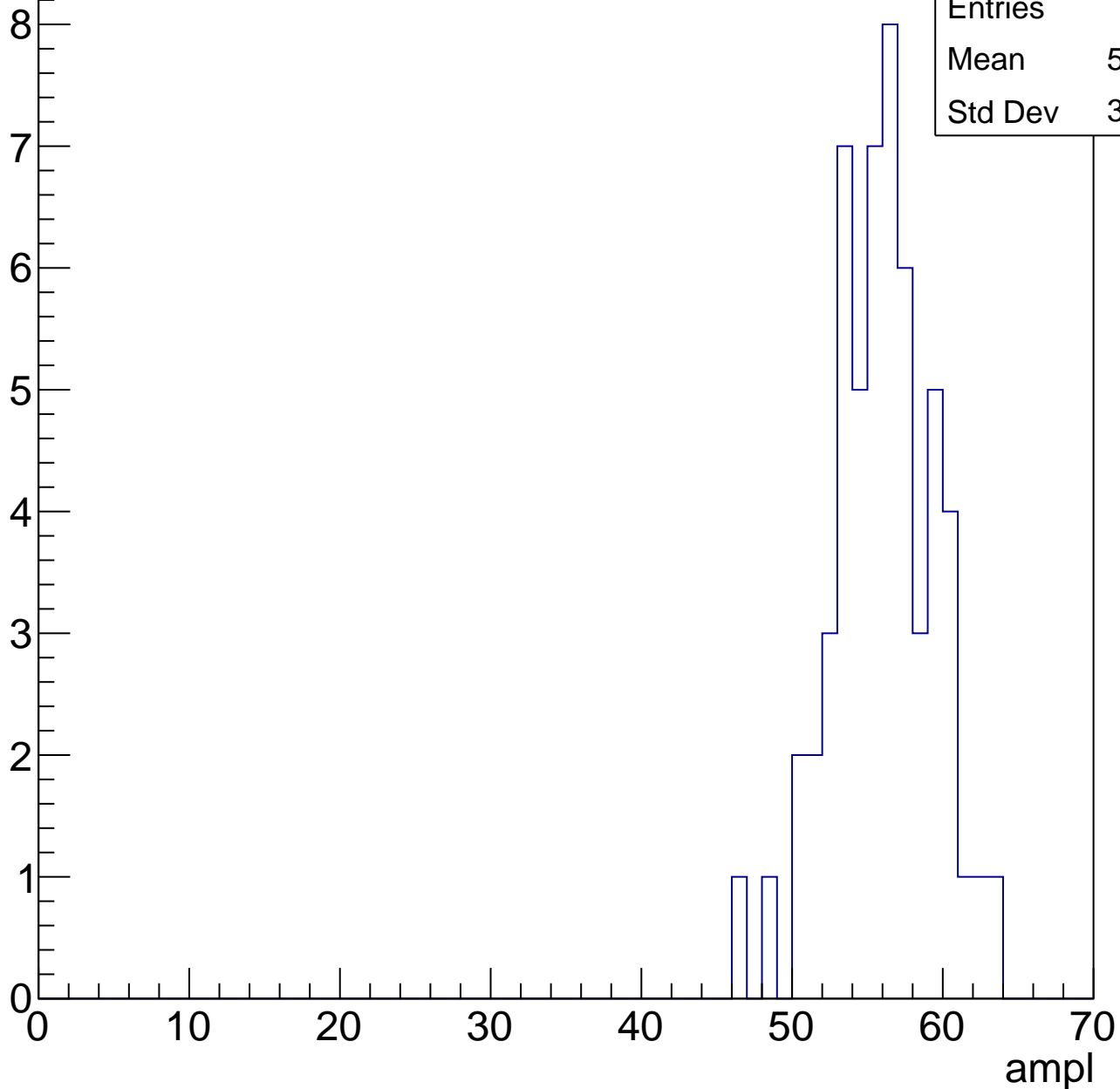
Entries	75
Mean	49.11
Std Dev	3.285



# B0L002S, U8-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

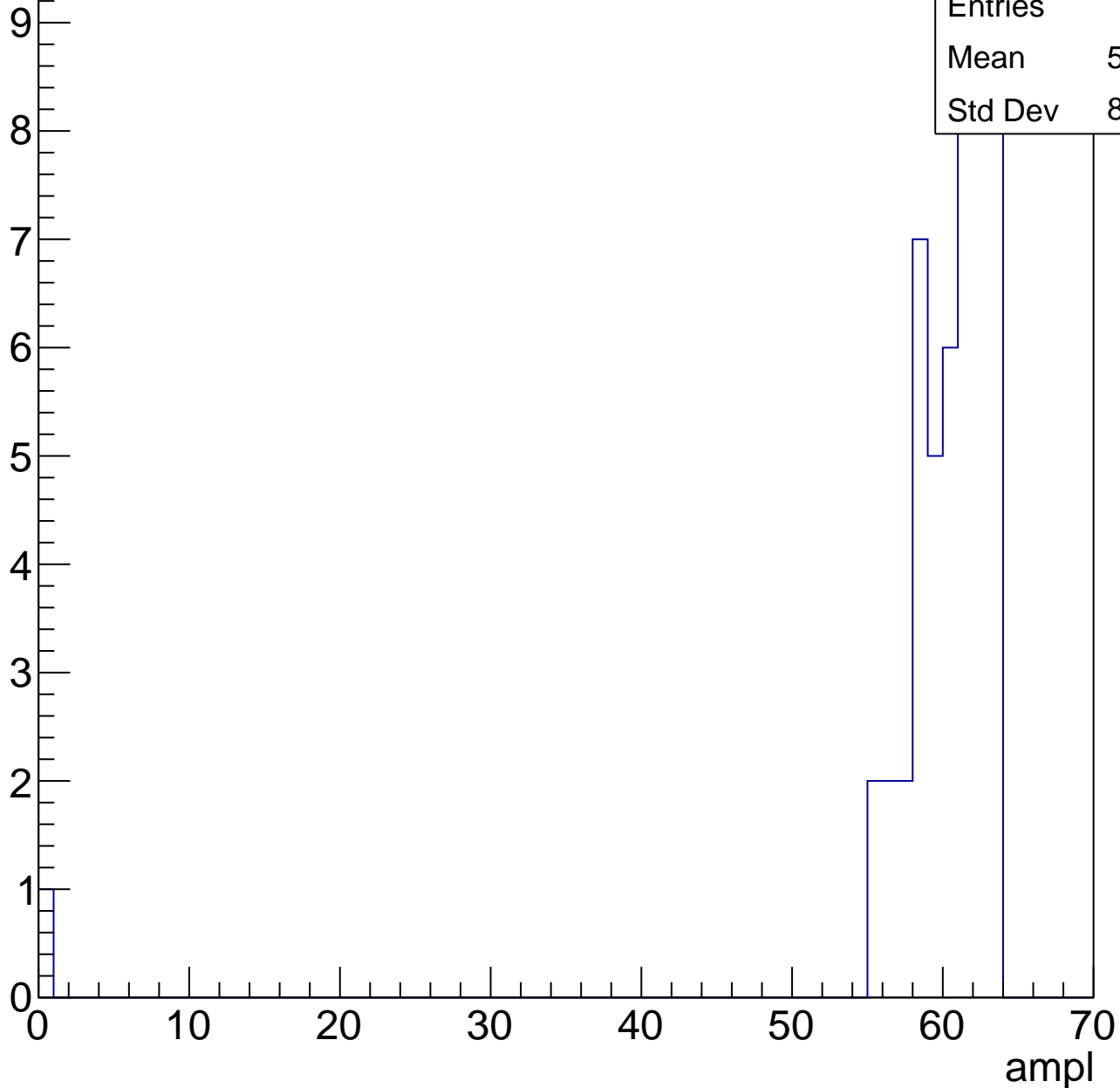
Entry



# B0L002S, U8-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



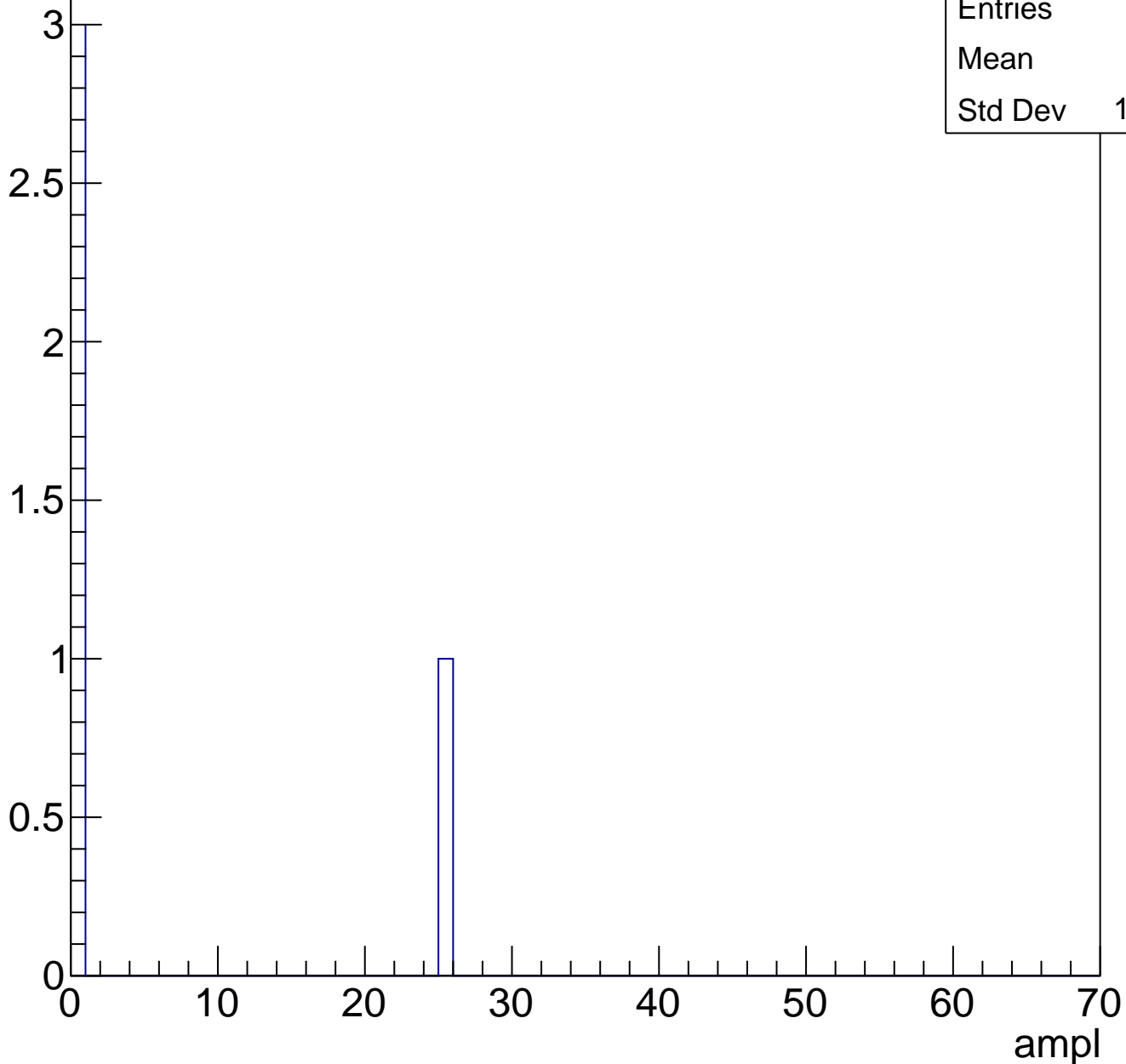
Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch19, adc0

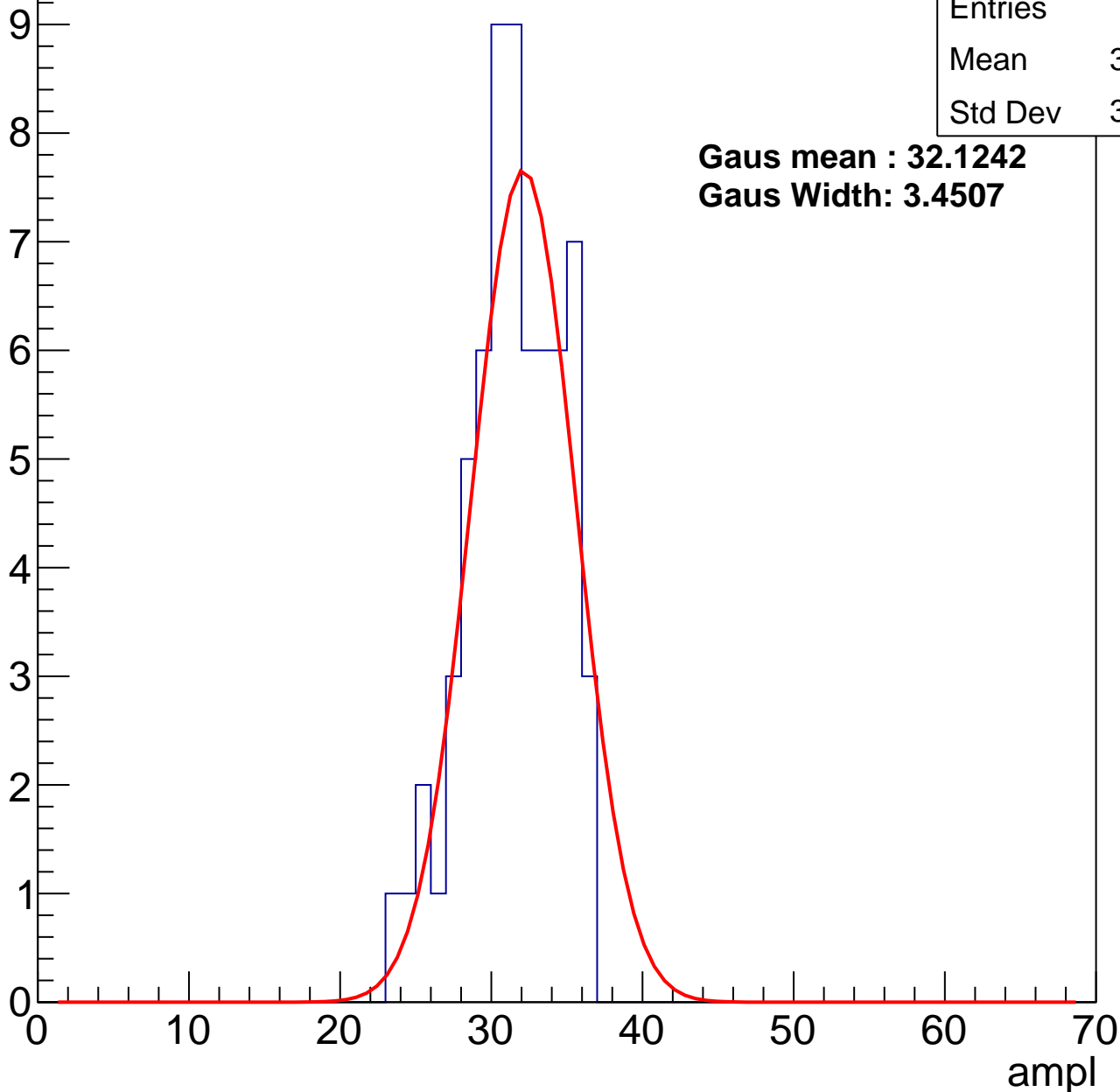
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.98
Std Dev	3.056

**Gaus mean : 32.1242**

**Gaus Width: 3.4507**



# B0L002S, U8-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	38.37
Std Dev	3.602

**Gaus mean : 38.6540**

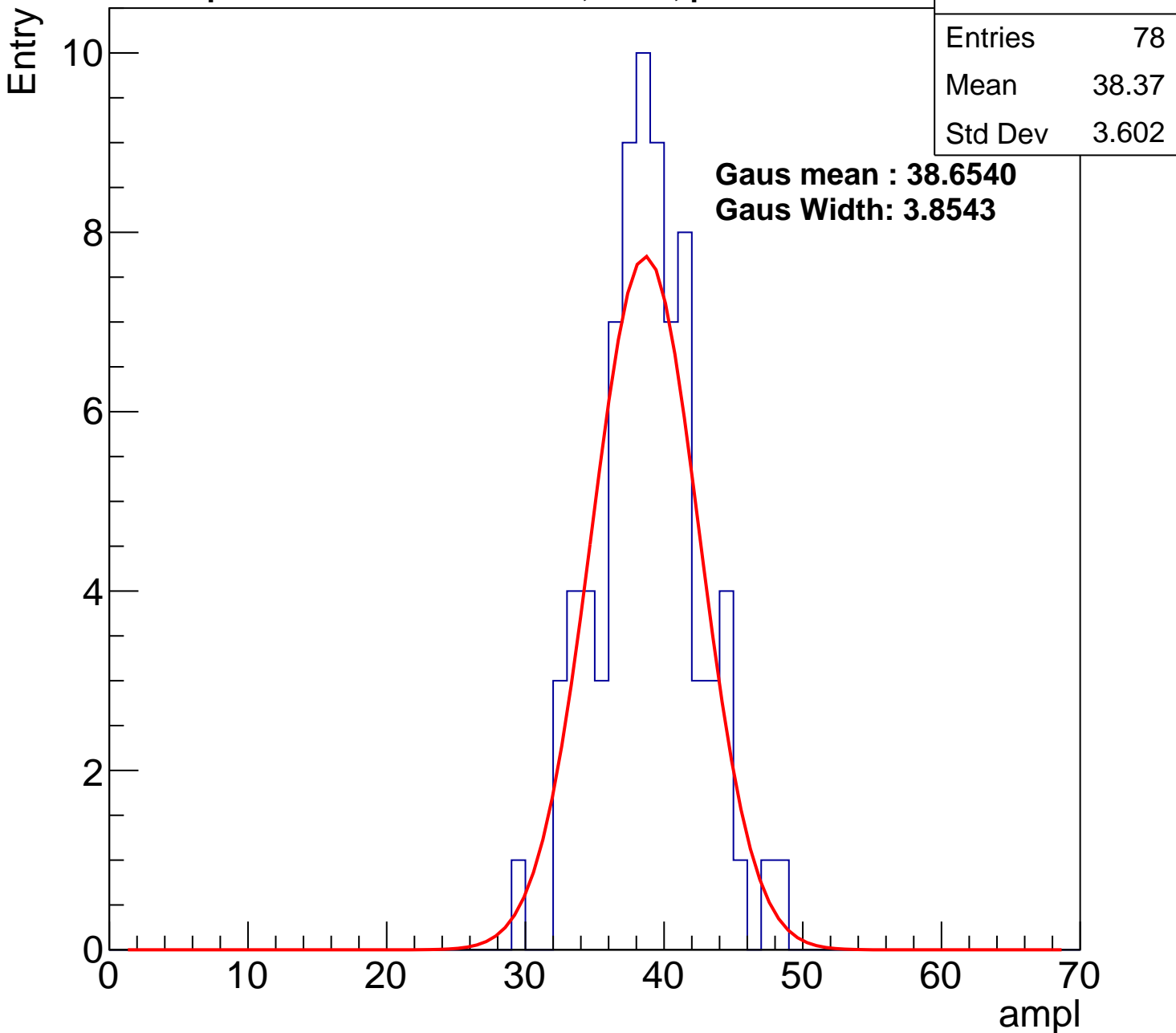
**Gaus Width: 3.8543**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch19, adc2

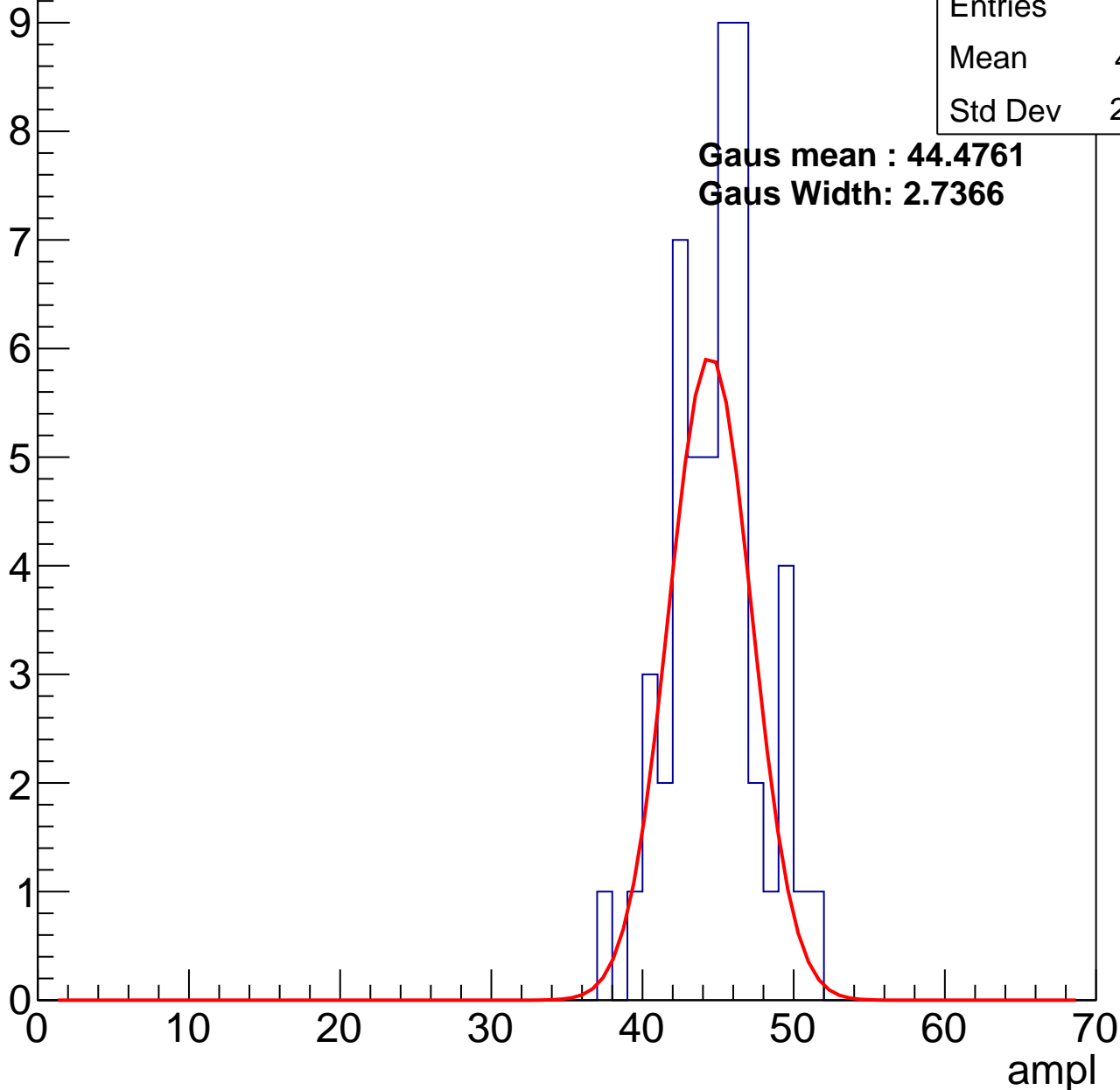
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	44.41
Std Dev	2.898

**Gaus mean : 44.4761**

**Gaus Width: 2.7366**

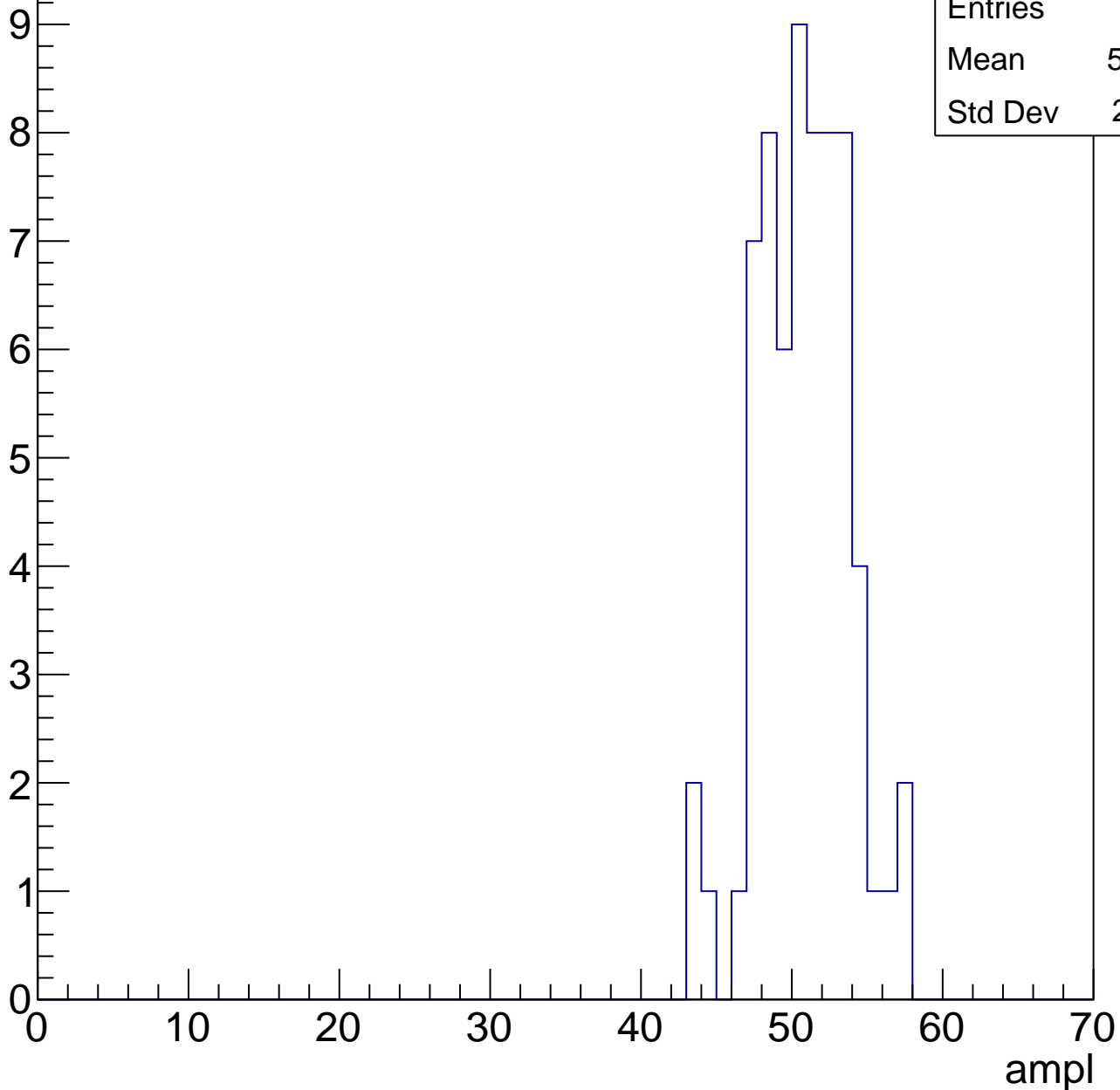


# B0L002S, U8-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.33
Std Dev	2.961



# B0L002S, U8-ch19, adc4

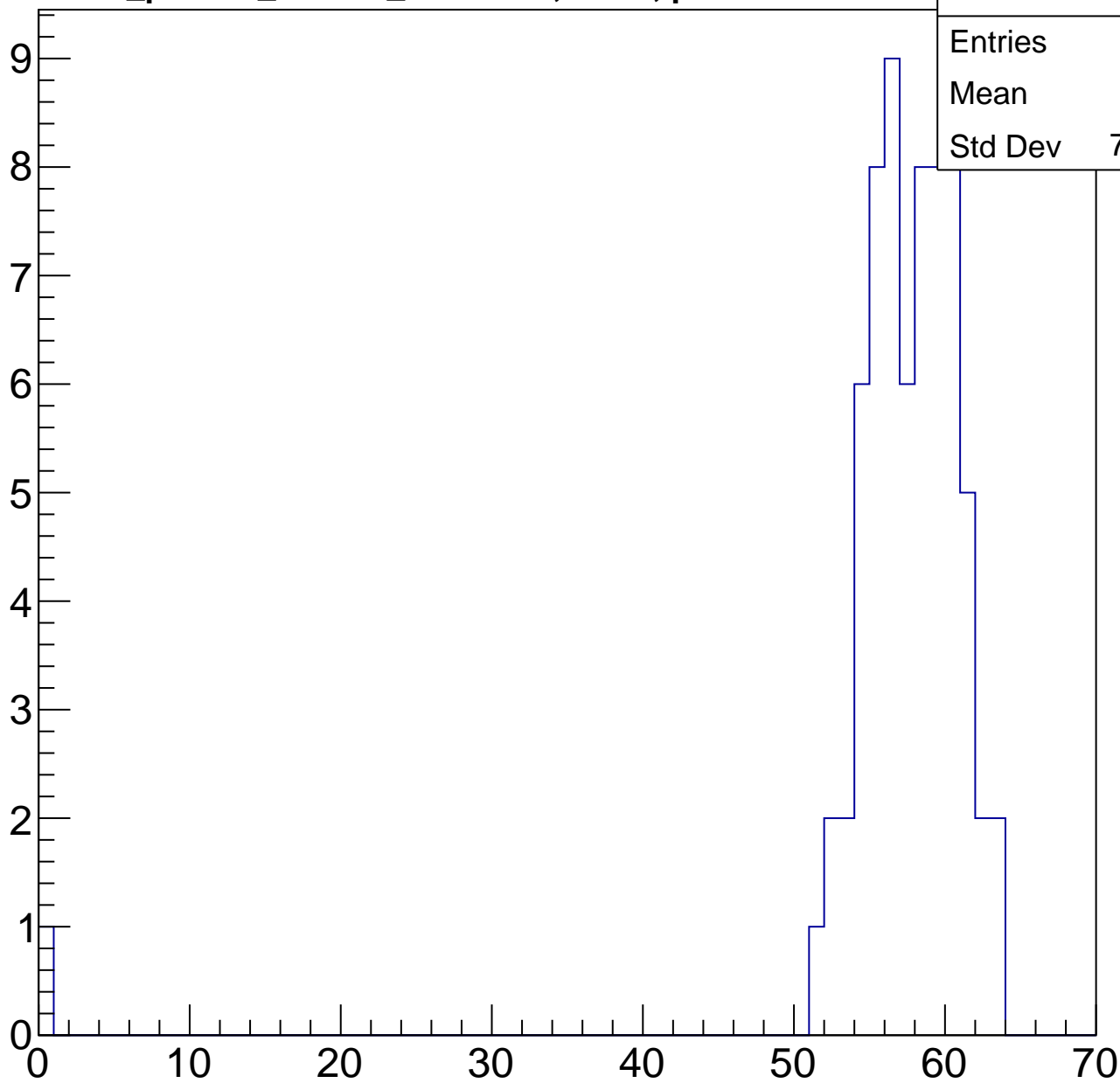
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	68
Mean	56.5
Std Dev	7.435

ampl

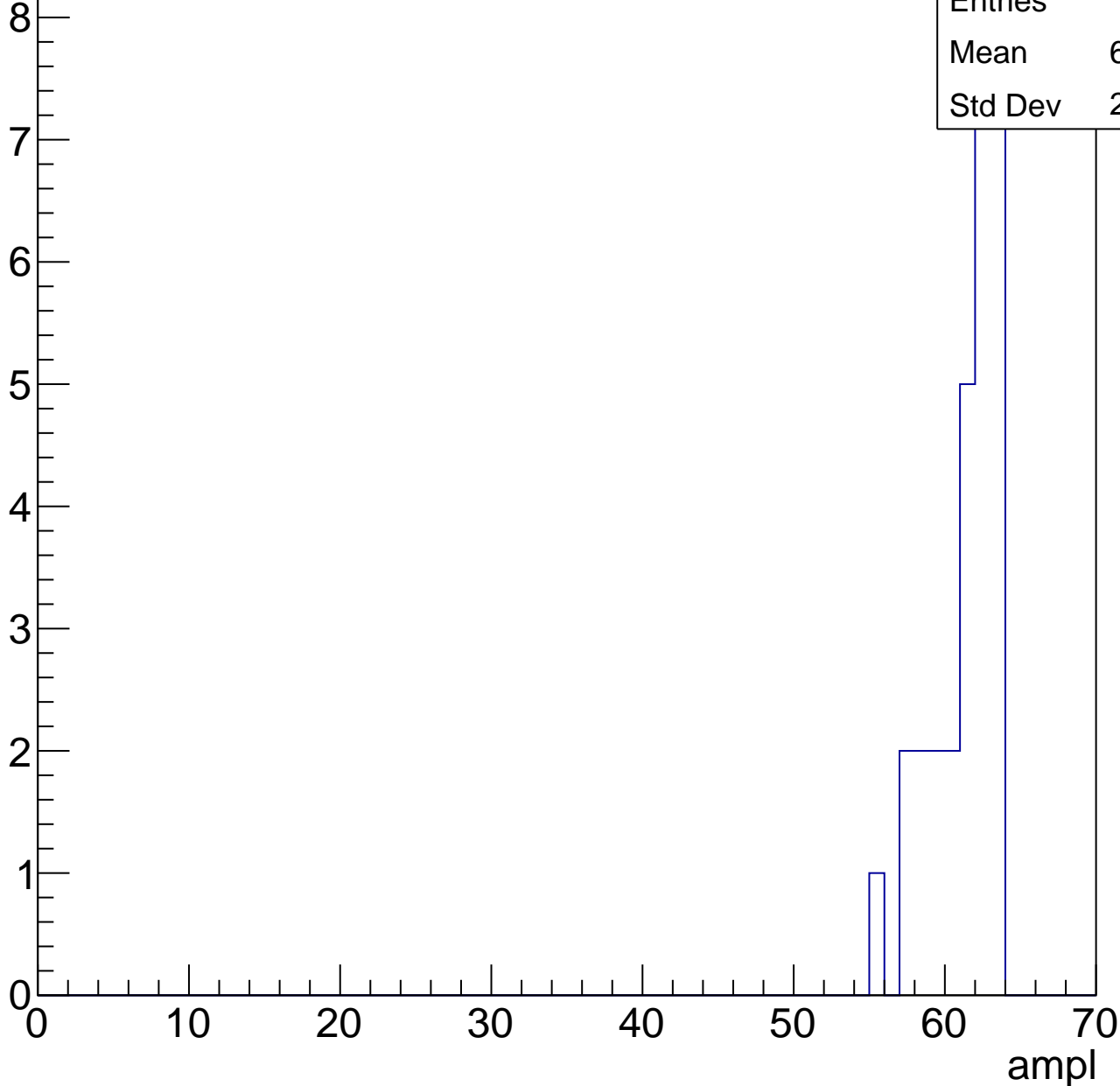


# B0L002S, U8-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.93
Std Dev	2.128



# B0L002S, U8-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch20, adc0

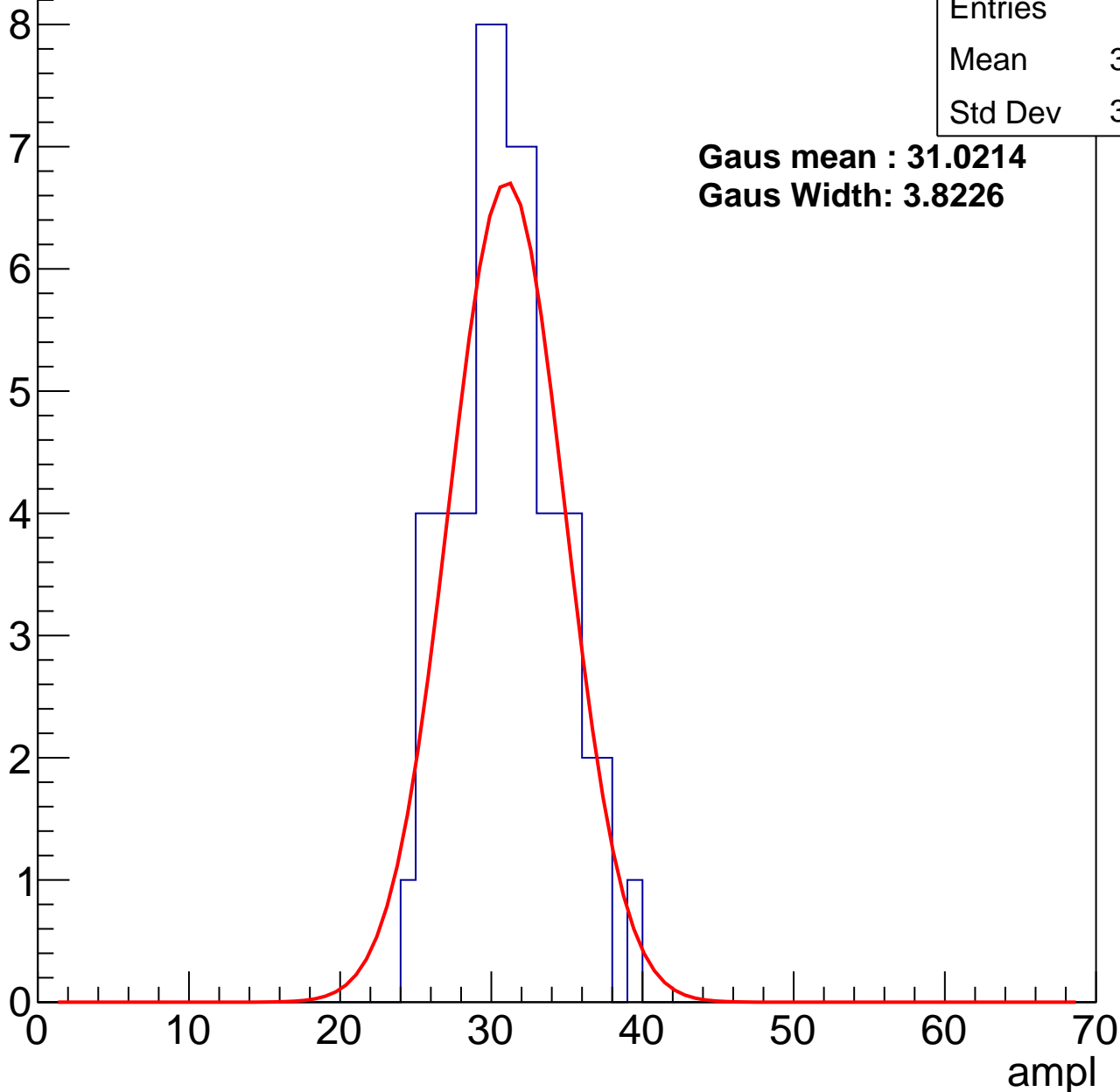
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.53
Std Dev	3.373

**Gaus mean : 31.0214**

**Gaus Width: 3.8226**



# B0L002S, U8-ch20, adc1

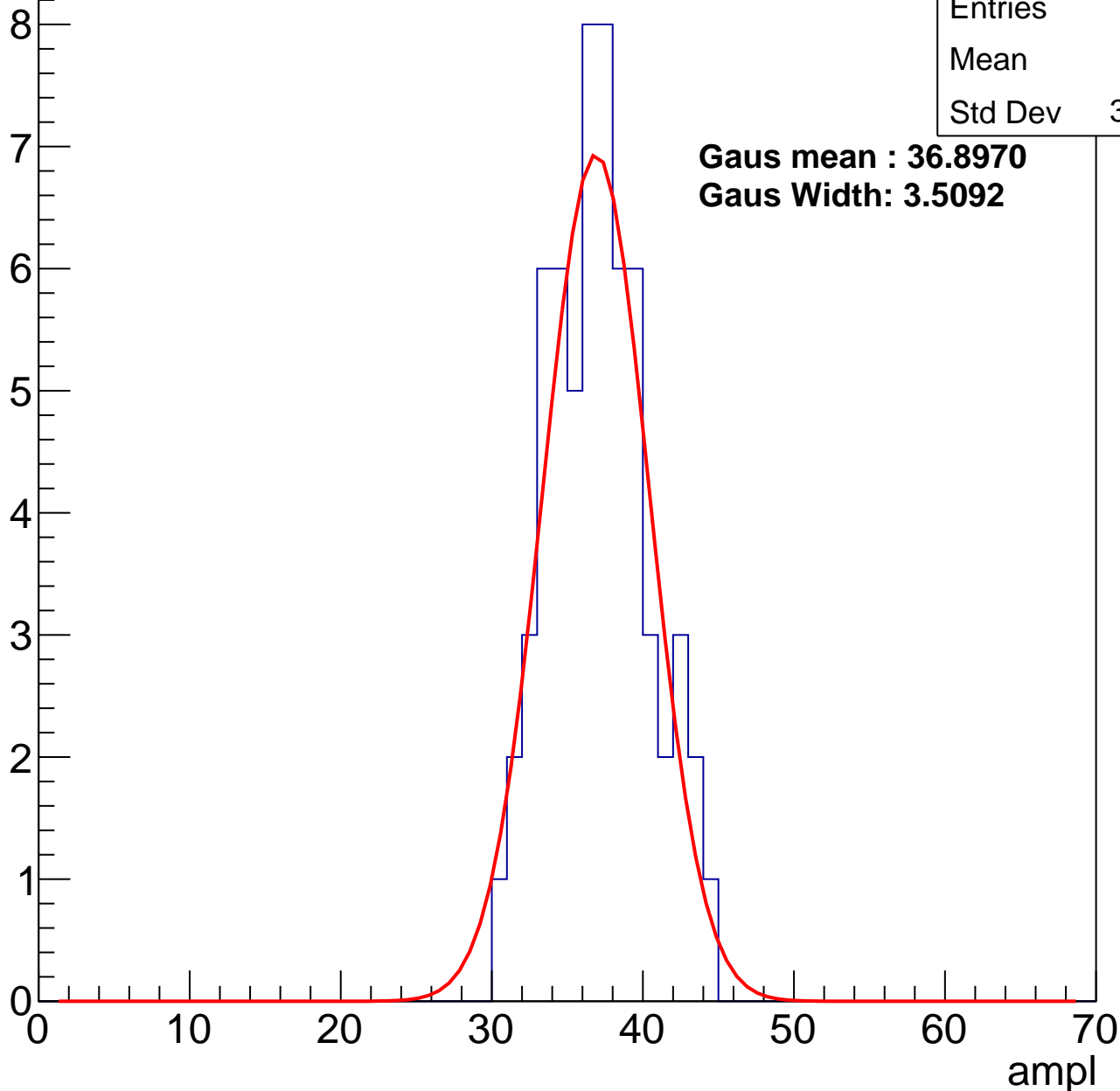
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.6
Std Dev	3.235

**Gaus mean : 36.8970**

**Gaus Width: 3.5092**



# B0L002S, U8-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.46
Std Dev	3.206

**Gaus mean : 44.1393**

**Gaus Width: 4.3245**

10

8

6

4

2

0

0

10

20

30

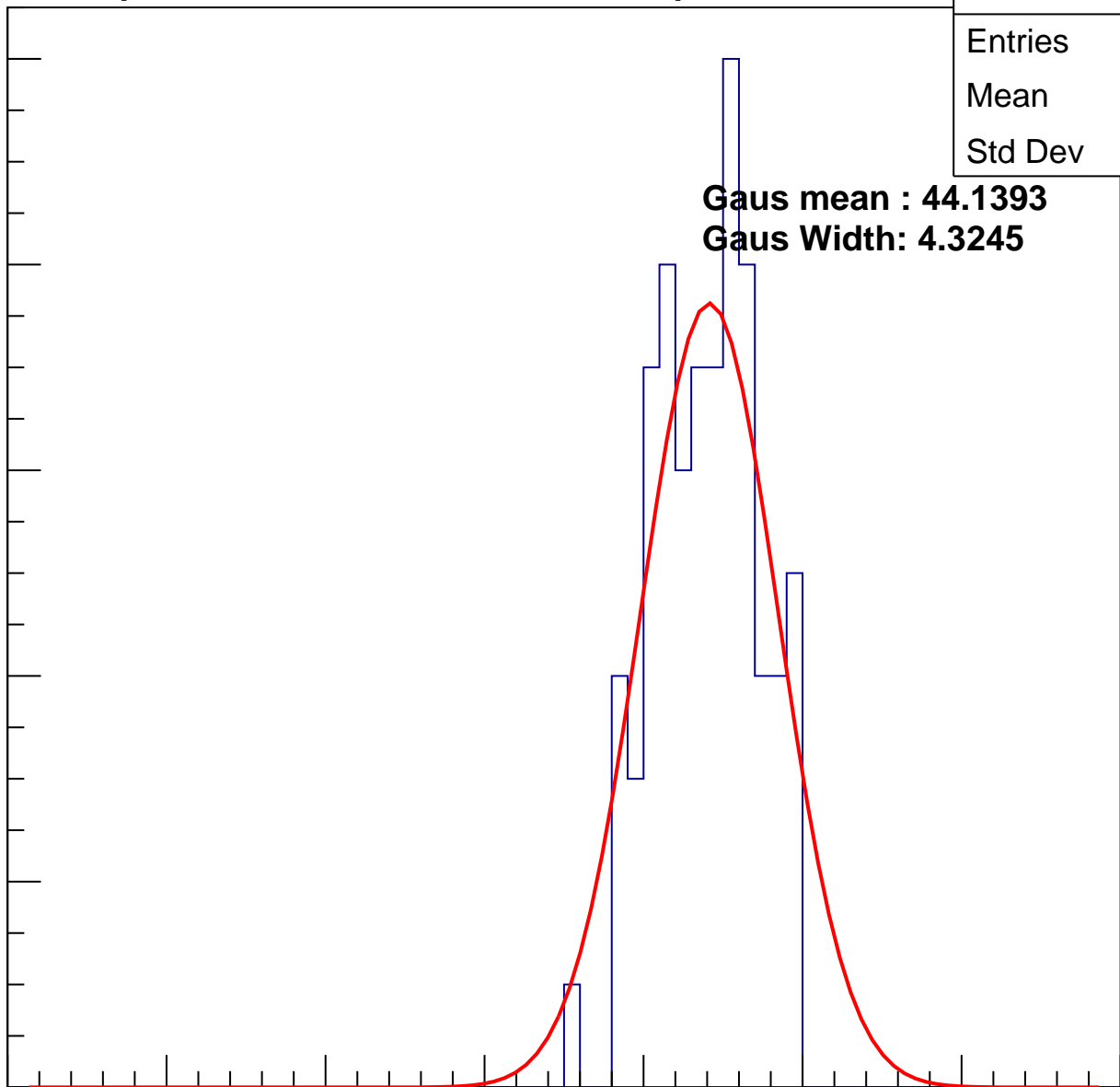
40

50

60

70

ampl

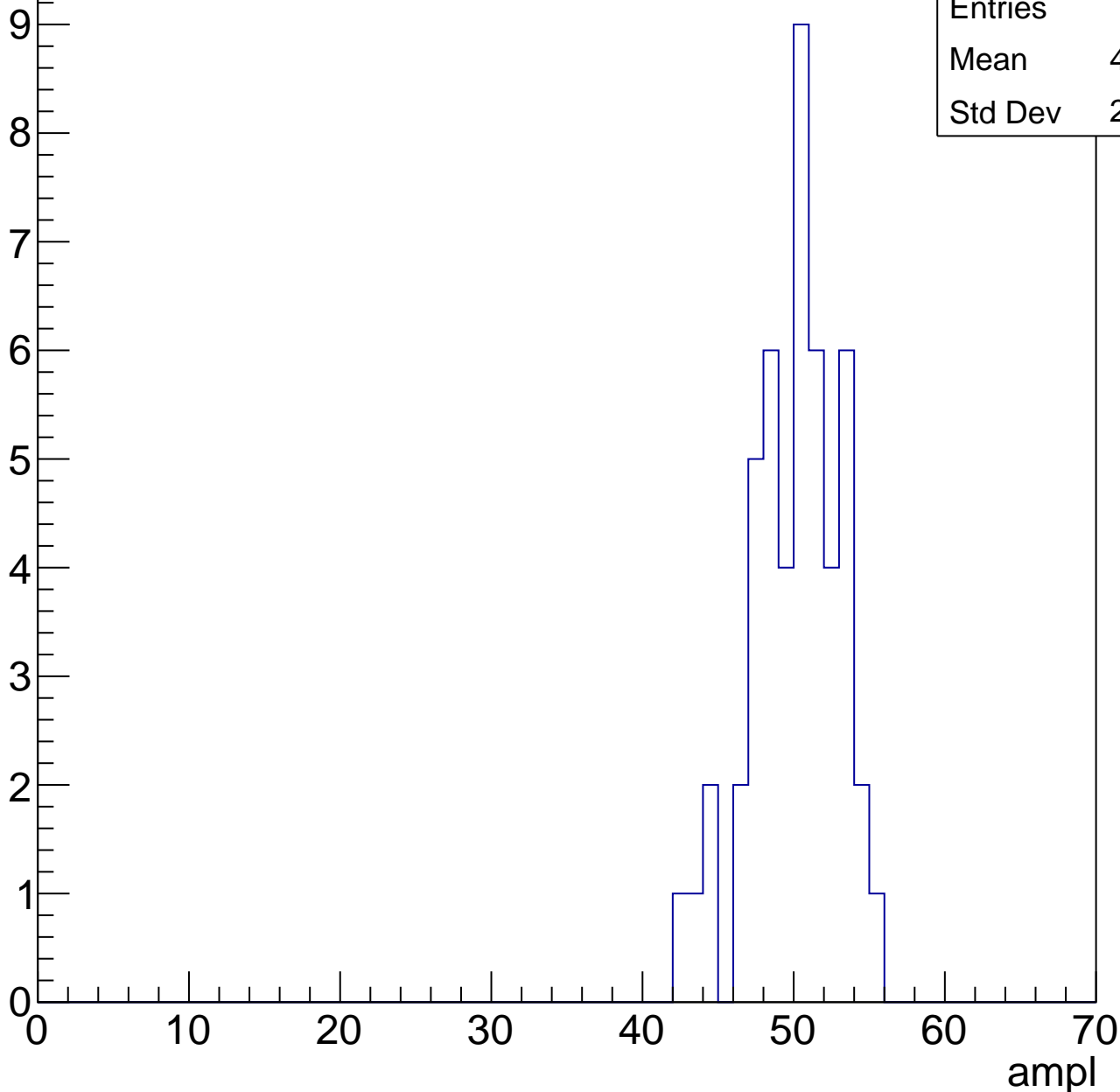


# B0L002S, U8-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

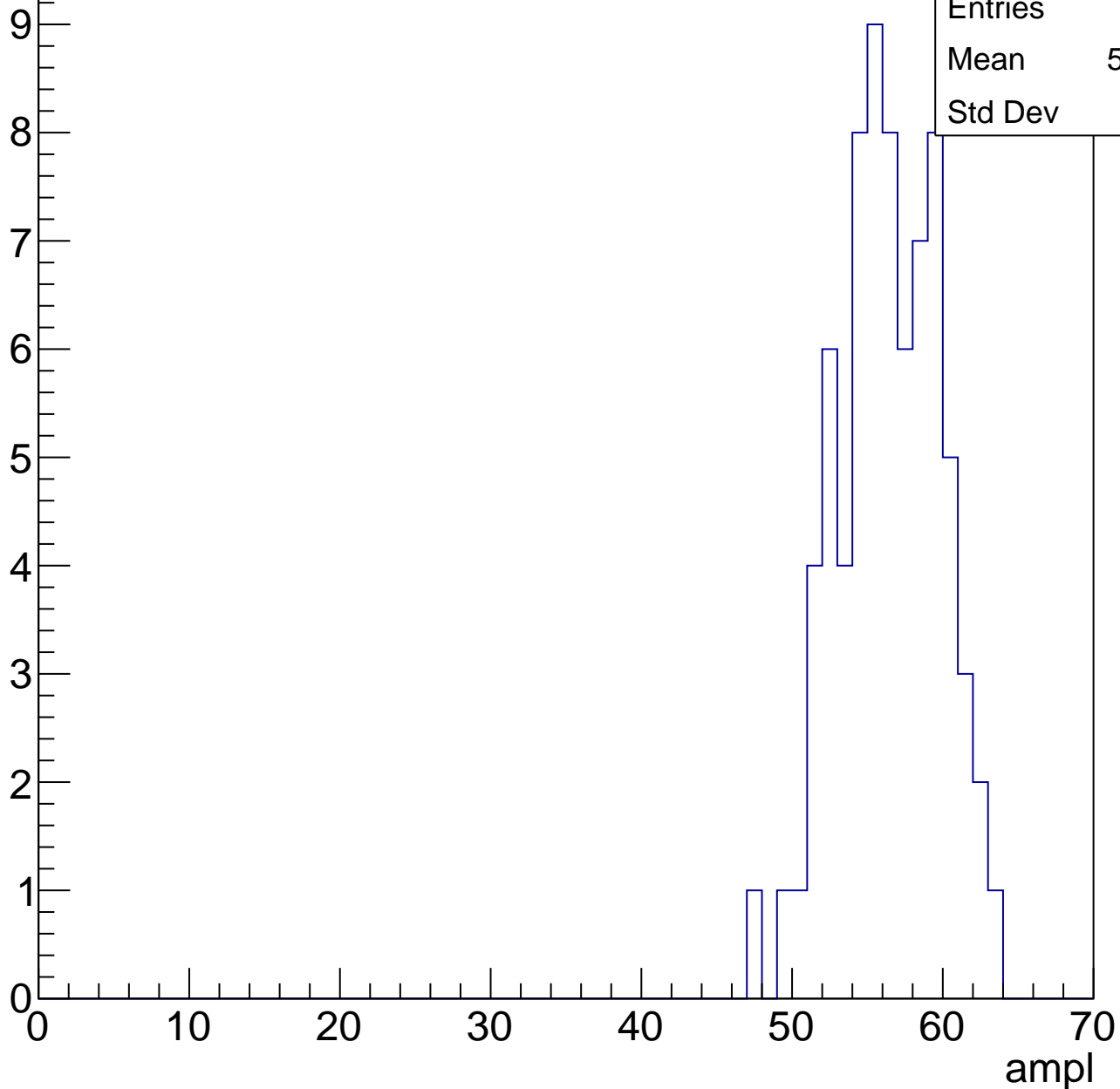
Entries	49
Mean	49.57
Std Dev	2.907



# B0L002S, U8-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

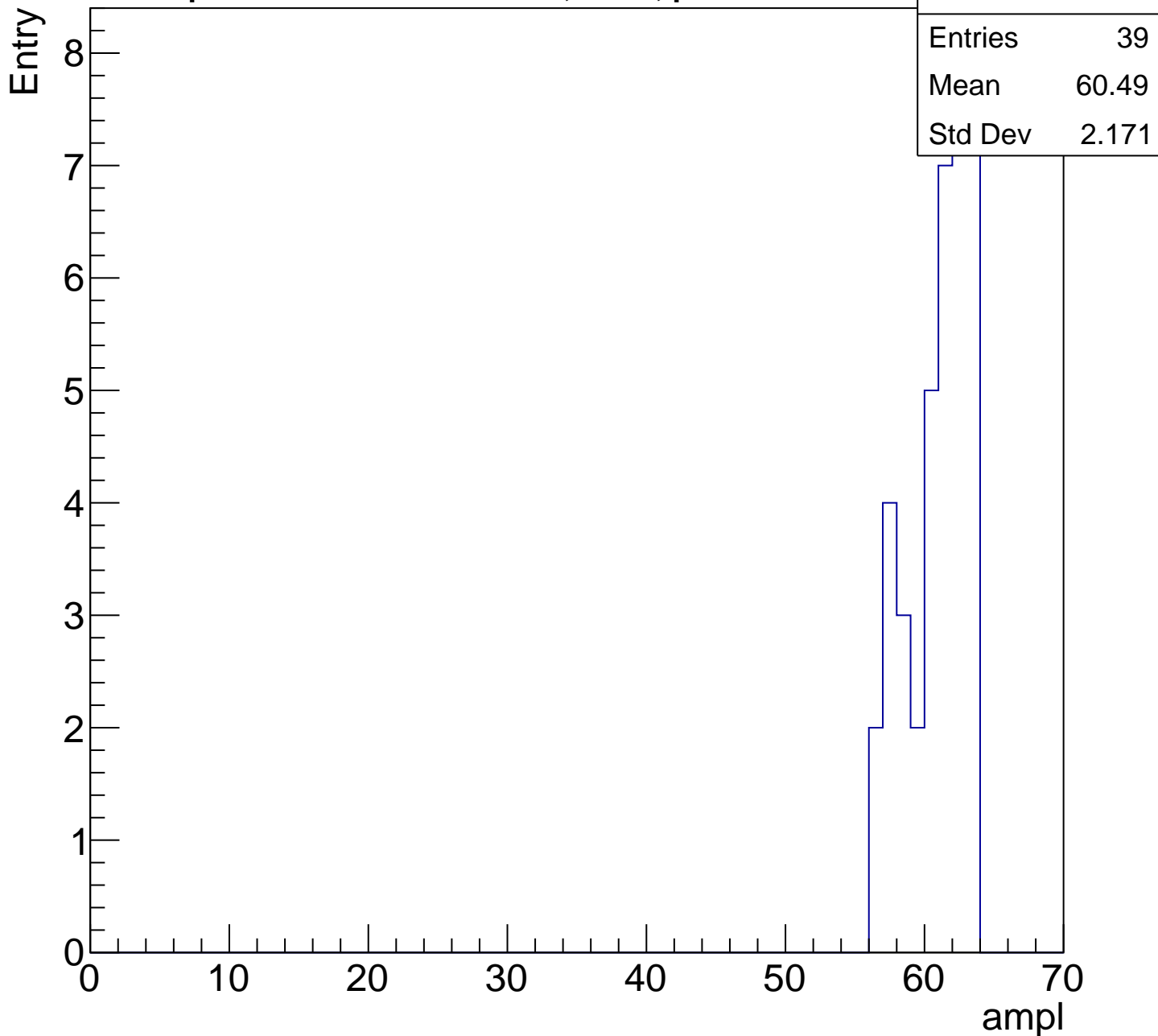
Entry



Entries	74
Mean	55.93
Std Dev	3.33

# B0L002S, U8-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch21, adc0

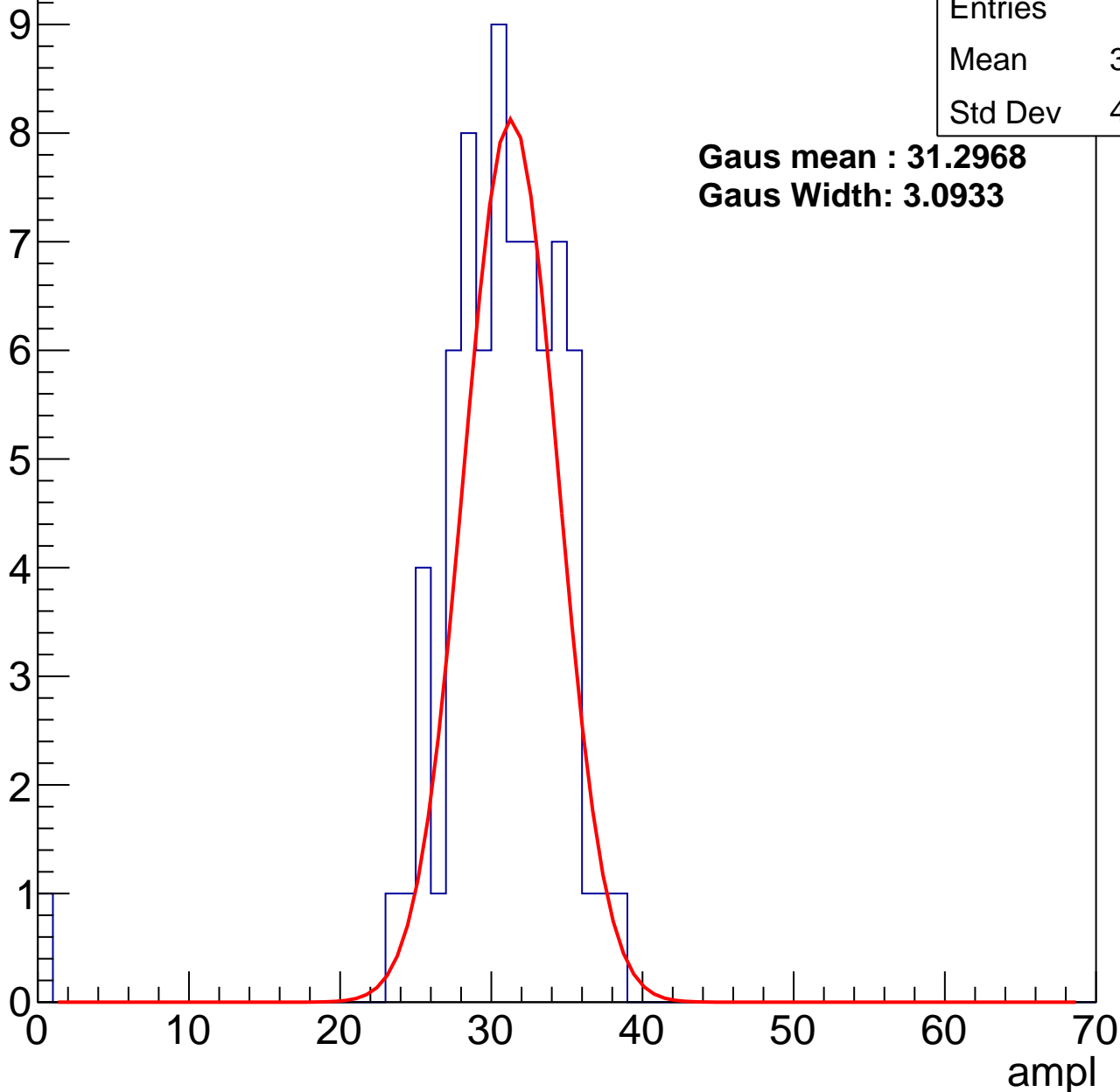
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.15
Std Dev	4.813

**Gaus mean : 31.2968**

**Gaus Width: 3.0933**



# B0L002S, U8-ch21, adc1

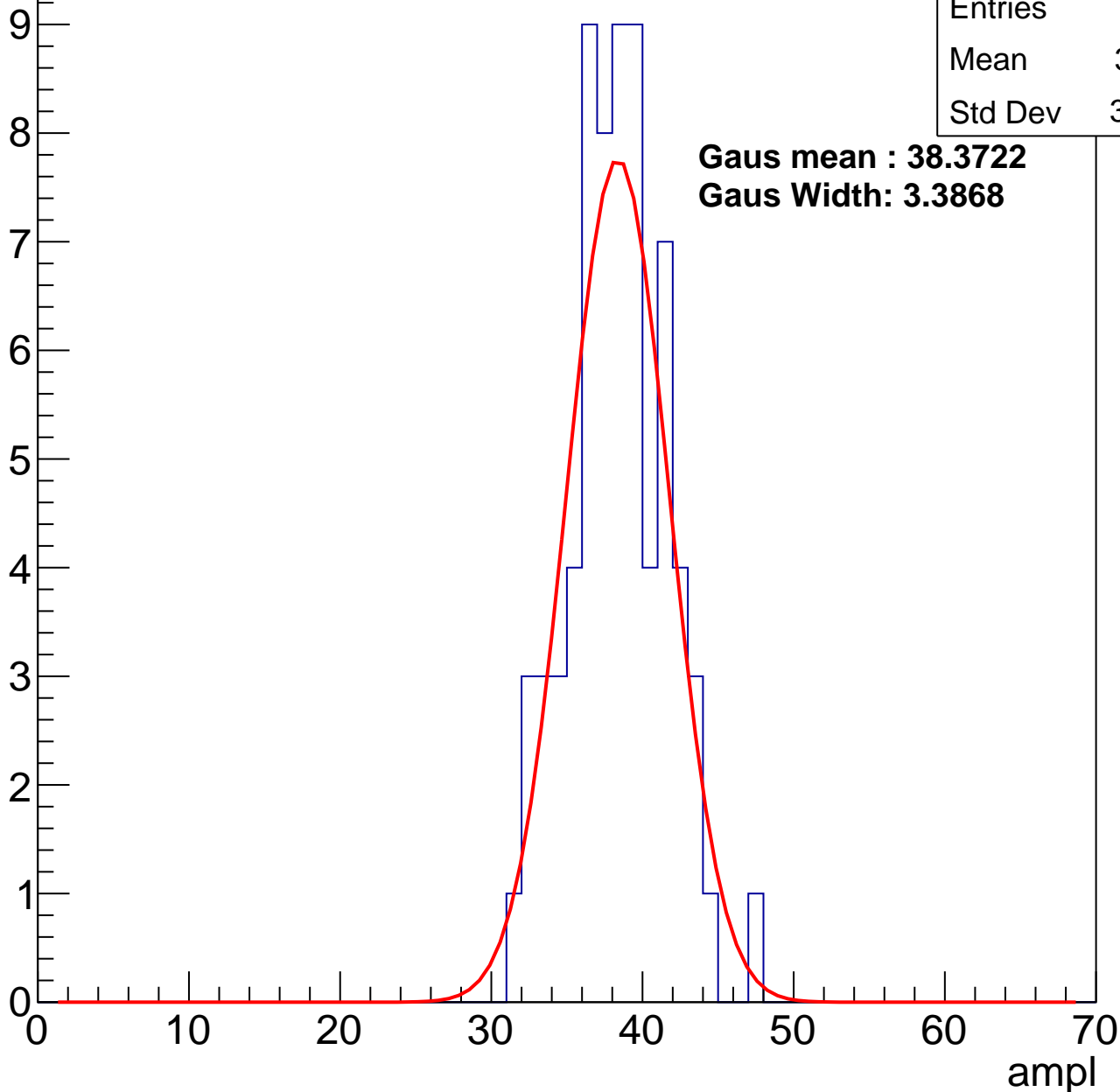
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.91
Std Dev	3.193

**Gaus mean : 38.3722**

**Gaus Width: 3.3868**



# B0L002S, U8-ch21, adc2

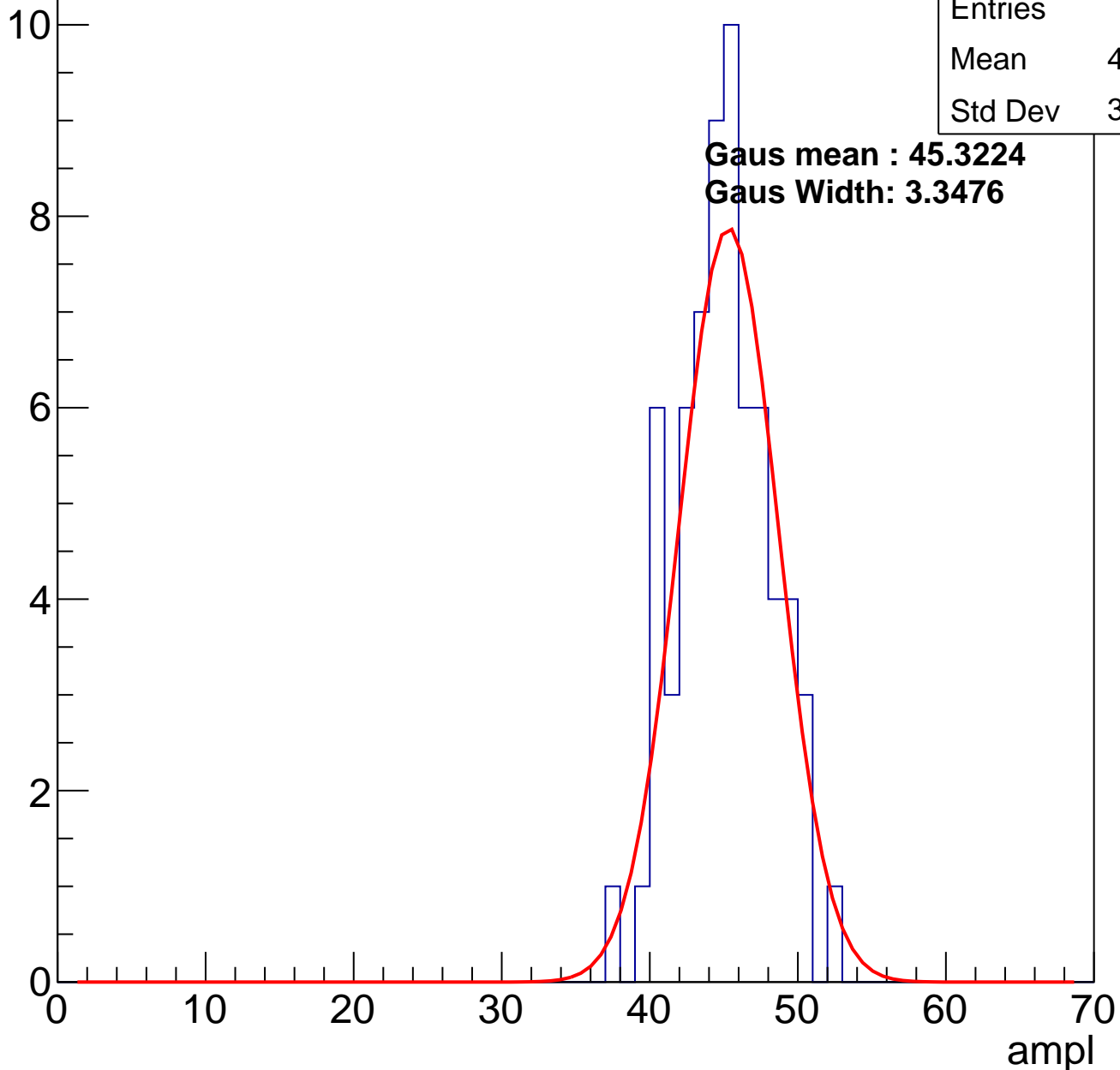
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	44.57
Std Dev	3.068

**Gaus mean : 45.3224**

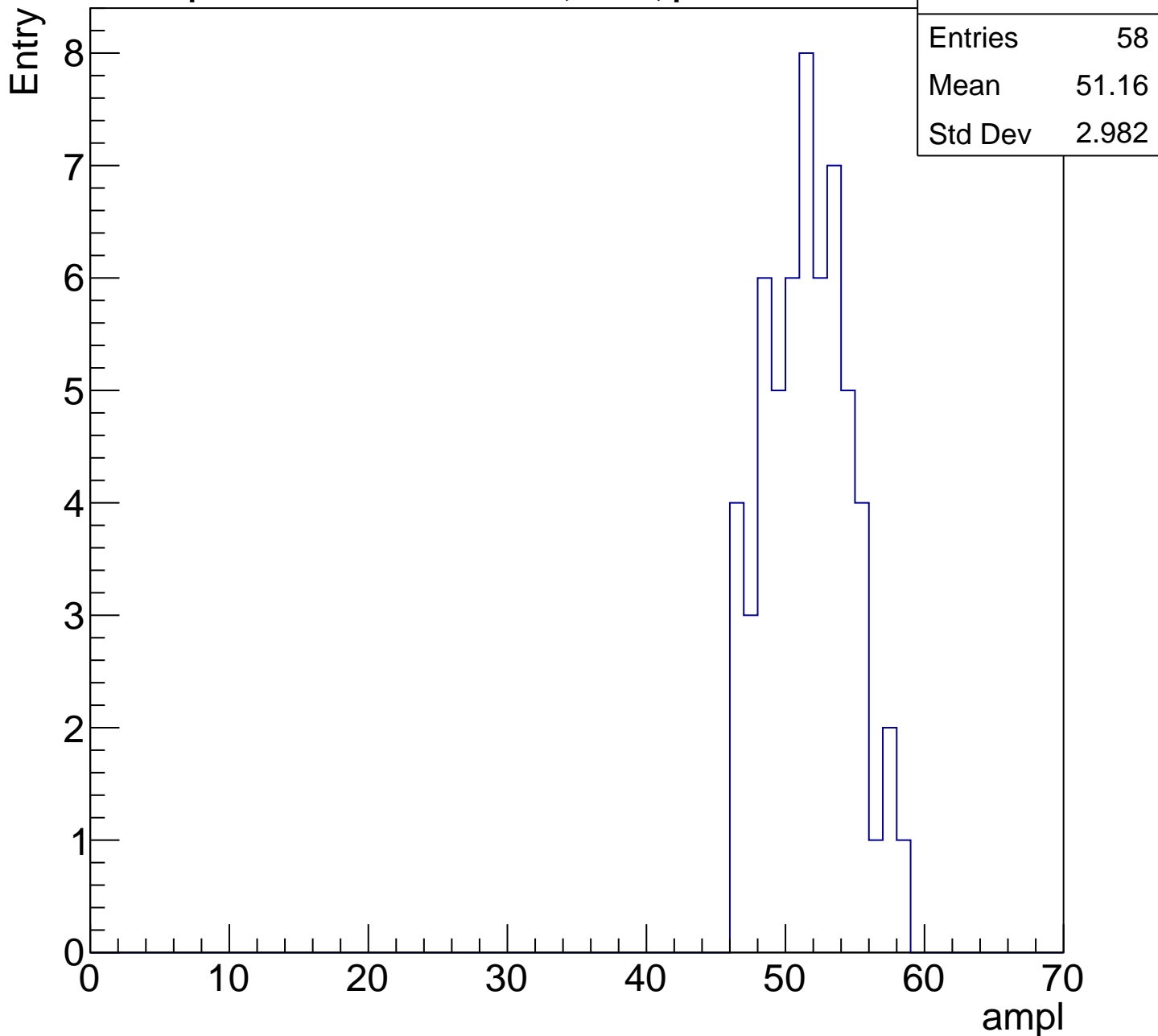
**Gaus Width: 3.3476**

Entry



# B0L002S, U8-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

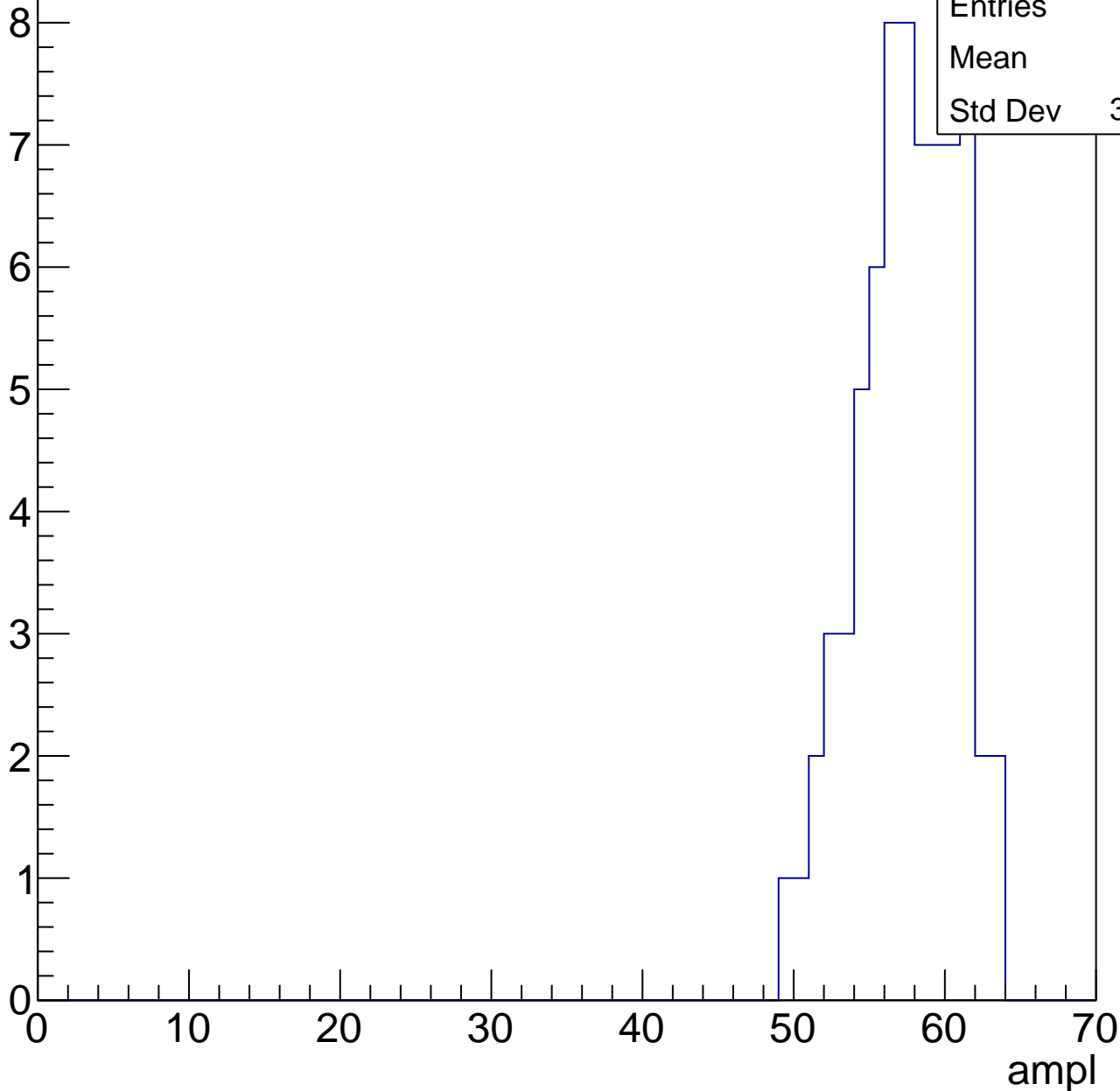


# B0L002S, U8-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	57.1
Std Dev	3.234

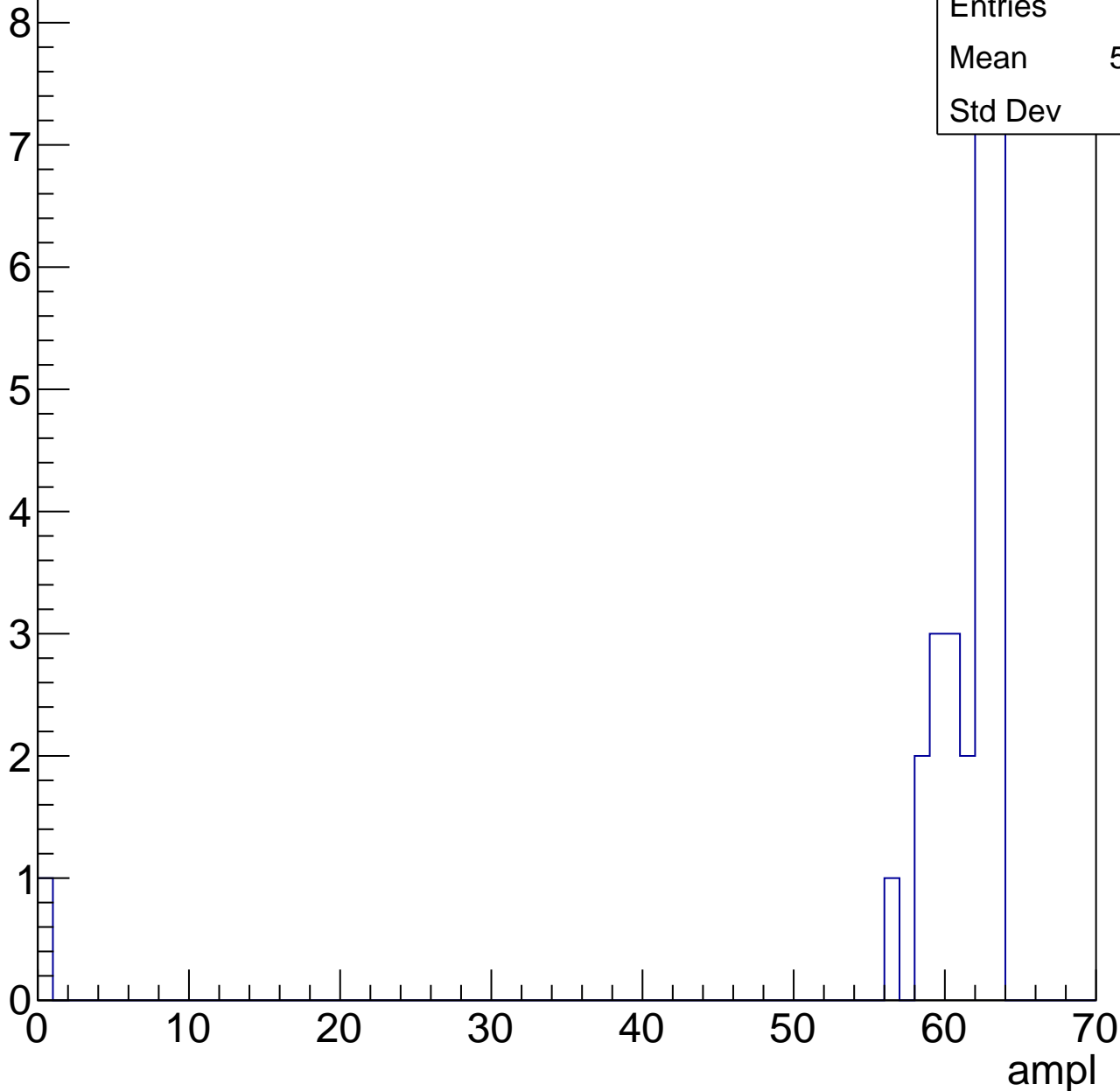


# B0L002S, U8-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.96
Std Dev	11.5



# B0L002S, U8-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch22, adc0

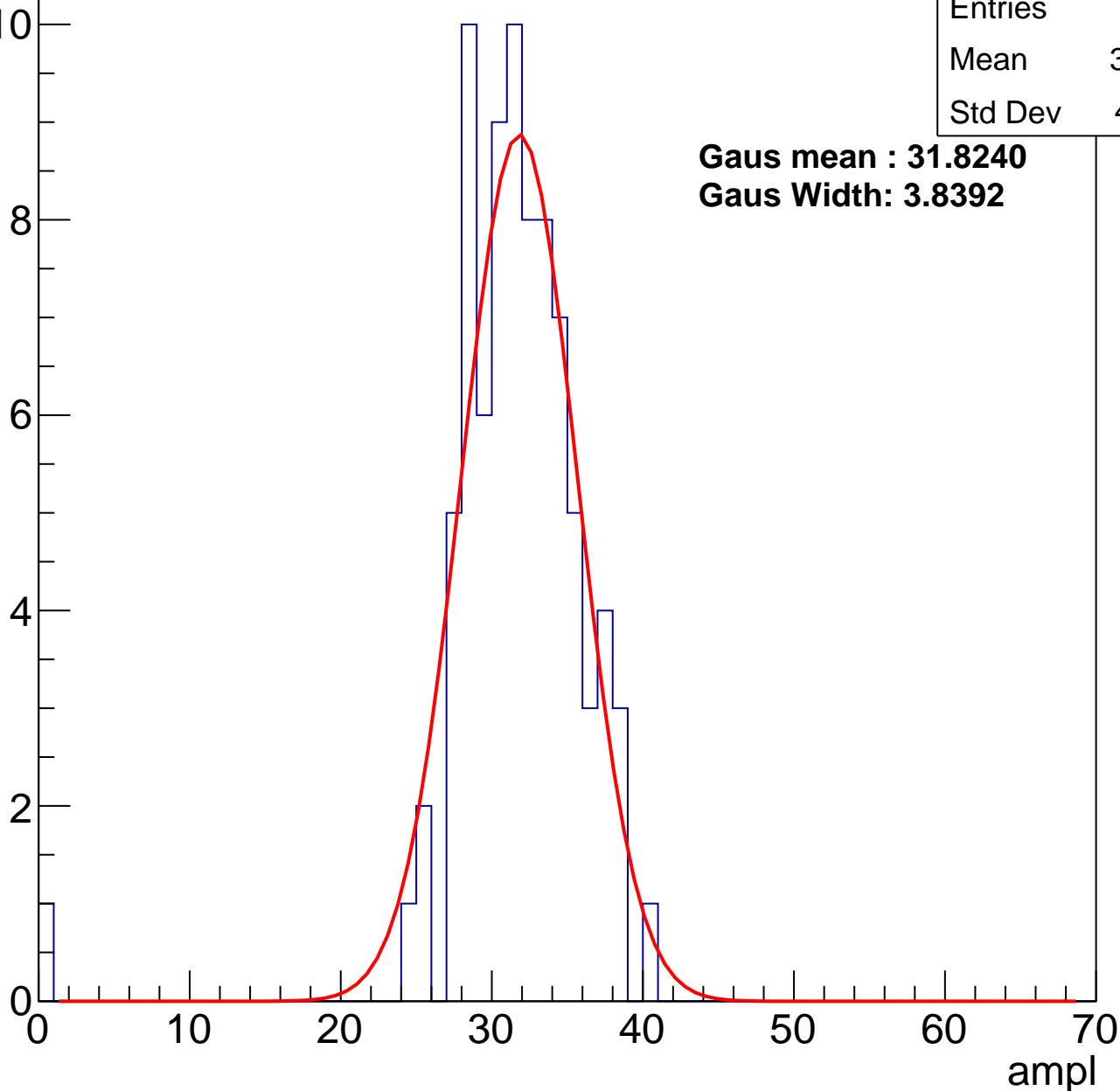
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	83
Mean	31.16
Std Dev	4.801

**Gaus mean : 31.8240**

**Gaus Width: 3.8392**



# B0L002S, U8-ch22, adc1

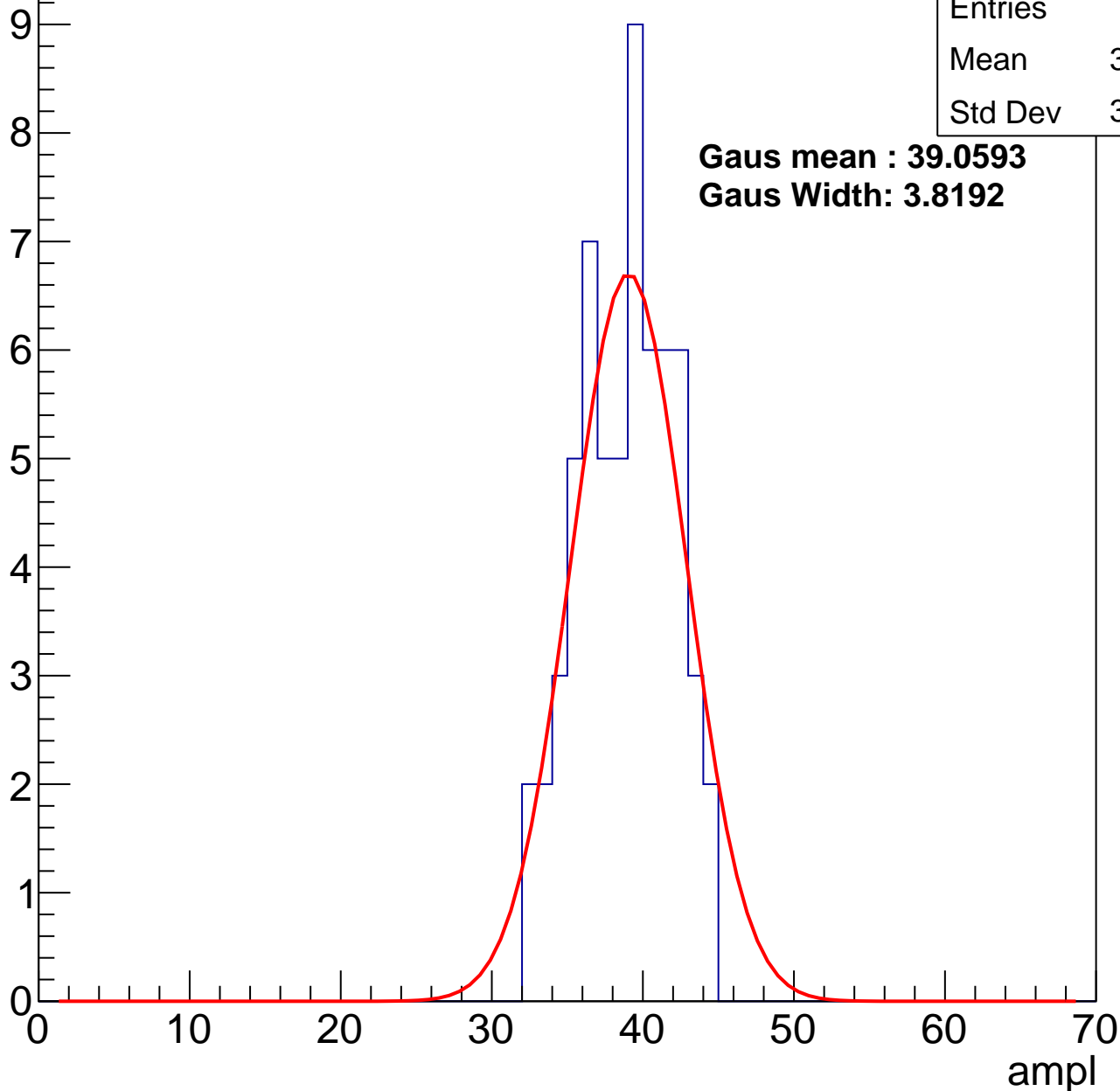
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.36
Std Dev	3.057

**Gaus mean : 39.0593**

**Gaus Width: 3.8192**



# B0L002S, U8-ch22, adc2

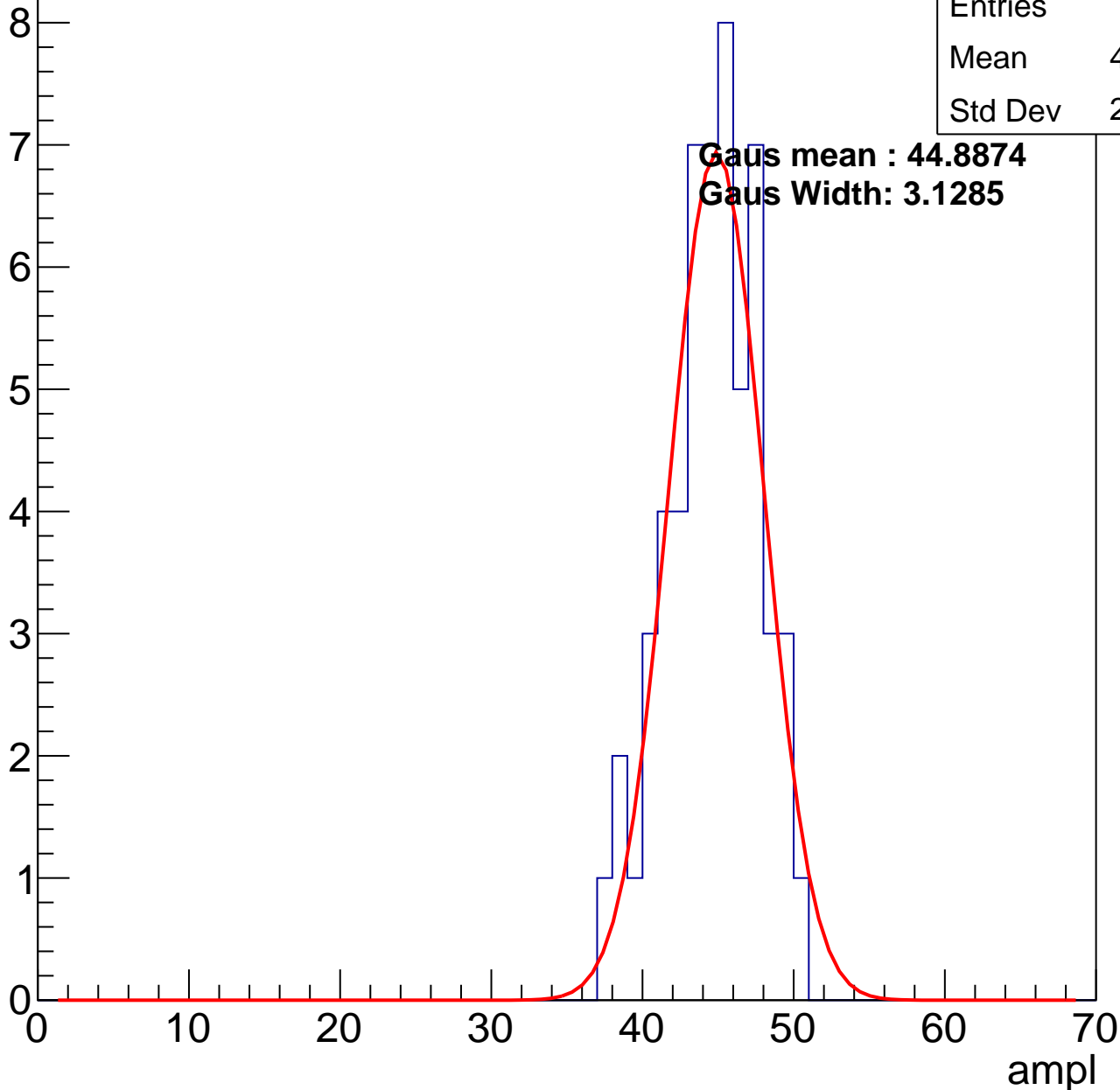
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.16
Std Dev	2.993

Gaus mean : 44.8874

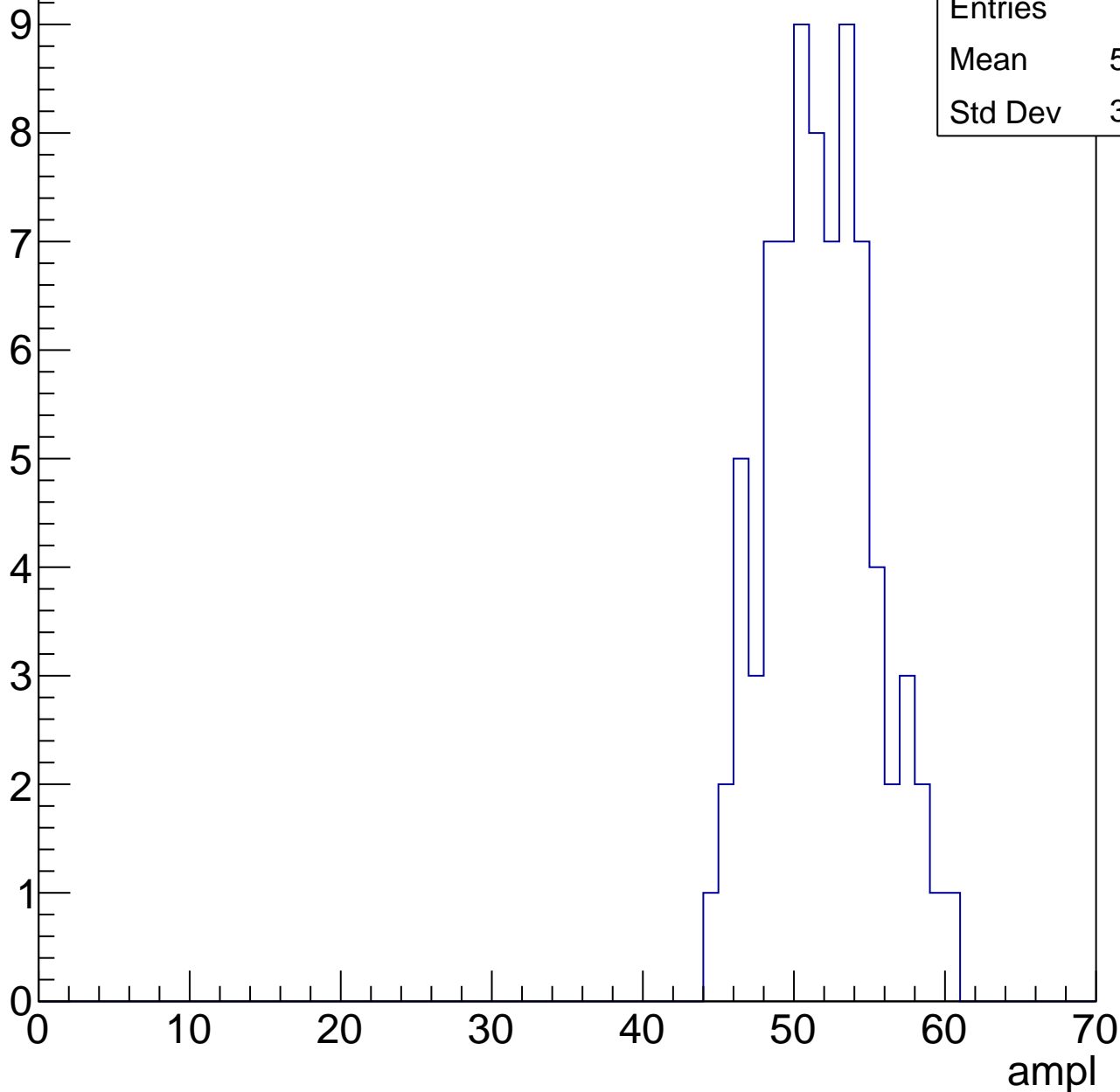
Gaus Width: 3.1285



# B0L002S, U8-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

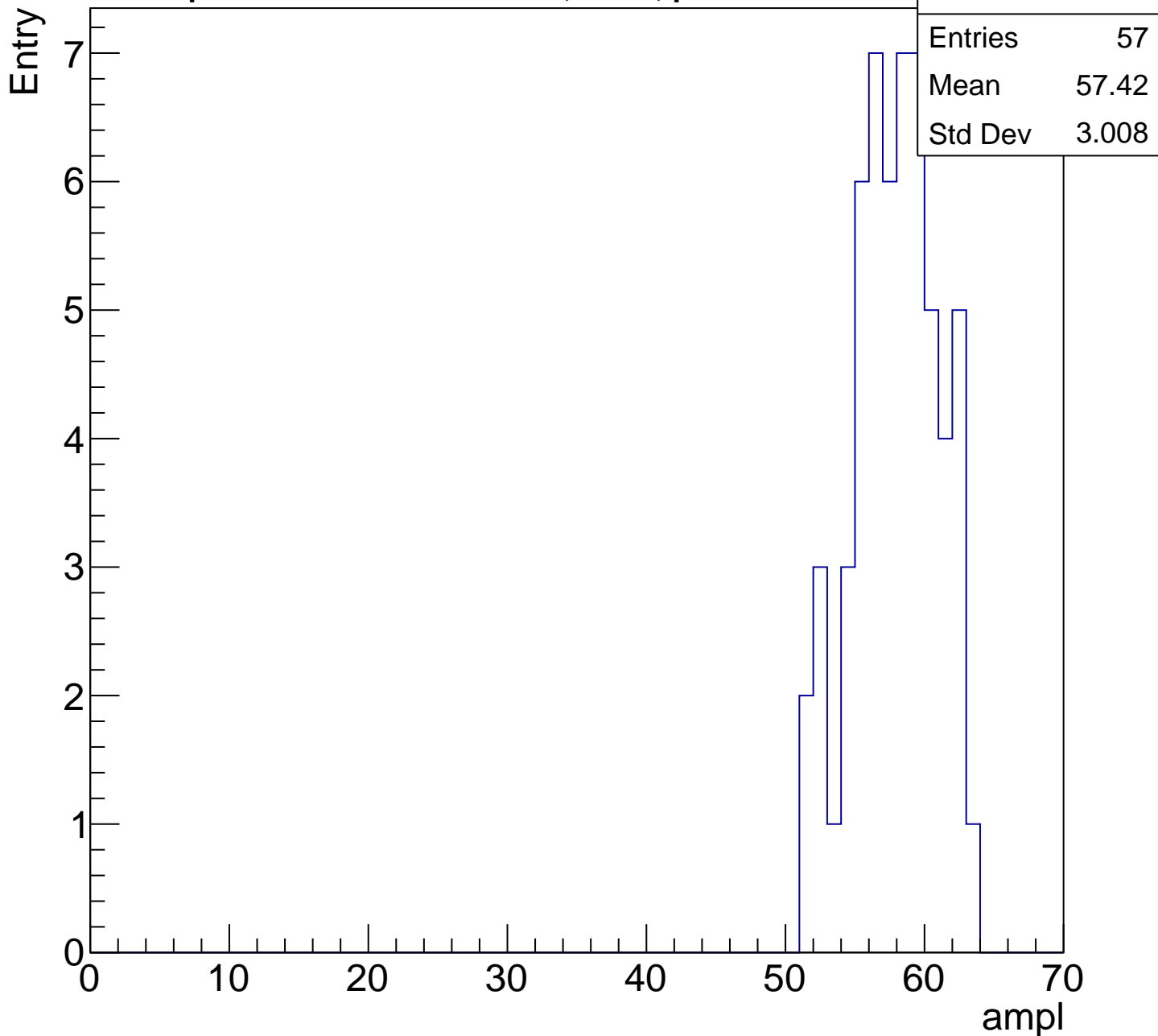
Entry



Entries	78
Mean	51.27
Std Dev	3.507

# B0L002S, U8-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

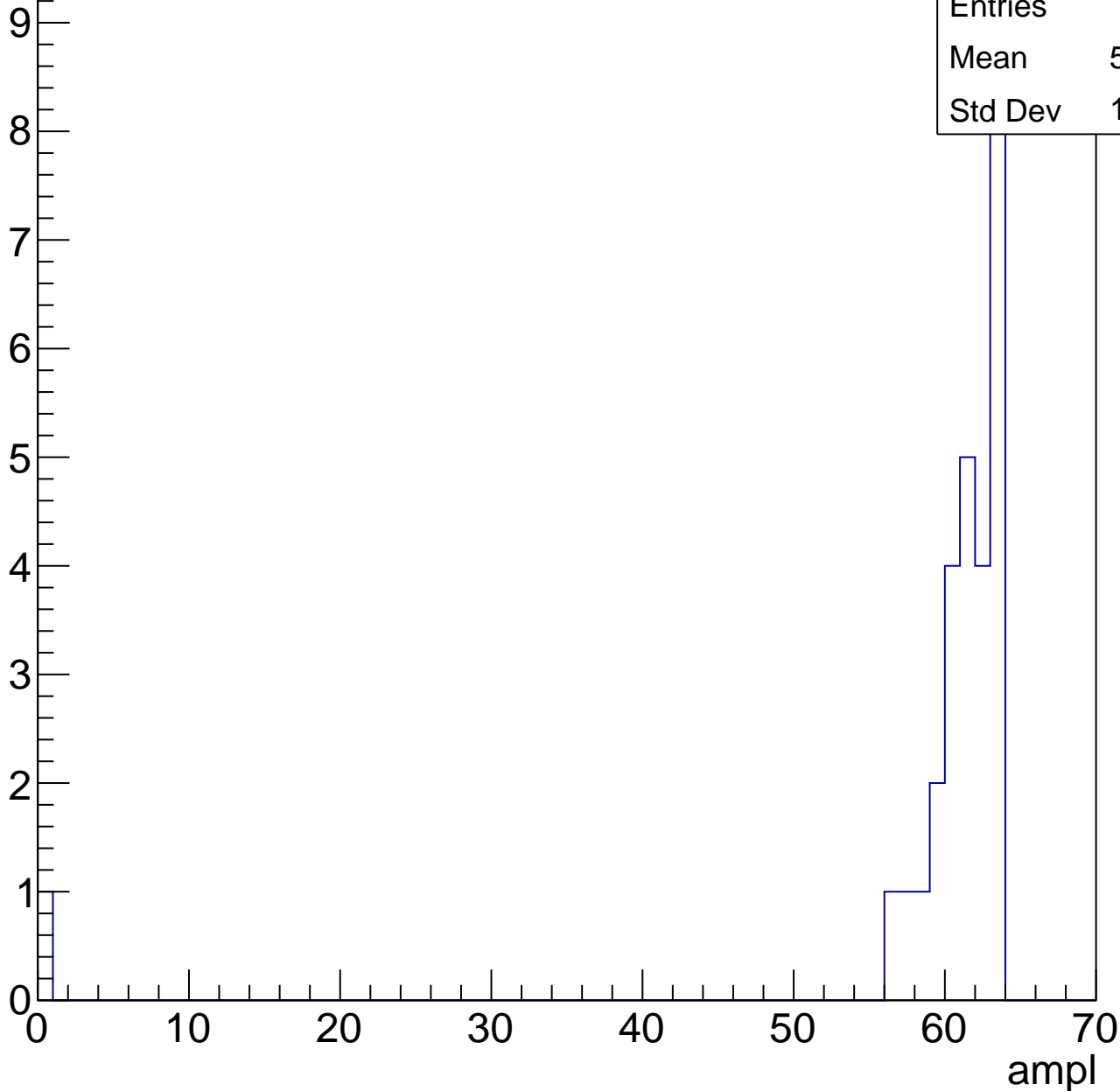


# B0L002S, U8-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.89
Std Dev	11.49



# B0L002S, U8-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch23, adc0

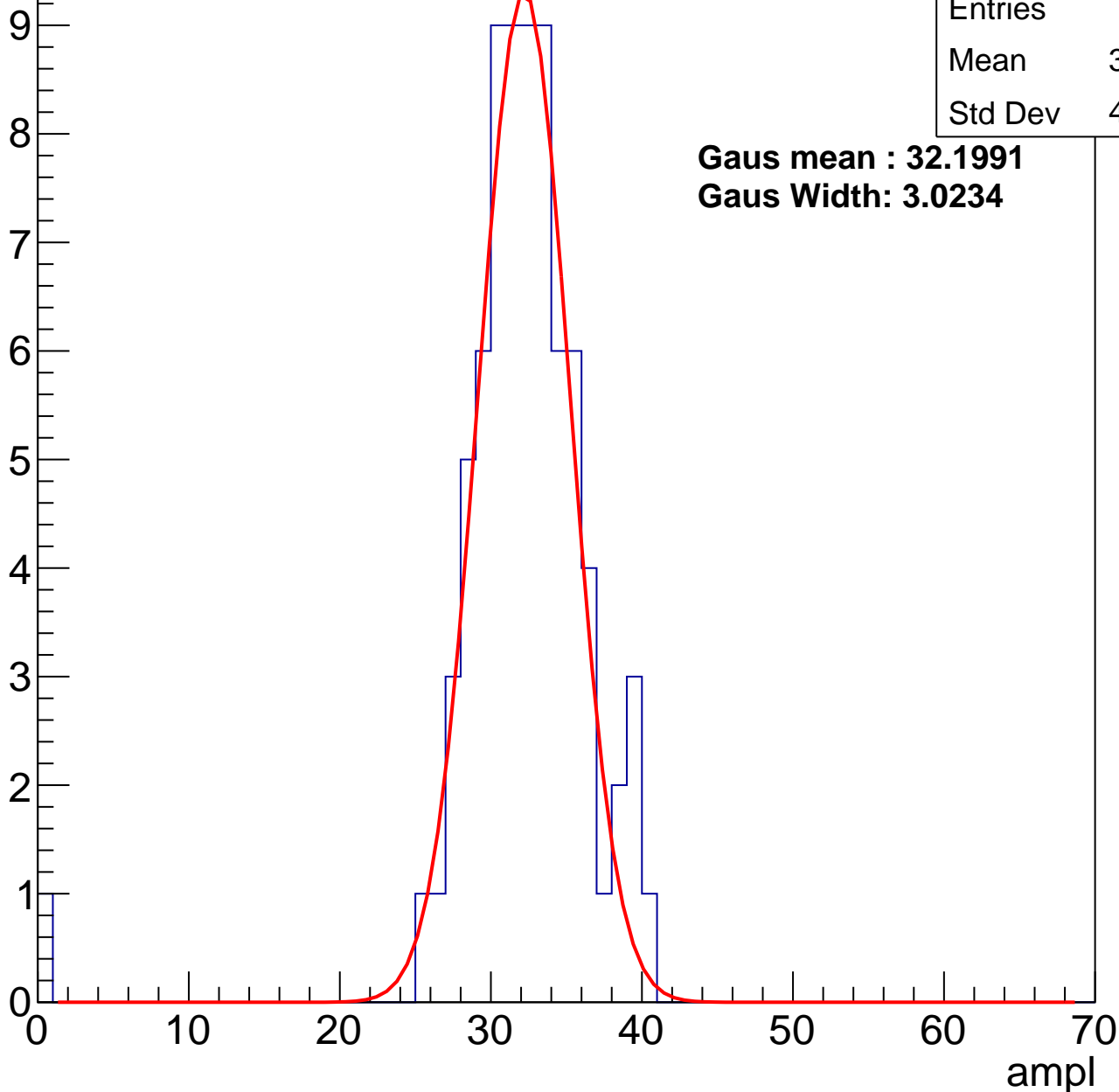
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.68
Std Dev	4.886

**Gaus mean : 32.1991**

**Gaus Width: 3.0234**



# B0L002S, U8-ch23, adc1

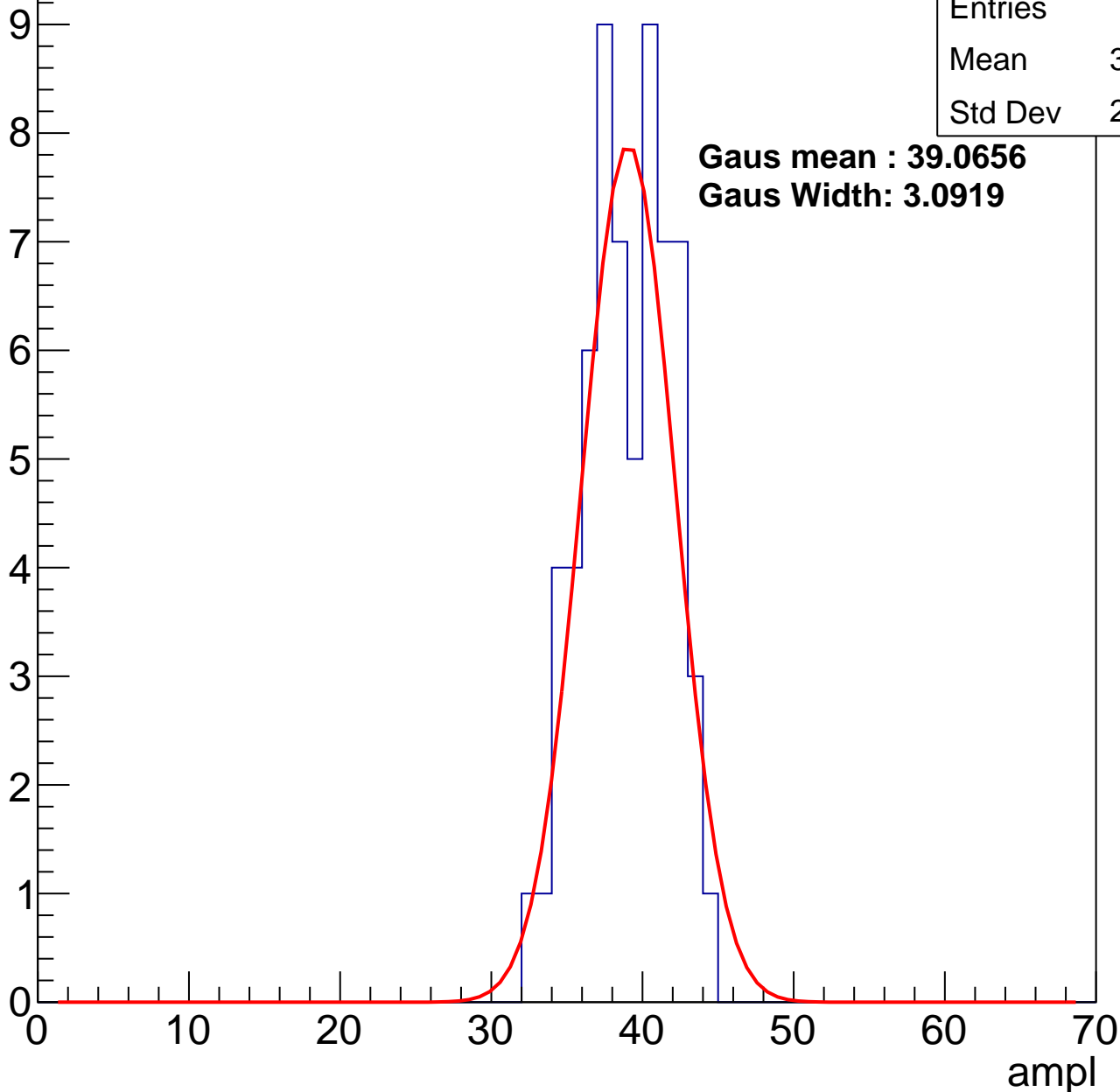
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.52
Std Dev	2.806

**Gaus mean : 39.0656**

**Gaus Width: 3.0919**



# B0L002S, U8-ch23, adc2

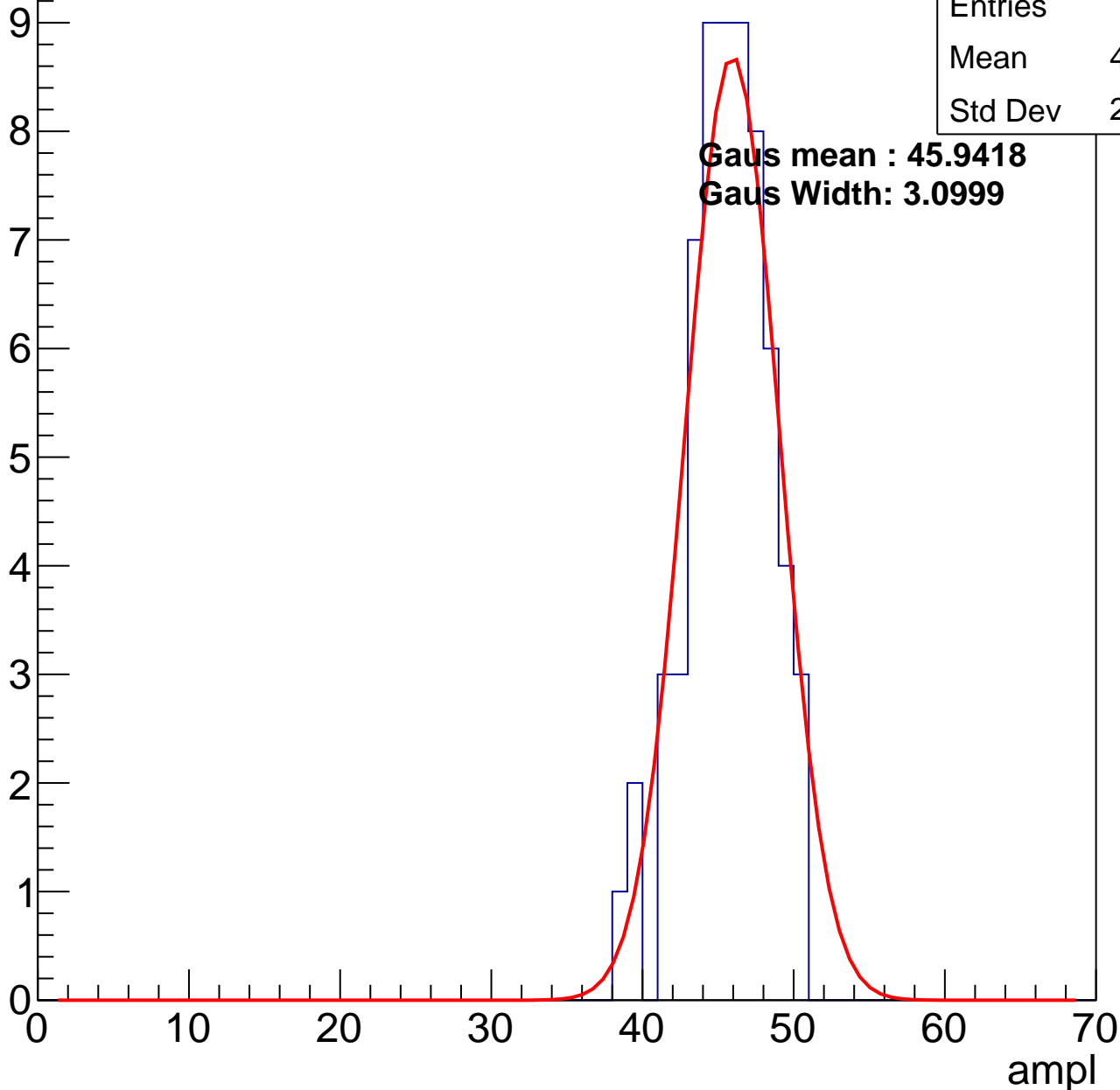
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45.17
Std Dev	2.696

**Gaus mean : 45.9418**

**Gaus Width: 3.0999**

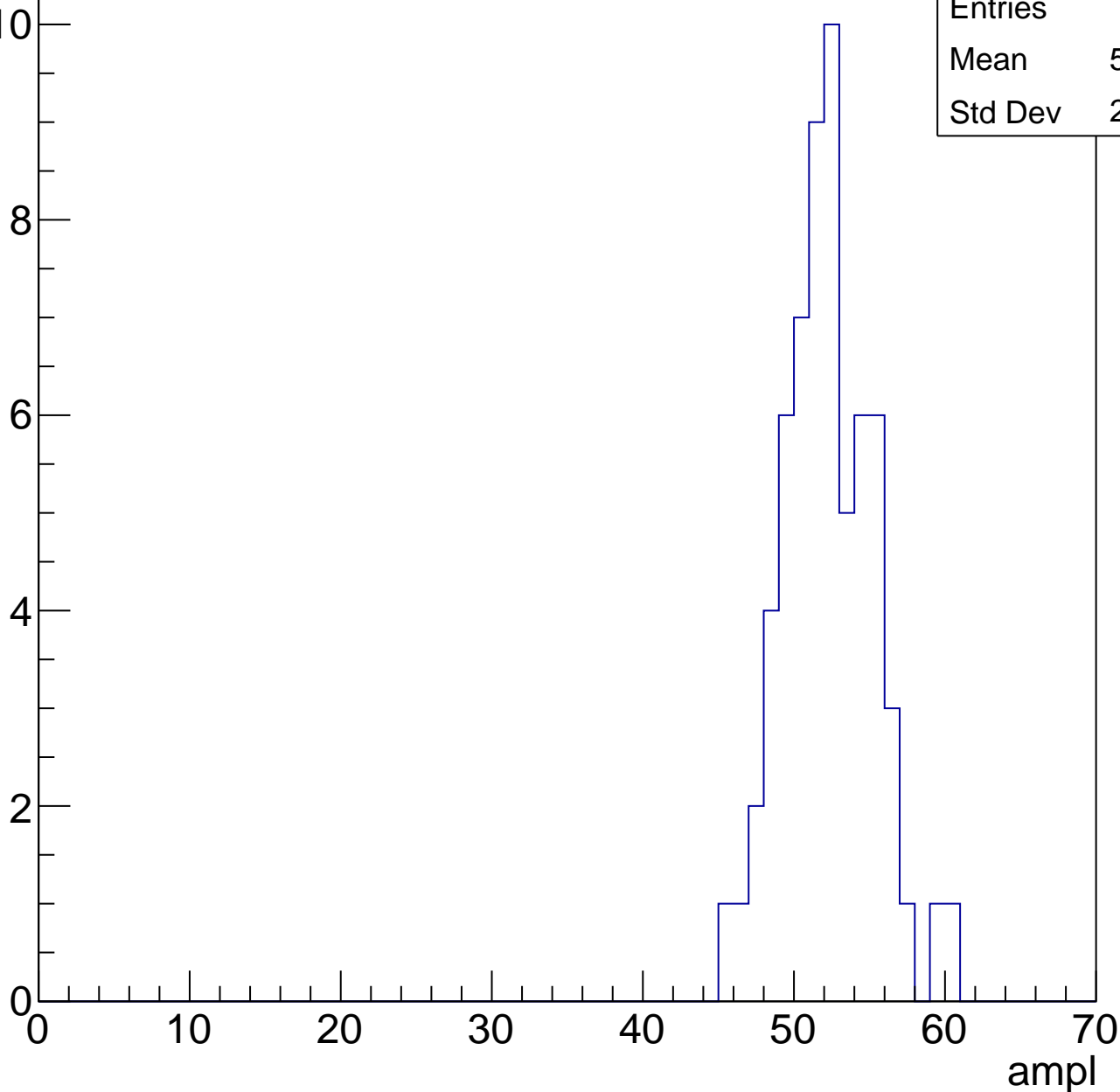


# B0L002S, U8-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

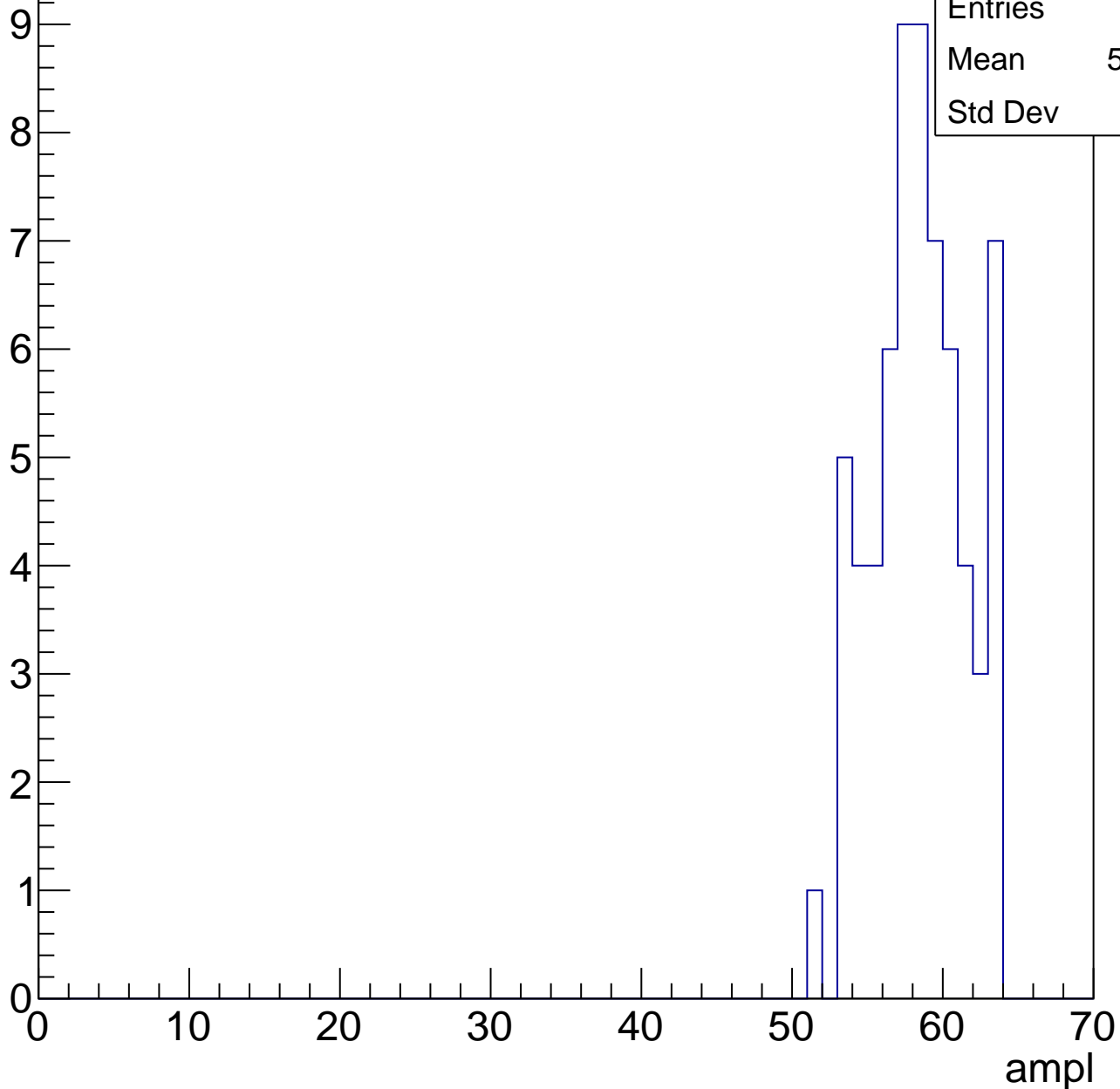
Entries	63
Mean	51.79
Std Dev	2.972



# B0L002S, U8-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

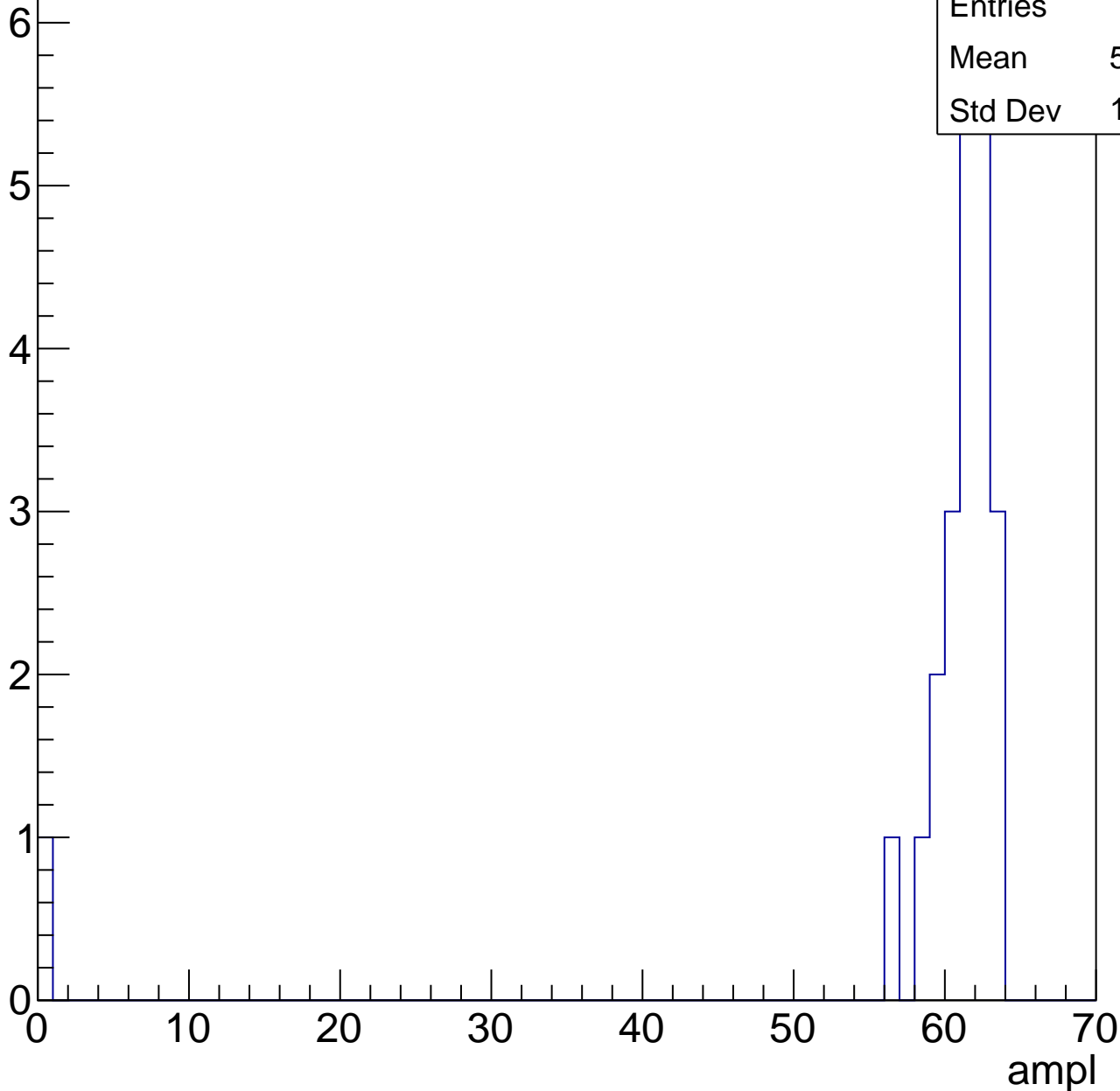


# B0L002S, U8-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.22
Std Dev	12.52



# B0L002S, U8-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch24, adc0

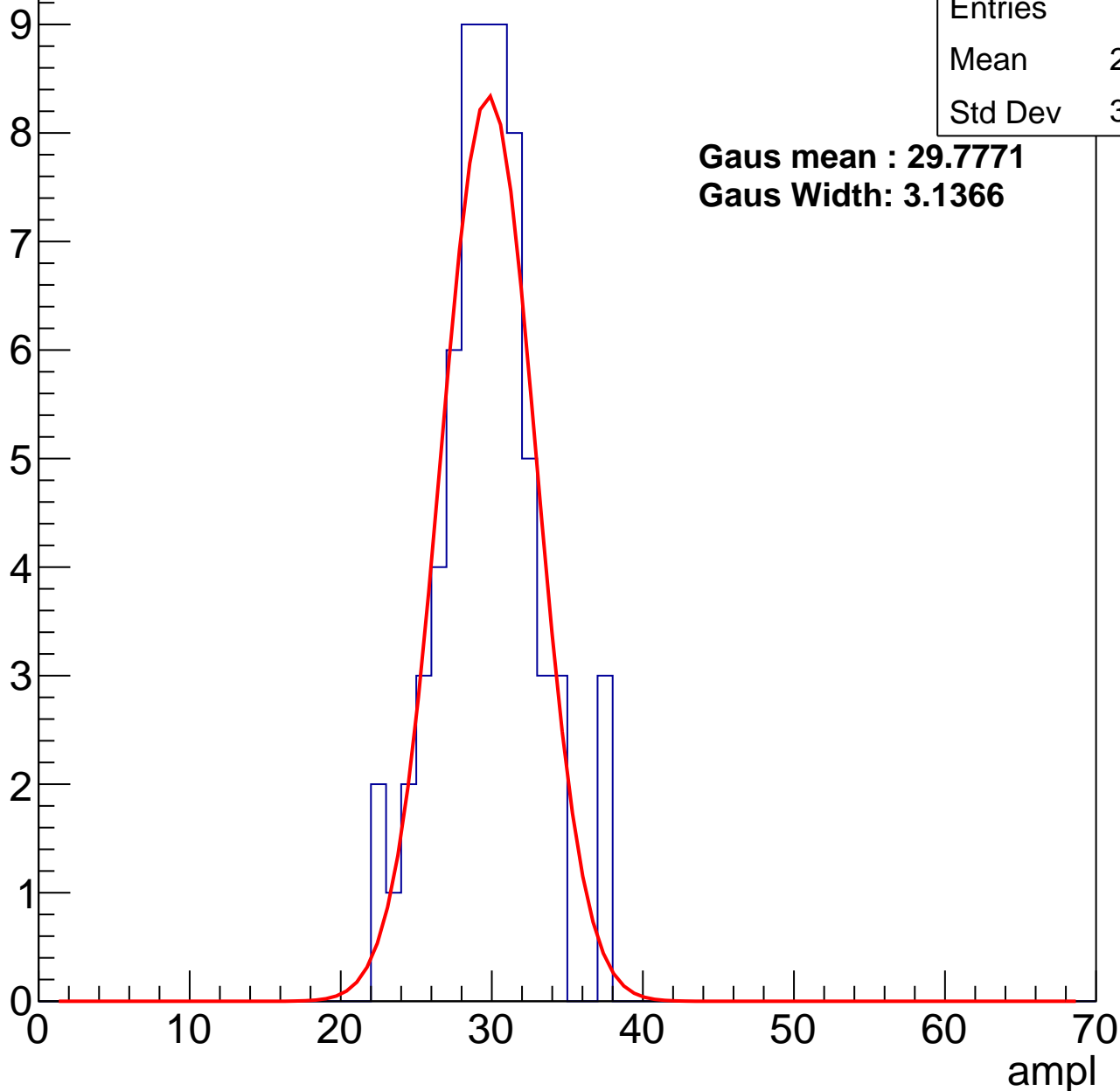
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.24
Std Dev	3.228

**Gaus mean : 29.7771**

**Gaus Width: 3.1366**



# B0L002S, U8-ch24, adc1

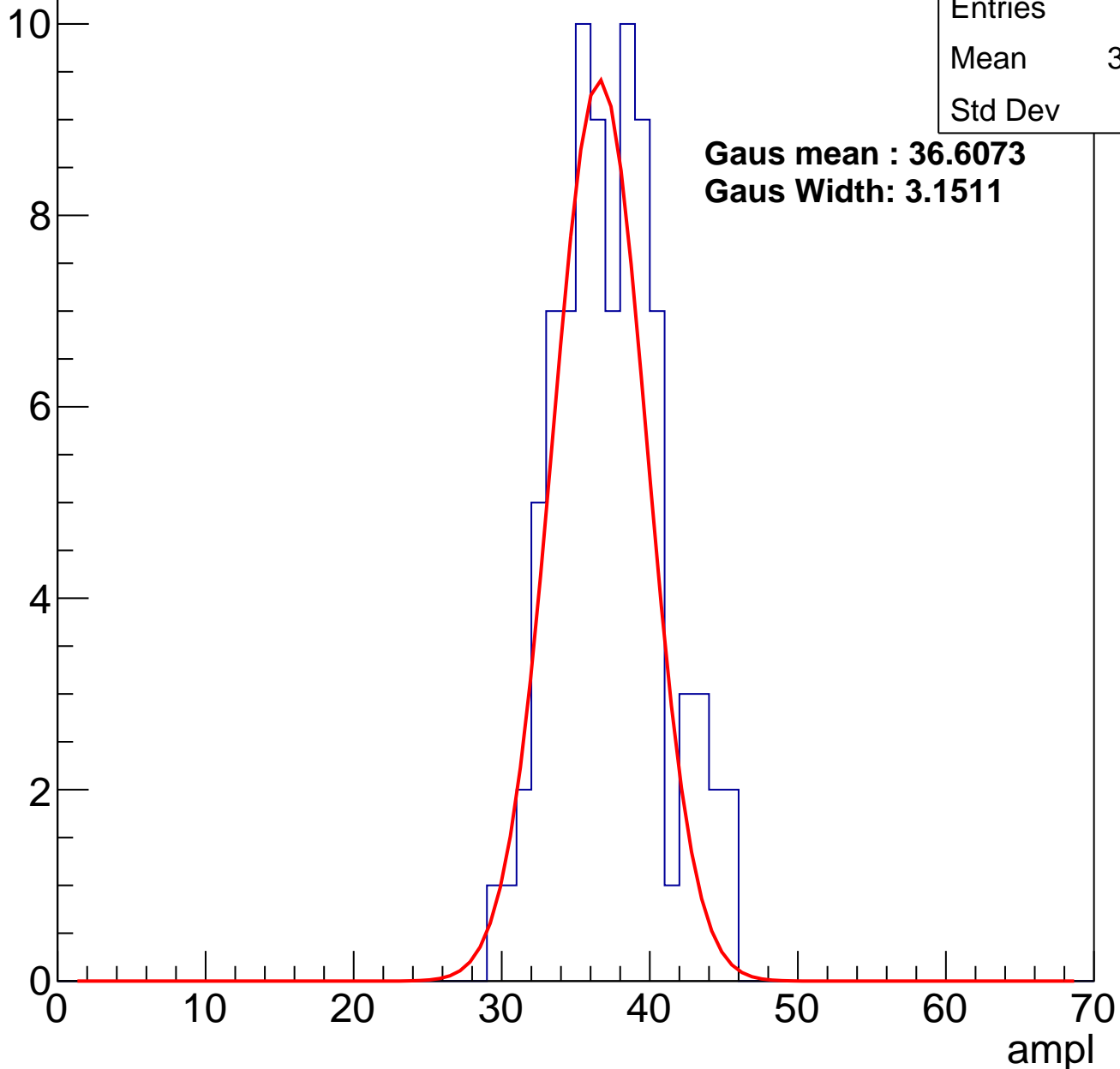
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	86
Mean	36.84
Std Dev	3.52

**Gaus mean : 36.6073**

**Gaus Width: 3.1511**

Entry



# B0L002S, U8-ch24, adc2

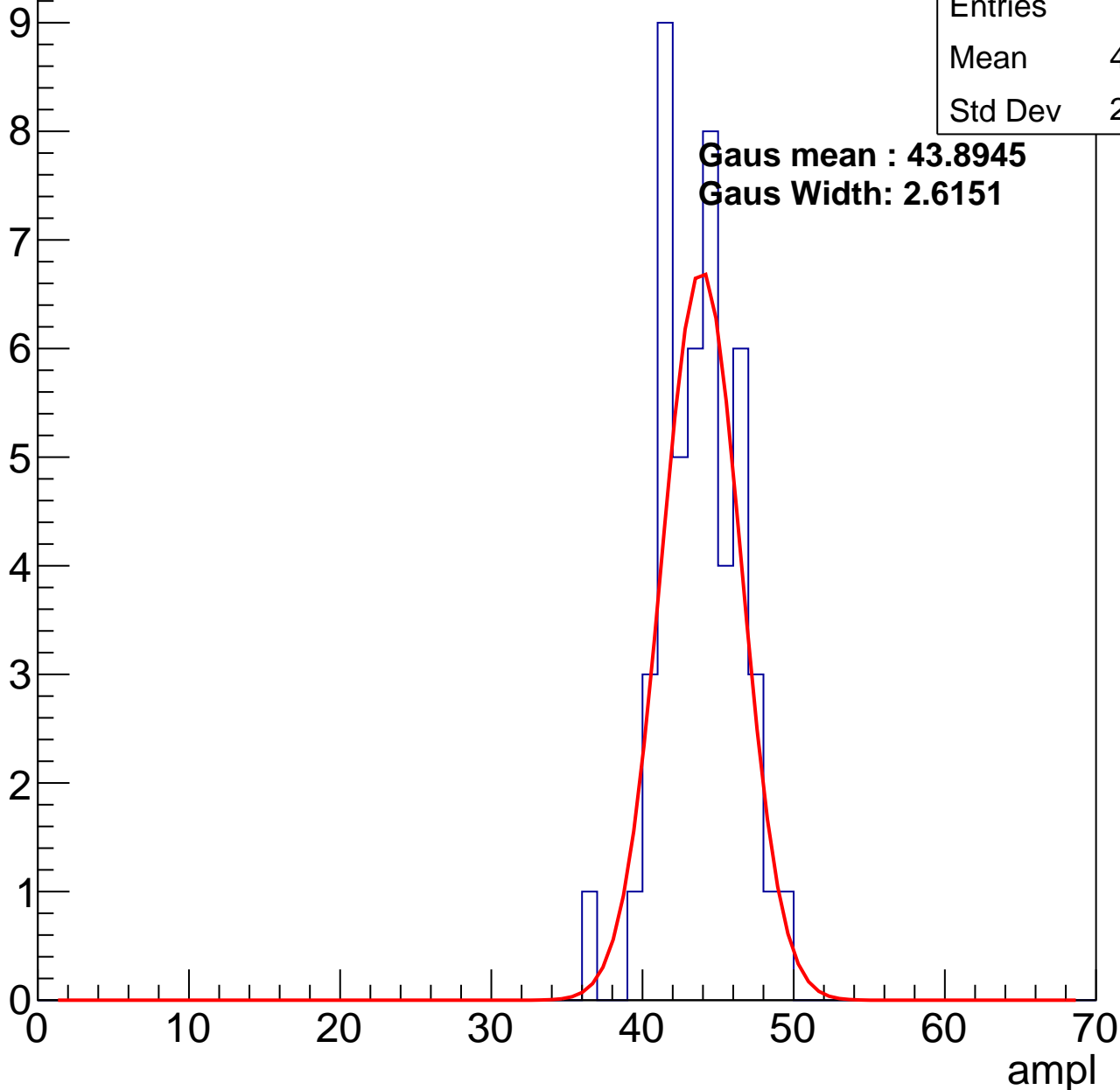
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.29
Std Dev	2.565

**Gaus mean : 43.8945**

**Gaus Width: 2.6151**

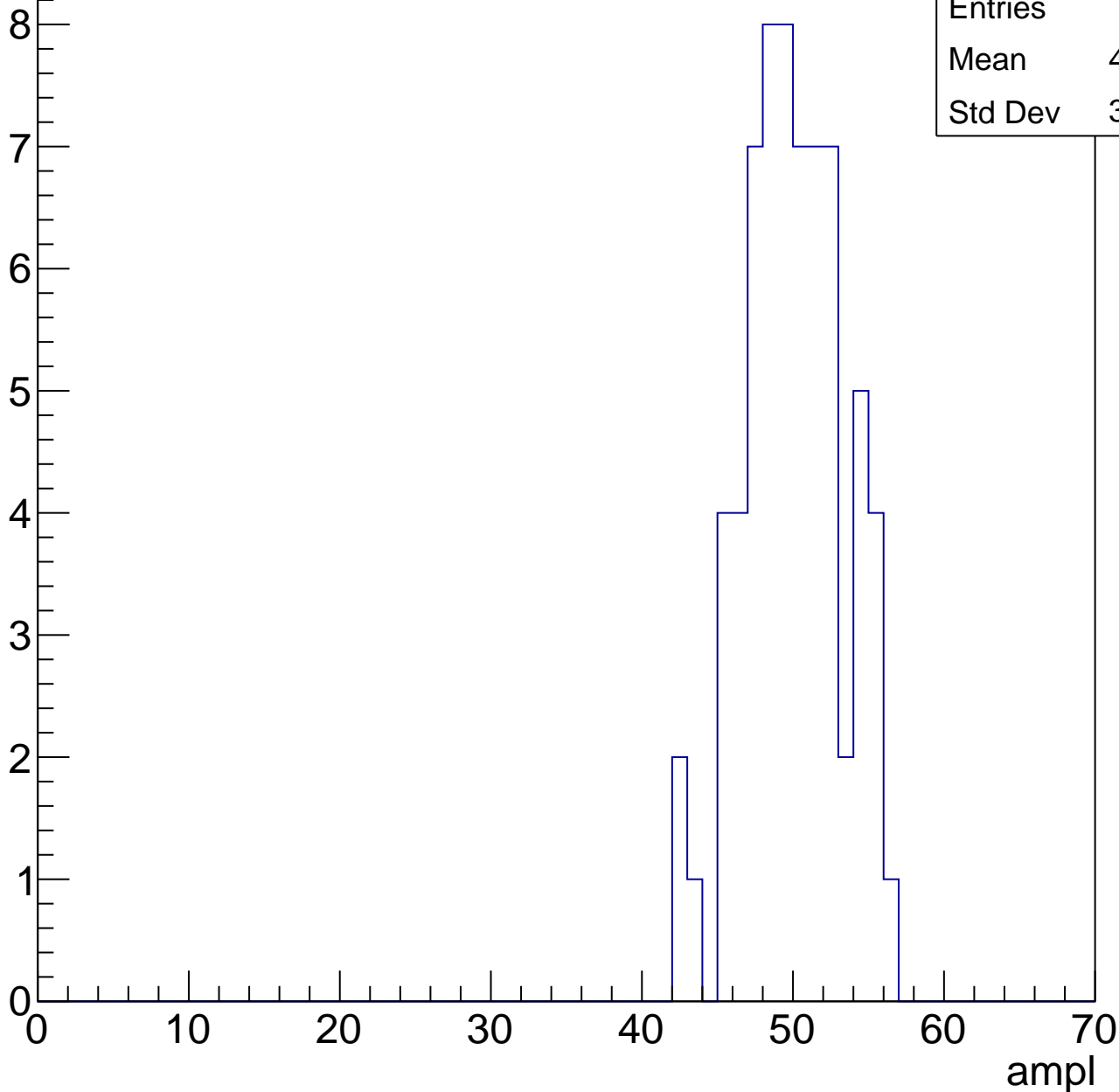


# B0L002S, U8-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	49.54
Std Dev	3.229

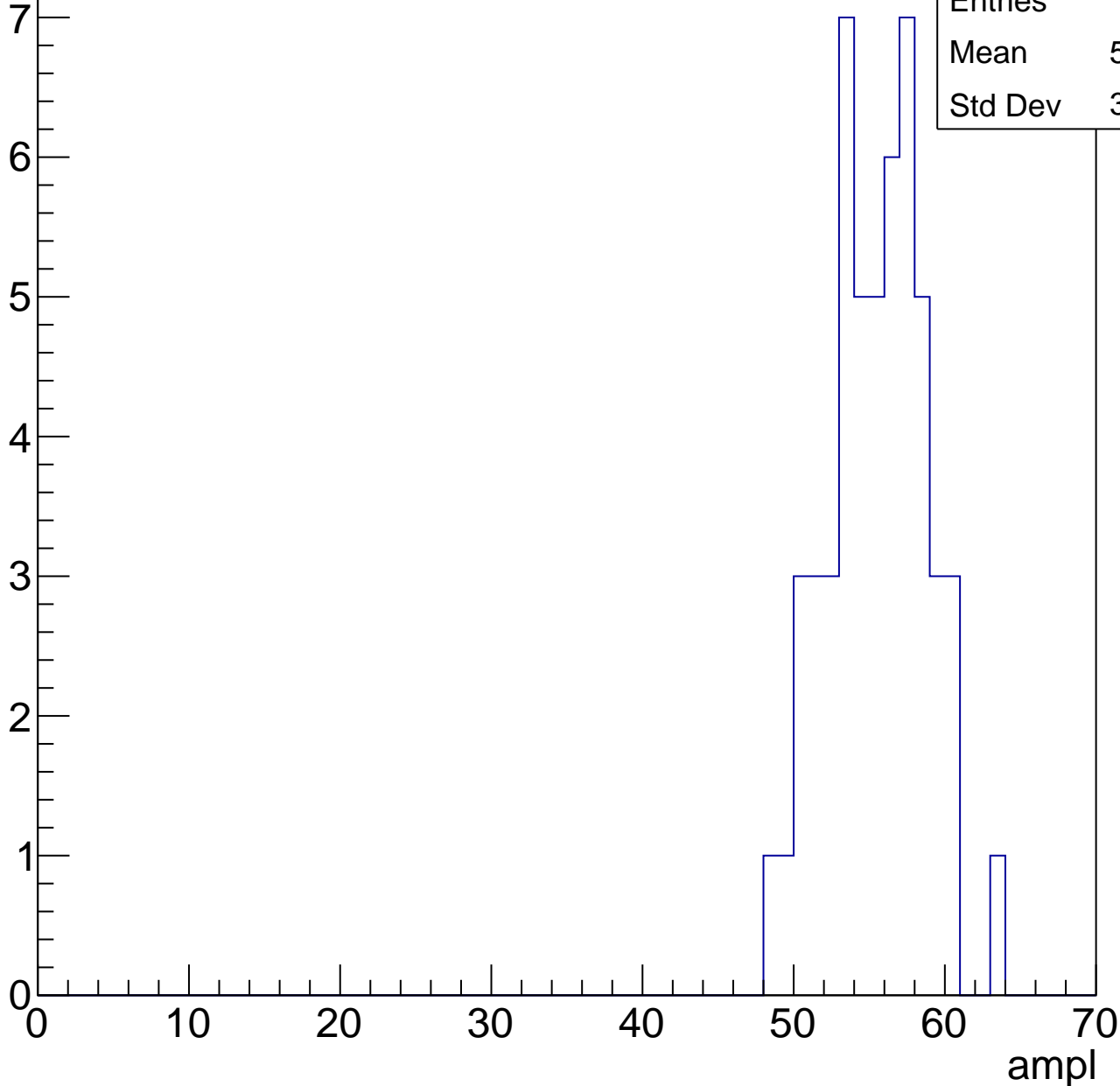


# B0L002S, U8-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	55.04
Std Dev	3.174

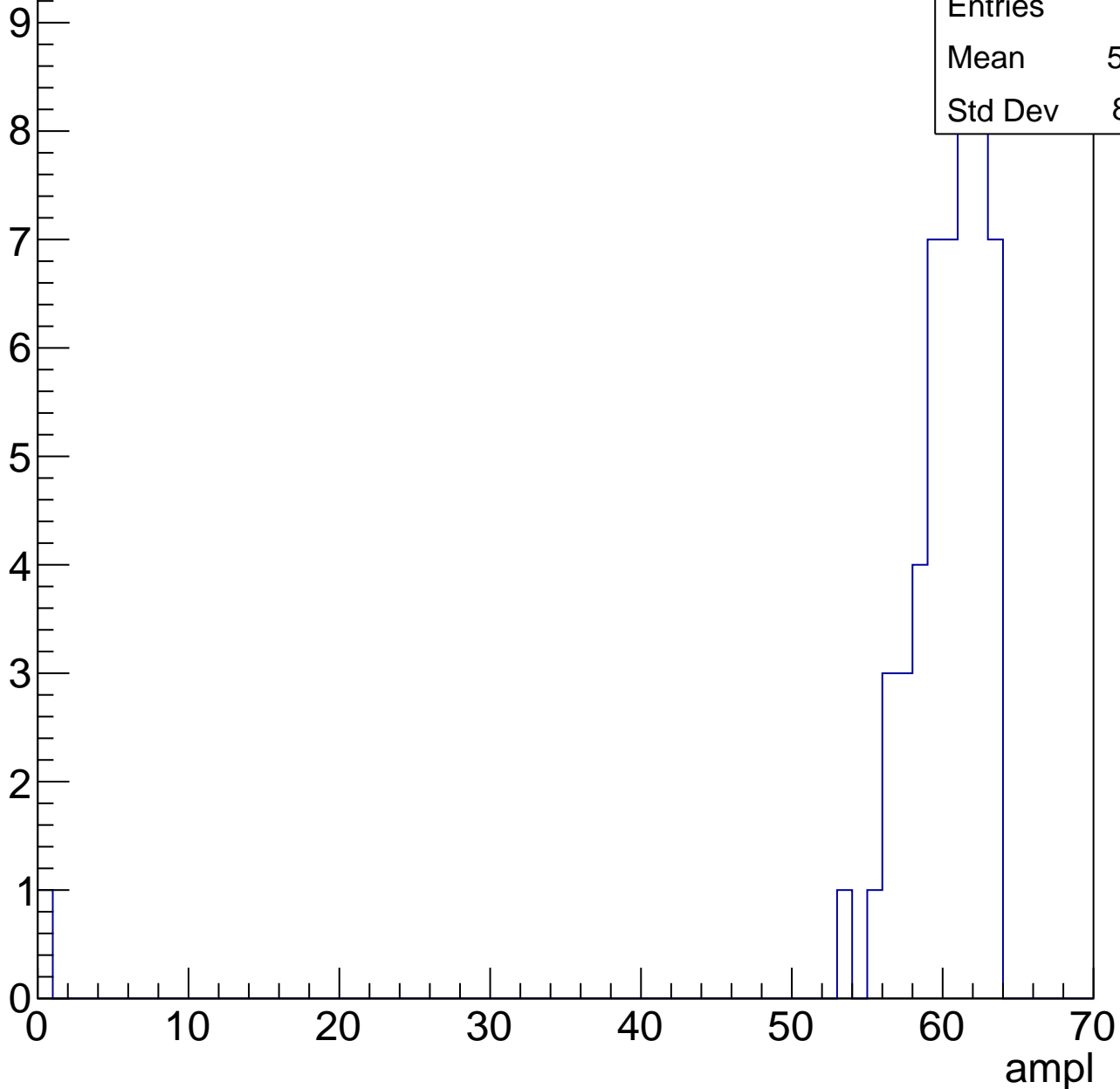


# B0L002S, U8-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

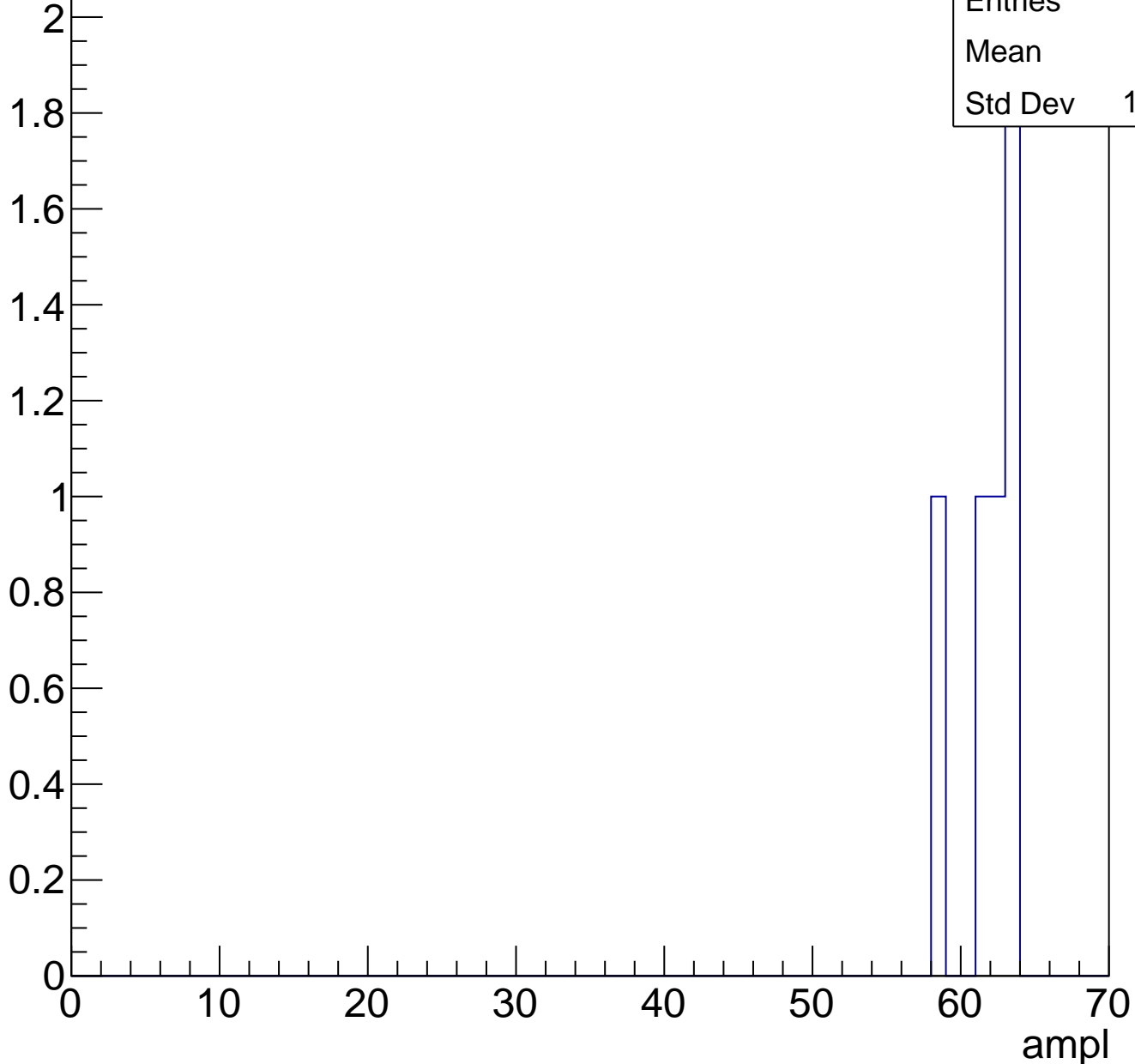
Entries	52
Mean	58.85
Std Dev	8.561



# B0L002S, U8-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	5
Mean	61.4
Std Dev	1.855



# B0L002S, U8-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	30.59
Std Dev	3.444

**Gaus mean : 31.2925**

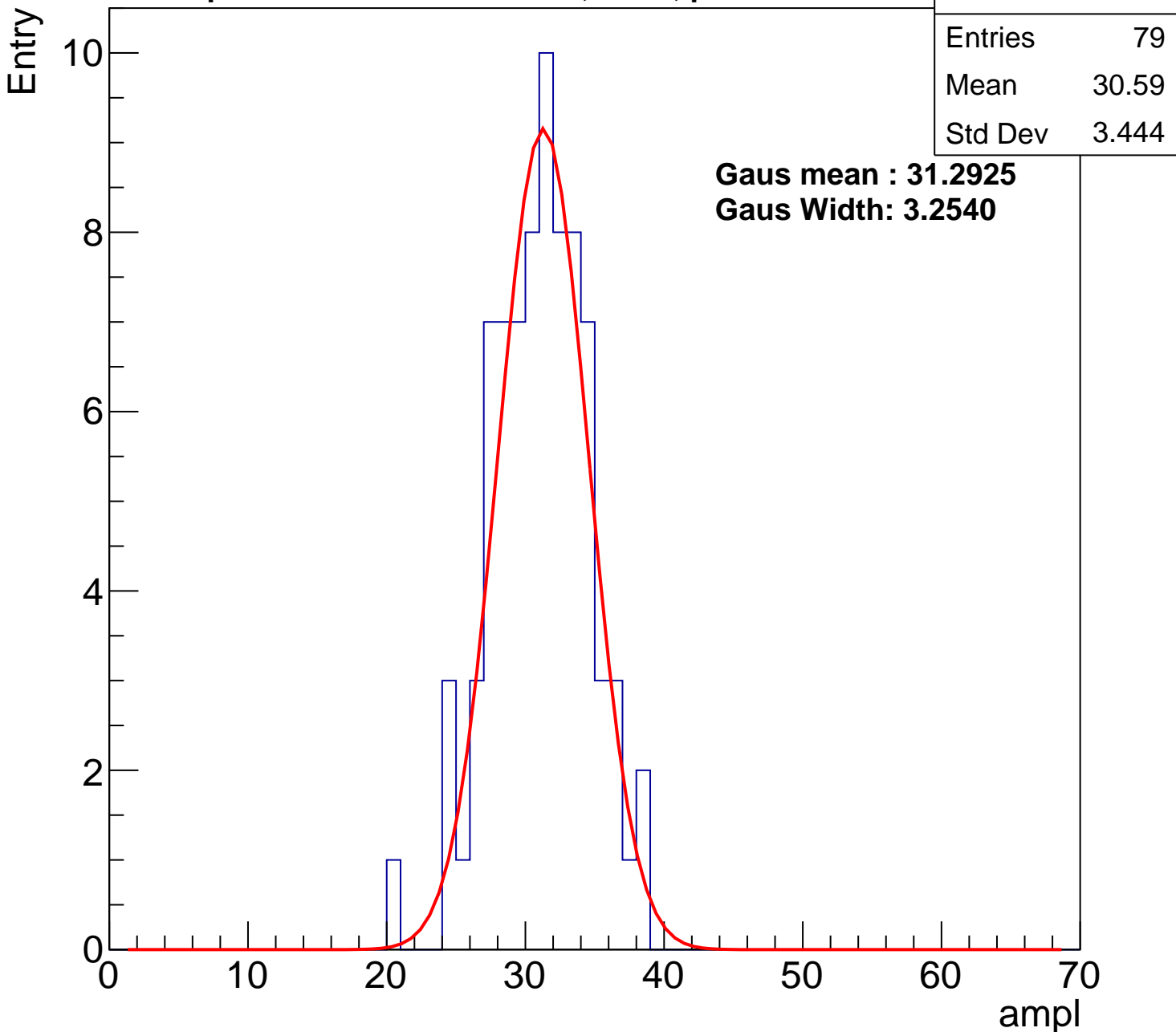
**Gaus Width: 3.2540**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch25, adc1

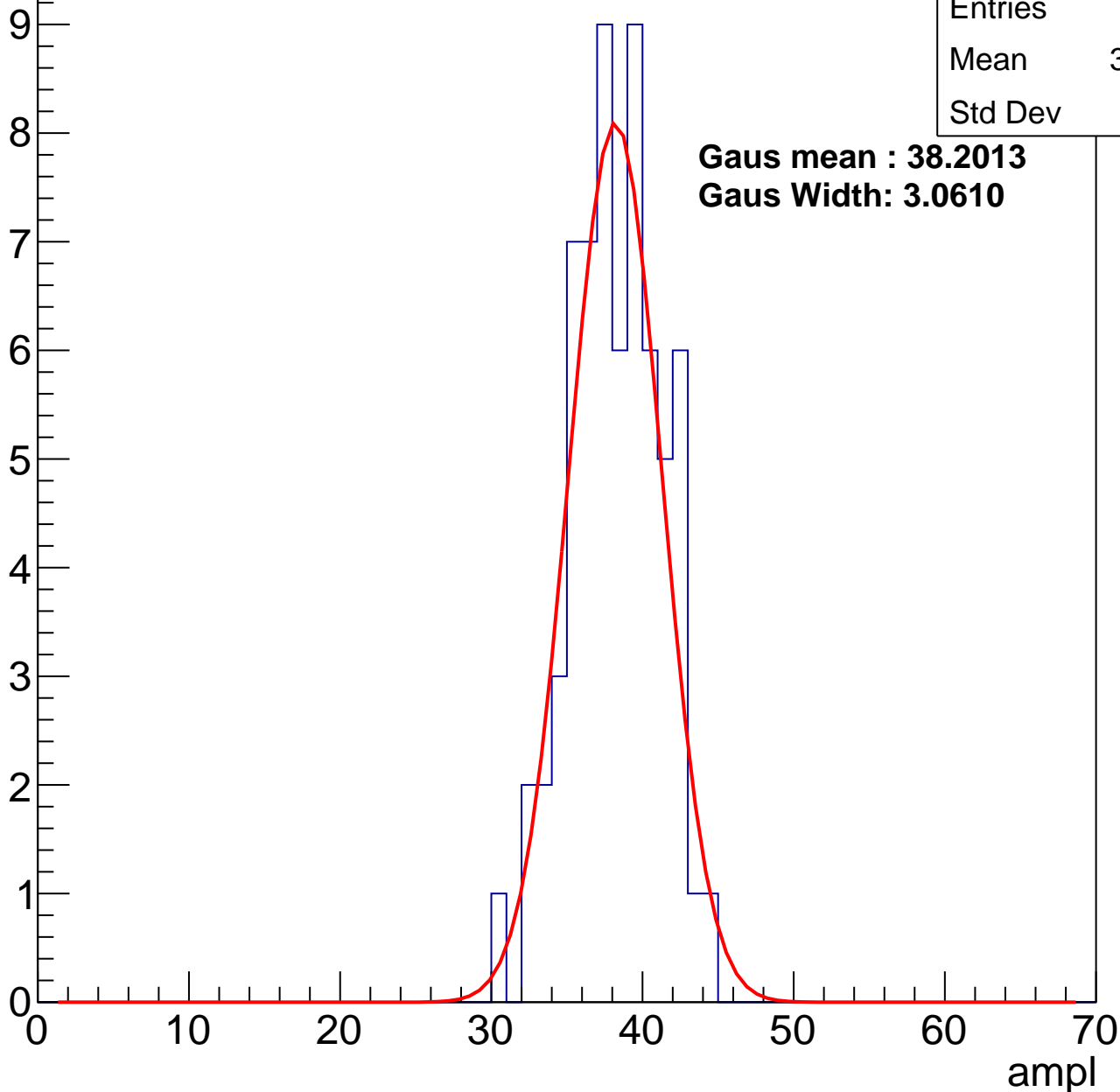
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.77
Std Dev	2.95

**Gaus mean : 38.2013**

**Gaus Width: 3.0610**



# B0L002S, U8-ch25, adc2

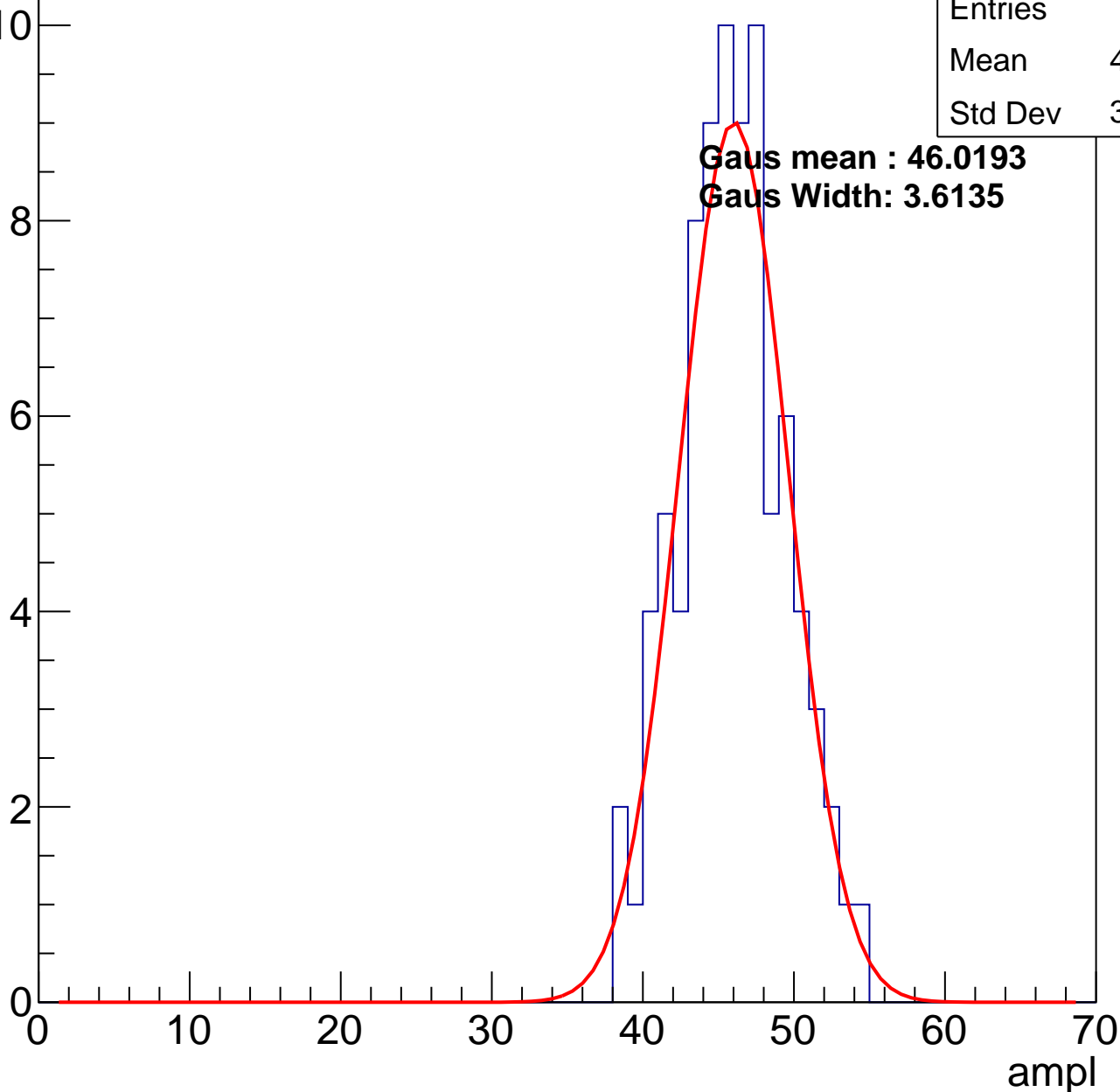
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	84
Mean	45.48
Std Dev	3.473

**Gaus mean : 46.0193**

**Gaus Width: 3.6135**

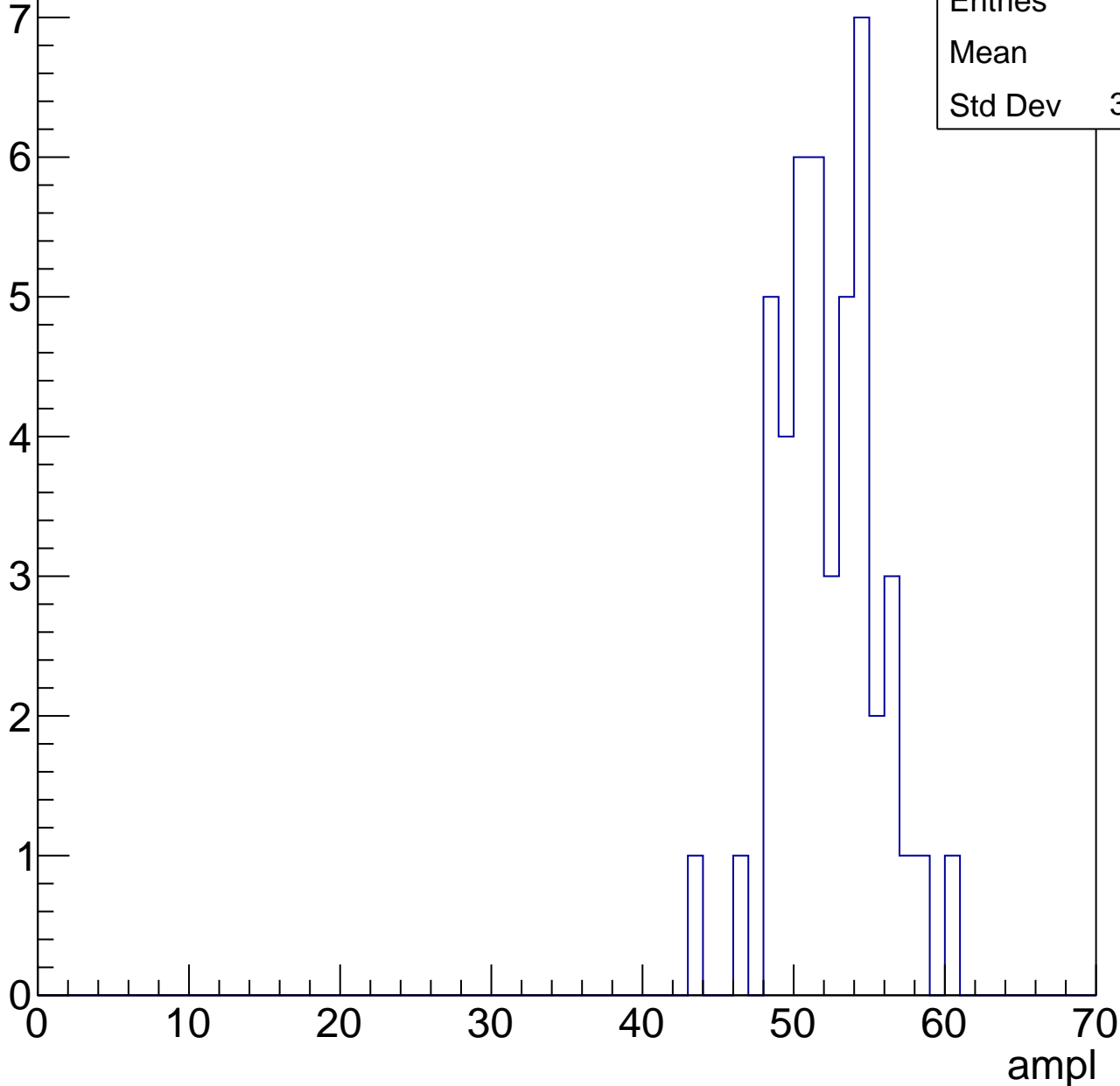


# B0L002S, U8-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

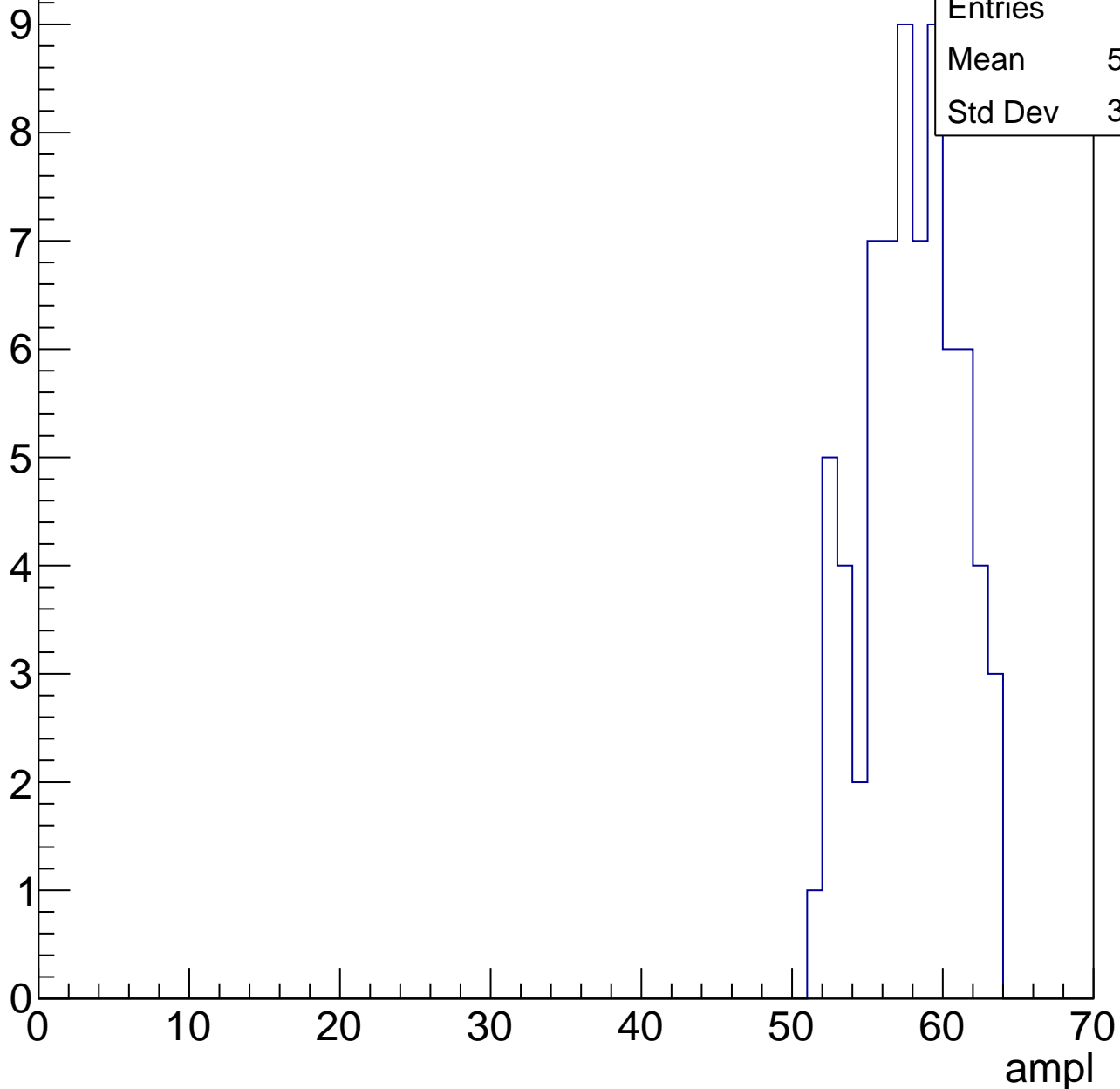
Entries	46
Mean	51.8
Std Dev	3.248



# B0L002S, U8-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



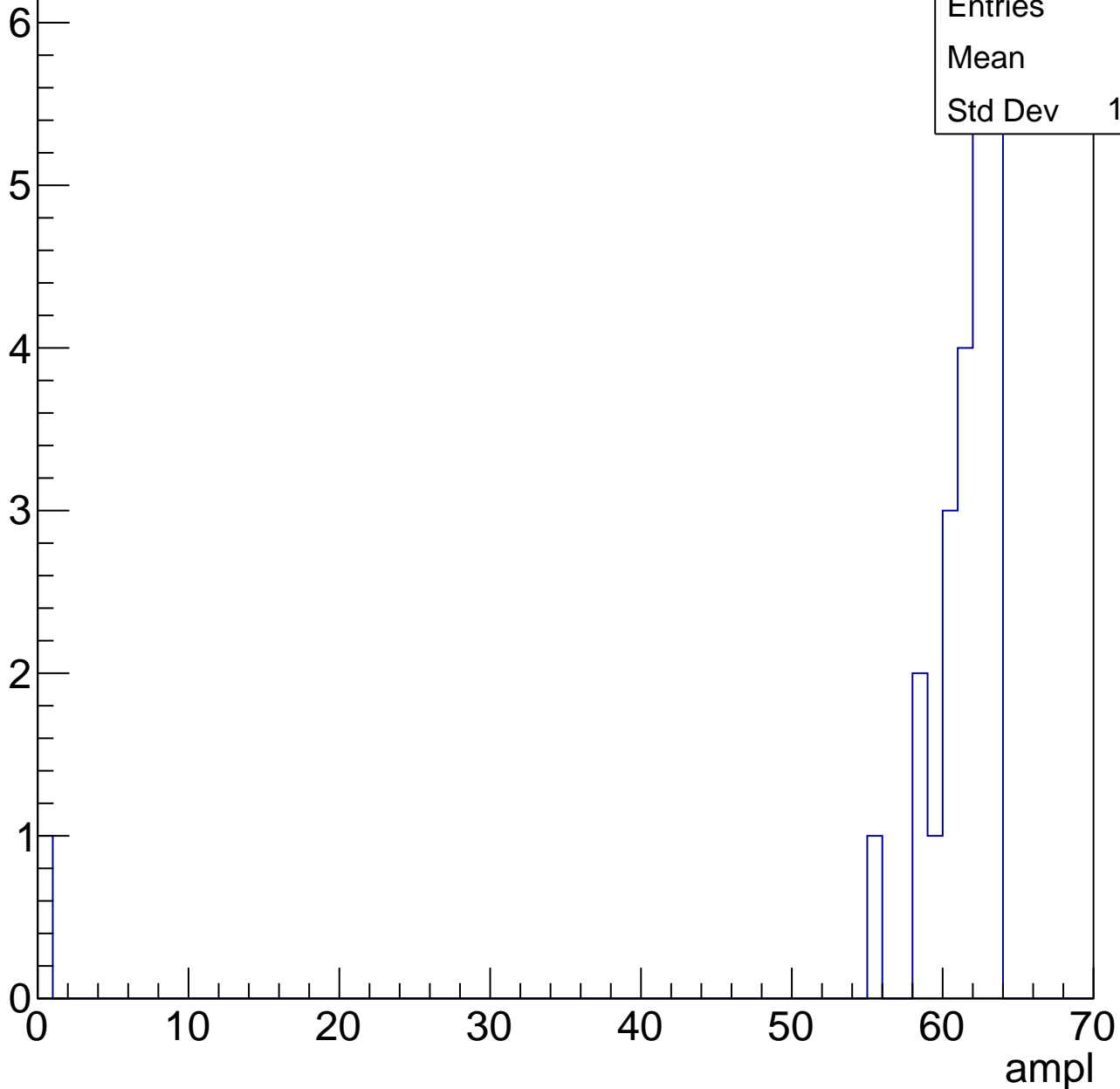
Entries	70
Mean	57.44
Std Dev	3.083

# B0L002S, U8-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.5
Std Dev	12.35



# B0L002S, U8-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch26, adc0

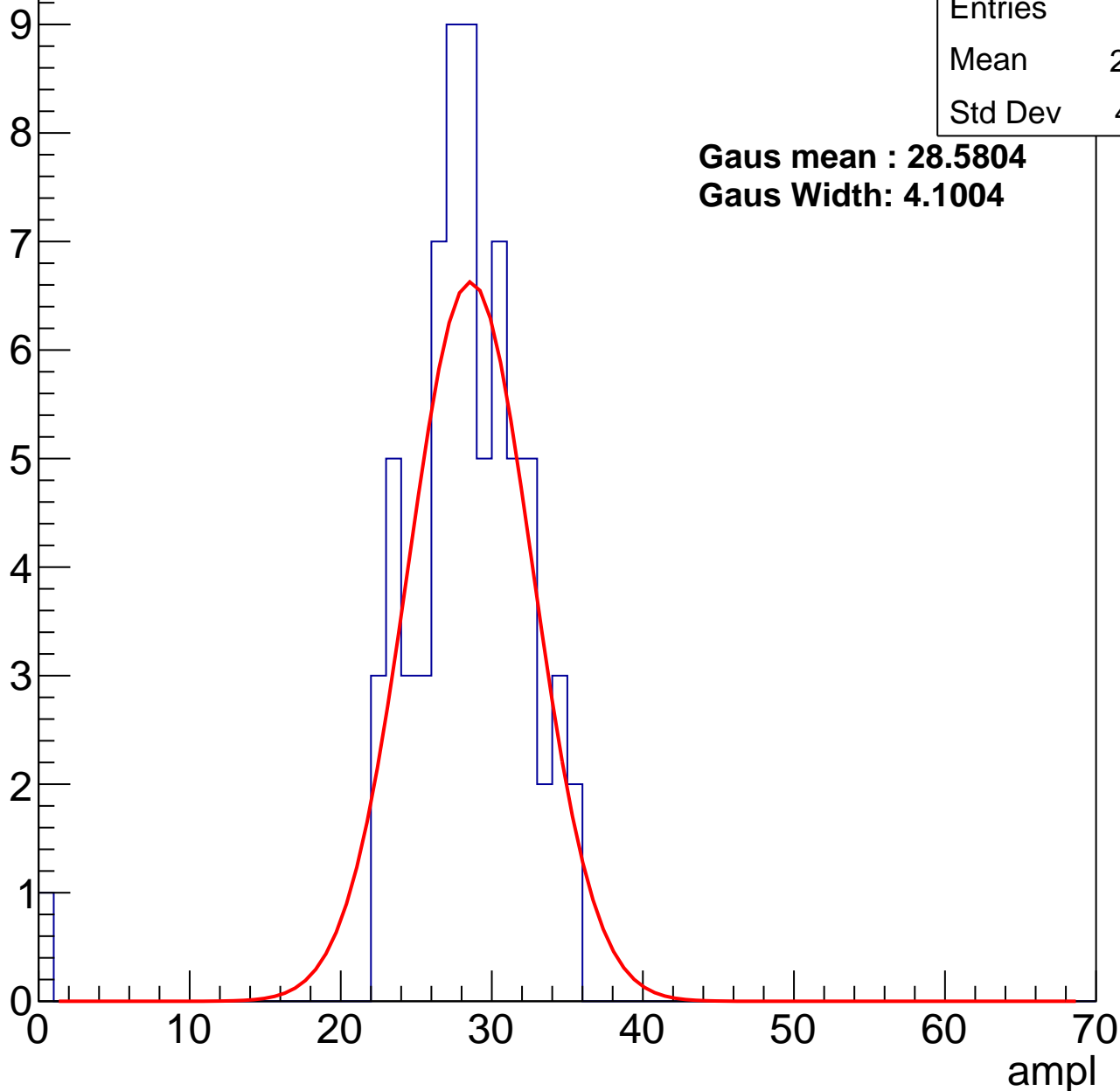
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	27.72
Std Dev	4.721

**Gaus mean : 28.5804**

**Gaus Width: 4.1004**



# B0L002S, U8-ch26, adc1

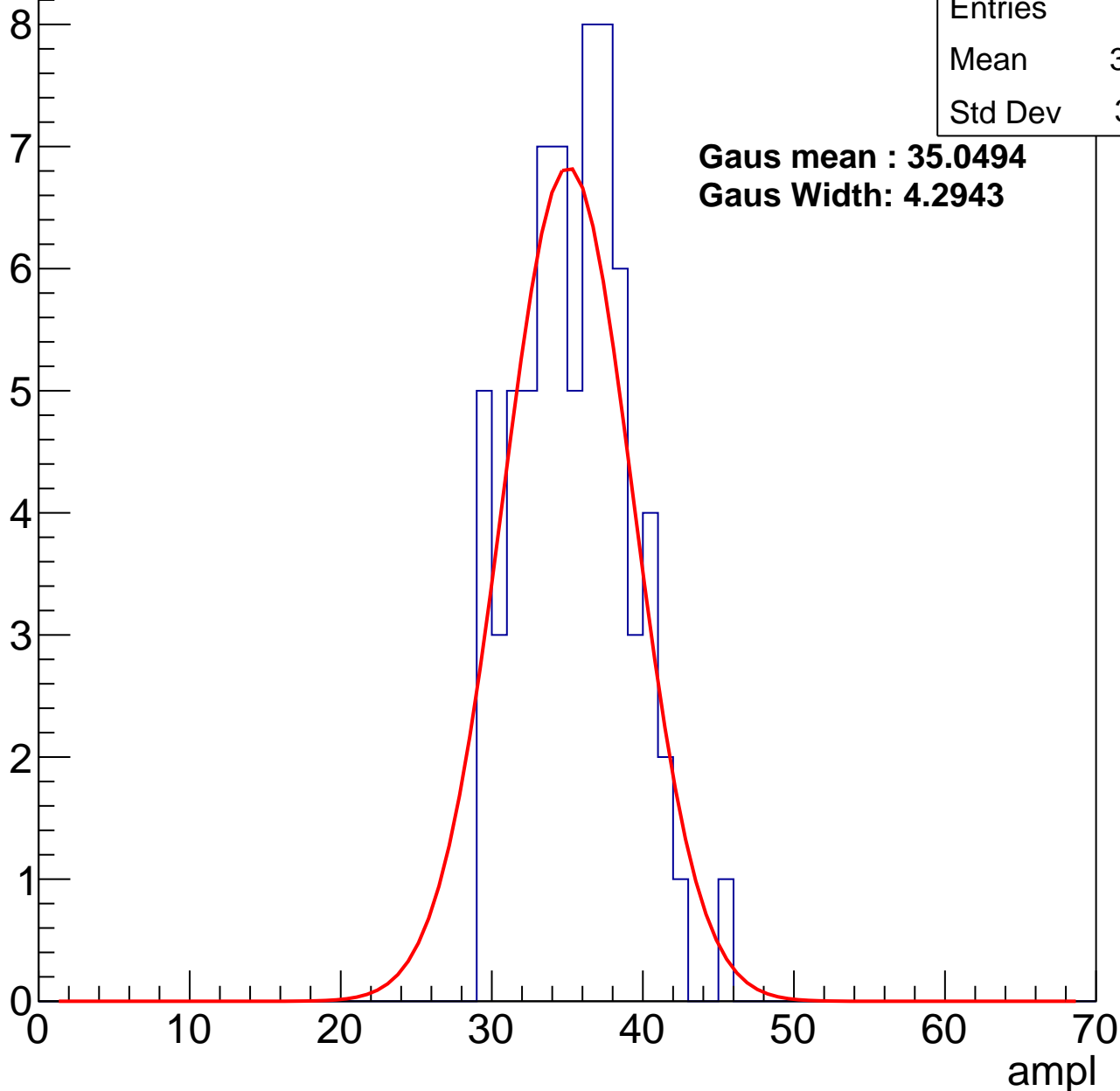
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.03
Std Dev	3.541

**Gaus mean : 35.0494**

**Gaus Width: 4.2943**



# B0L002S, U8-ch26, adc2

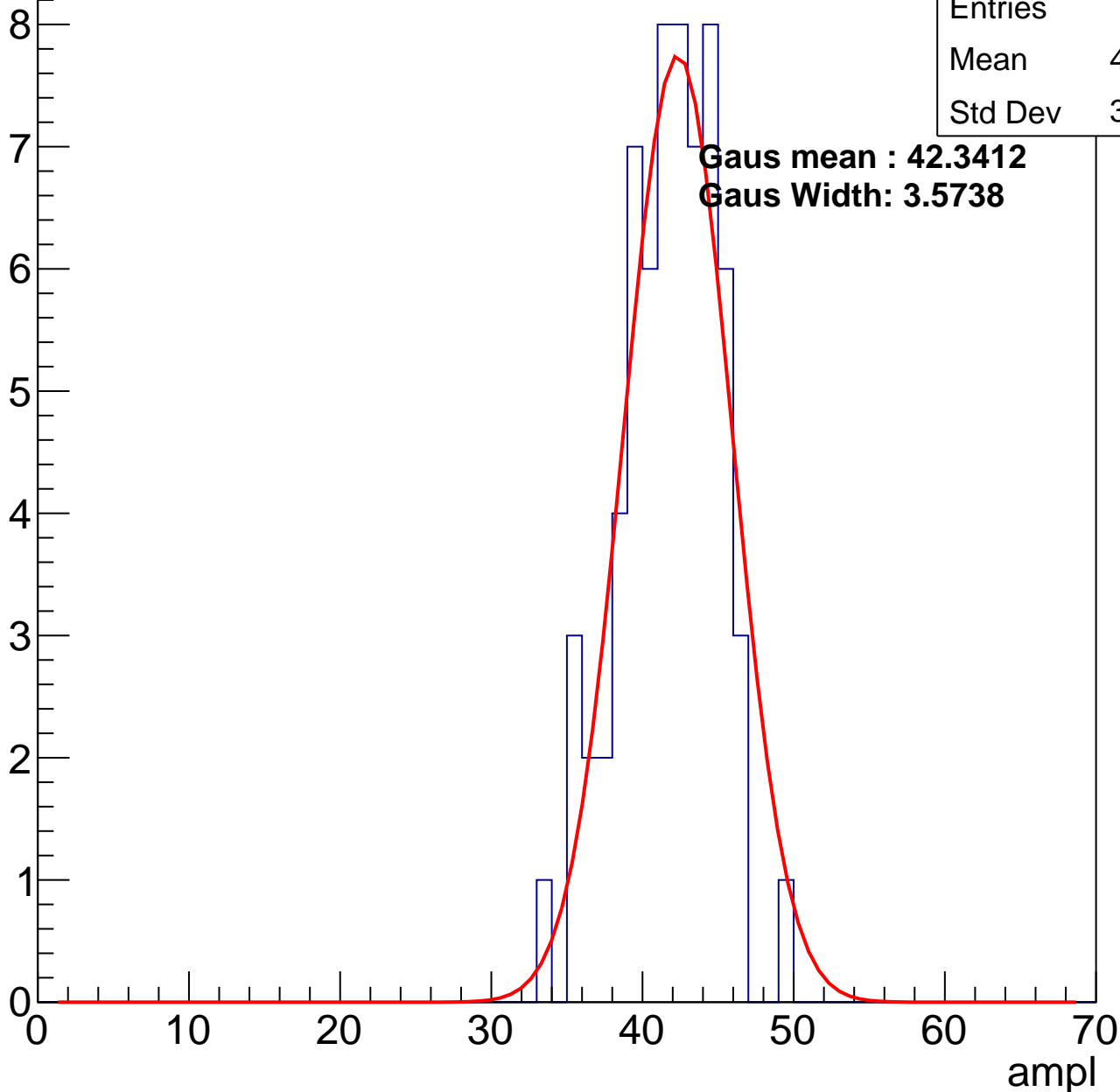
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	41.26
Std Dev	3.169

**Gaus mean : 42.3412**

**Gaus Width: 3.5738**



# B0L002S, U8-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

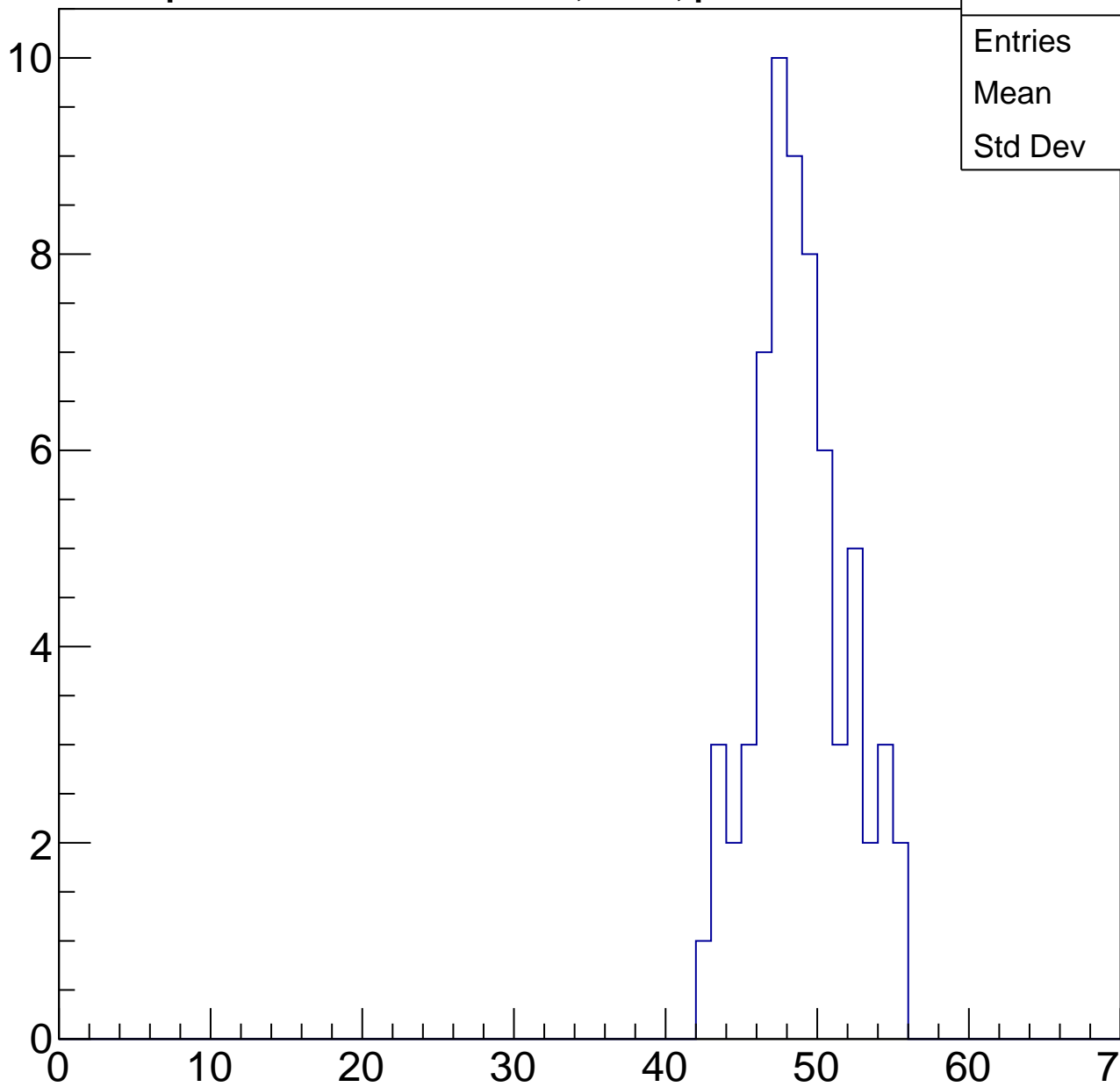
Entries	64
Mean	48.45
Std Dev	3.036

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

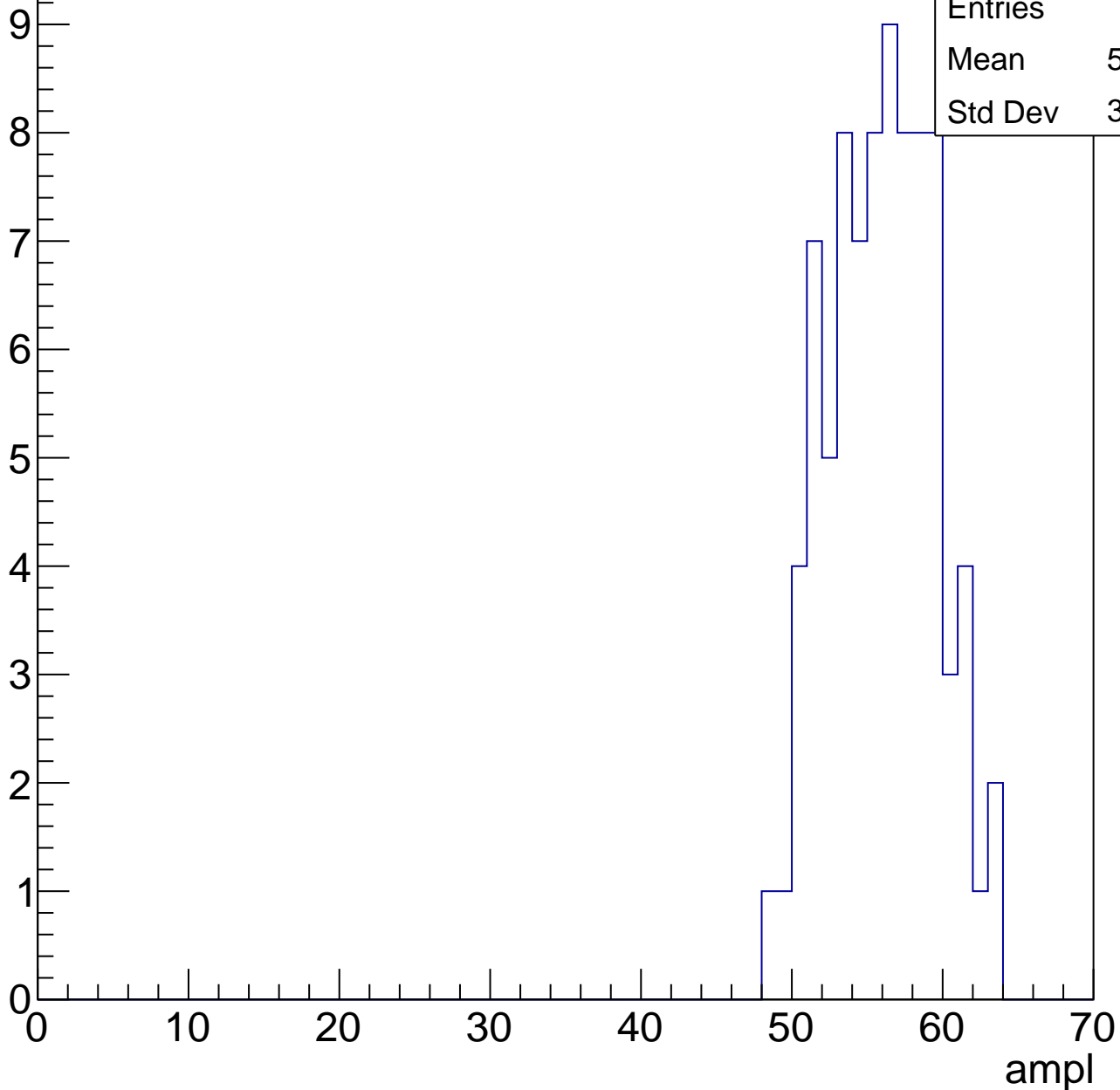


# B0L002S, U8-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	84
Mean	55.52
Std Dev	3.442



# B0L002S, U8-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

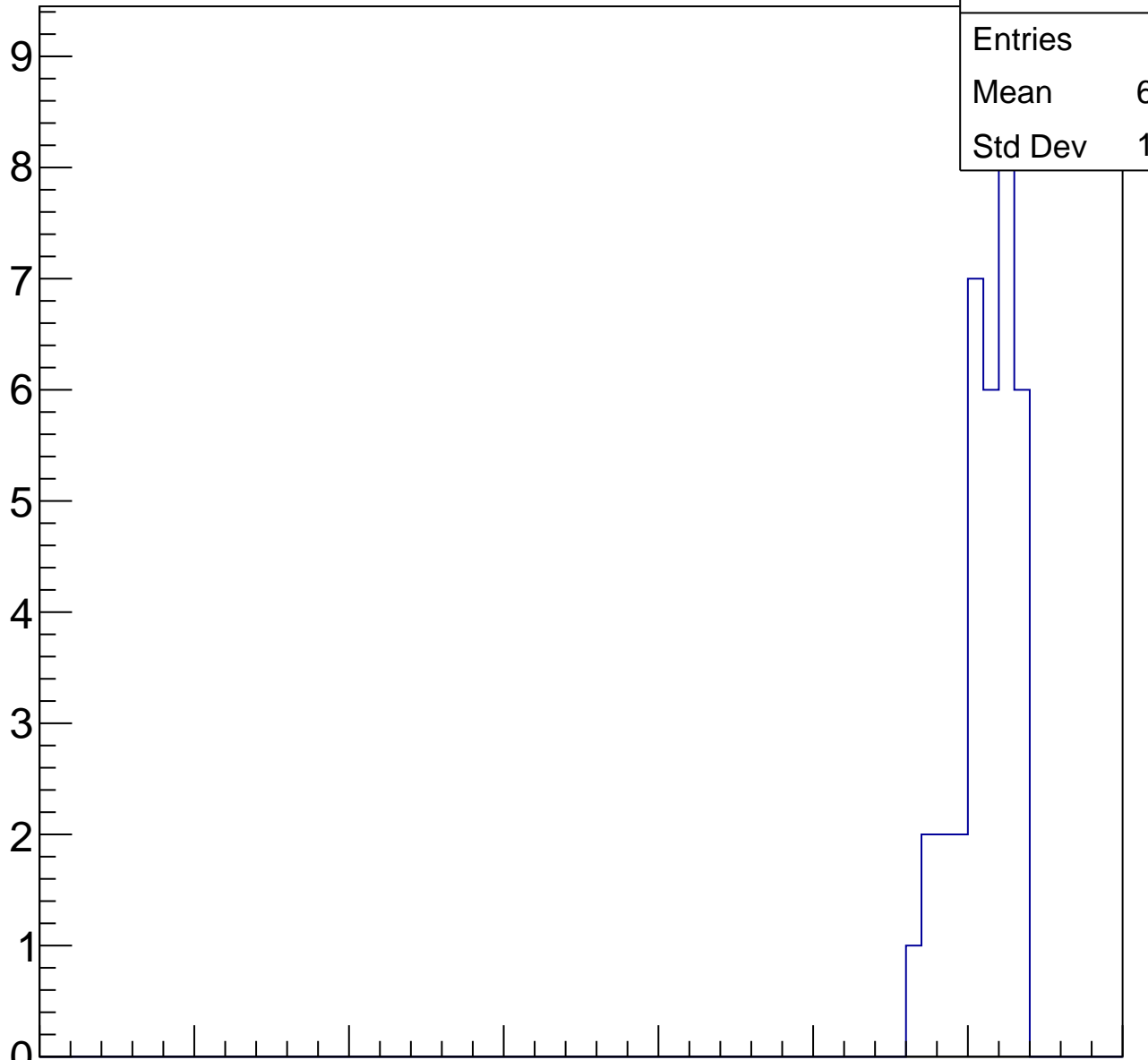
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	35
Mean	60.74
Std Dev	1.857

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch27, adc0

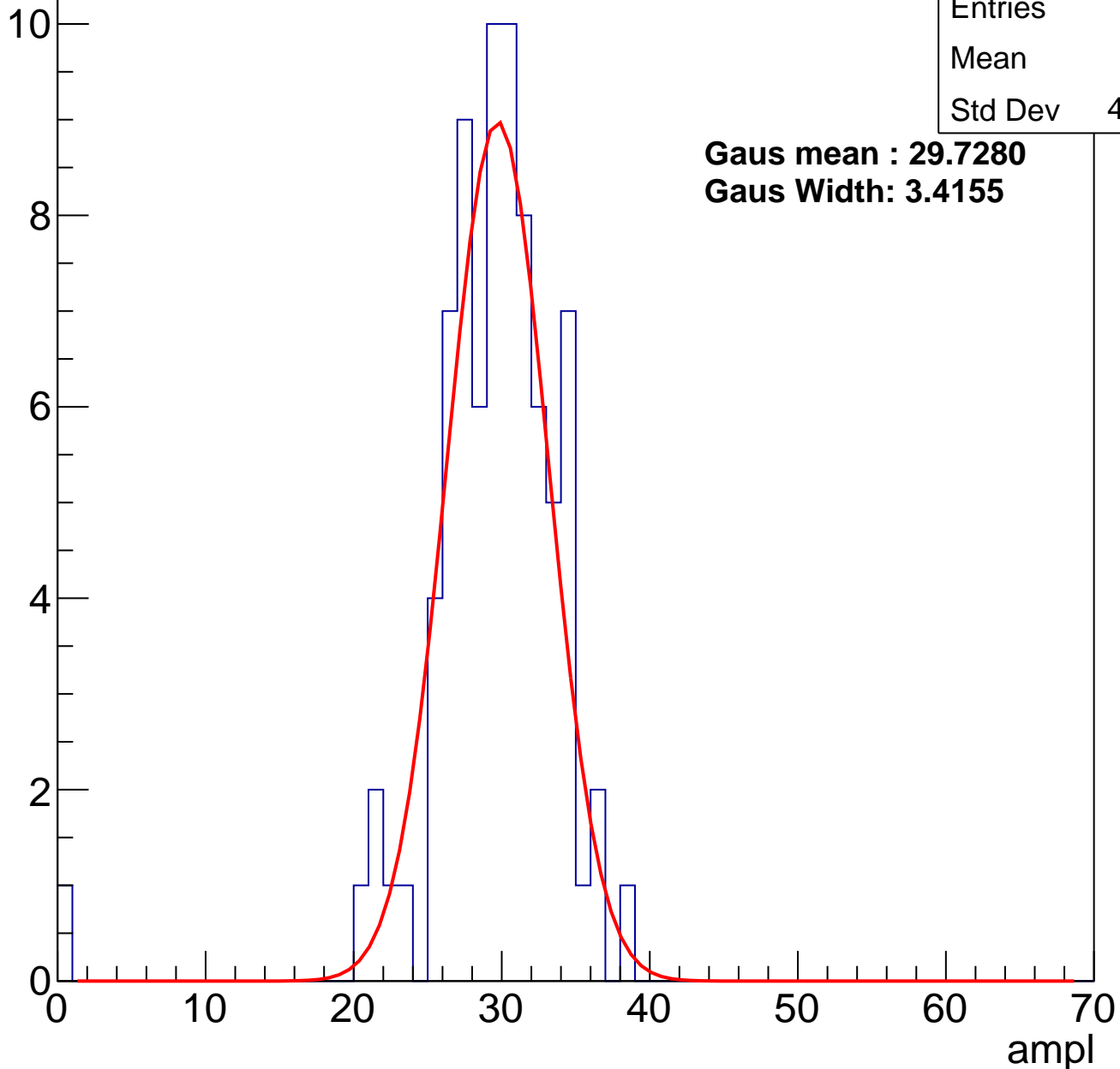
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	82
Mean	29
Std Dev	4.775

**Gaus mean : 29.7280**

**Gaus Width: 3.4155**

Entry



# B0L002S, U8-ch27, adc1

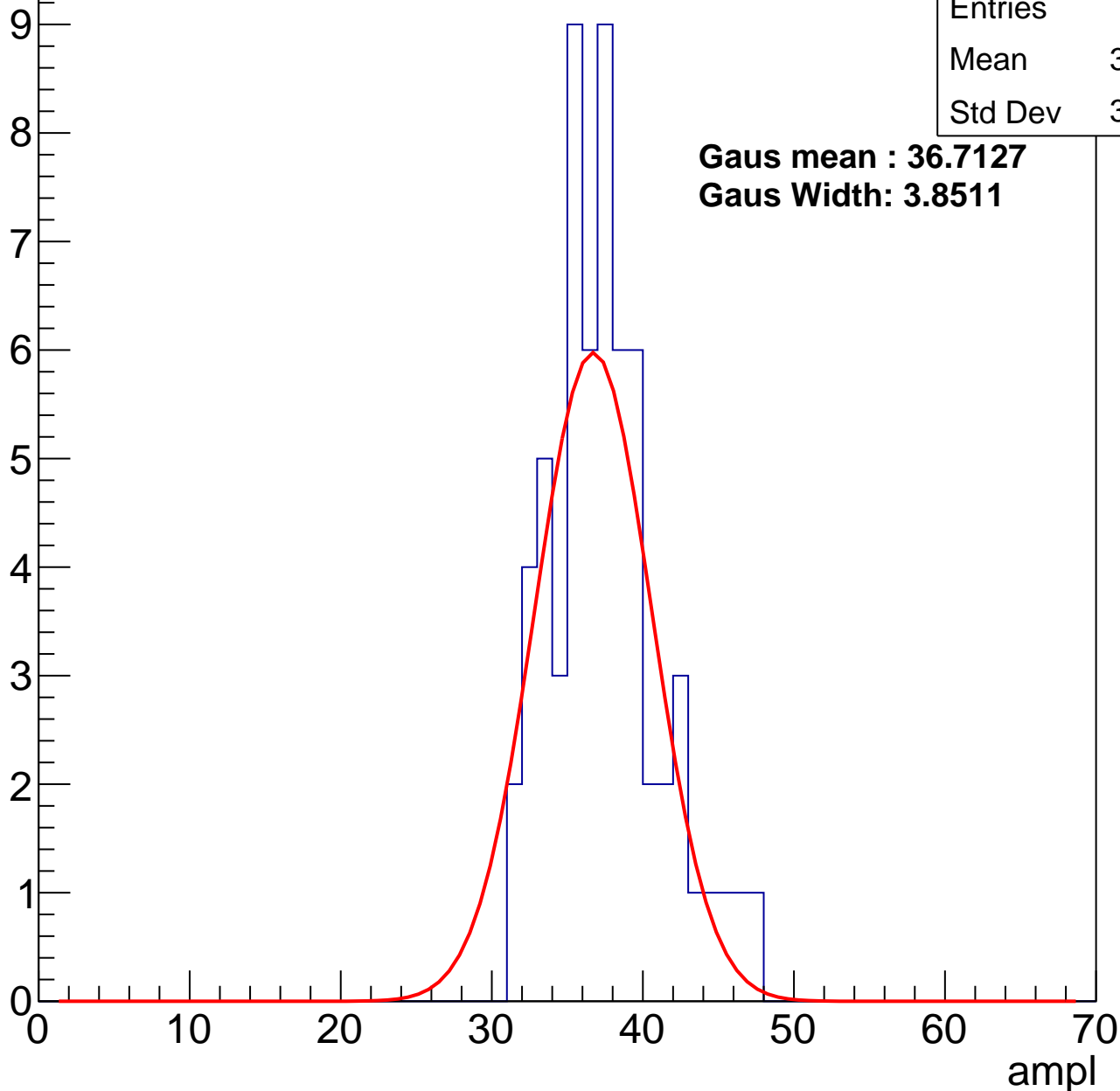
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.03
Std Dev	3.605

**Gaus mean : 36.7127**

**Gaus Width: 3.8511**



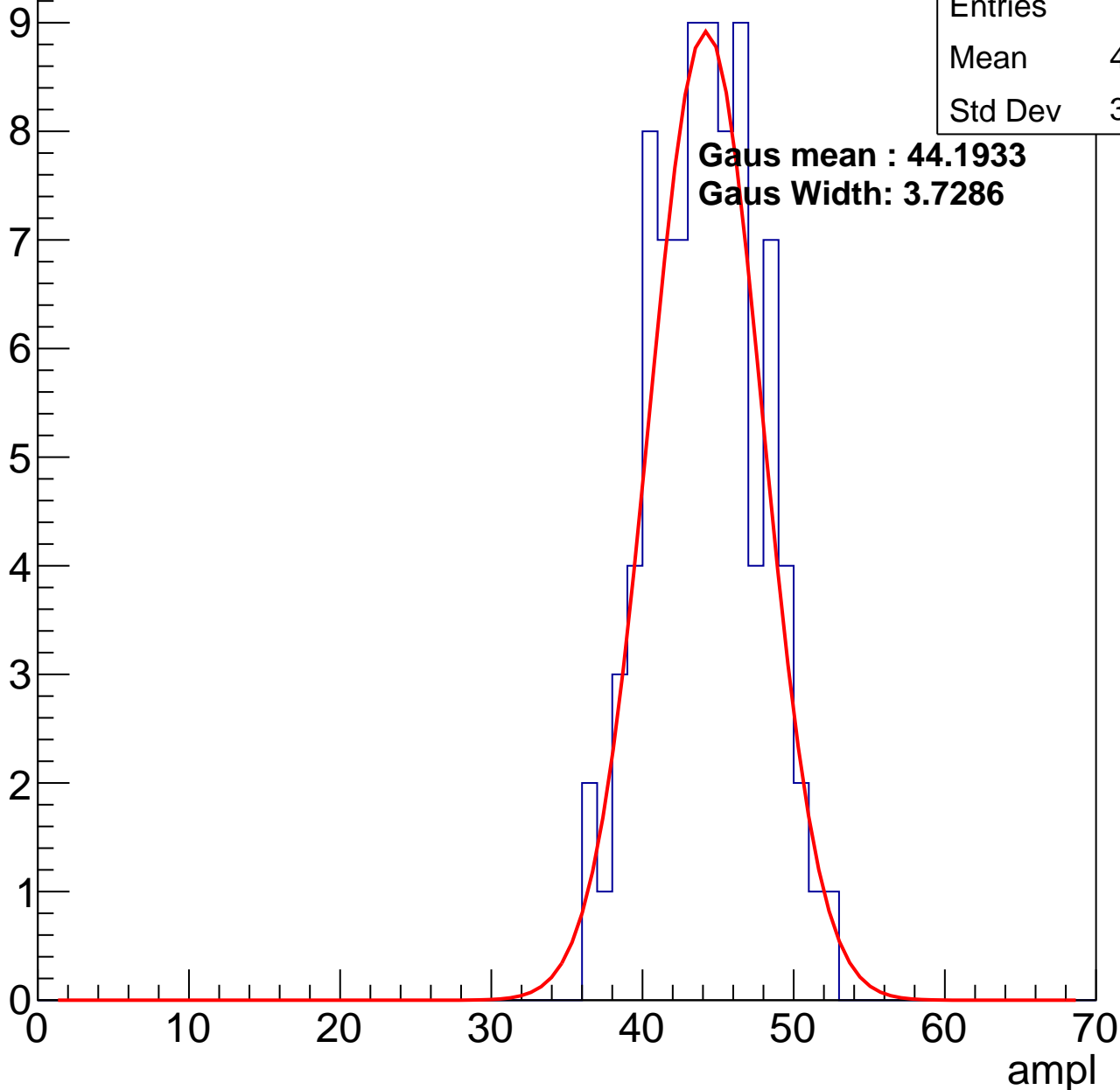
# B0L002S, U8-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	86
Mean	43.72
Std Dev	3.549

**Gaus mean : 44.1933**  
**Gaus Width: 3.7286**

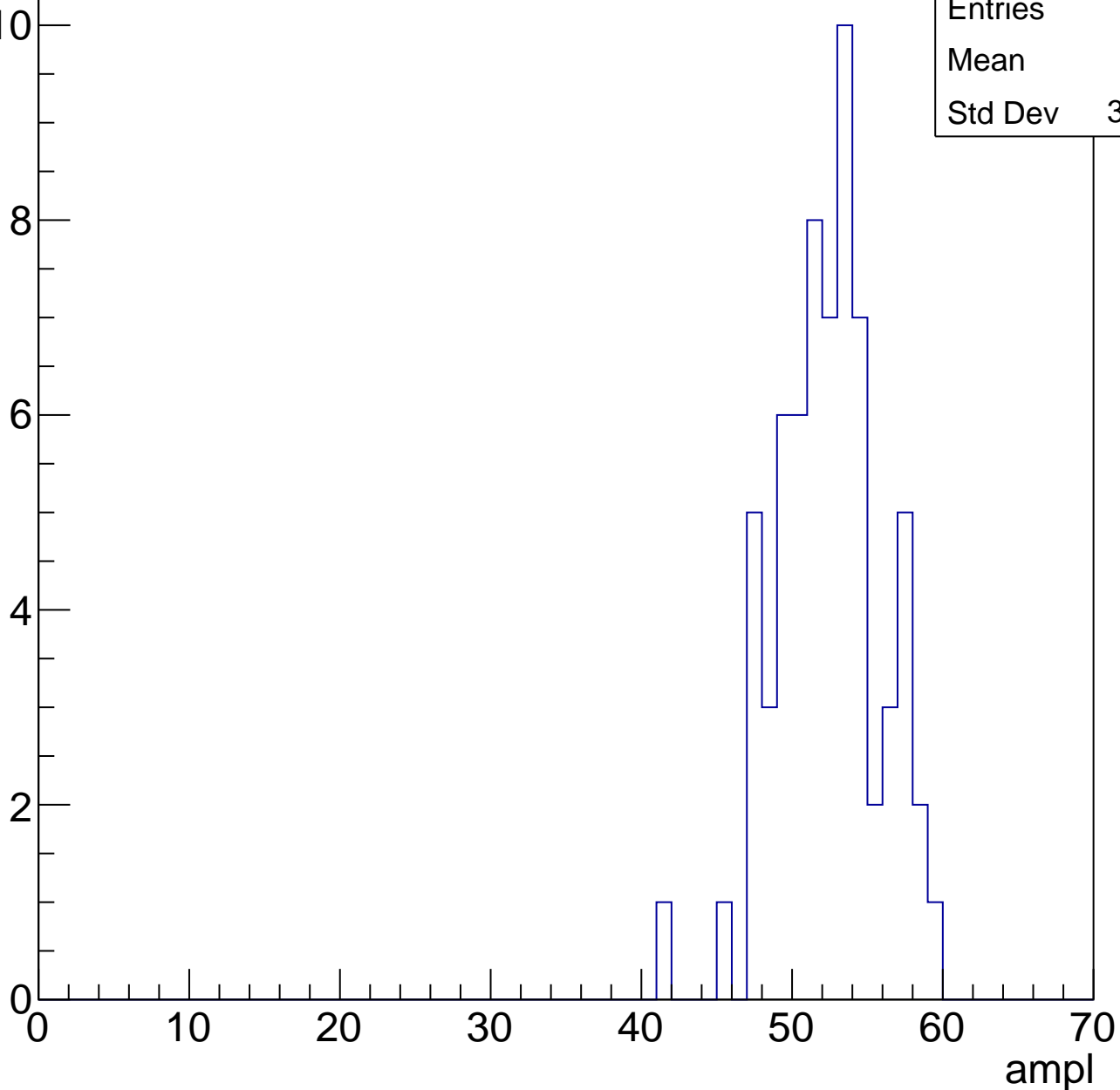


# B0L002S, U8-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	51.9
Std Dev	3.408

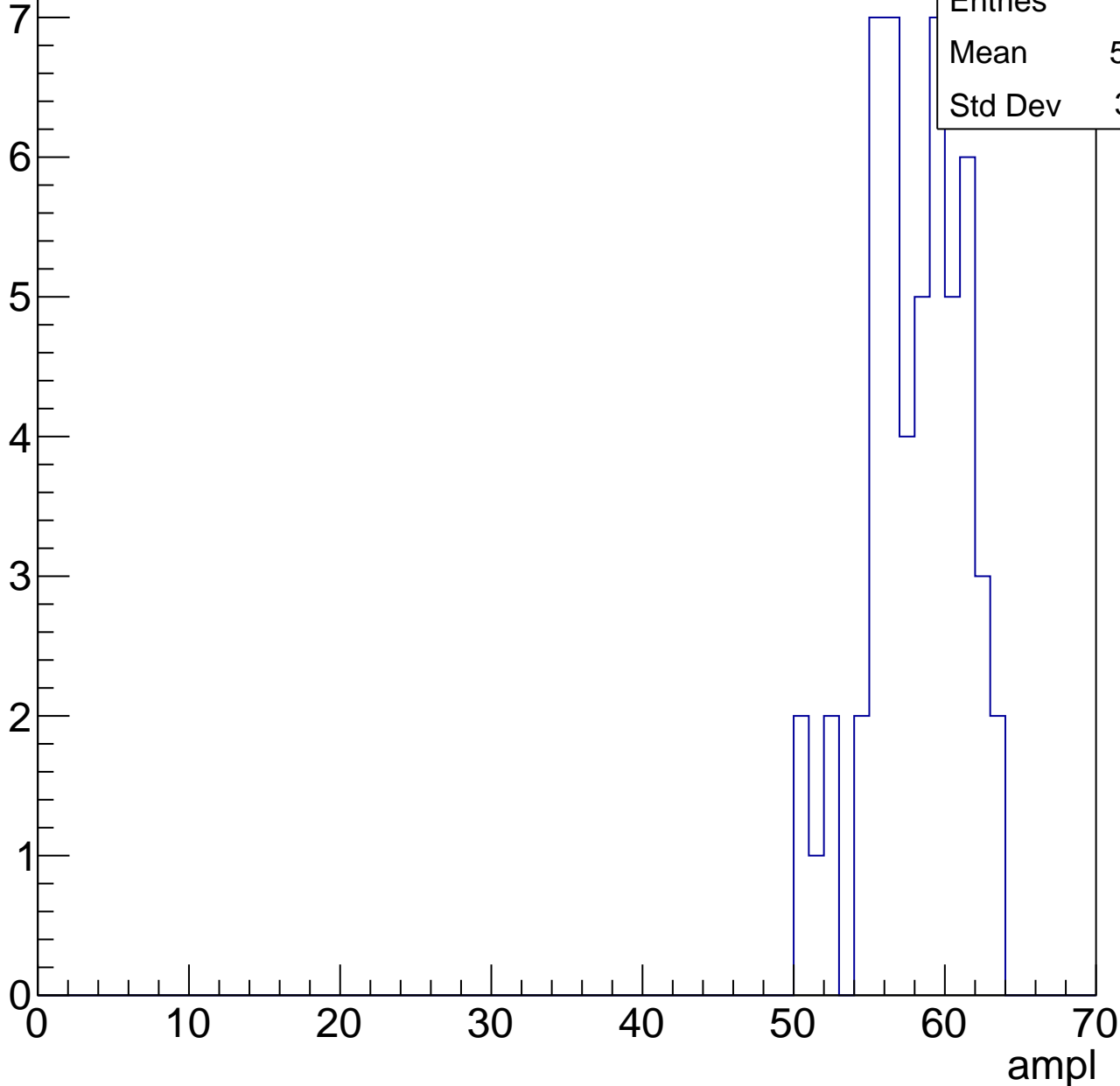


# B0L002S, U8-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.53
Std Dev	3.201

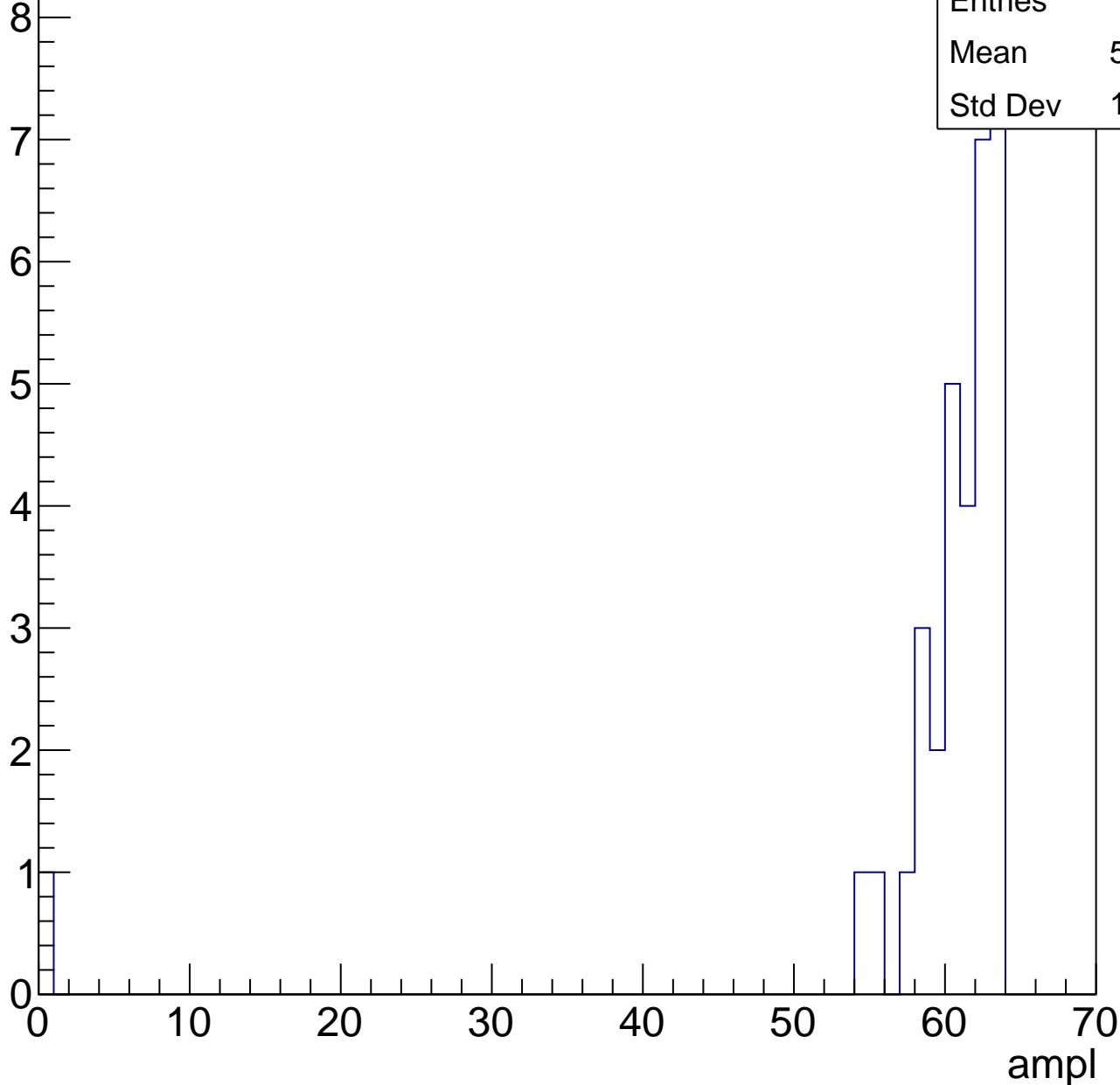


# B0L002S, U8-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.79
Std Dev	10.65



# B0L002S, U8-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch28, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	32.12
Std Dev	3.386

**Gaus mean : 32.5075**

**Gaus Width: 3.4502**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L002S, U8-ch28, adc1

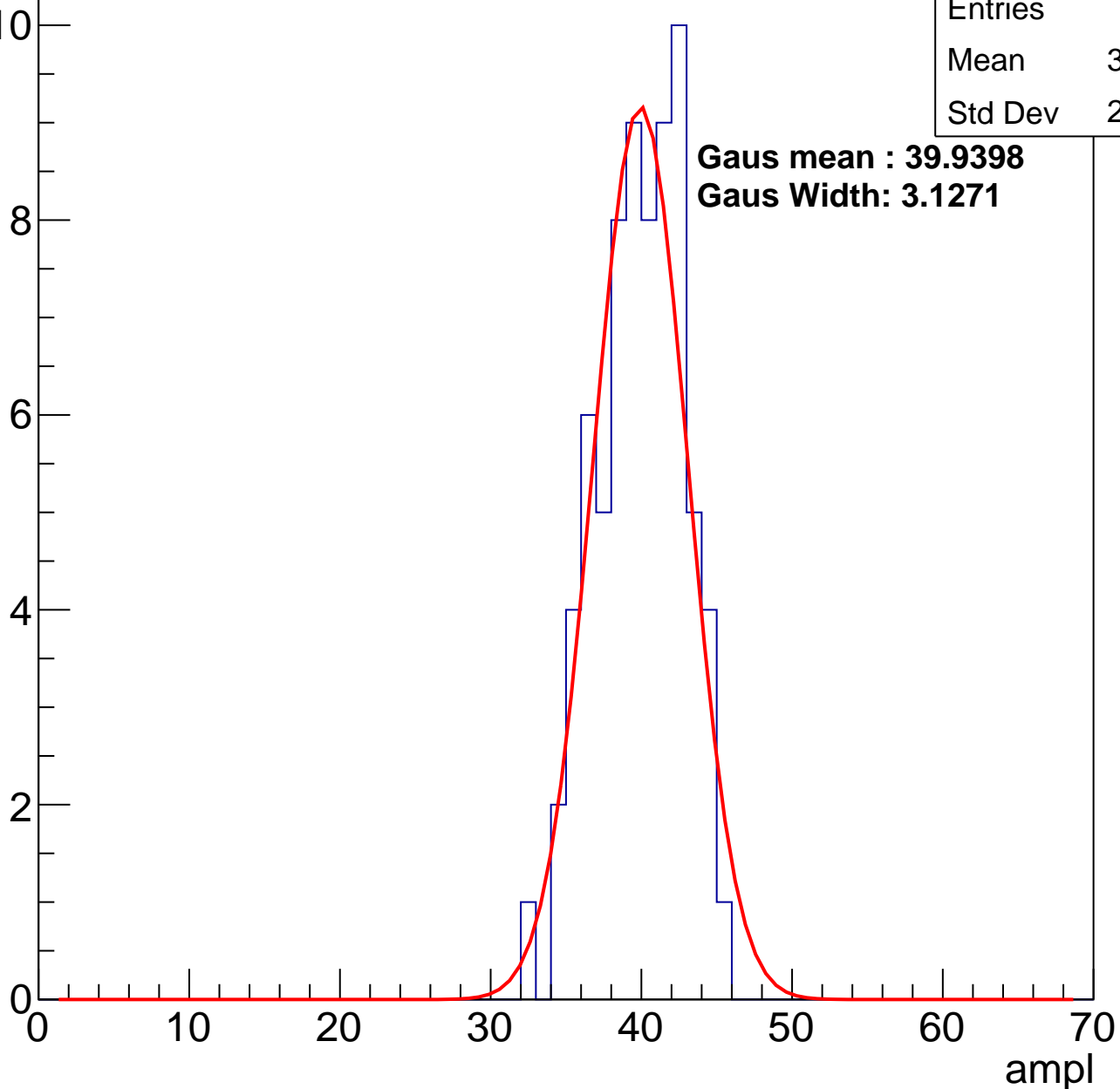
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	39.46
Std Dev	2.838

**Gaus mean : 39.9398**

**Gaus Width: 3.1271**



# B0L002S, U8-ch28, adc2

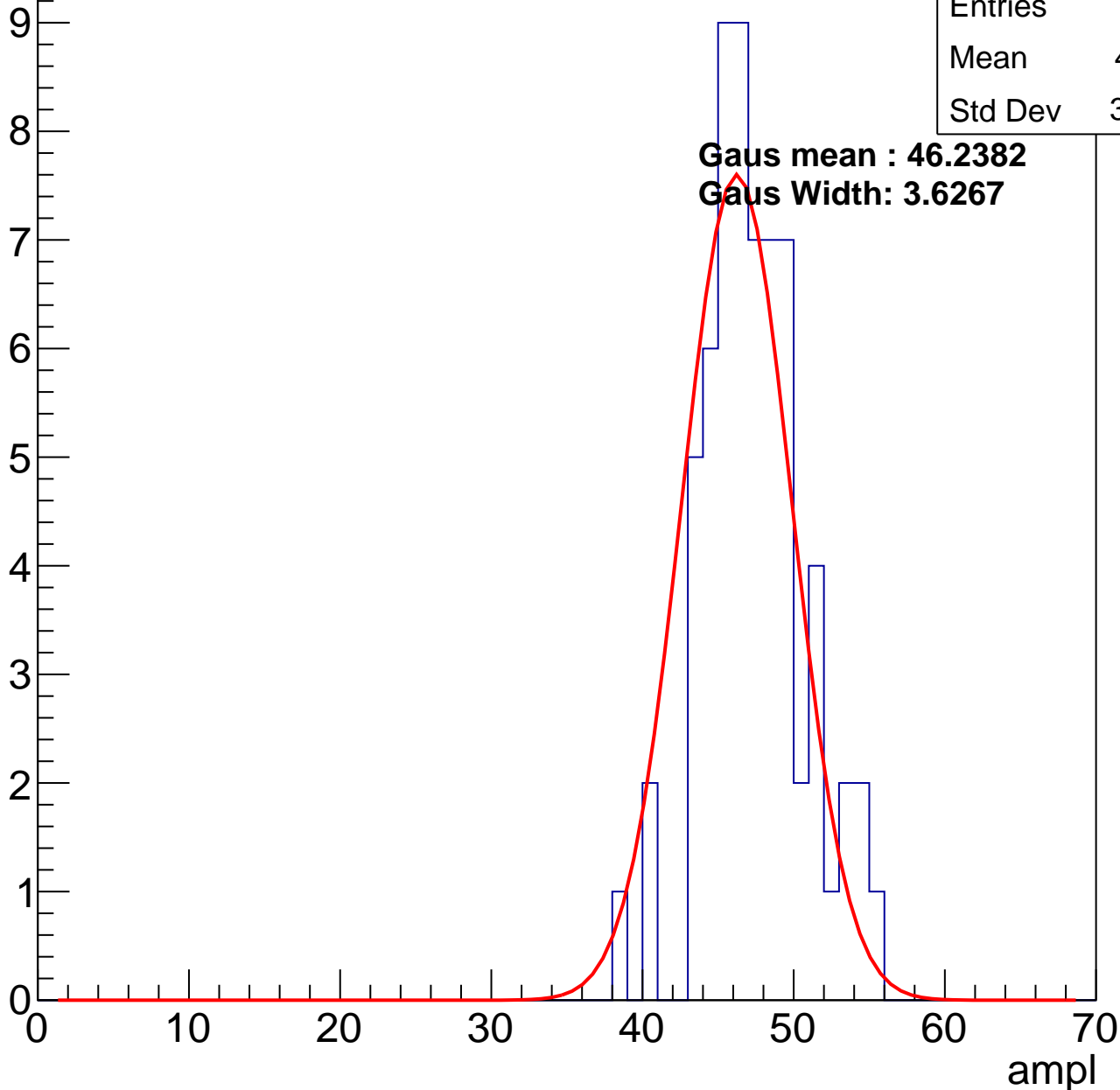
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	46.91
Std Dev	3.359

**Gaus mean : 46.2382**

**Gaus Width: 3.6267**

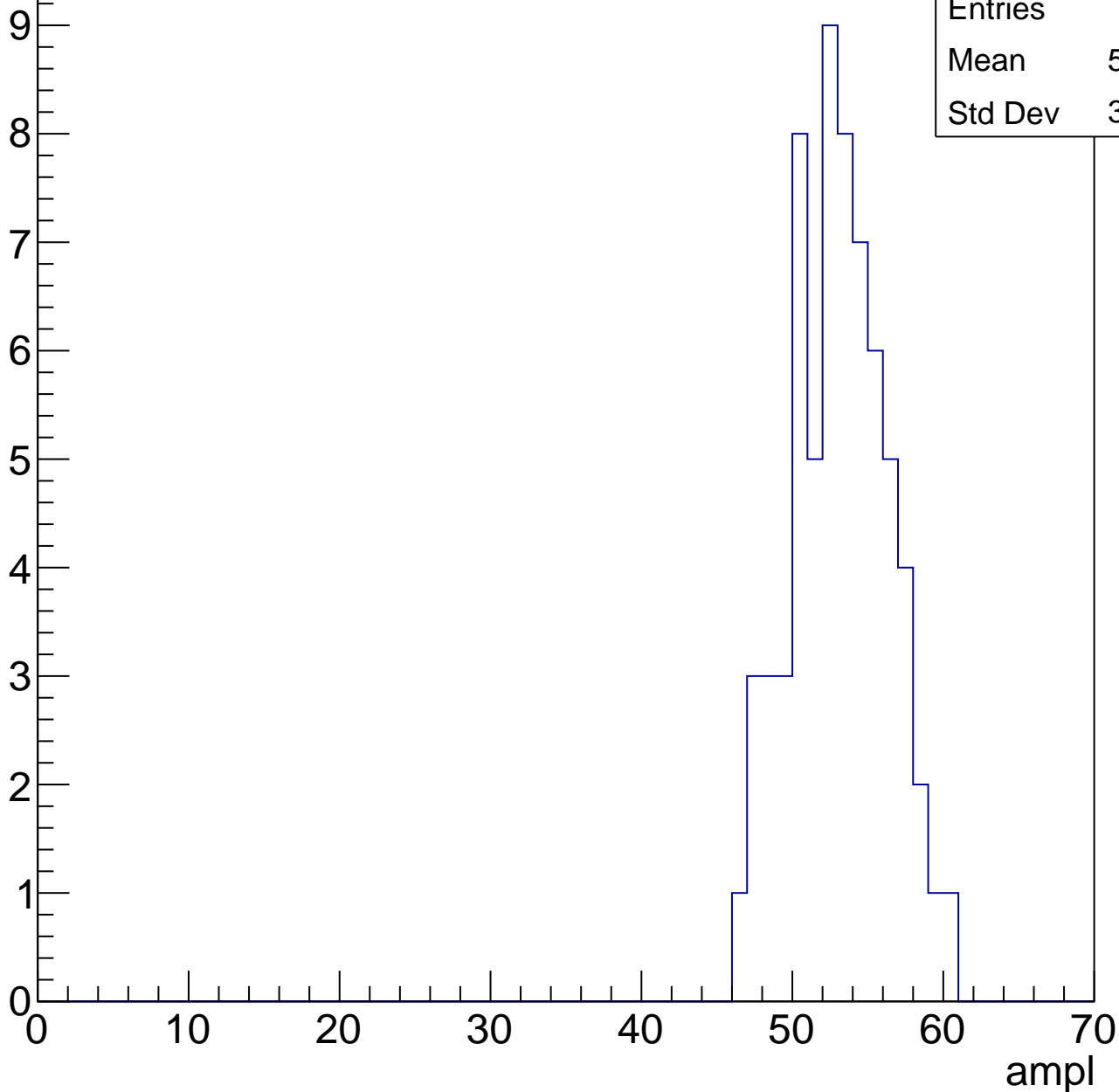


# B0L002S, U8-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	52.67
Std Dev	3.135

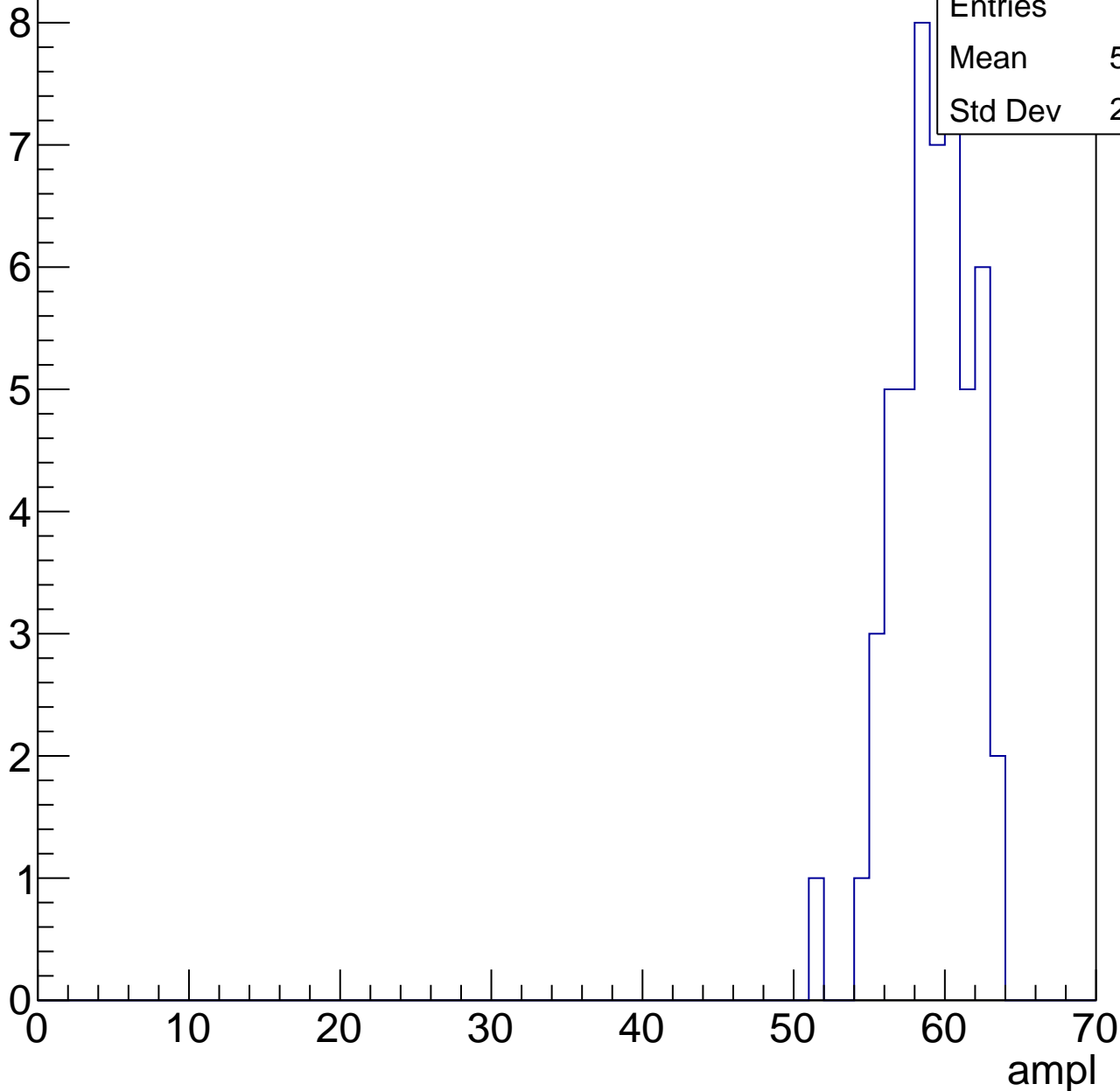


# B0L002S, U8-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	58.73
Std Dev	2.506

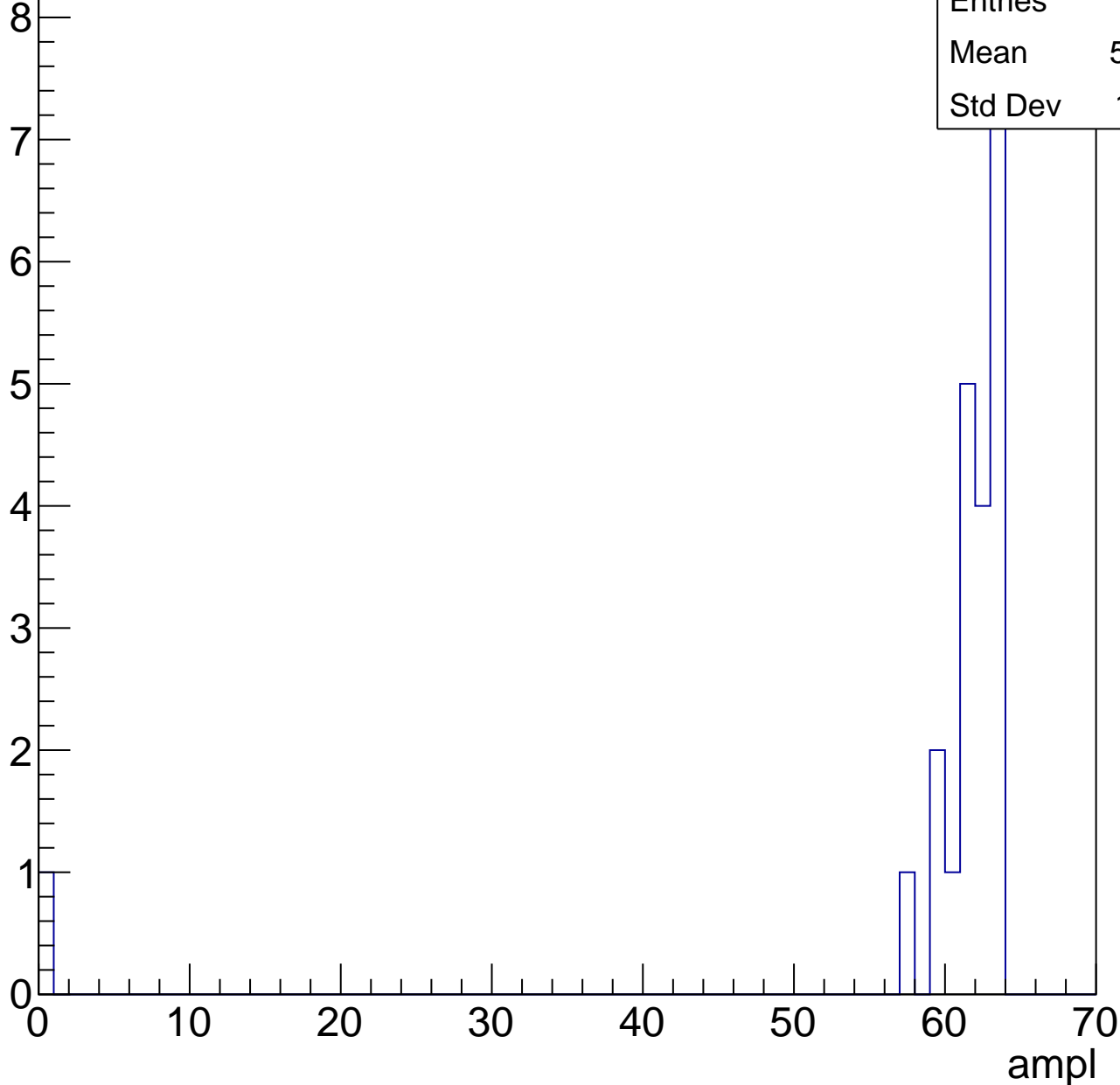


# B0L002S, U8-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.73
Std Dev	12.91



# B0L002S, U8-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch29, adc0

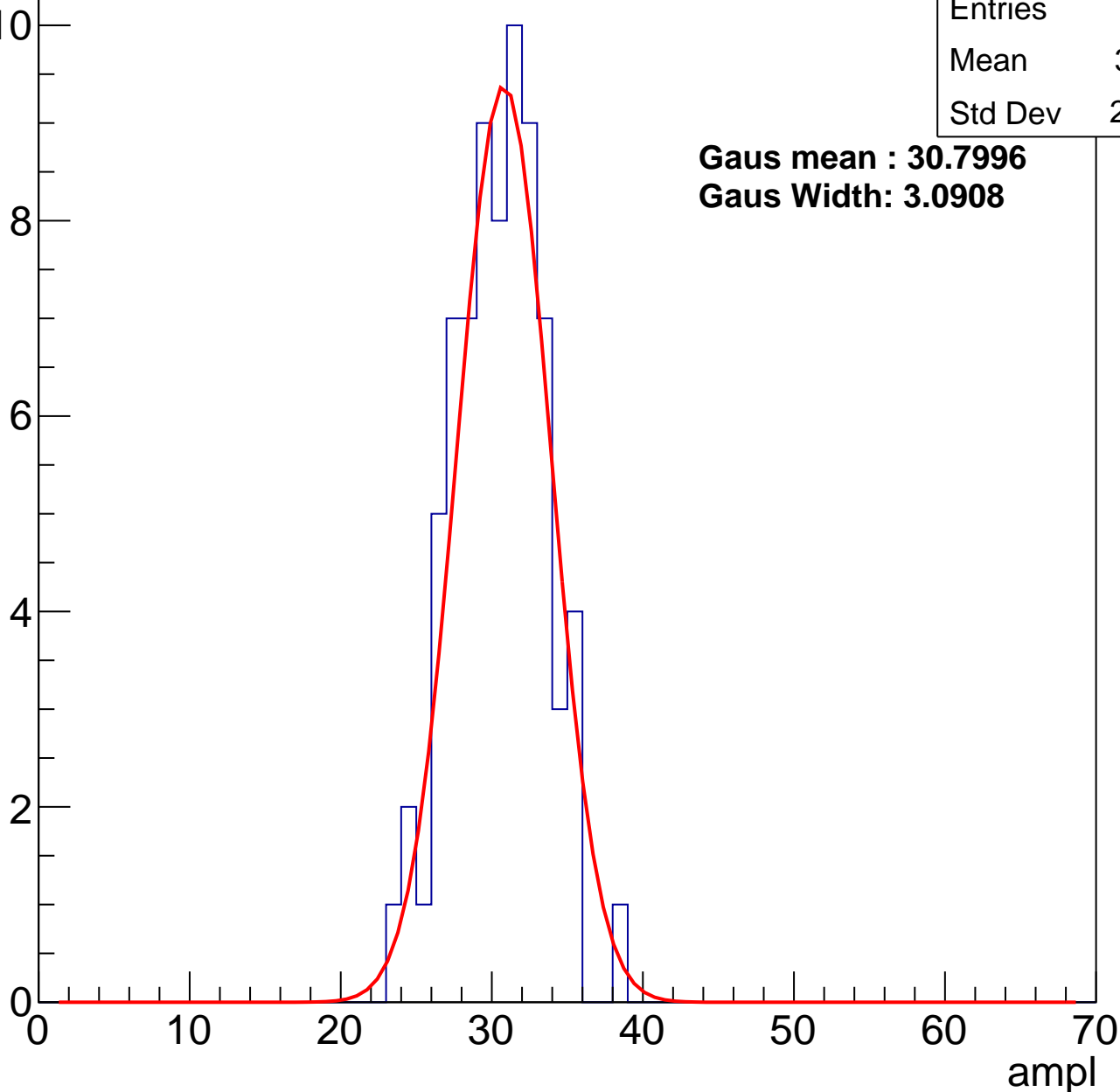
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.01
Std Dev	2.957

**Gaus mean : 30.7996**

**Gaus Width: 3.0908**



# B0L002S, U8-ch29, adc1

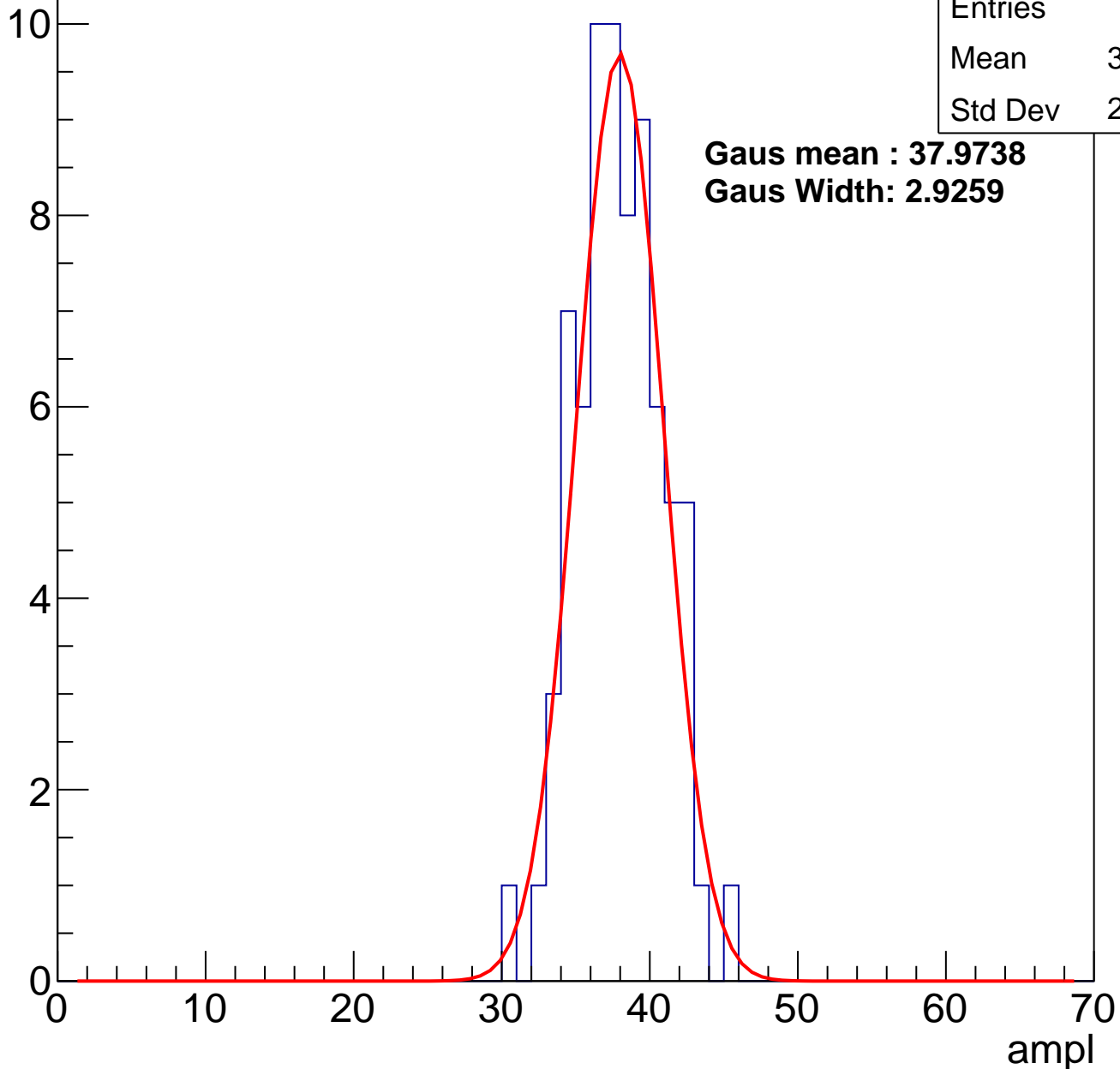
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	37.49
Std Dev	2.877

**Gaus mean : 37.9738**

**Gaus Width: 2.9259**

Entry



# B0L002S, U8-ch29, adc2

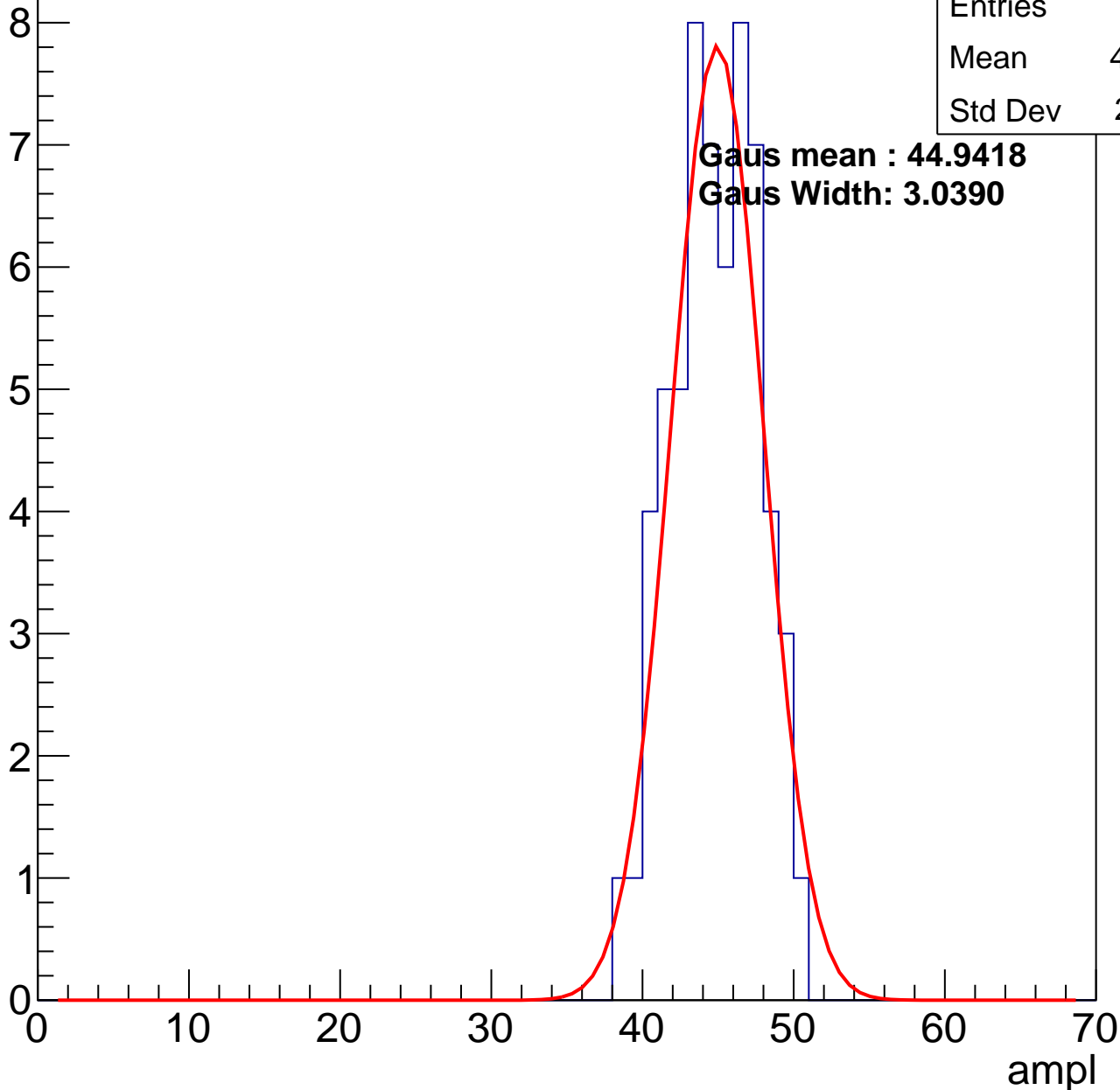
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.33
Std Dev	2.791

**Gaus mean : 44.9418**

**Gaus Width: 3.0390**

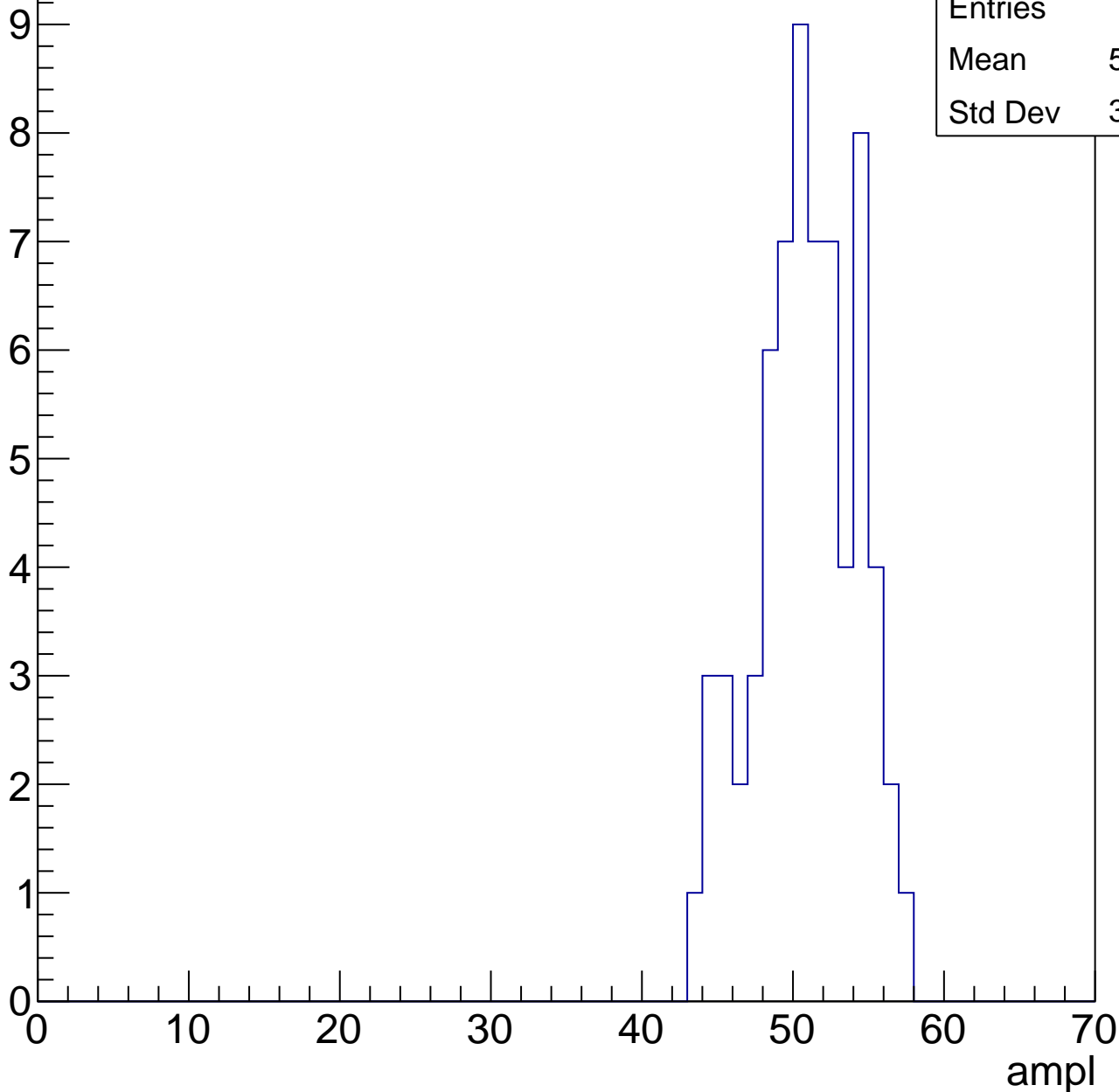


# B0L002S, U8-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

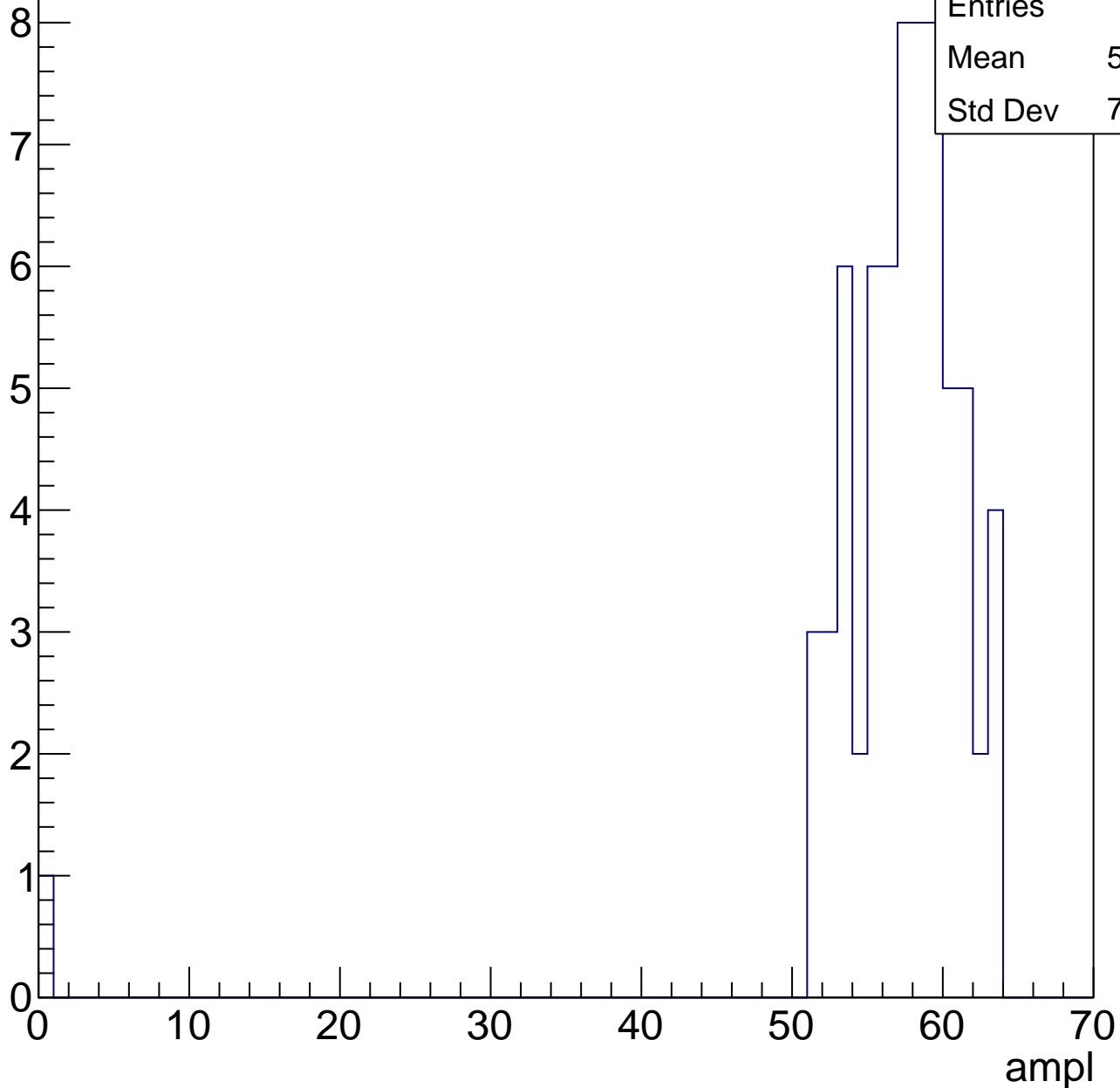
Entries	67
Mean	50.42
Std Dev	3.302



# B0L002S, U8-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

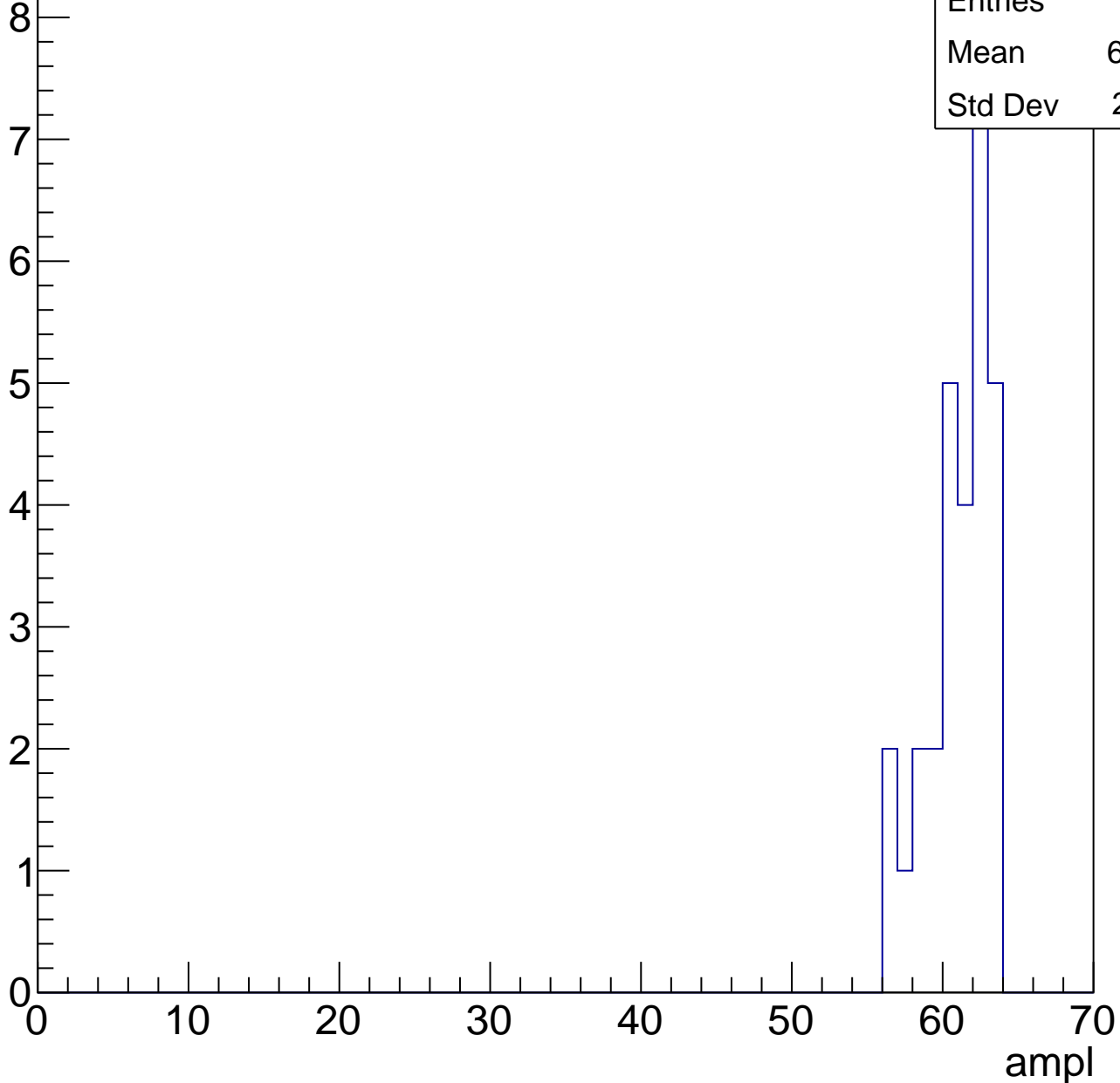


# B0L002S, U8-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	60.62
Std Dev	2.041



# B0L002S, U8-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L002S, U8-ch30, adc0

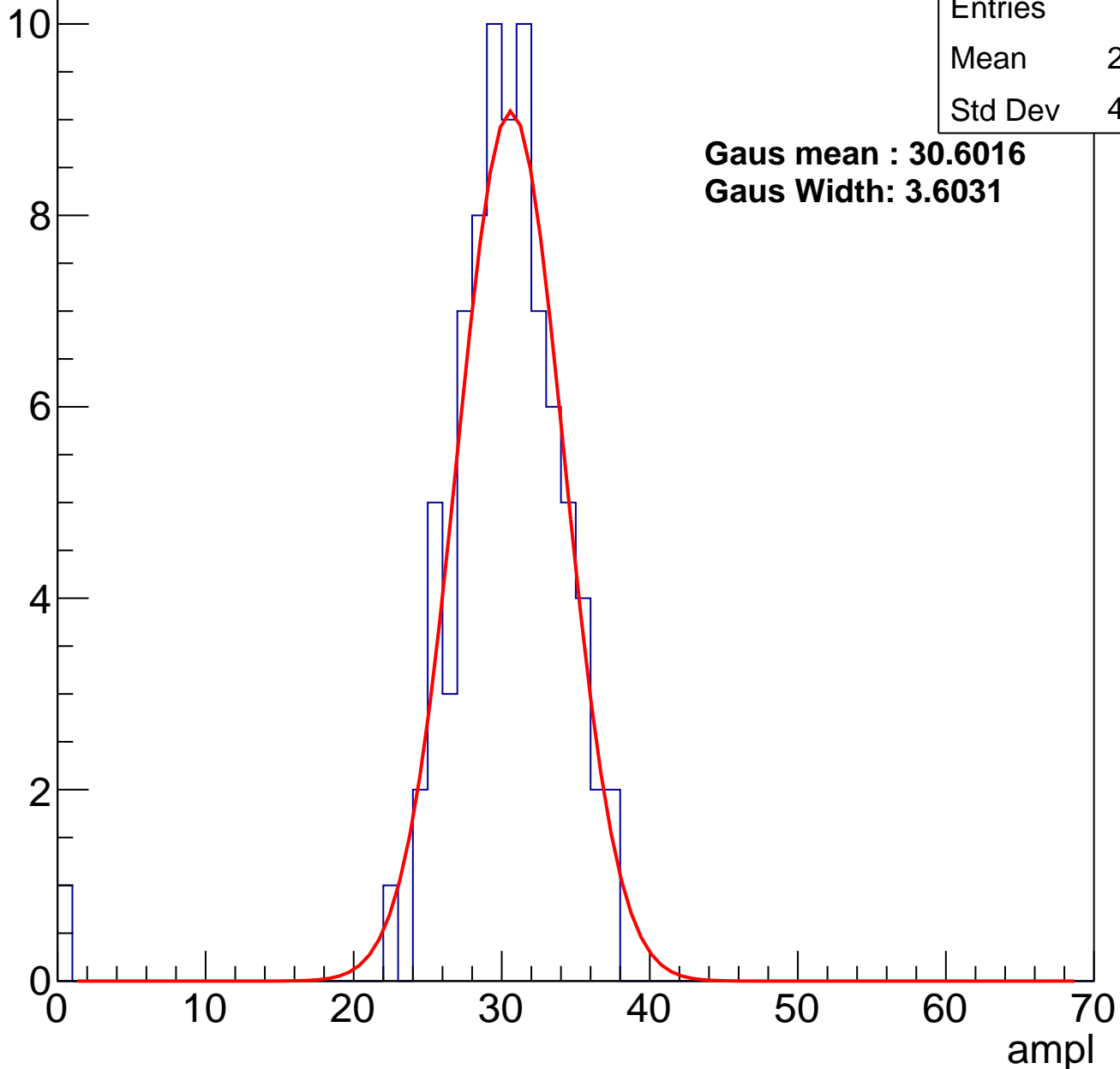
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	82
Mean	29.68
Std Dev	4.617

**Gaus mean : 30.6016**

**Gaus Width: 3.6031**

Entry



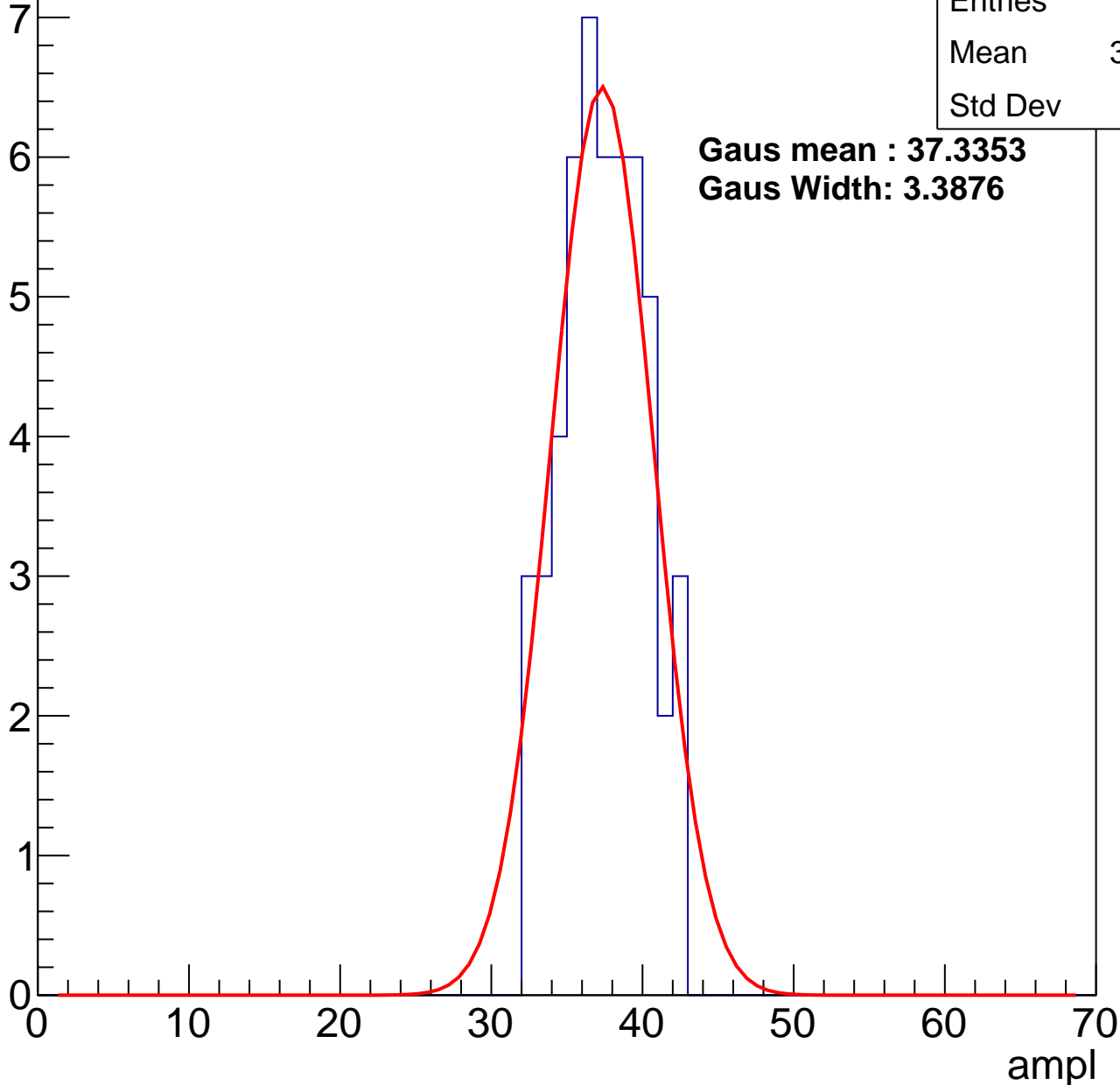
# B0L002S, U8-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	36.96
Std Dev	2.7

**Gaus mean : 37.3353**  
**Gaus Width: 3.3876**



# B0L002S, U8-ch30, adc2

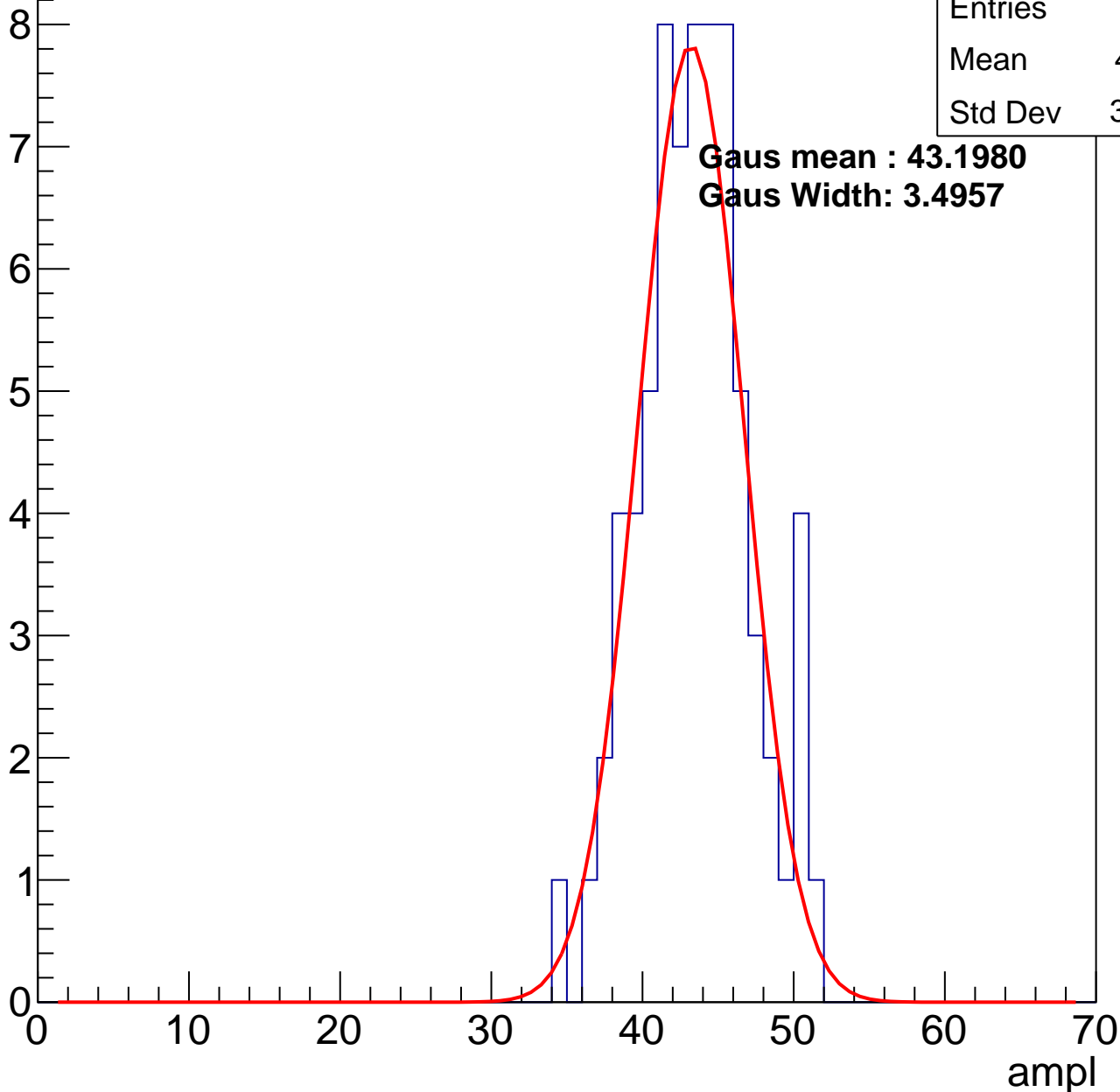
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.01
Std Dev	3.592

**Gaus mean : 43.1980**

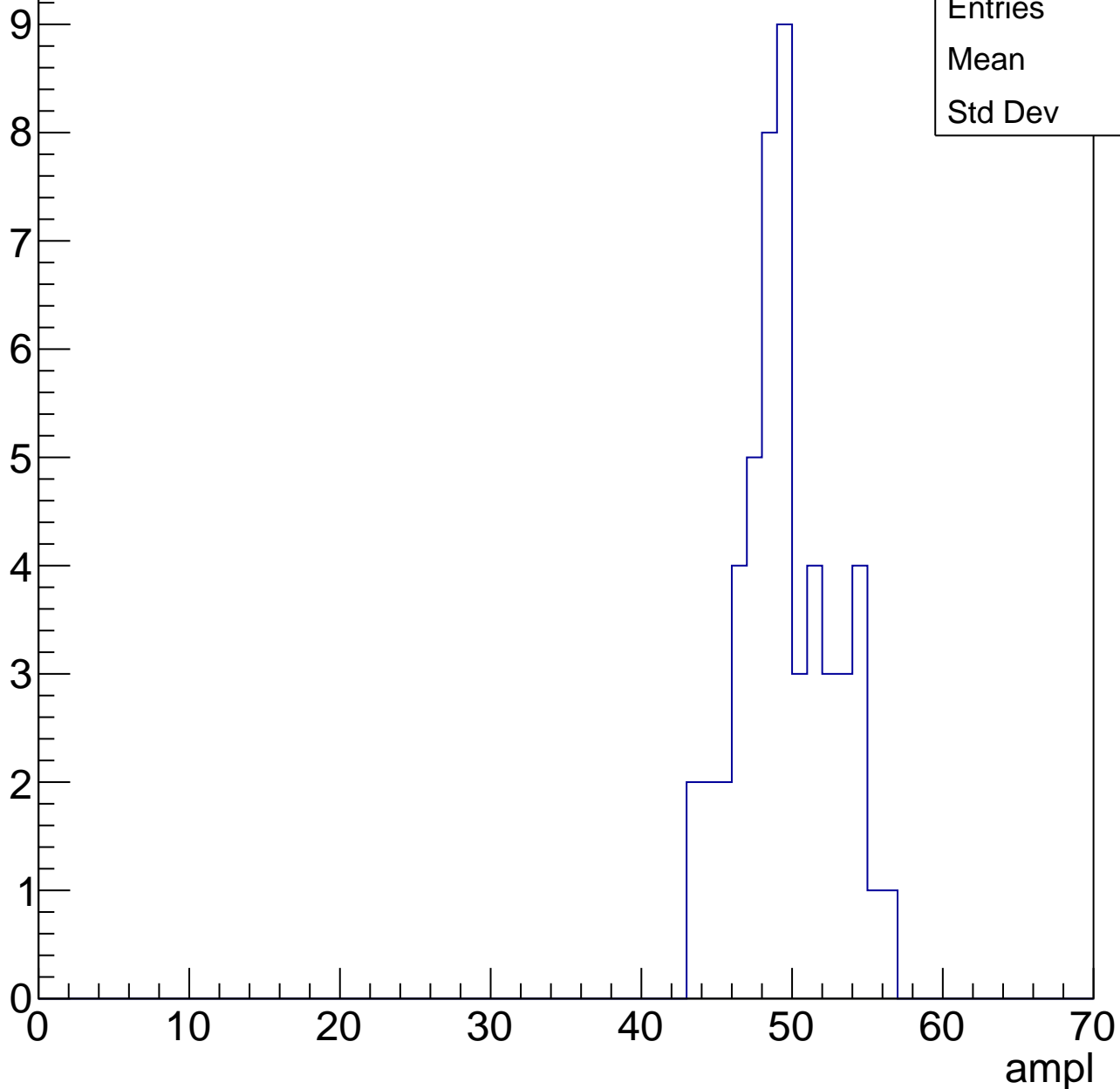
**Gaus Width: 3.4957**



# B0L002S, U8-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

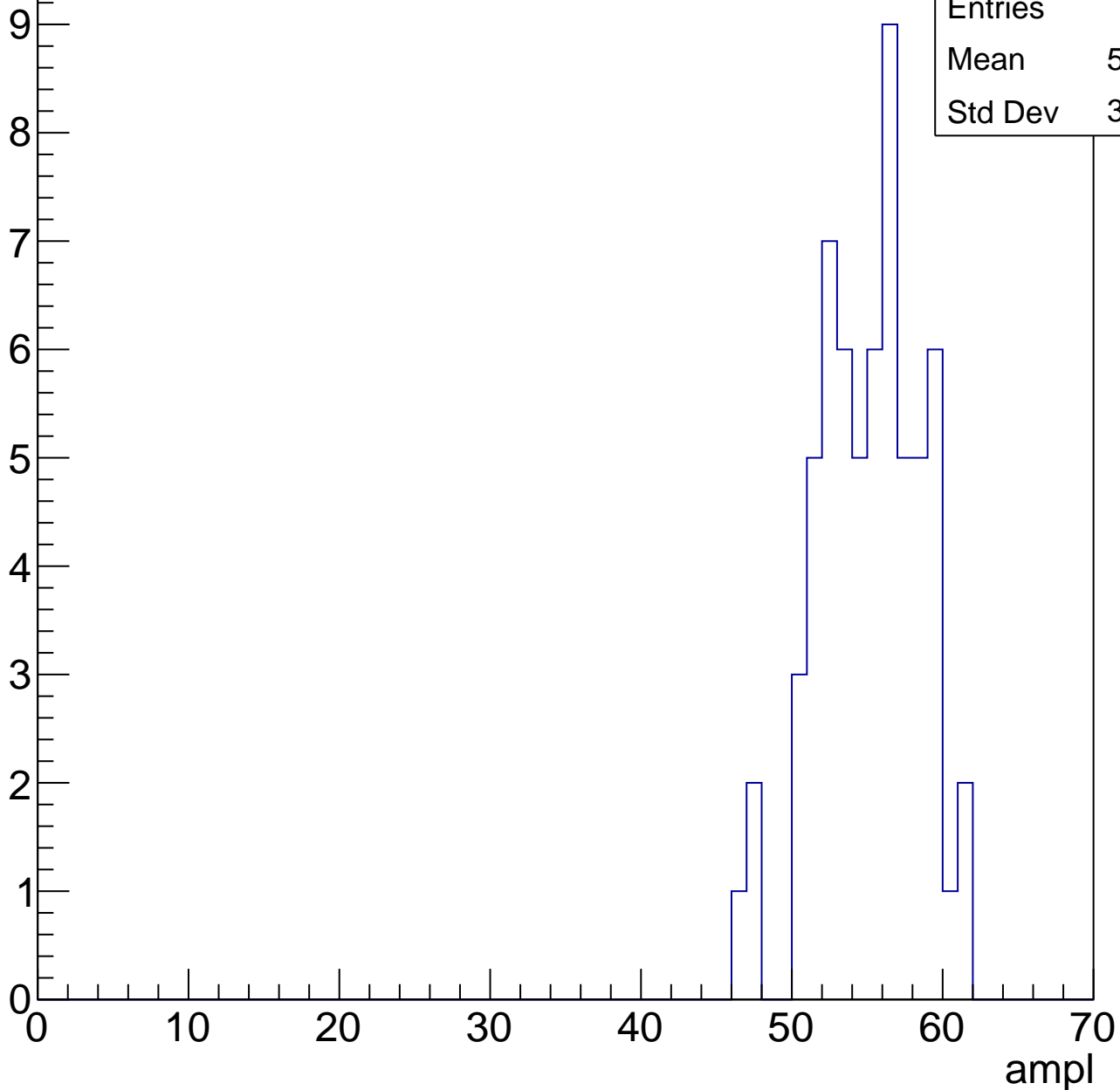


# B0L002S, U8-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	54.63
Std Dev	3.368



# B0L002S, U8-ch30, adc5

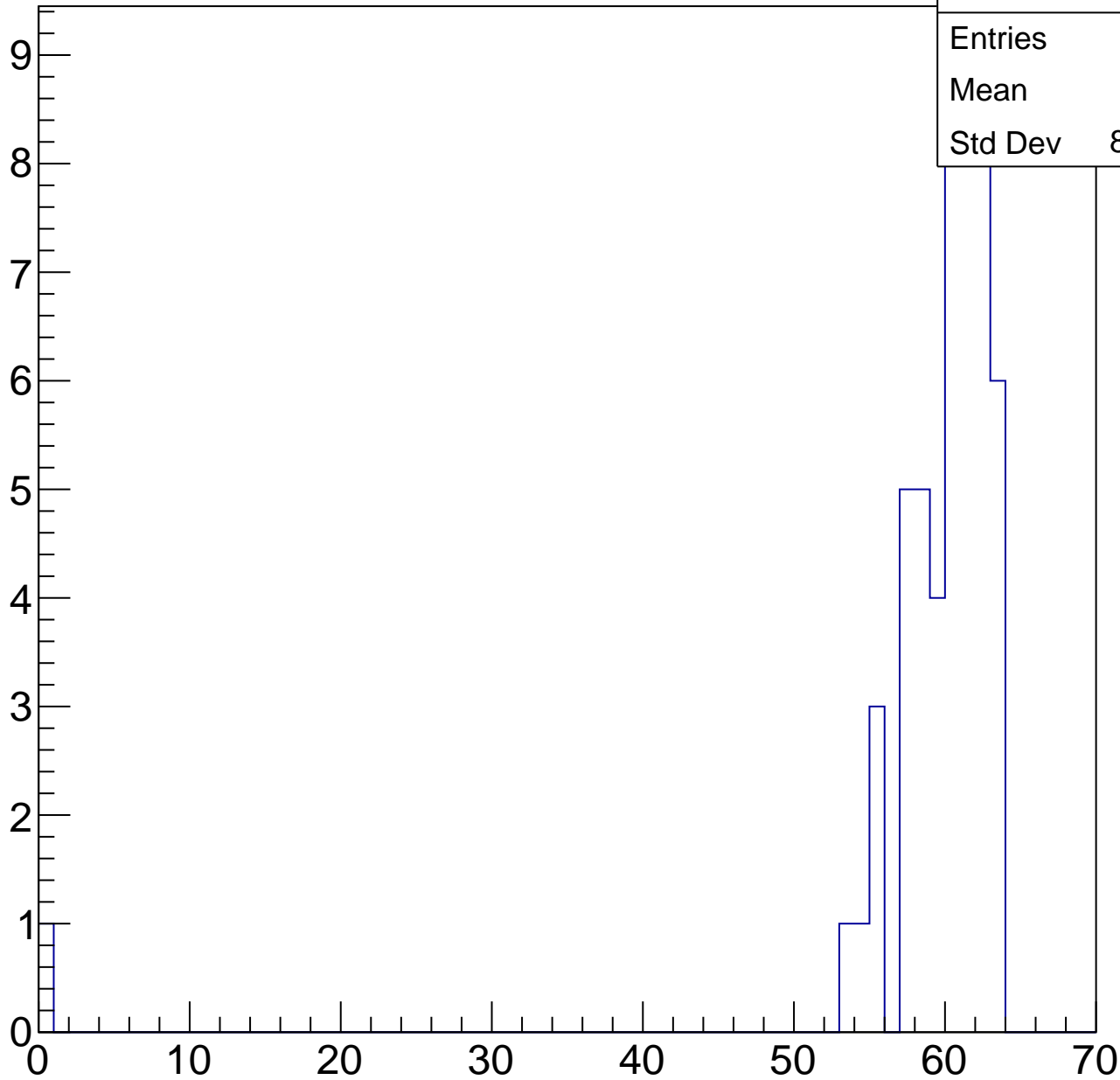
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.6
Std Dev	8.576

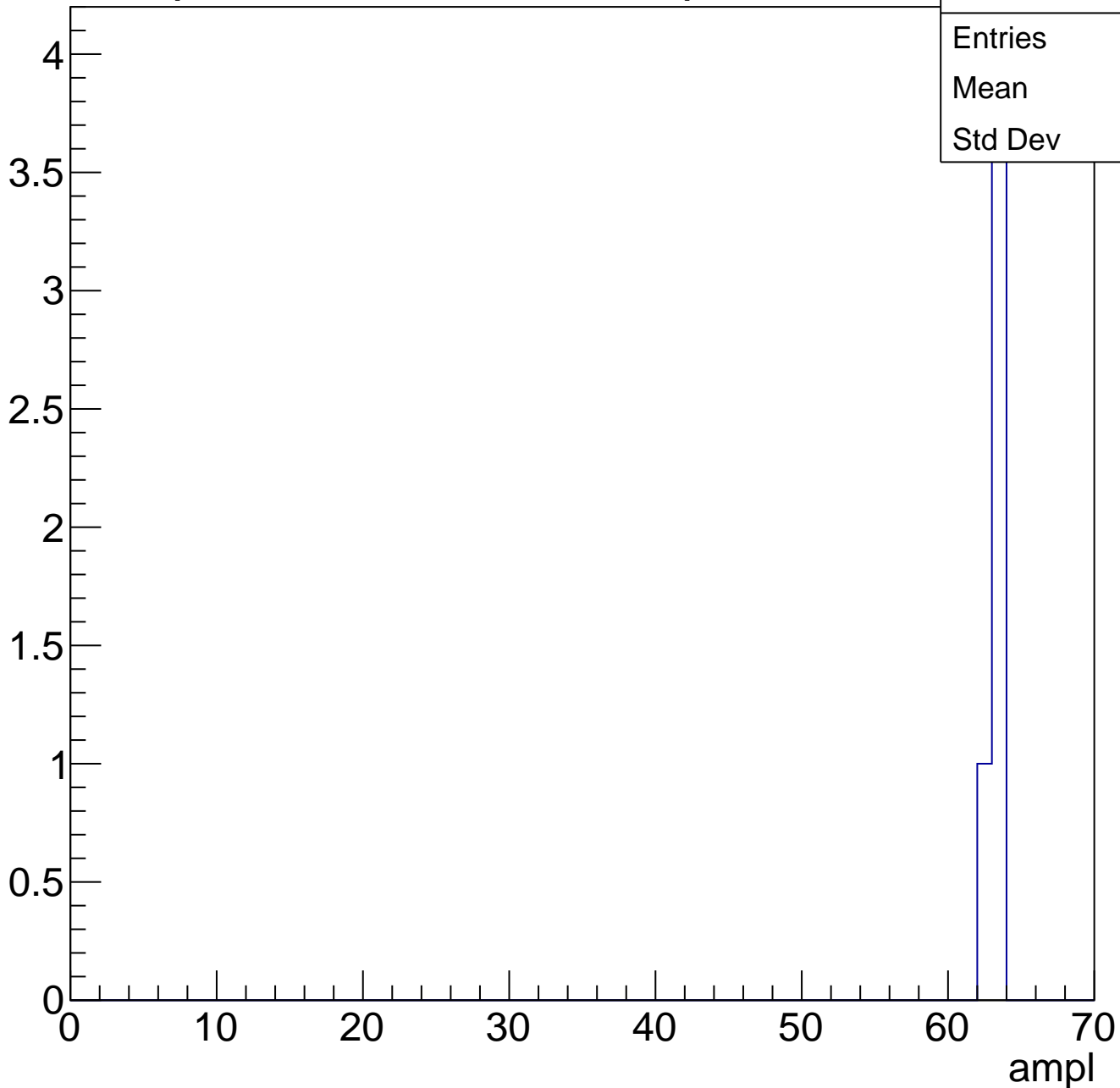
ampl



# B0L002S, U8-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	5
Mean	62.8
Std Dev	0.4



# B0L002S, U8-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch31, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	31.13
Std Dev	2.95

**Gaus mean : 31.5175**

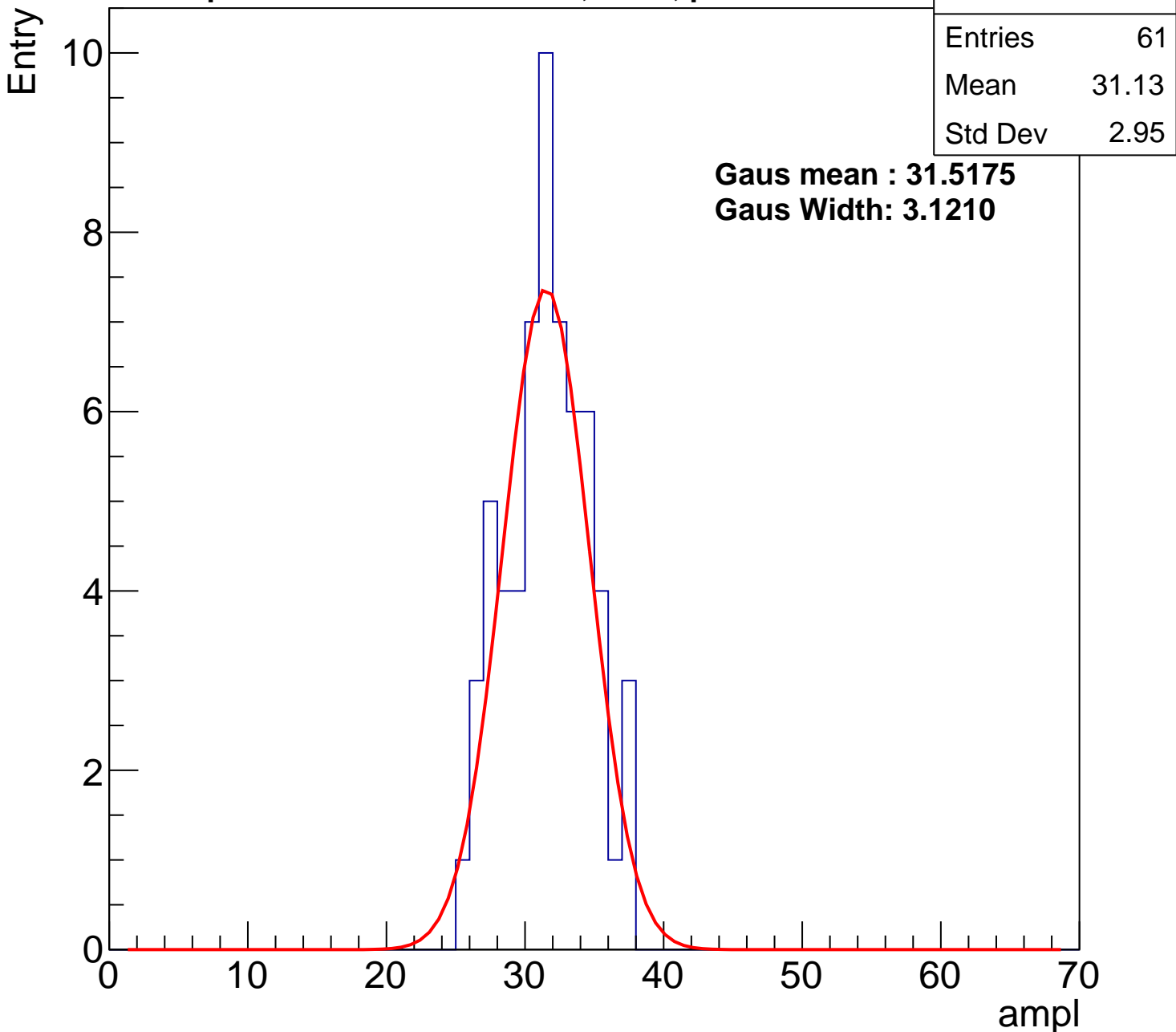
**Gaus Width: 3.1210**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch31, adc1

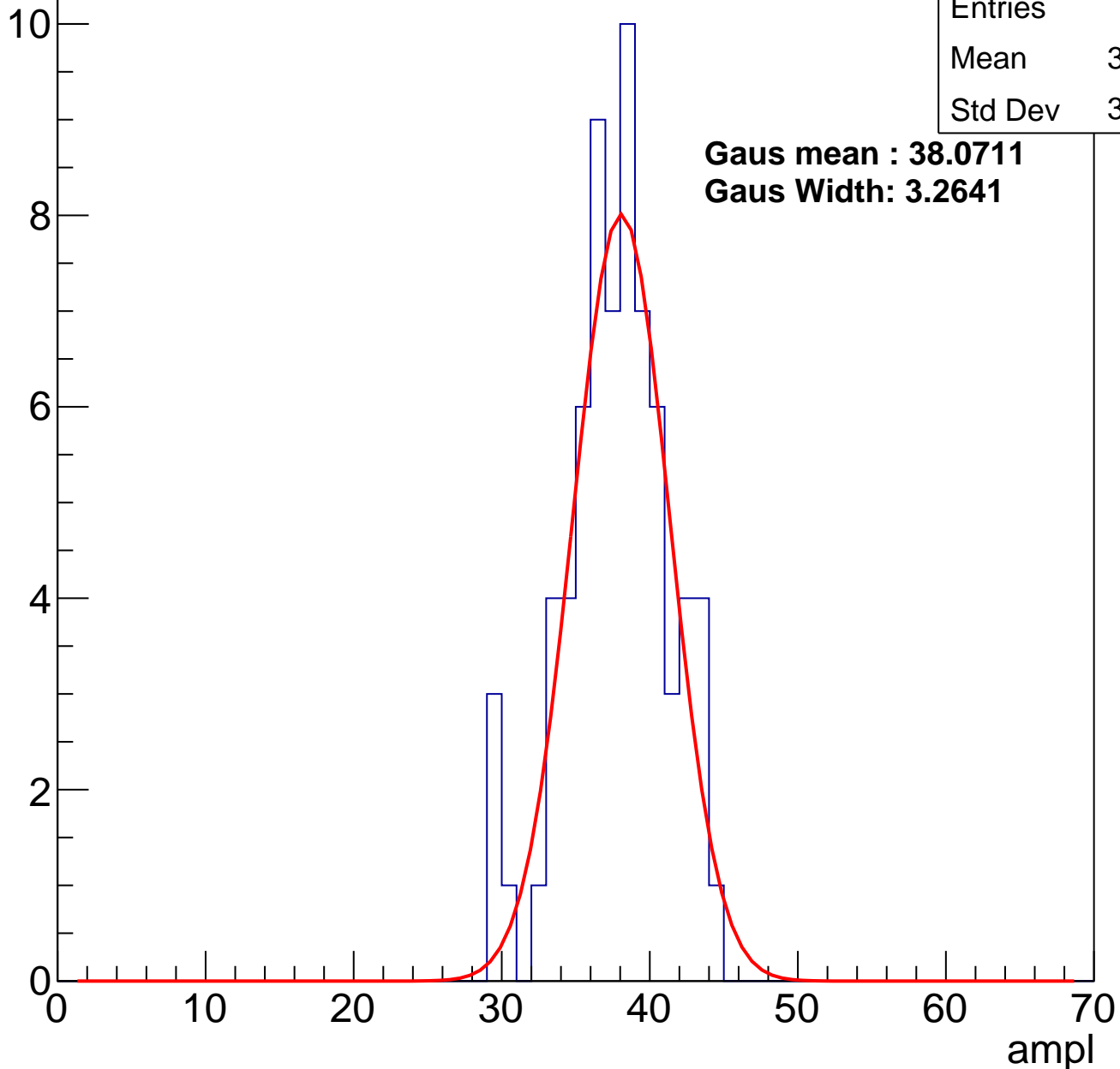
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	37.29
Std Dev	3.436

**Gaus mean : 38.0711**

**Gaus Width: 3.2641**

Entry



# B0L002S, U8-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	43.93
Std Dev	2.763

**Gaus mean : 43.8047**

**Gaus Width: 2.9433**

Entry

10

8

6

4

2

0

0

10

20

30

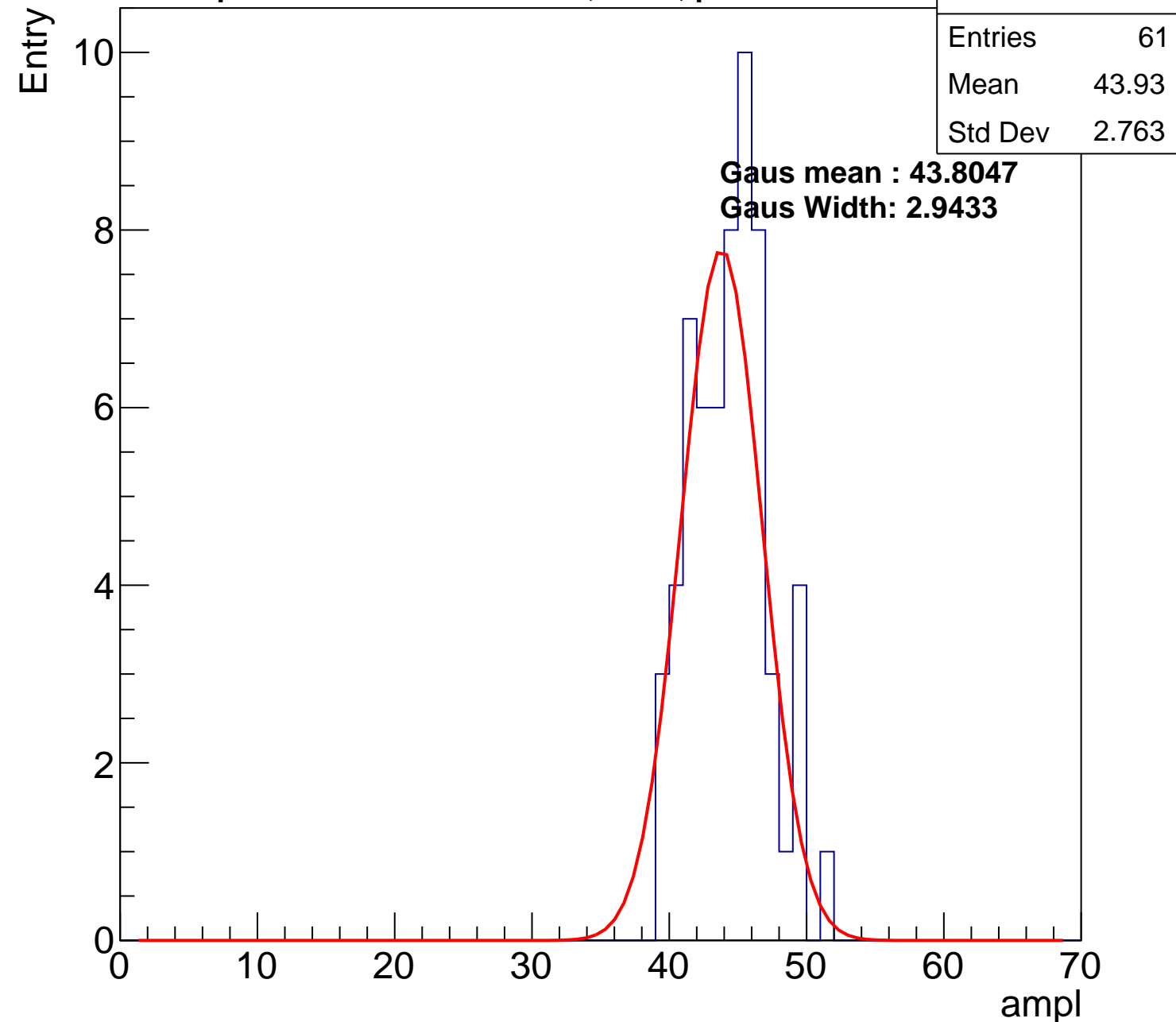
40

50

60

70

ampl

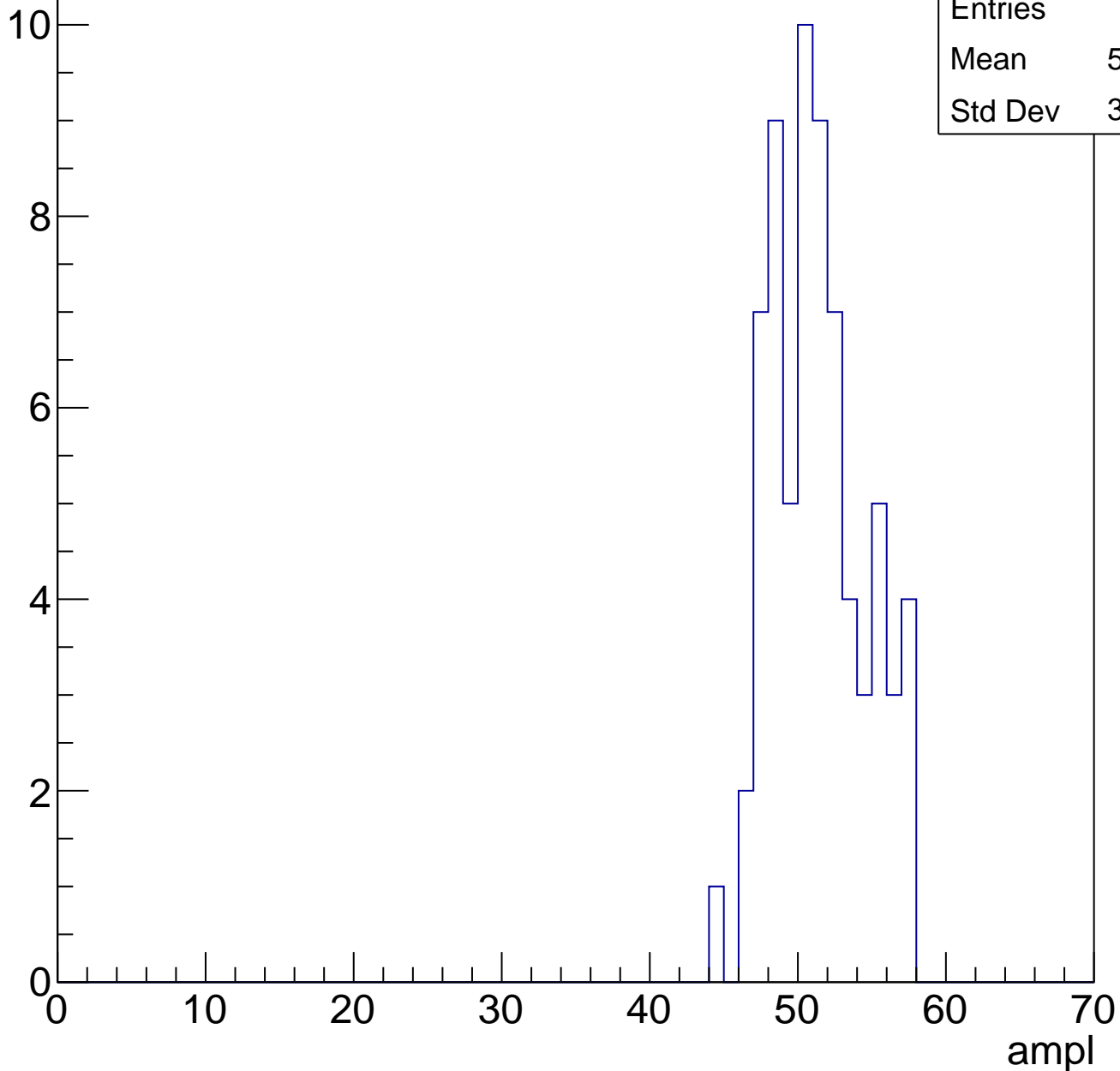


# B0L002S, U8-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	69
Mean	50.87
Std Dev	3.116

Entry

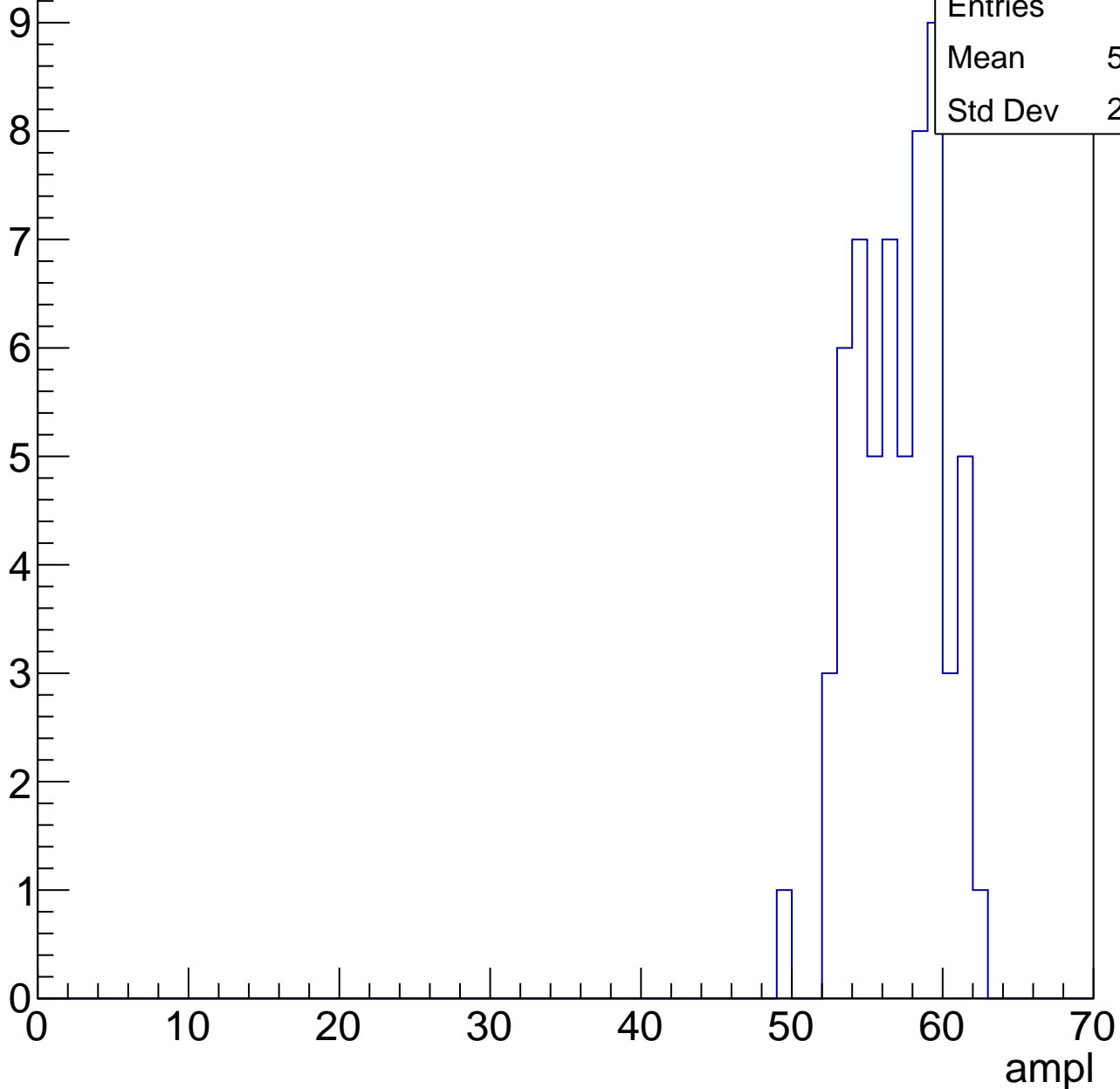


# B0L002S, U8-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.58
Std Dev	2.859

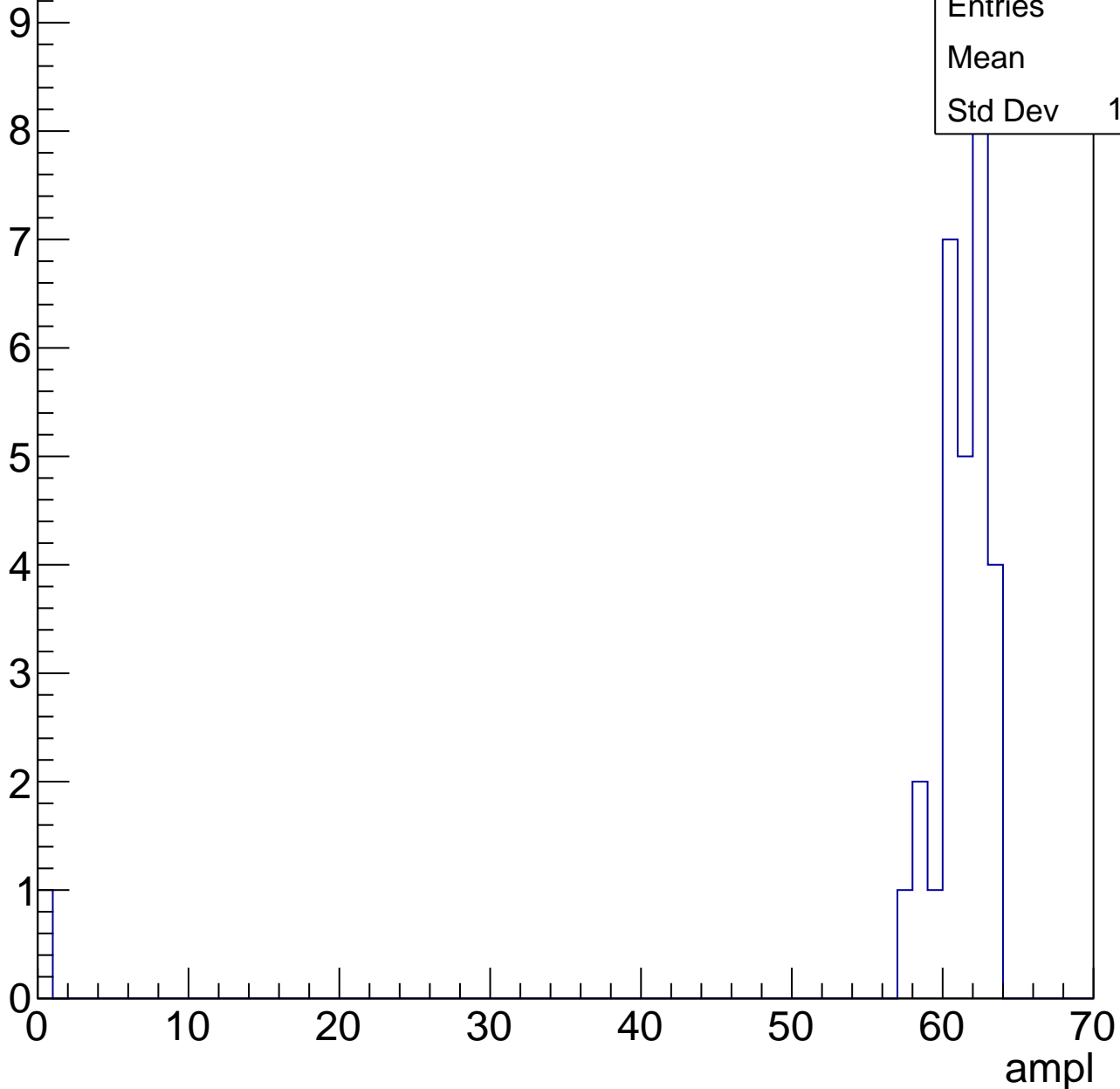


# B0L002S, U8-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

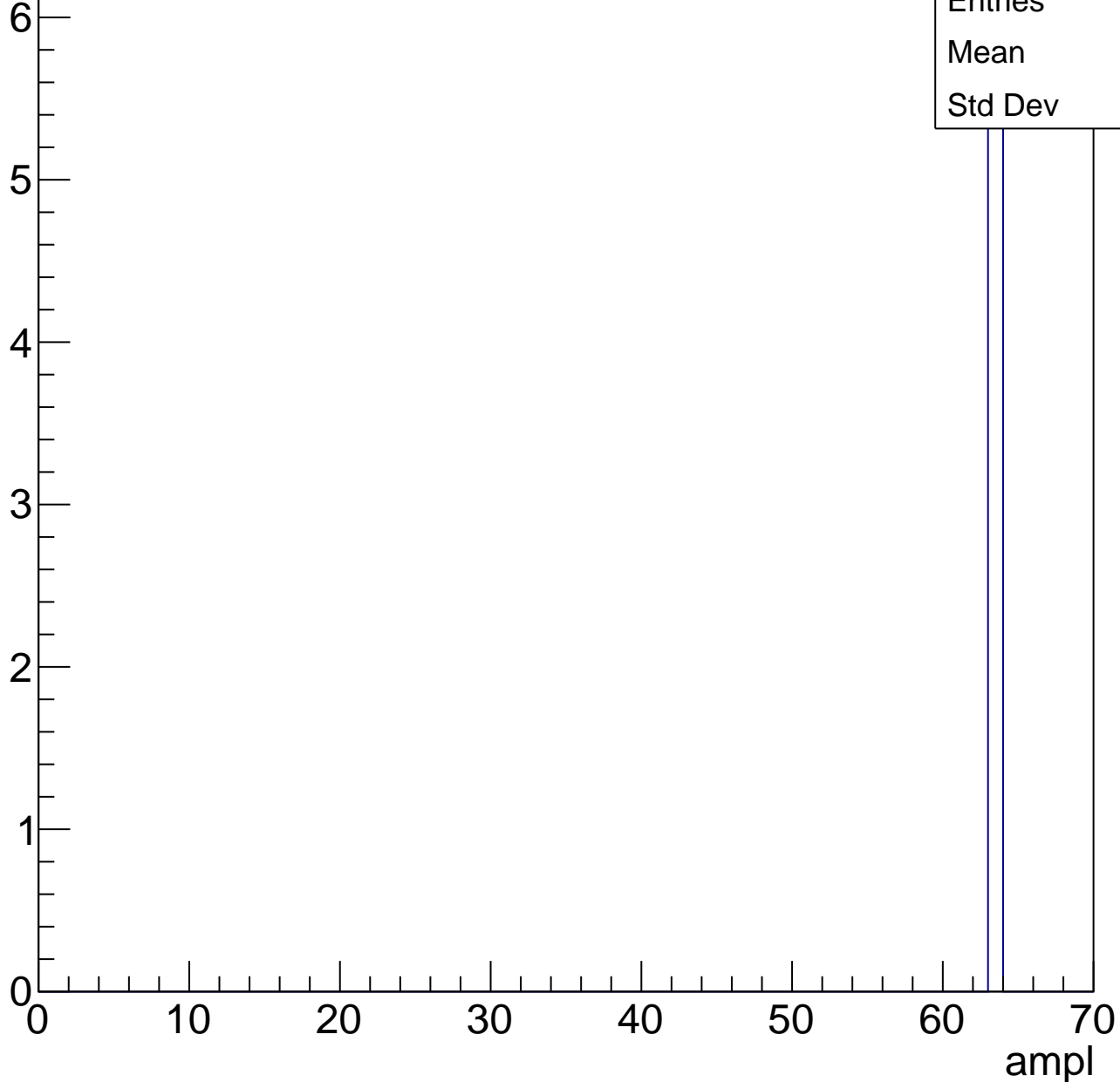
Entries	30
Mean	58.9
Std Dev	11.04



# B0L002S, U8-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

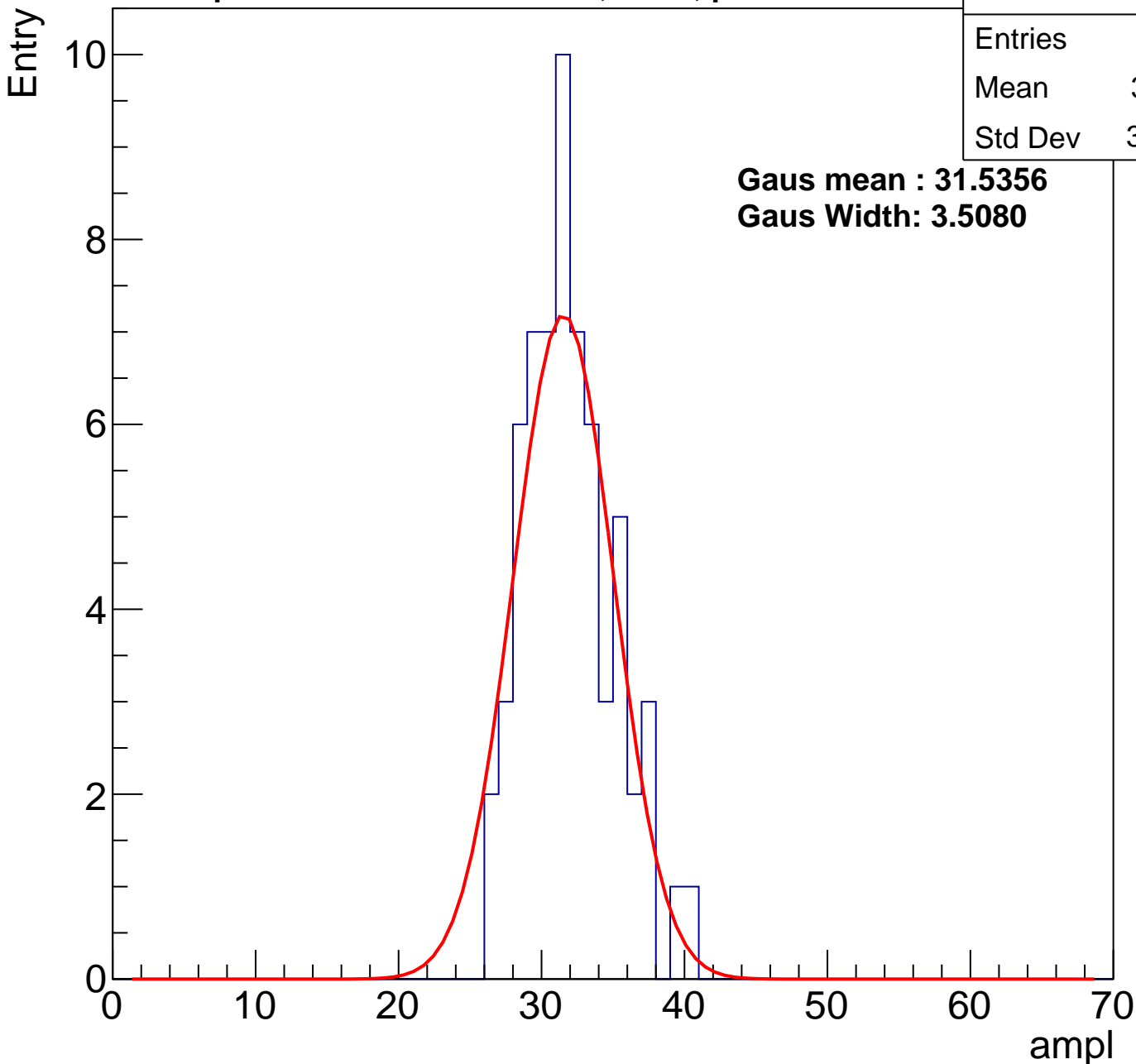
# B0L002S, U8-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	63
Mean	31.51
Std Dev	3.116

**Gaus mean : 31.5356**

**Gaus Width: 3.5080**



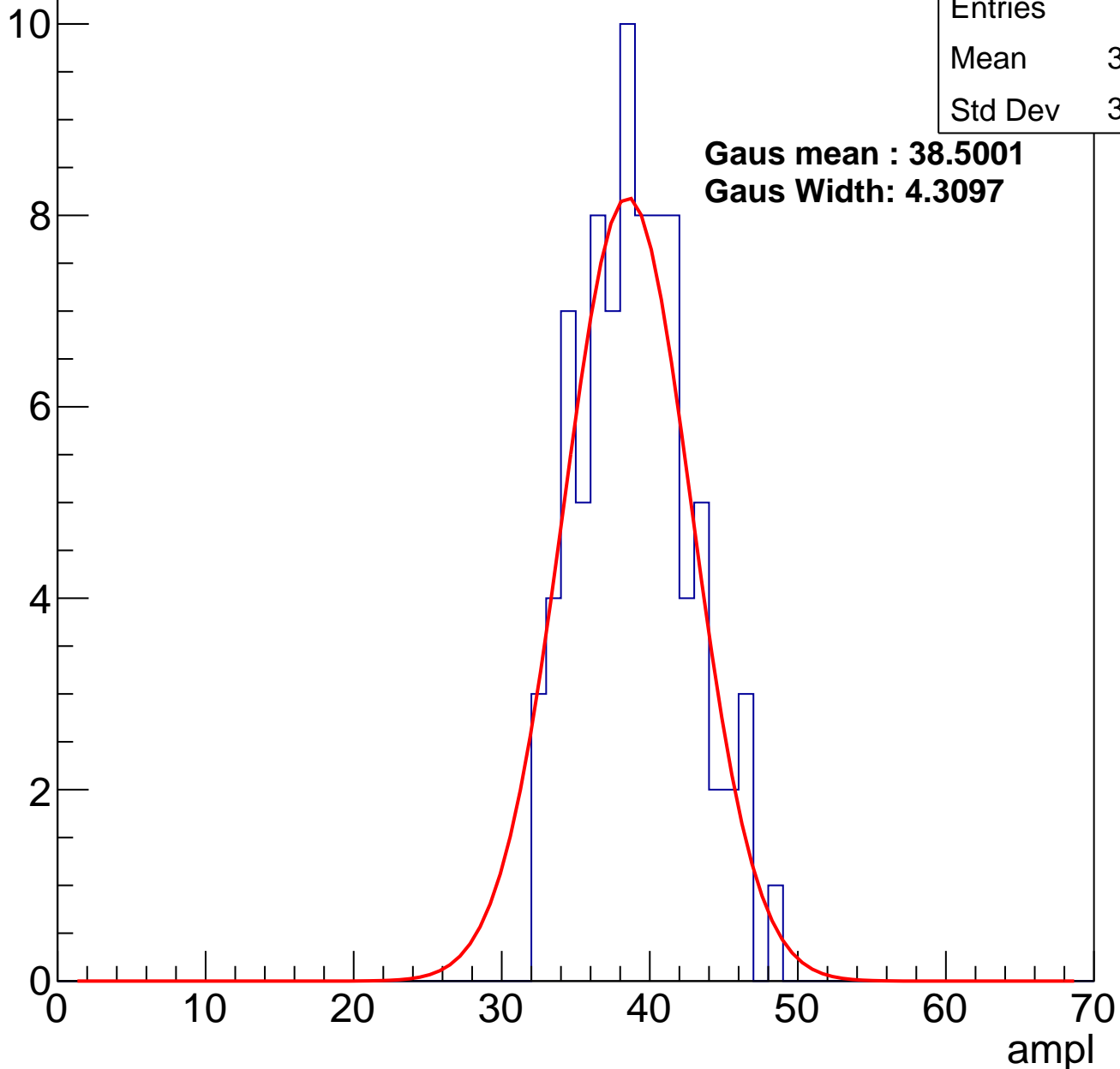
# B0L002S, U8-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	38.53
Std Dev	3.667

**Gaus mean : 38.5001**  
**Gaus Width: 4.3097**

Entry



# B0L002S, U8-ch32, adc2

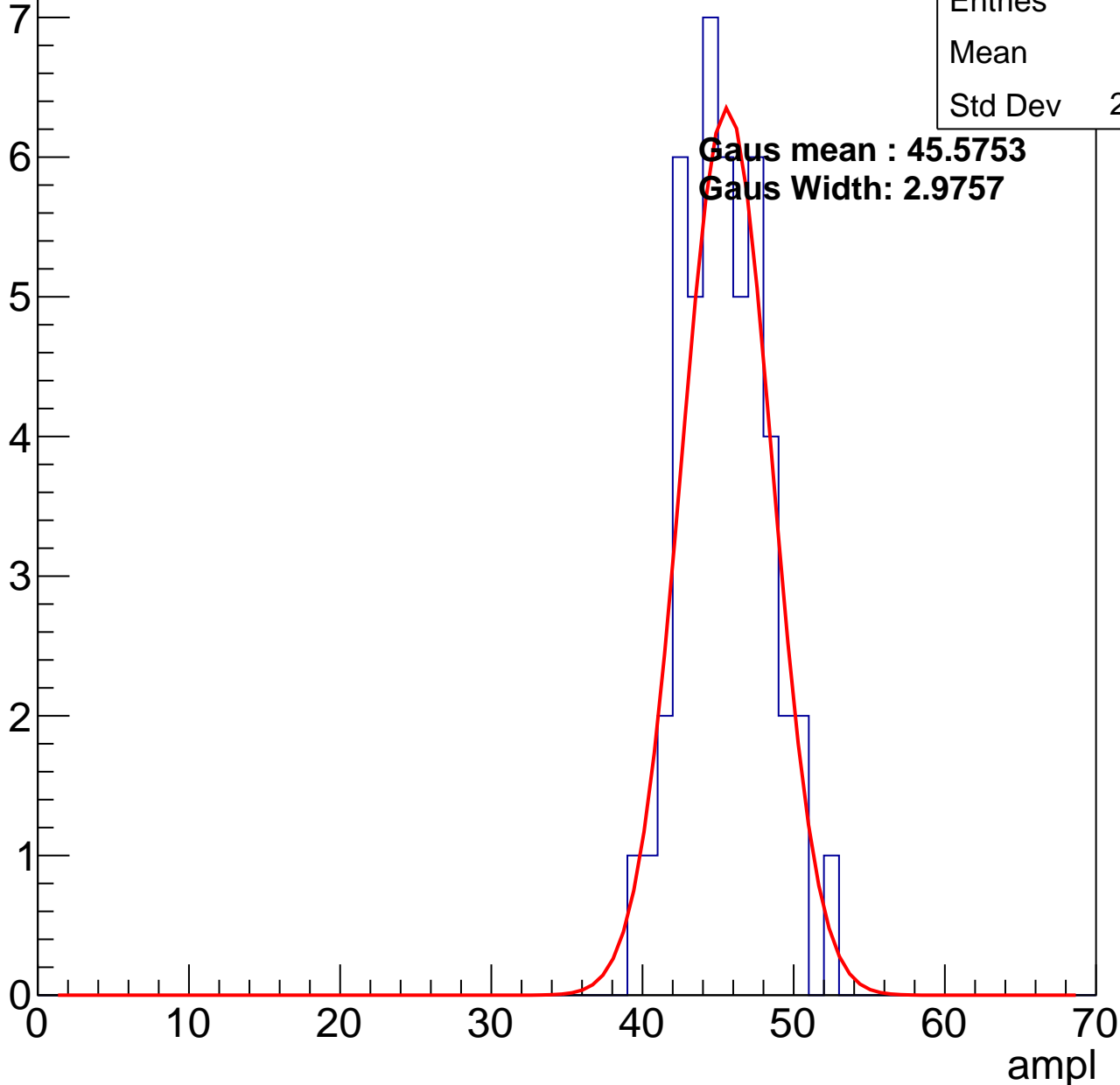
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	45
Std Dev	2.776

**Gaus mean : 45.5753**

**Gaus Width: 2.9757**

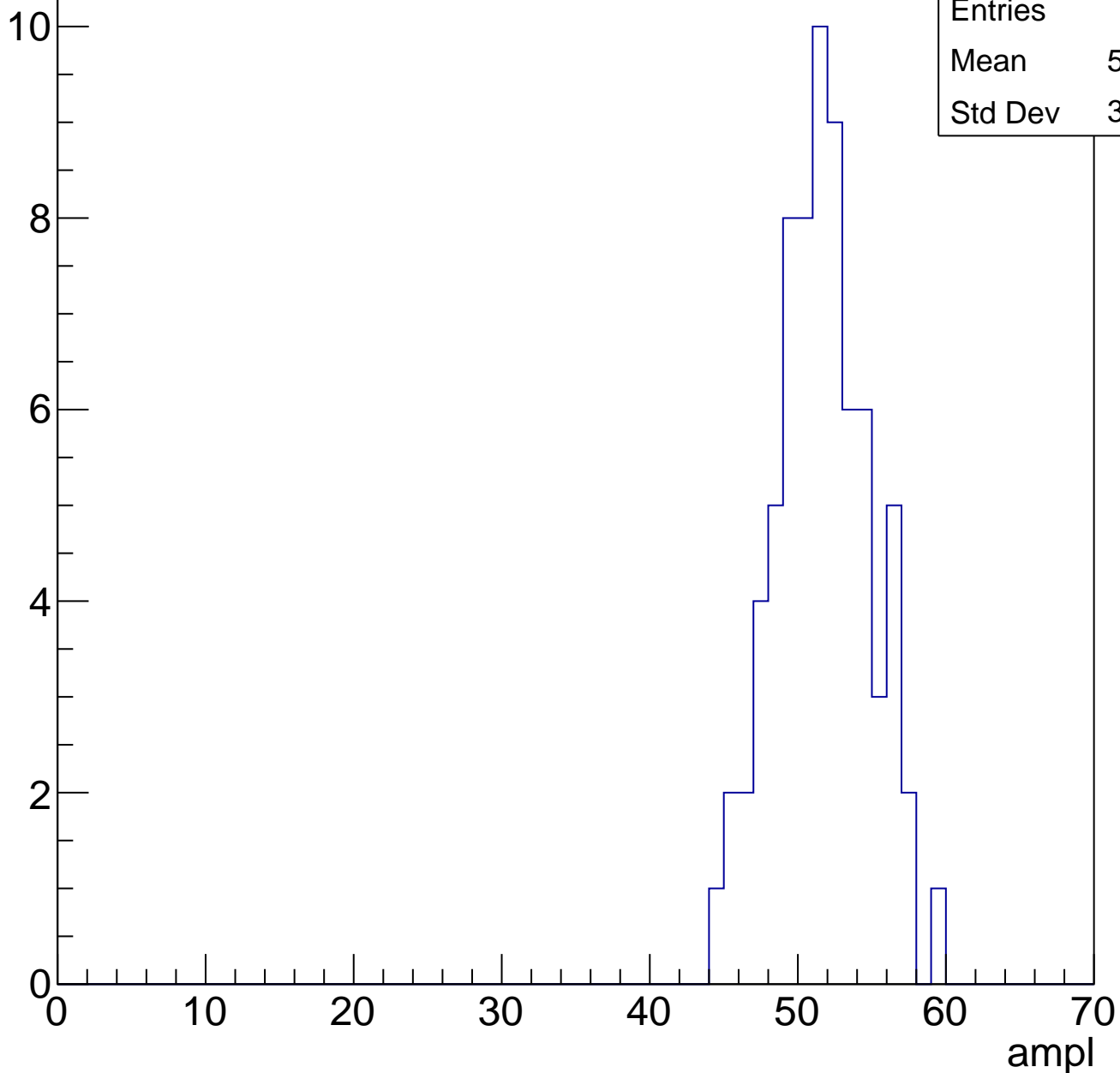


# B0L002S, U8-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	51.17
Std Dev	3.149

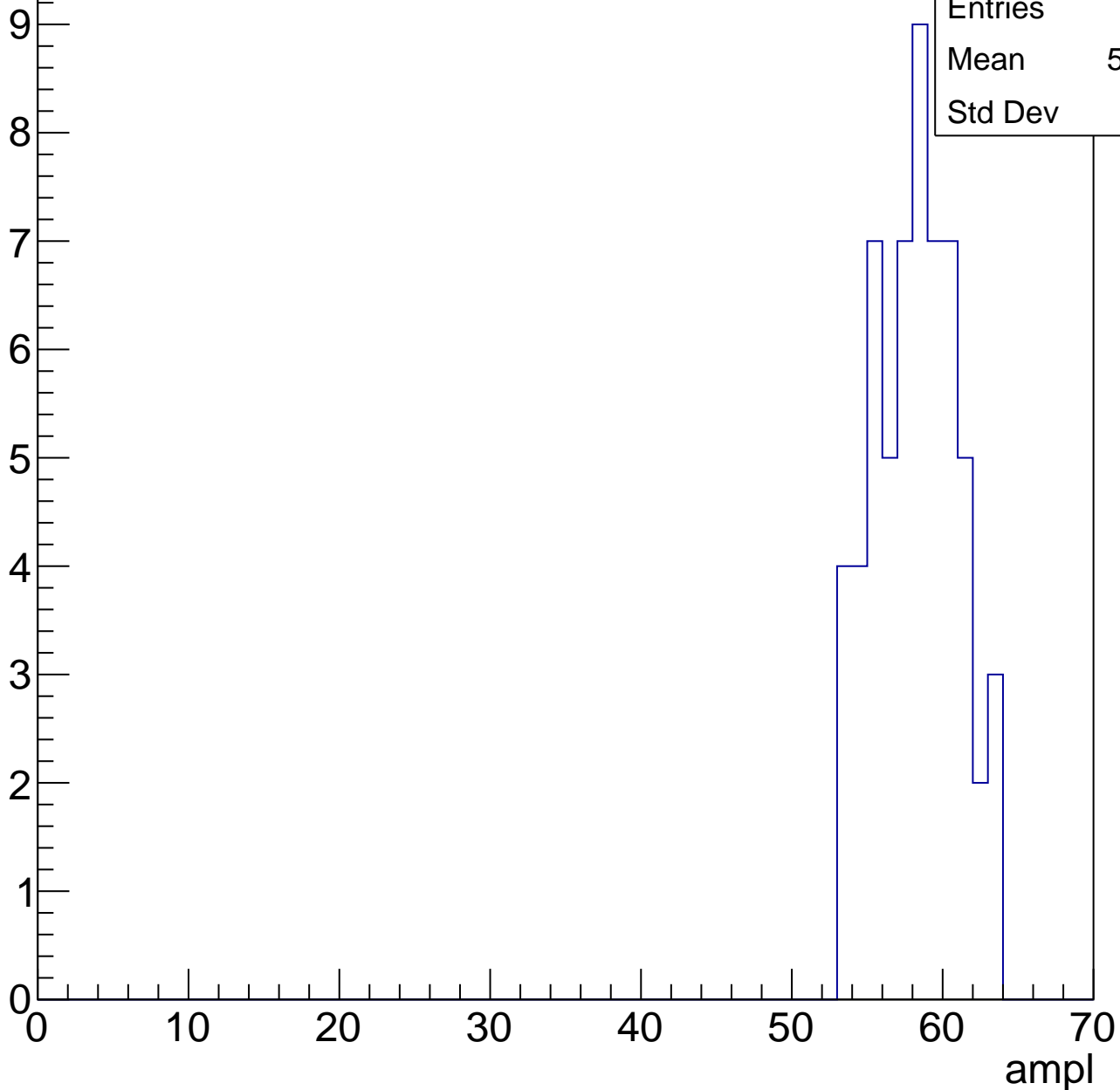


# B0L002S, U8-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.75
Std Dev	2.7

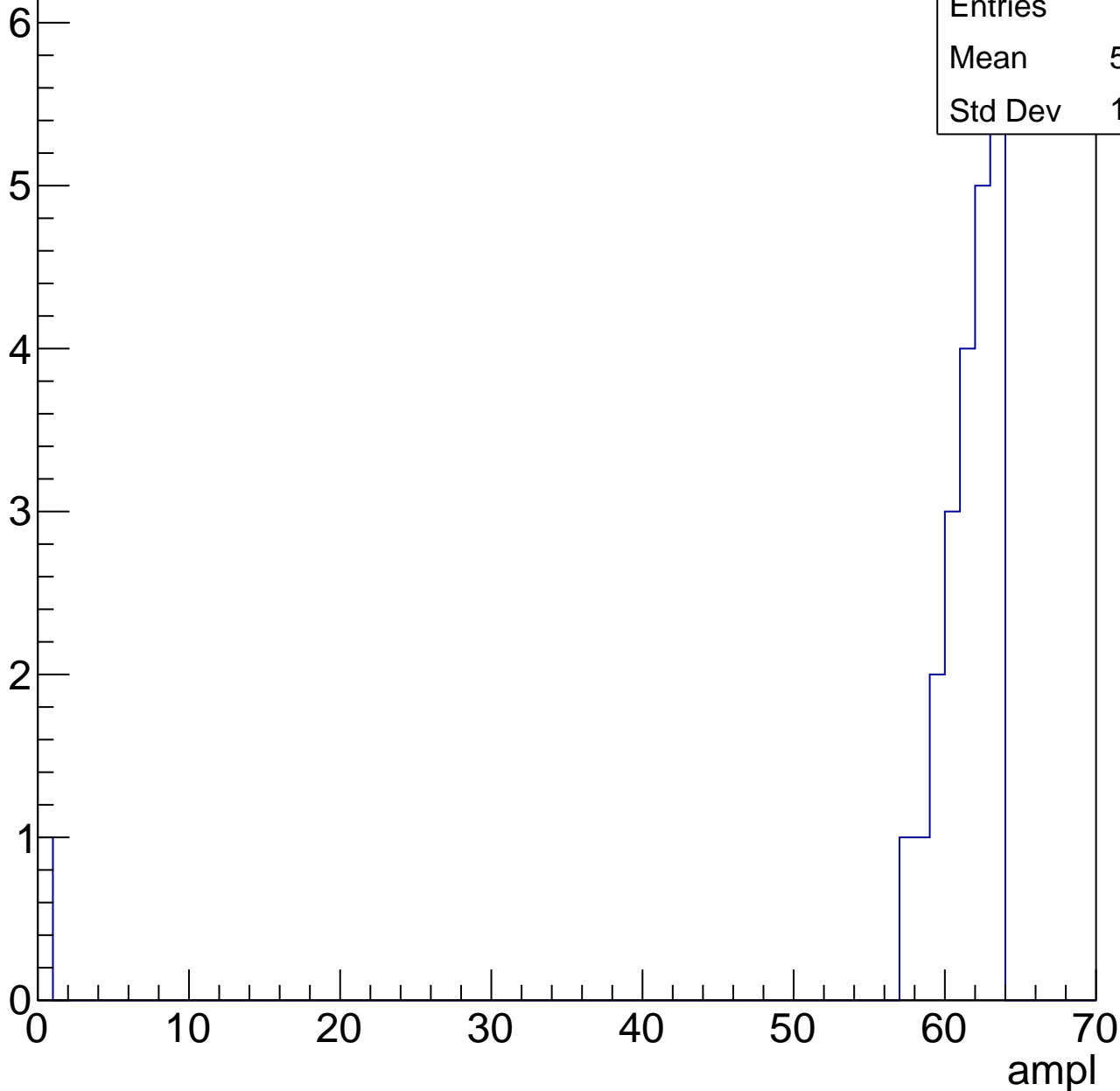


# B0L002S, U8-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.48
Std Dev	12.58



# B0L002S, U8-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

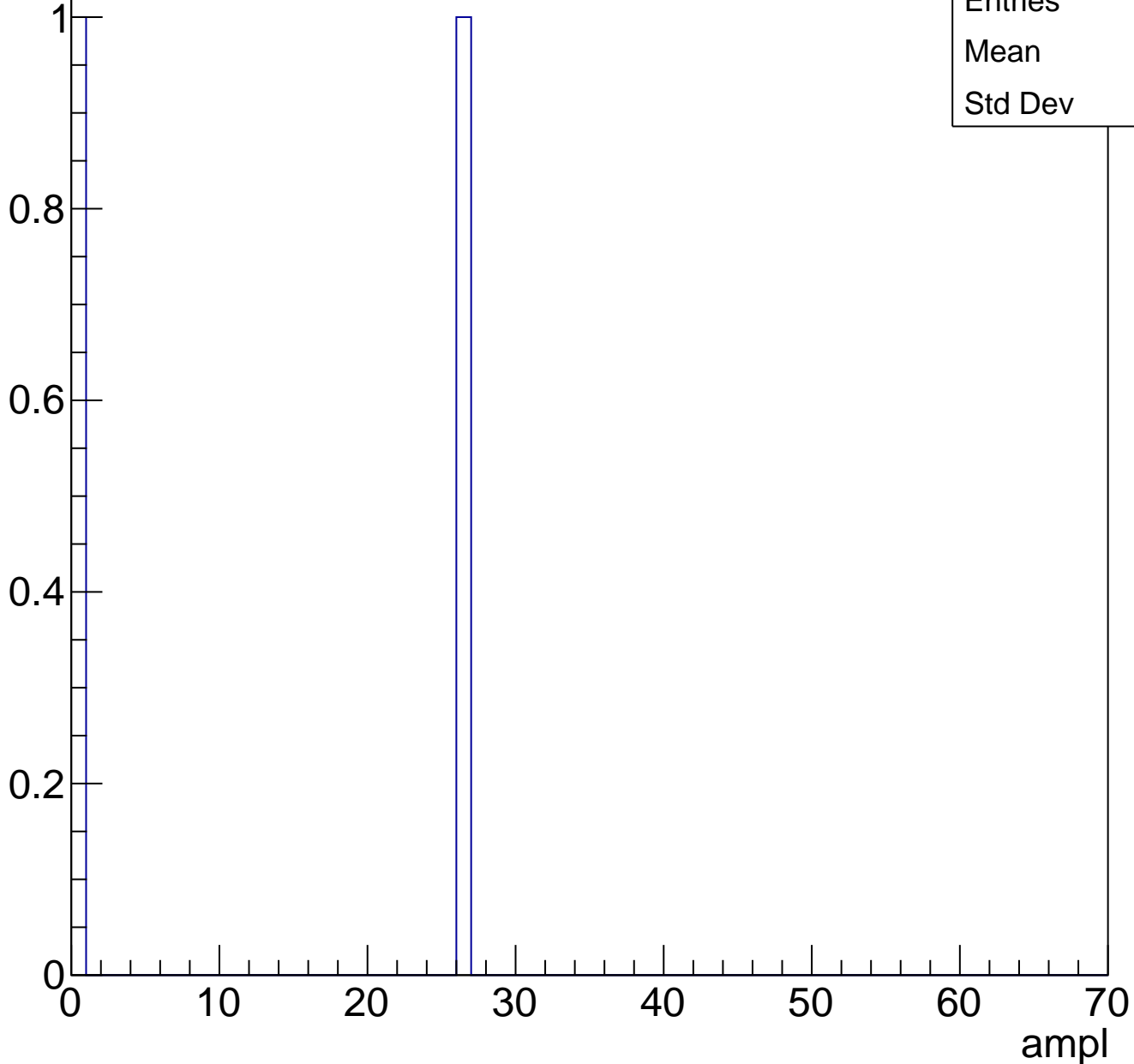




# B0L002S, U8-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	13
Std Dev	13

# B0L002S, U8-ch33, adc0

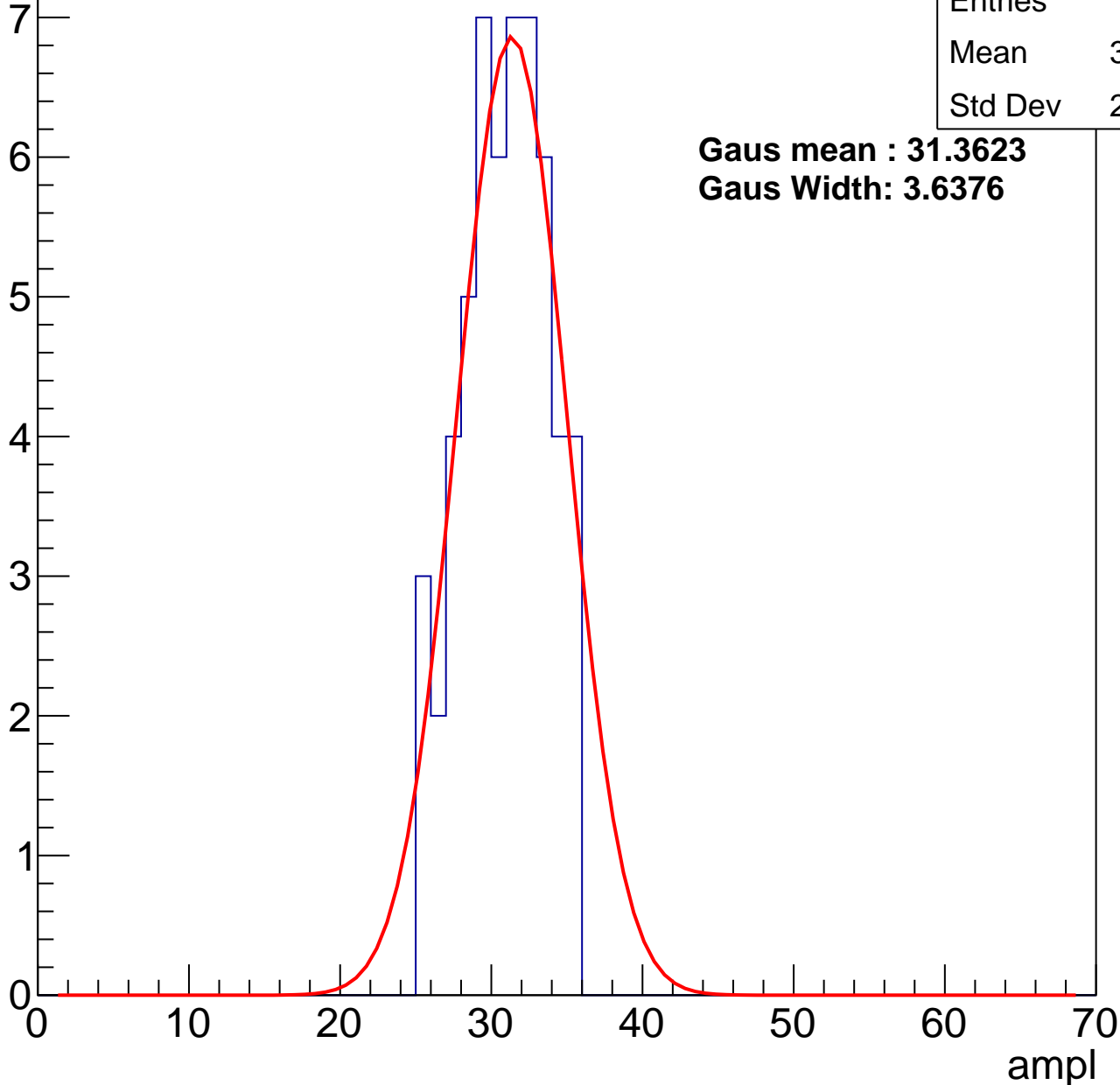
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	30.42
Std Dev	2.742

**Gaus mean : 31.3623**

**Gaus Width: 3.6376**

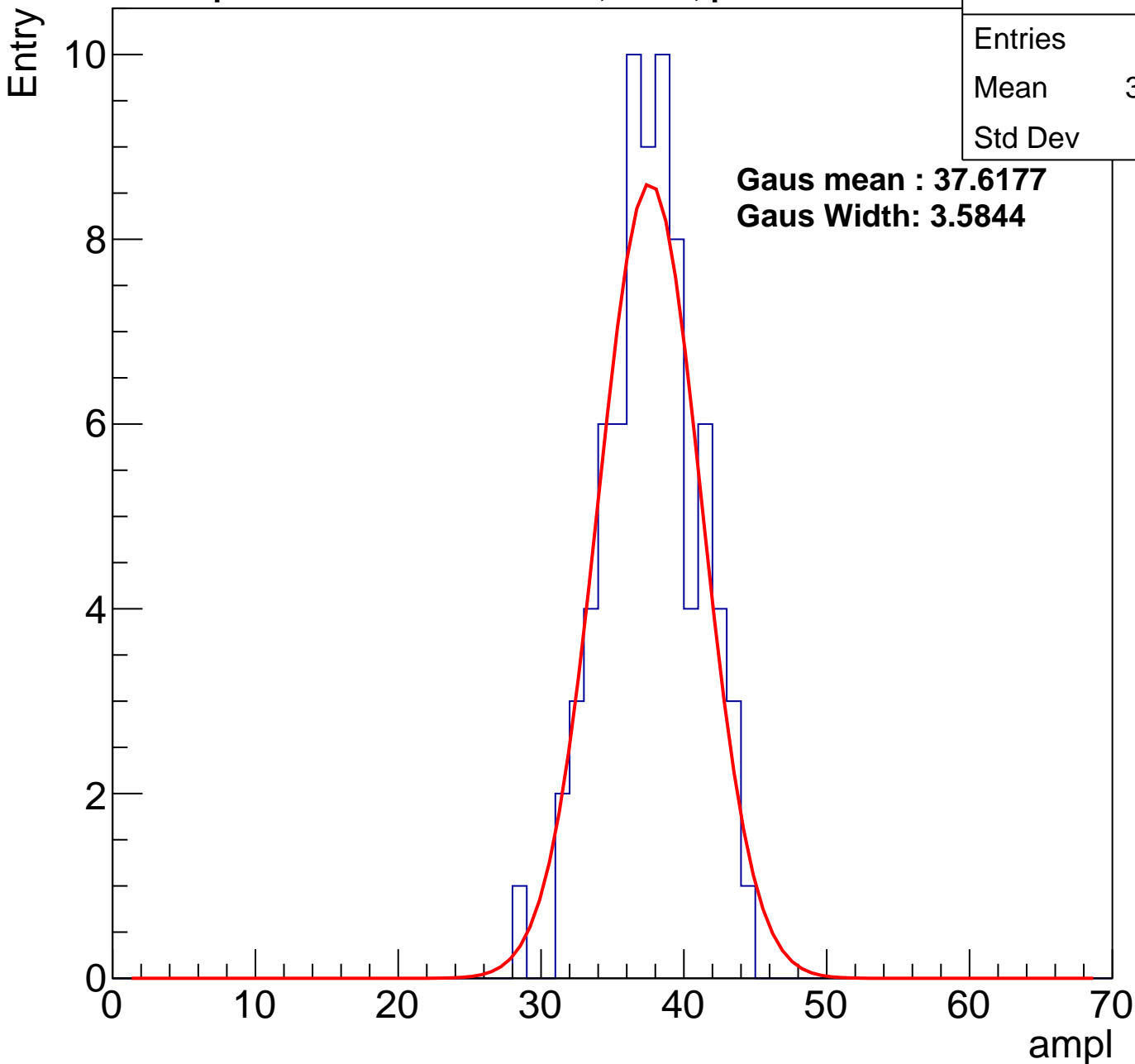


# B0L002S, U8-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	37.19
Std Dev	3.24

**Gaus mean : 37.6177**  
**Gaus Width: 3.5844**



# B0L002S, U8-ch33, adc2

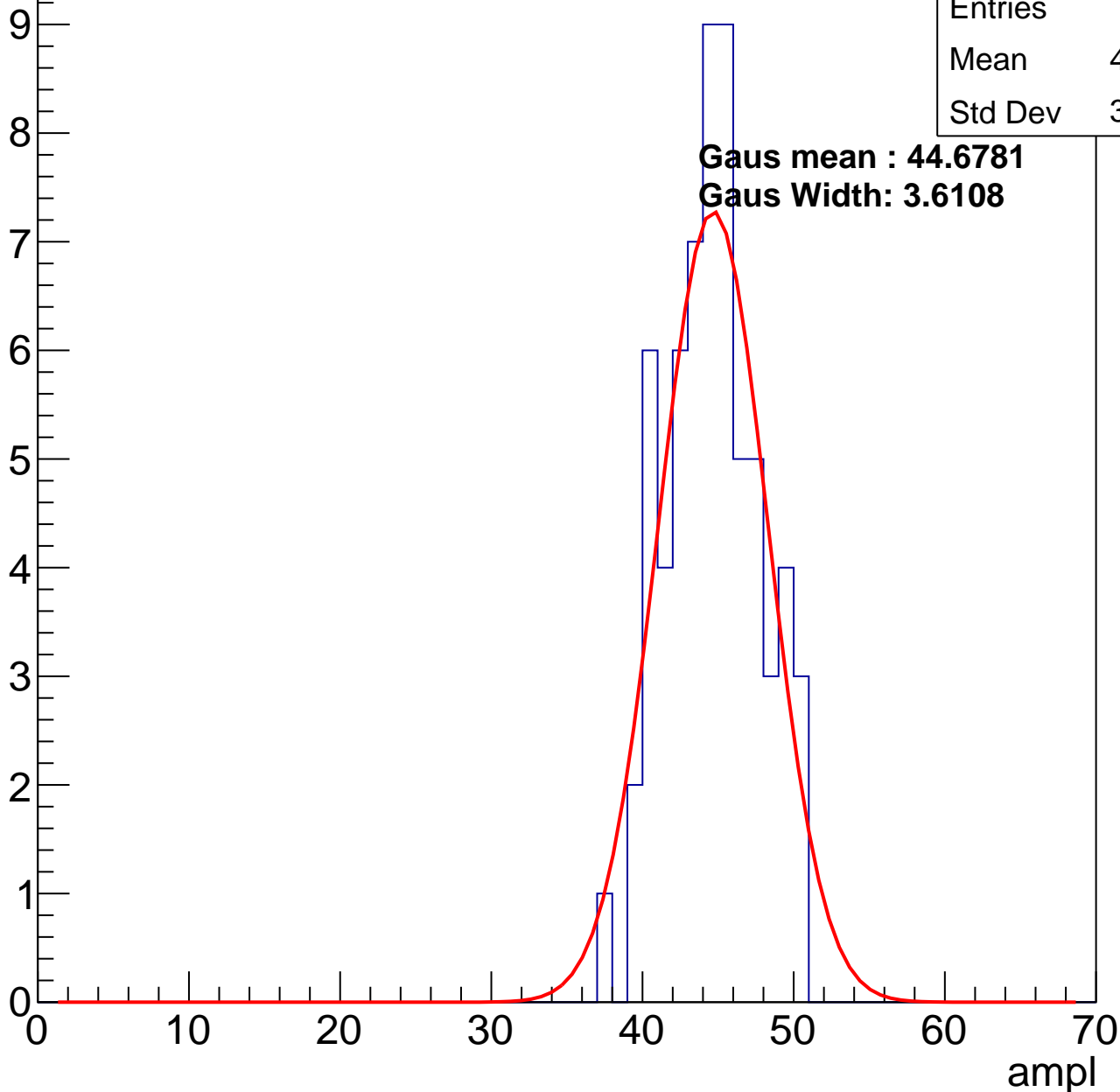
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.19
Std Dev	3.036

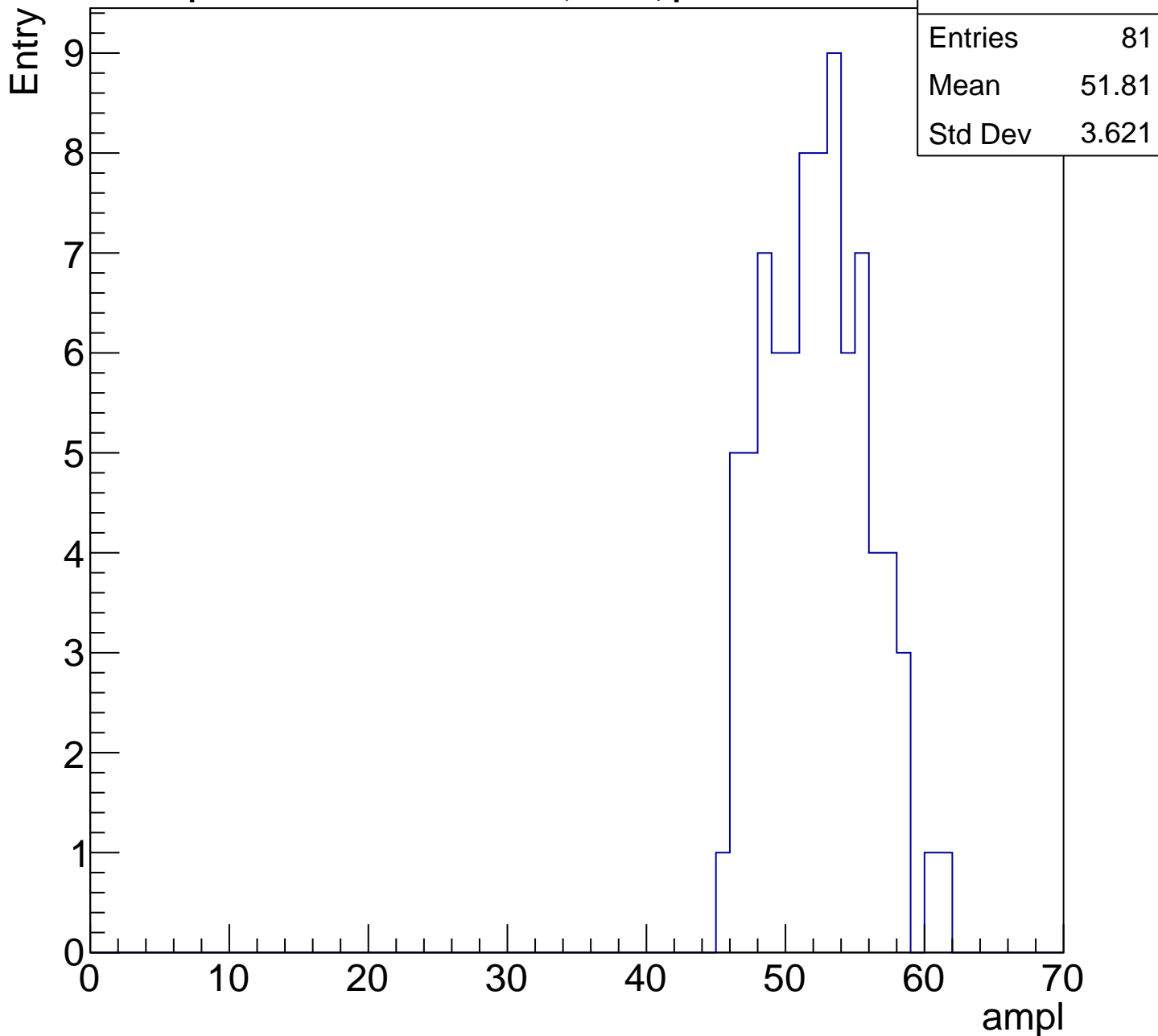
**Gaus mean : 44.6781**

**Gaus Width: 3.6108**



# B0L002S, U8-ch33, adc3

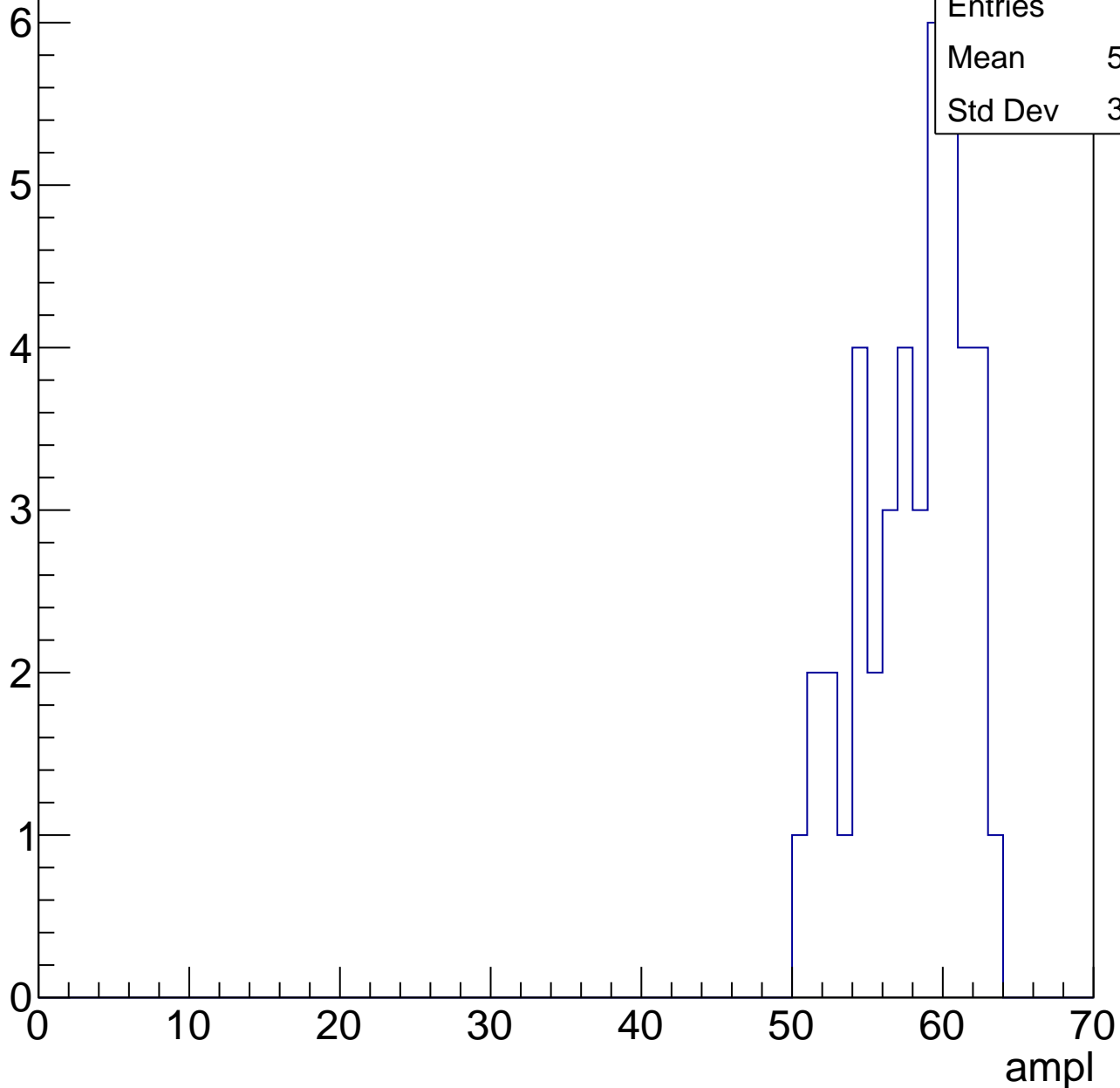
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

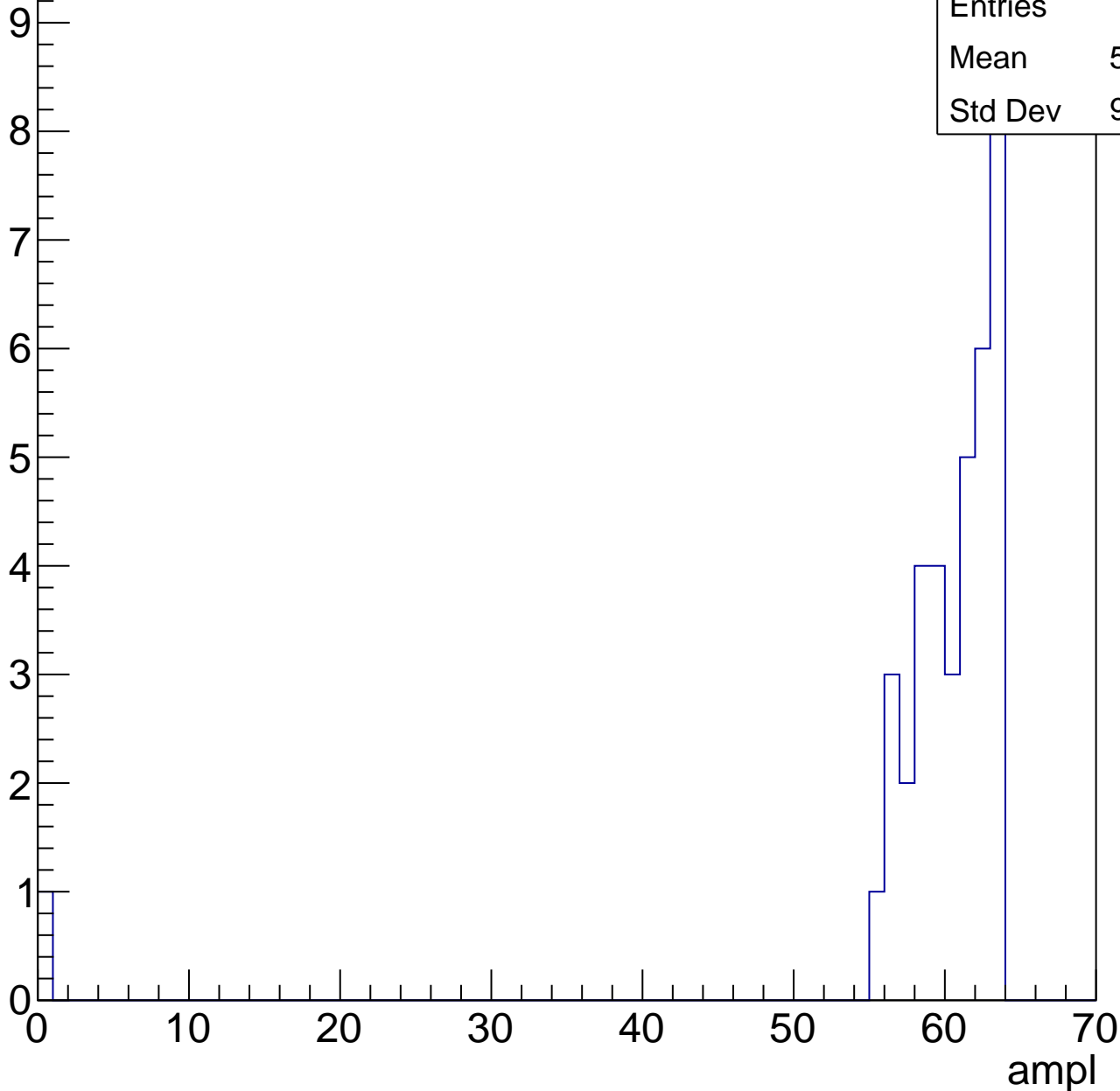


# B0L002S, U8-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.66
Std Dev	9.937



# B0L002S, U8-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch34, adc0

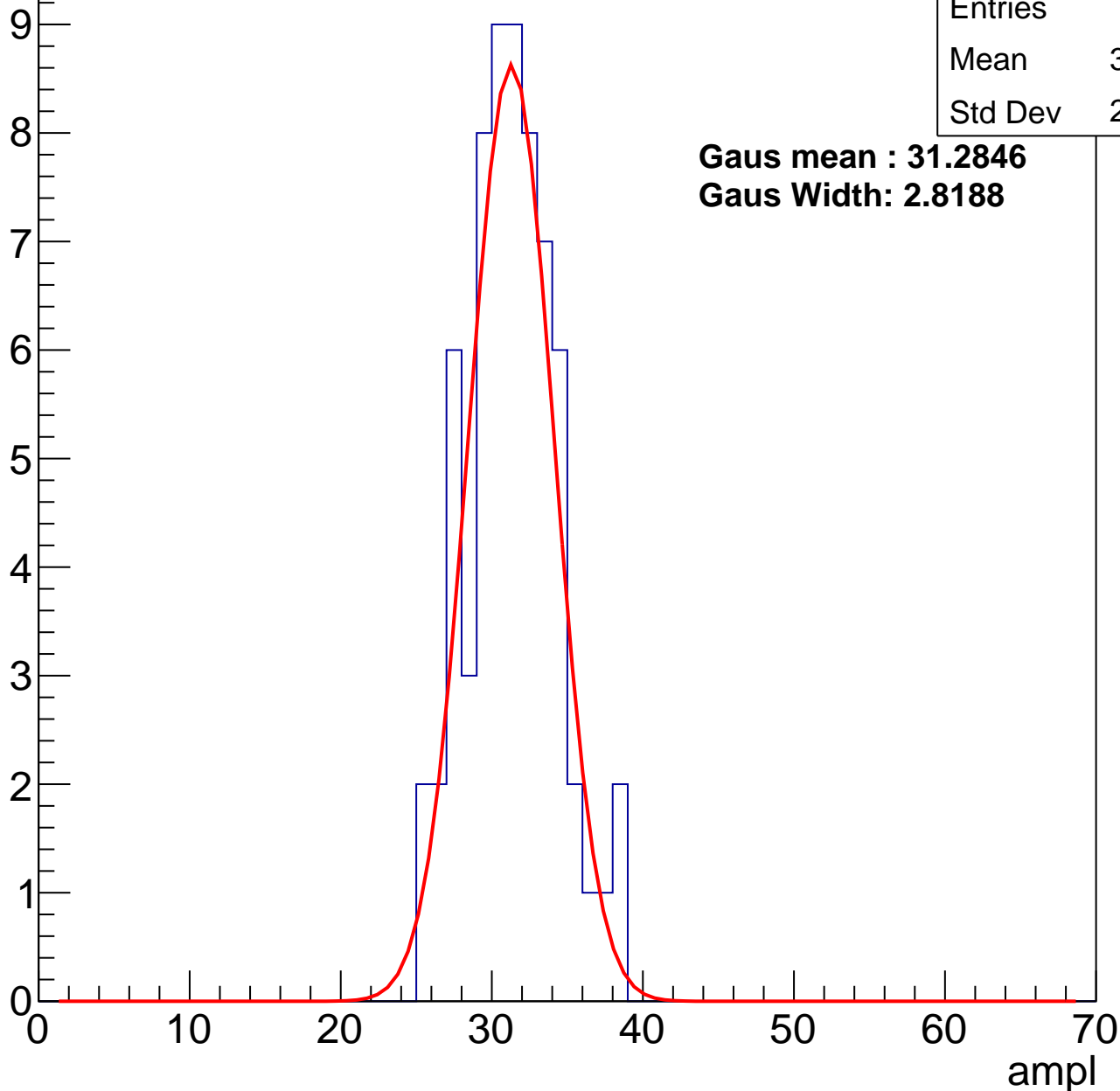
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.89
Std Dev	2.929

**Gaus mean : 31.2846**

**Gaus Width: 2.8188**



# B0L002S, U8-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	38.18
Std Dev	3.262

**Gaus mean : 38.3054**

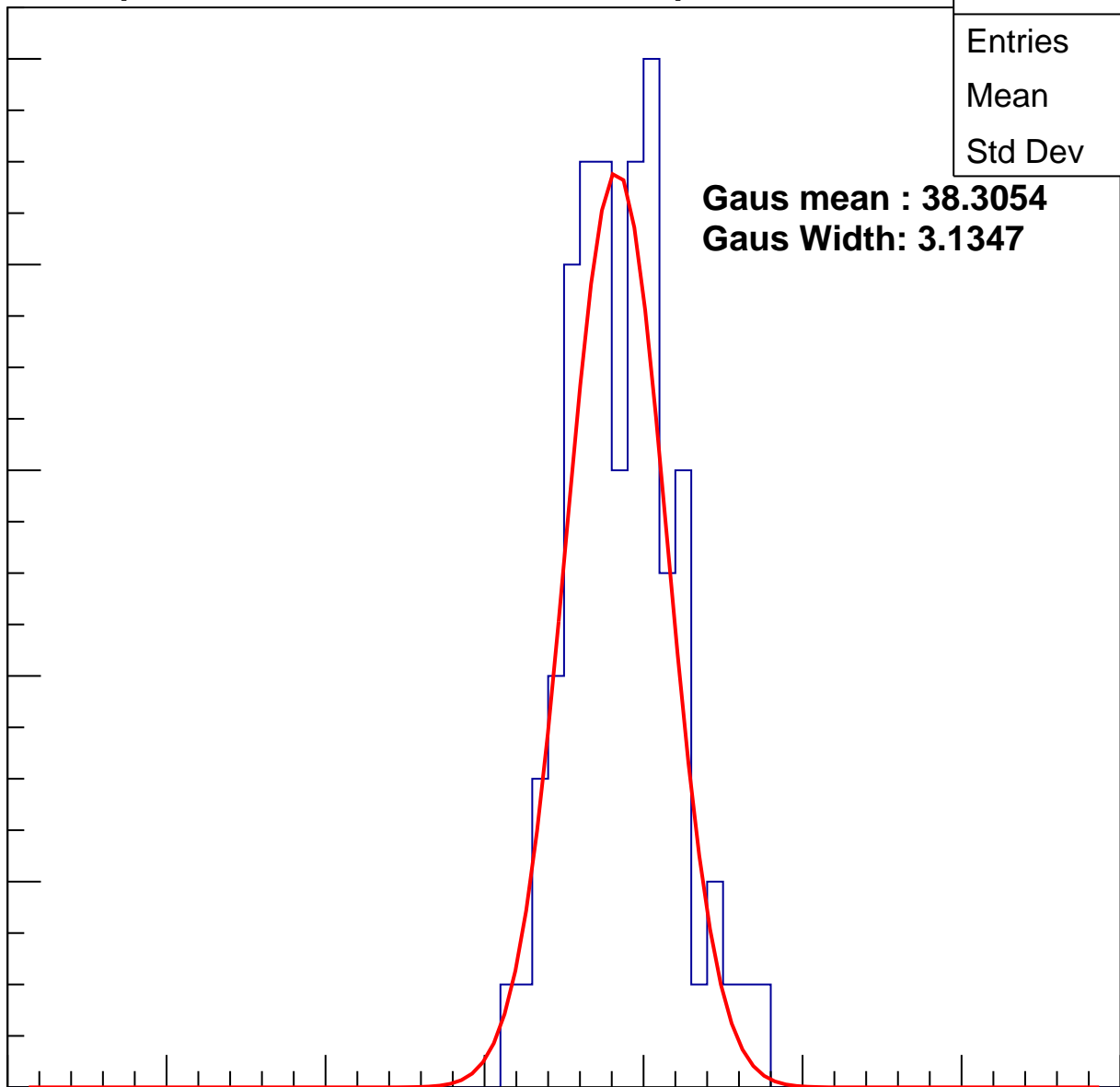
**Gaus Width: 3.1347**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch34, adc2

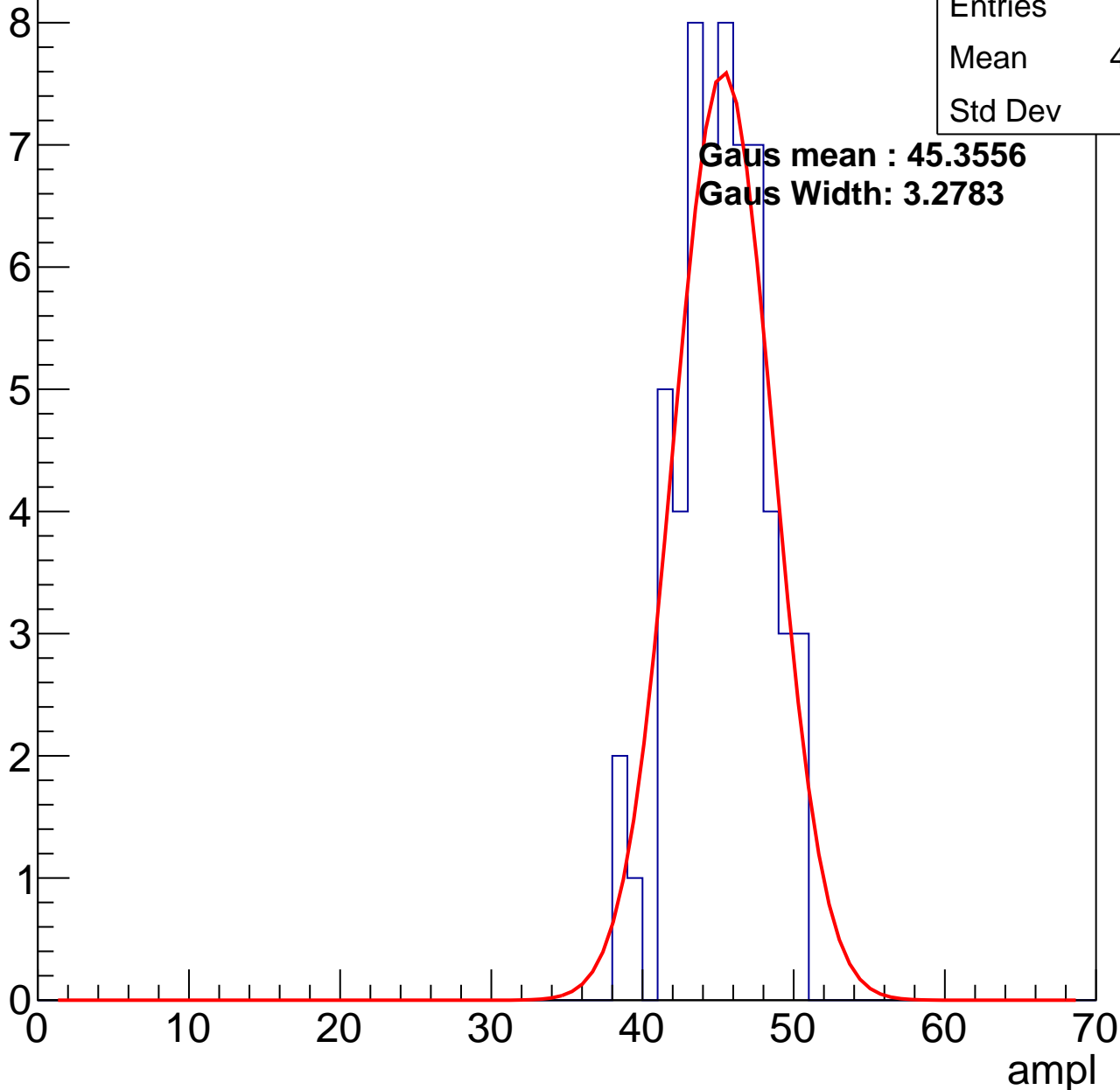
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.75
Std Dev	2.85

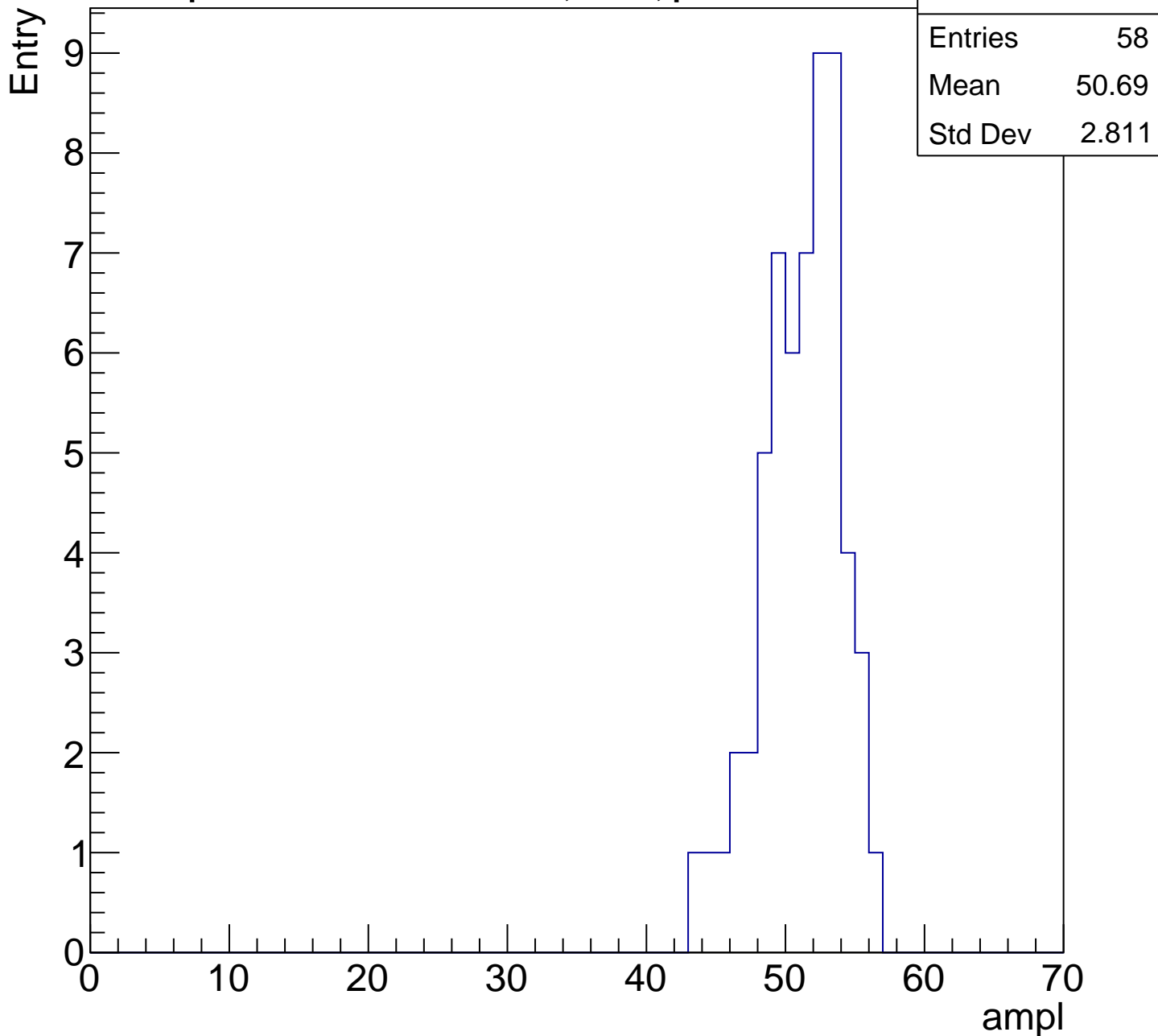
**Gaus mean : 45.3556**

**Gaus Width: 3.2783**



# B0L002S, U8-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

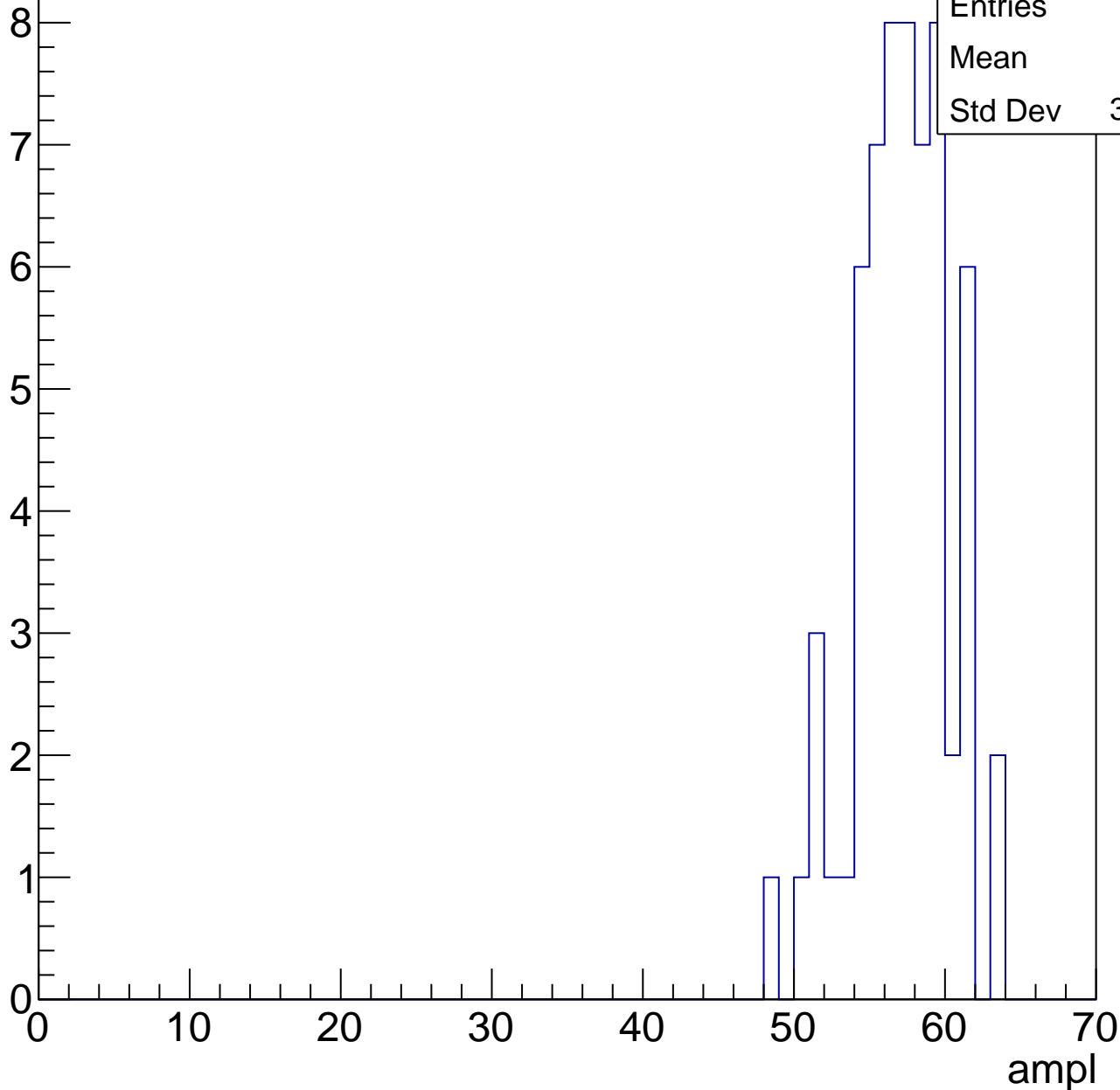


# B0L002S, U8-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.7
Std Dev	3.107



# B0L002S, U8-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

Entries	39
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Mean	59.23
------	-------

Std Dev	9.794
---------	-------

8

6

4

2

0

0

10

20

30

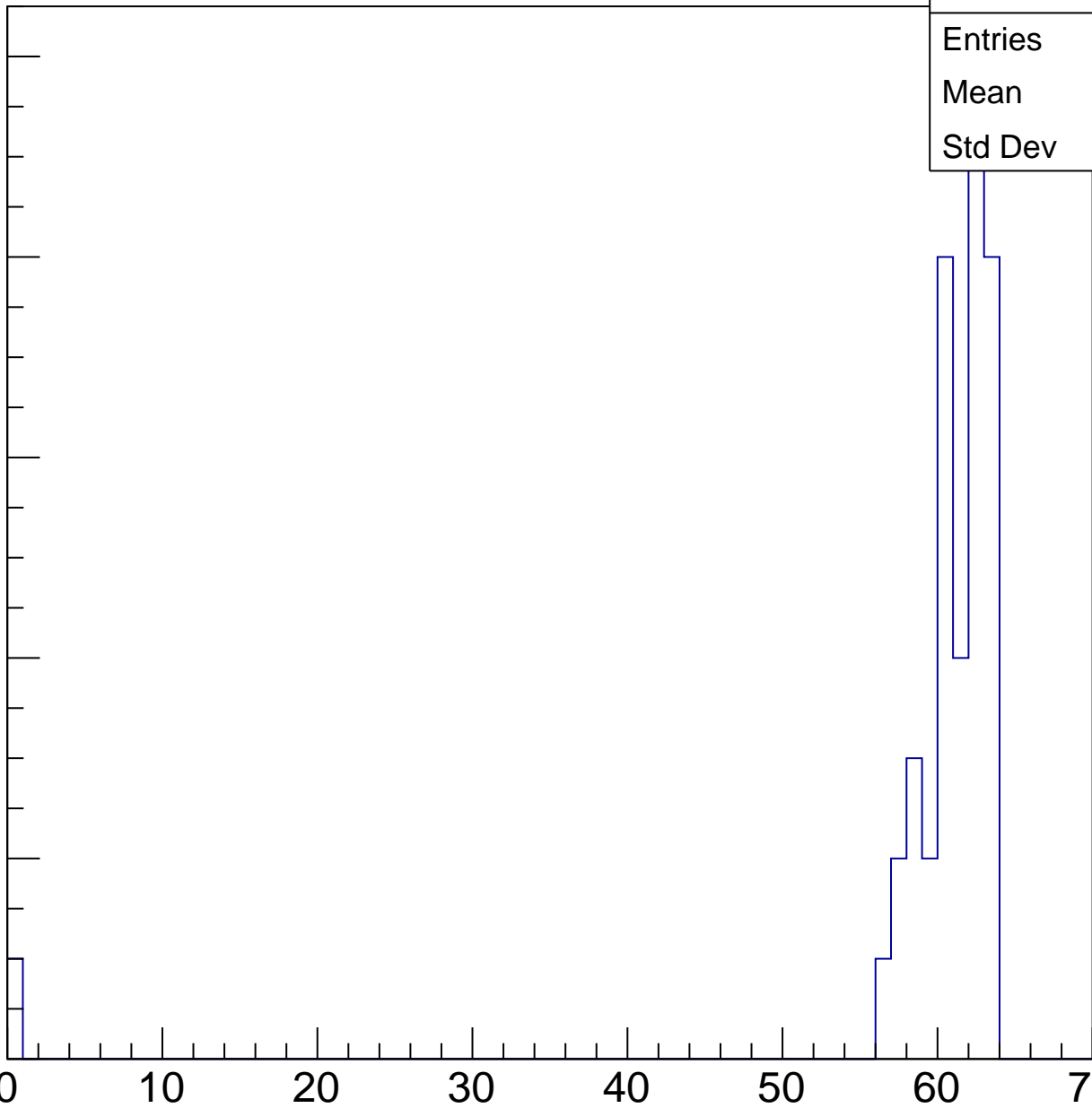
40

50

60

70

ampl



# B0L002S, U8-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



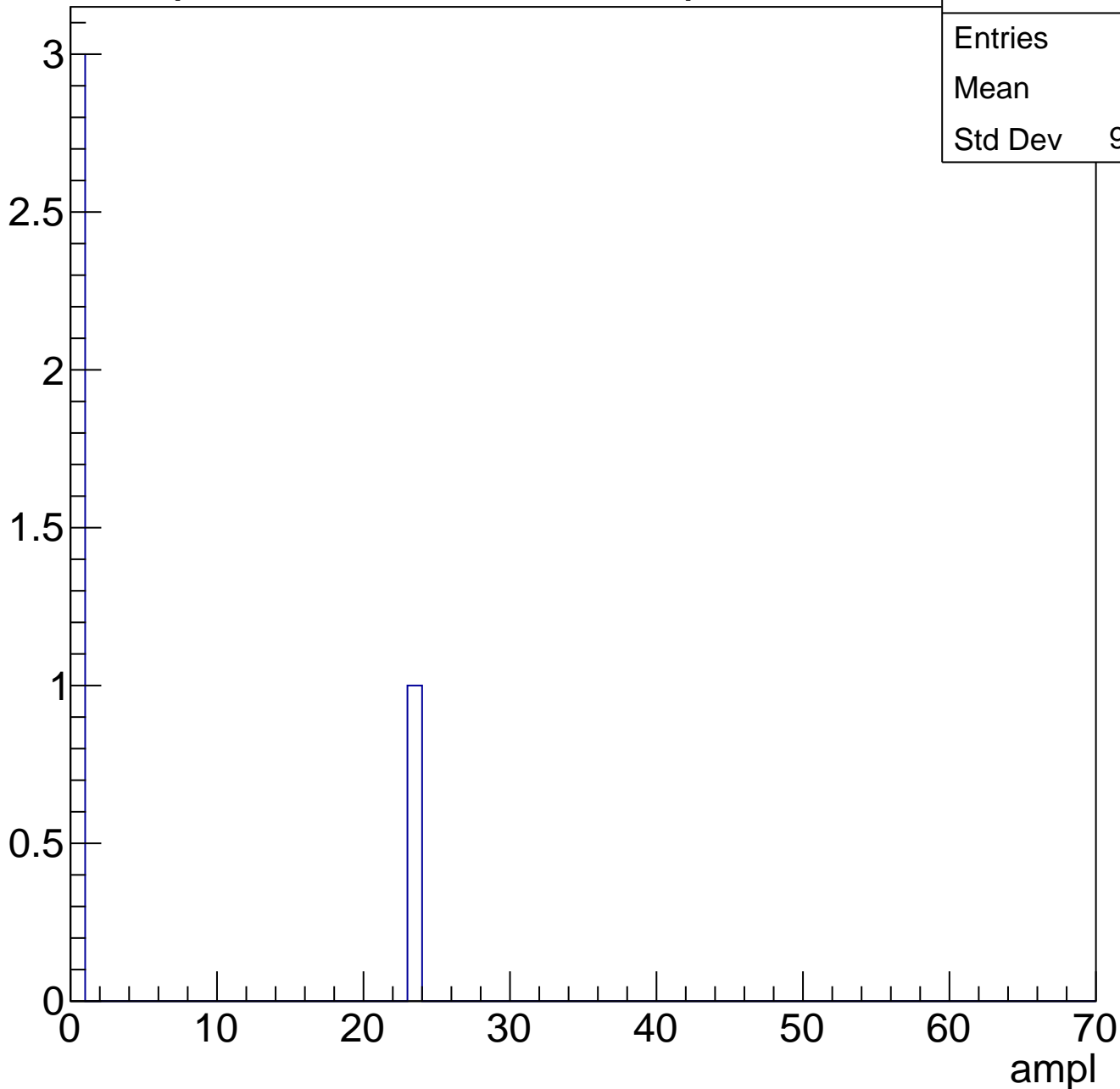
Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch35, adc0

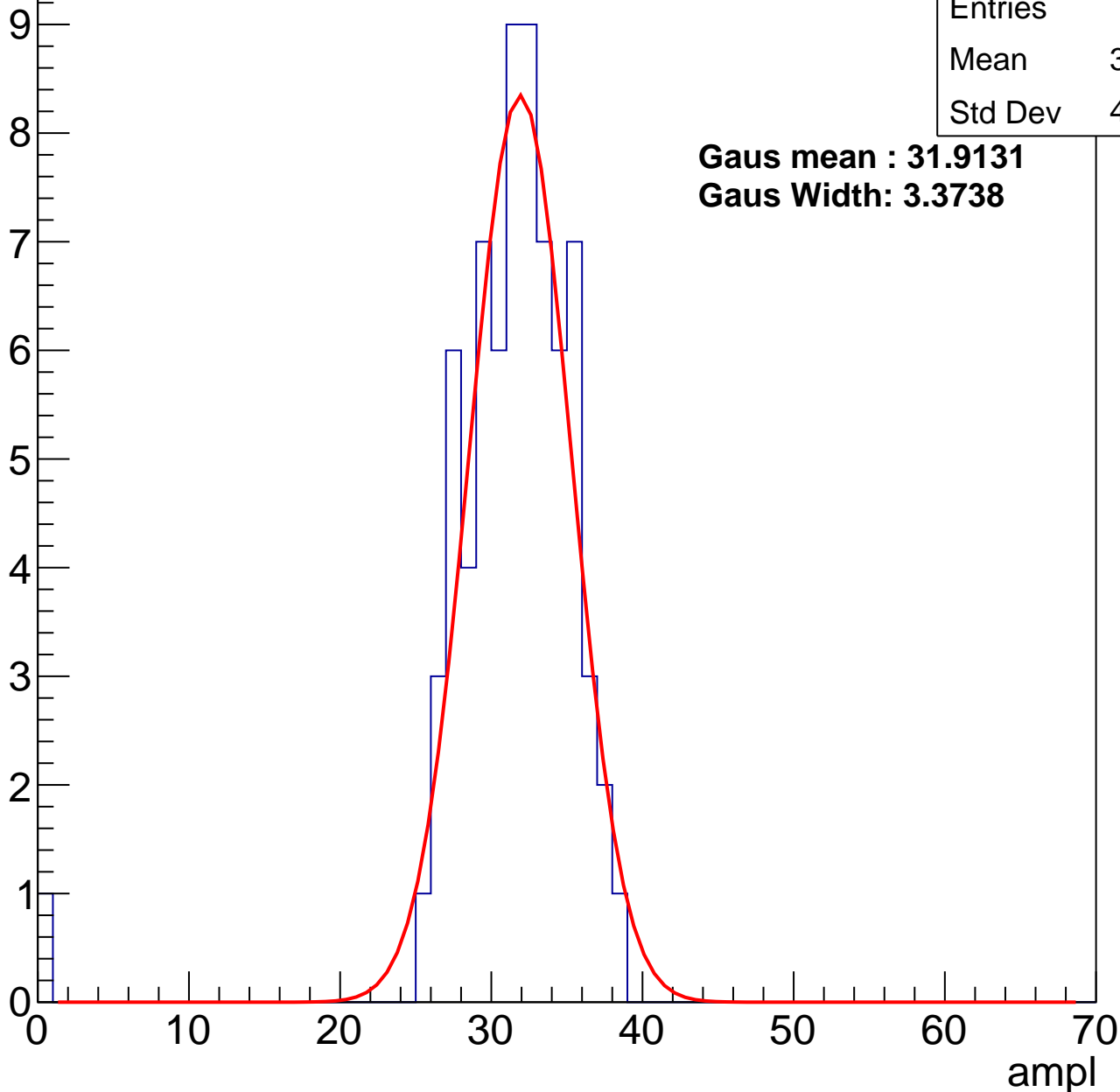
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.93
Std Dev	4.768

**Gaus mean : 31.9131**

**Gaus Width: 3.3738**



# B0L002S, U8-ch35, adc1

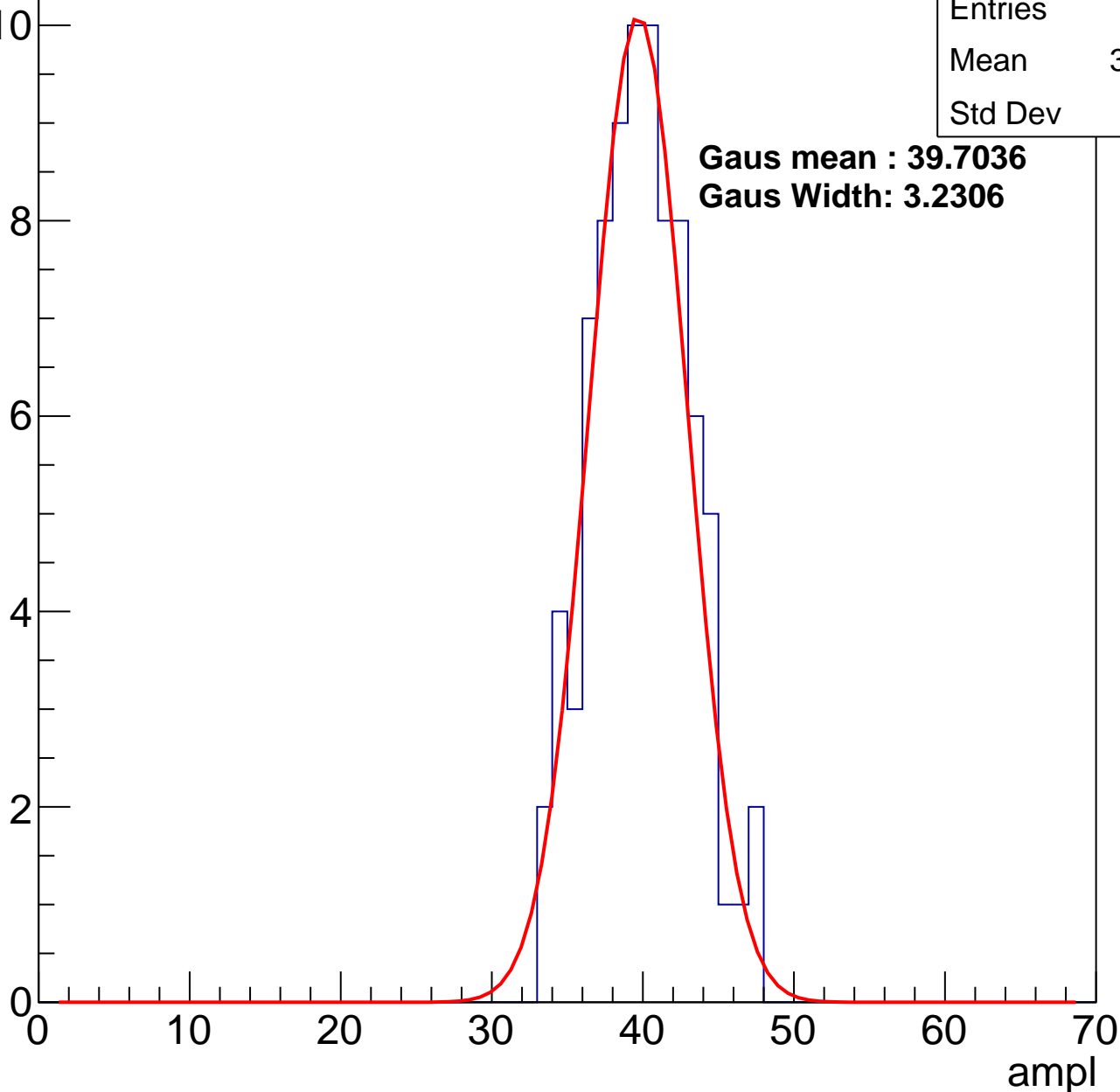
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	84
Mean	39.45
Std Dev	3.19

**Gaus mean : 39.7036**

**Gaus Width: 3.2306**



# B0L002S, U8-ch35, adc2

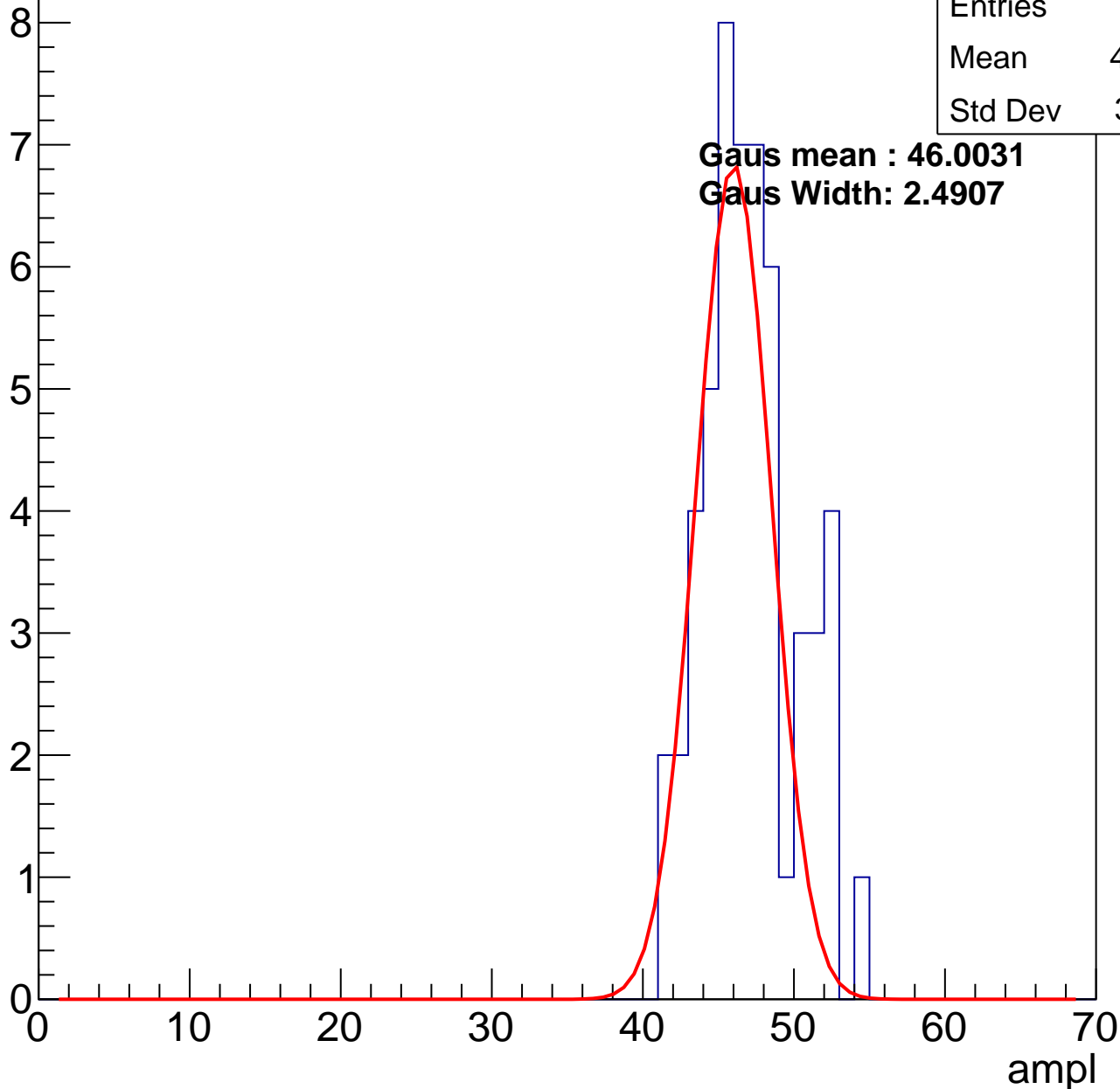
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	46.62
Std Dev	3.061

Gaus mean : 46.0031

Gaus Width: 2.4907

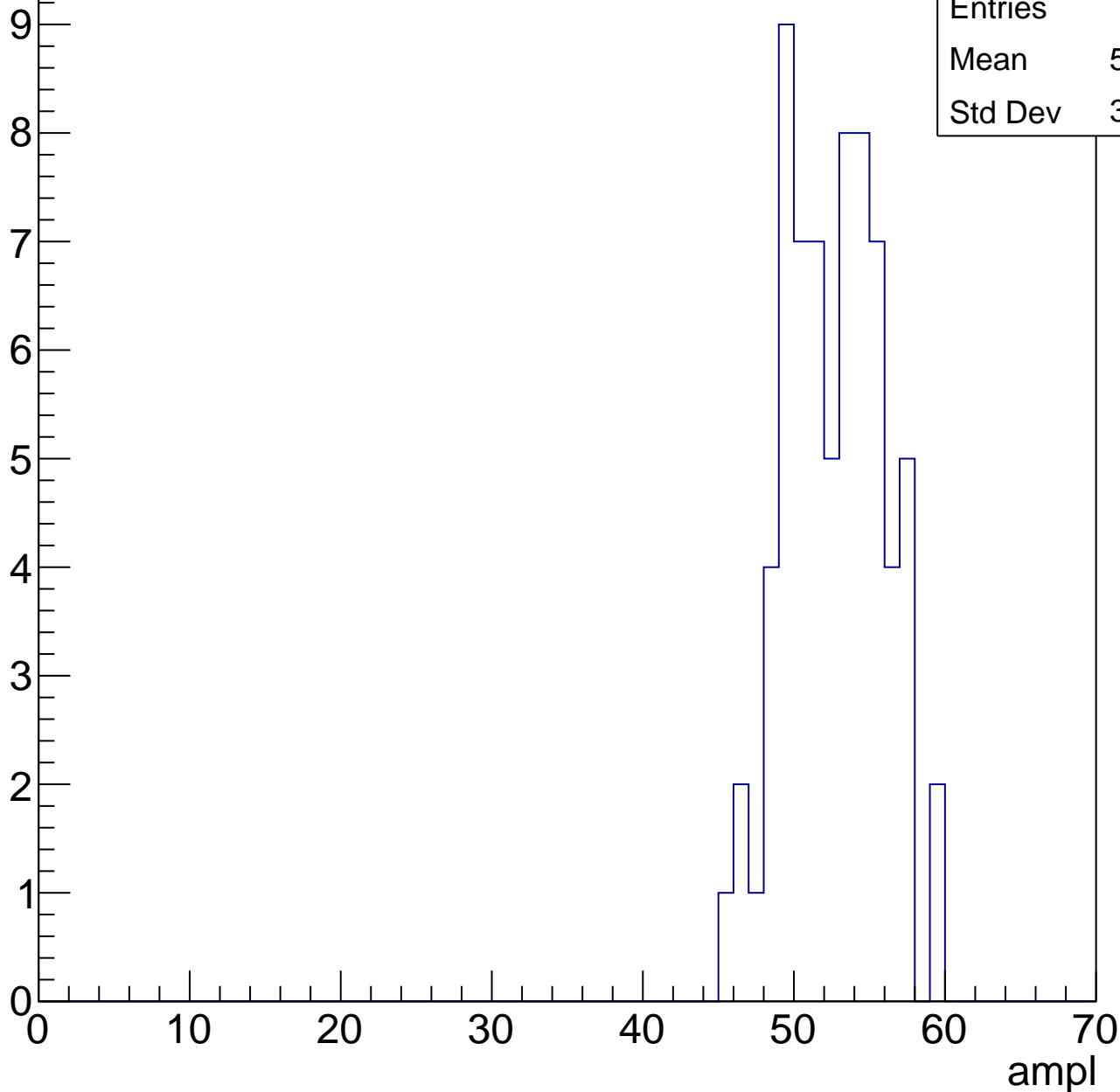


# B0L002S, U8-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	52.17
Std Dev	3.194

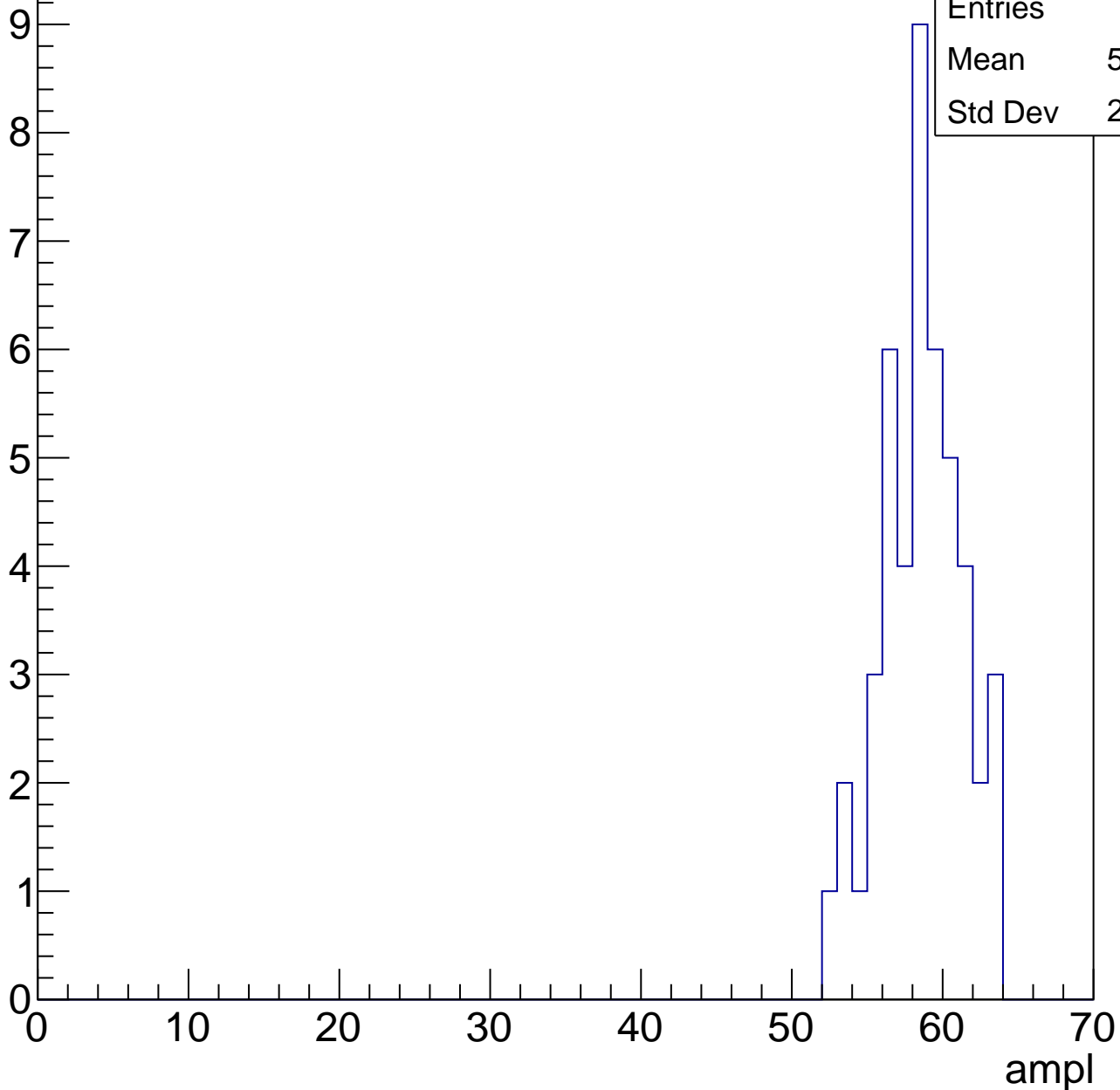


# B0L002S, U8-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.13
Std Dev	2.659

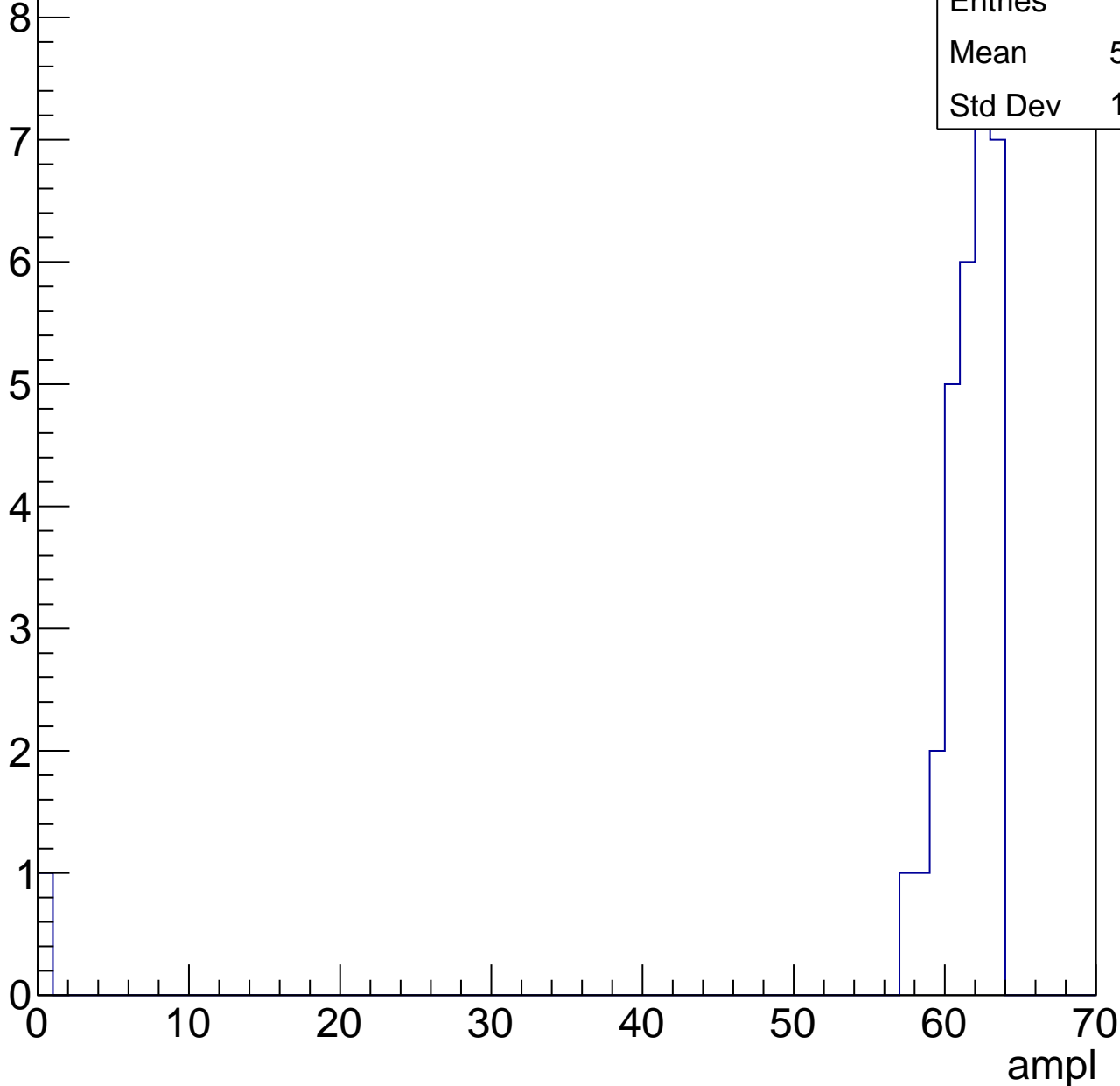


# B0L002S, U8-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.23
Std Dev	10.92



# B0L002S, U8-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch36, adc0

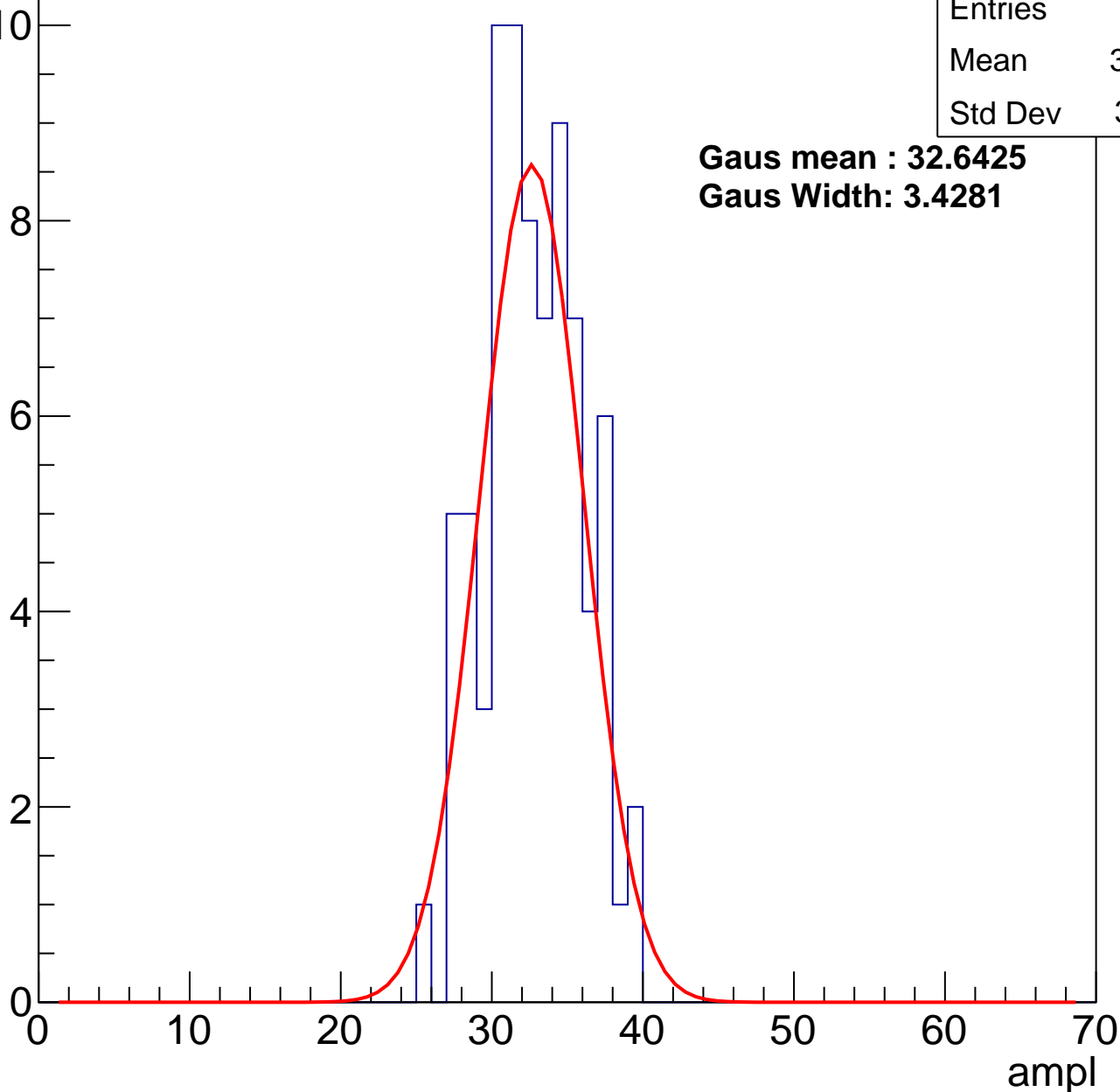
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	32.27
Std Dev	3.161

**Gaus mean : 32.6425**

**Gaus Width: 3.4281**



# B0L002S, U8-ch36, adc1

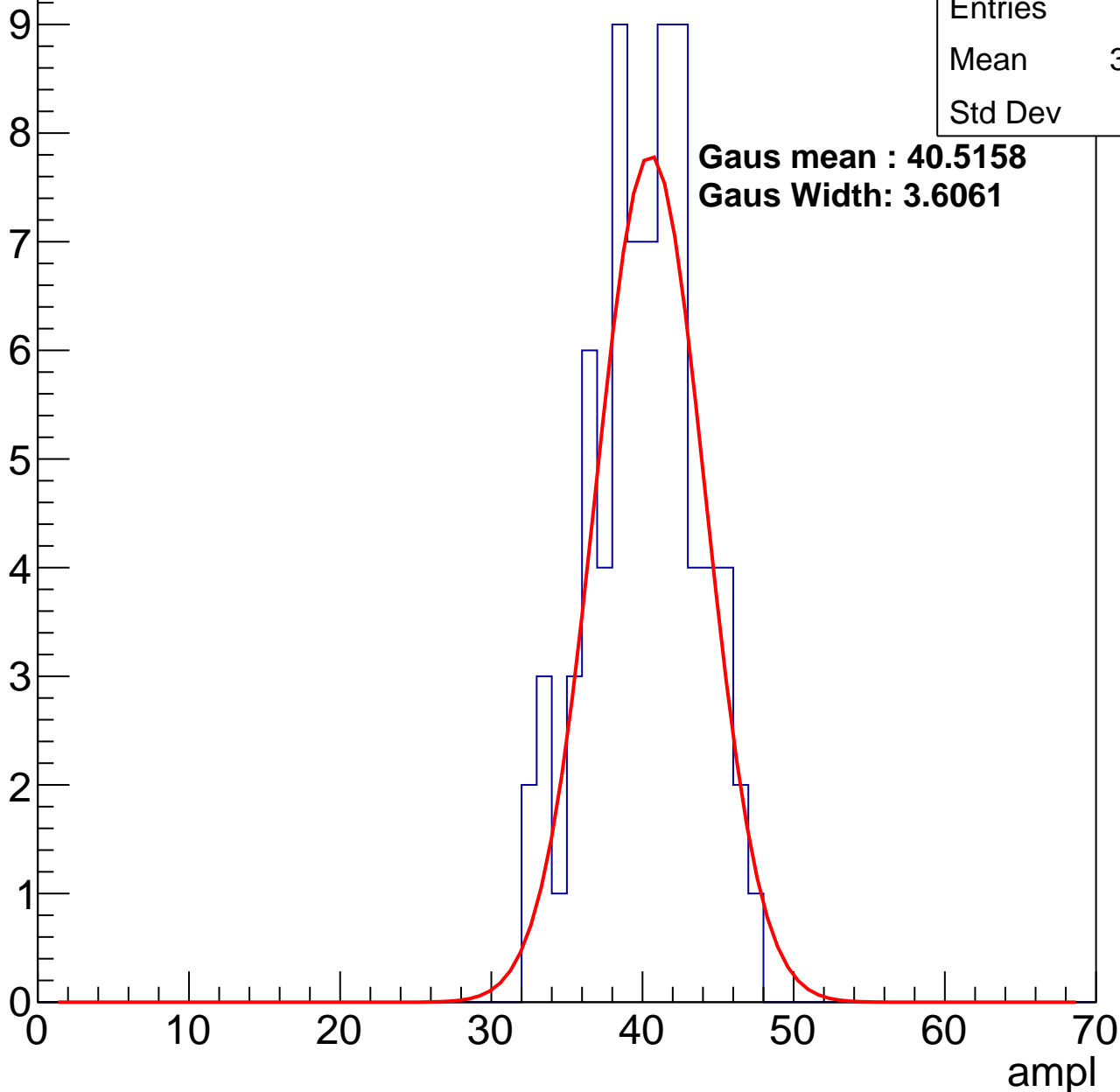
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	39.67
Std Dev	3.5

**Gaus mean : 40.5158**

**Gaus Width: 3.6061**



# B0L002S, U8-ch36, adc2

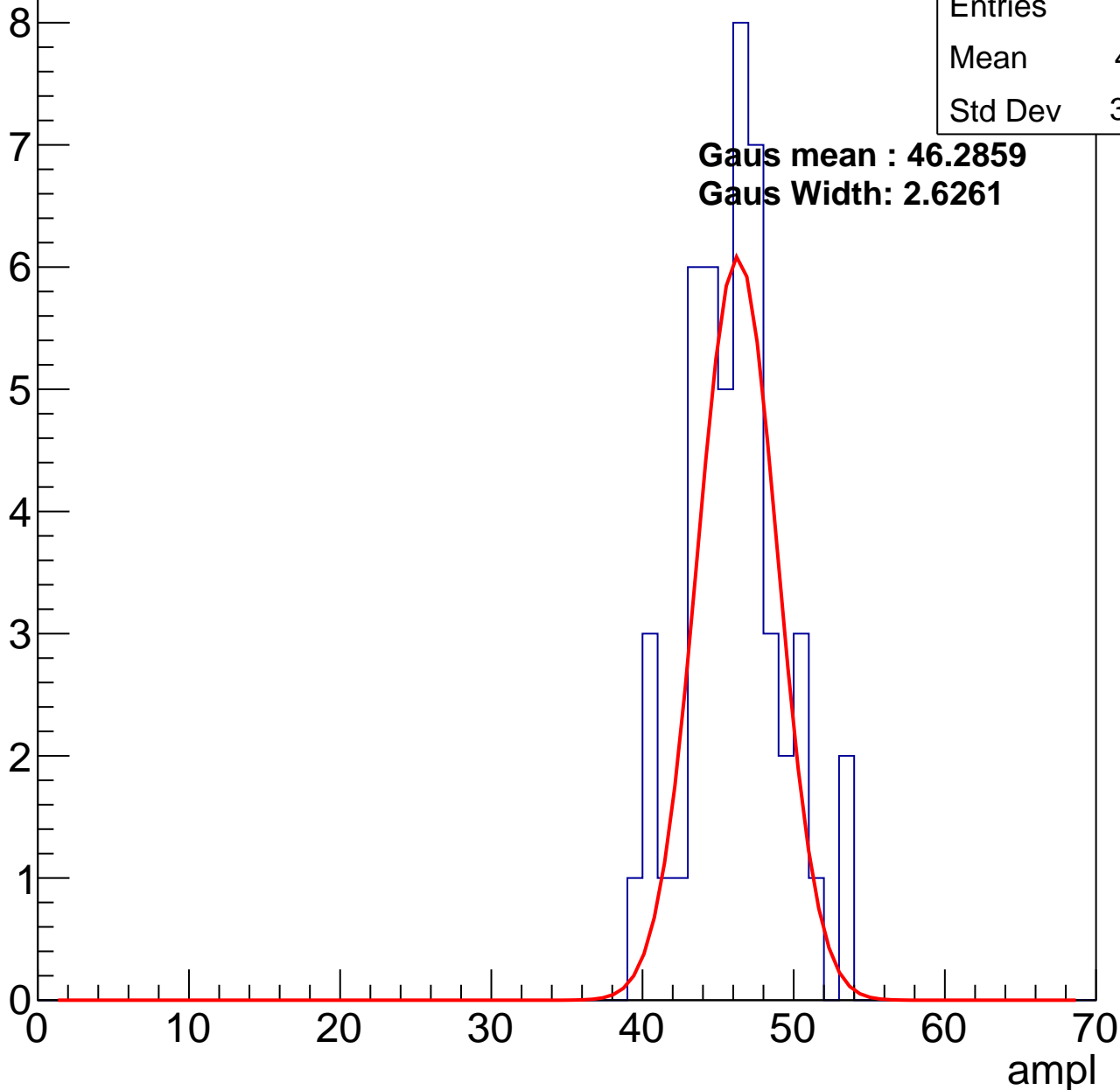
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	45.61
Std Dev	3.135

**Gaus mean : 46.2859**

**Gaus Width: 2.6261**

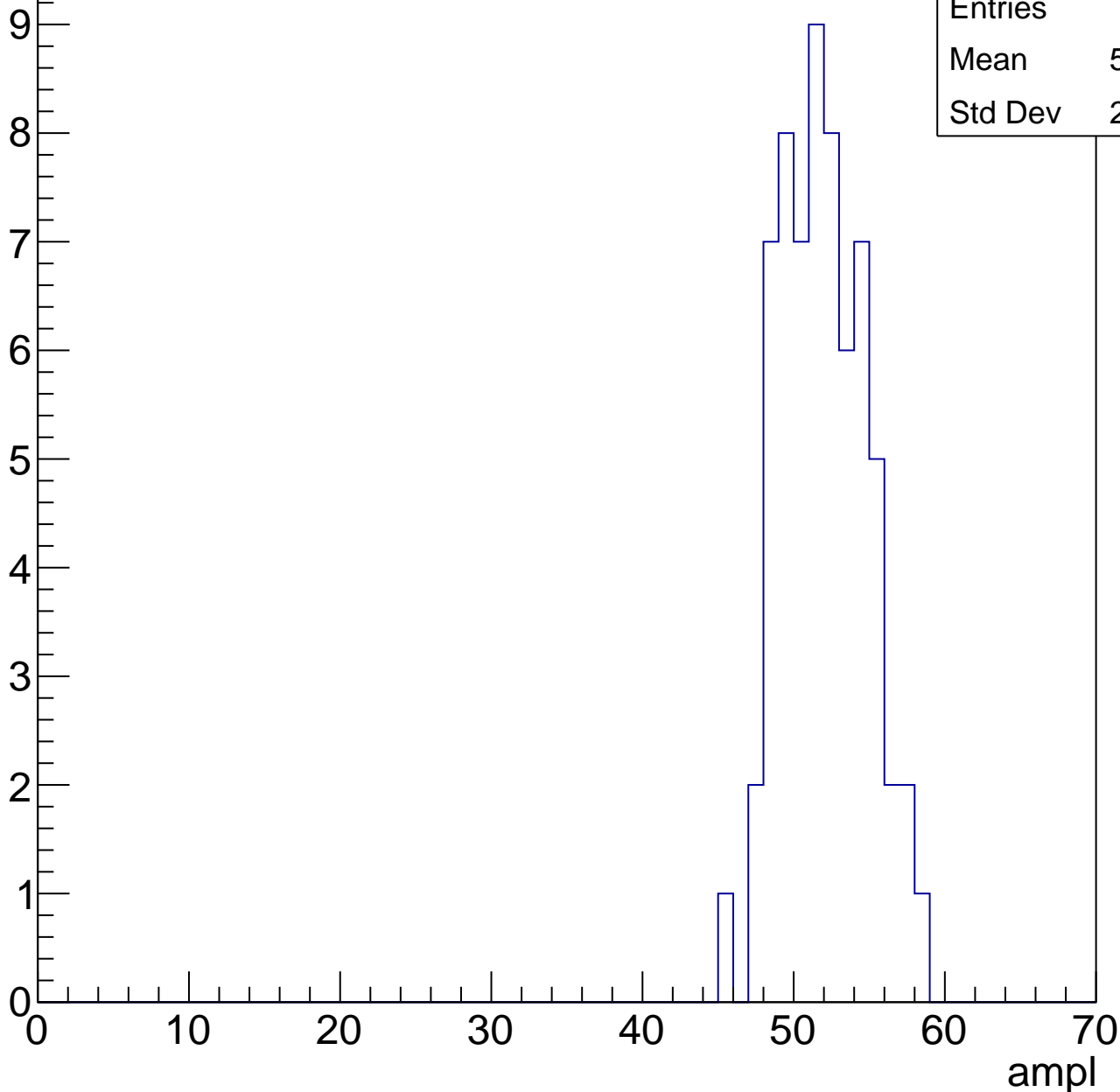


# B0L002S, U8-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

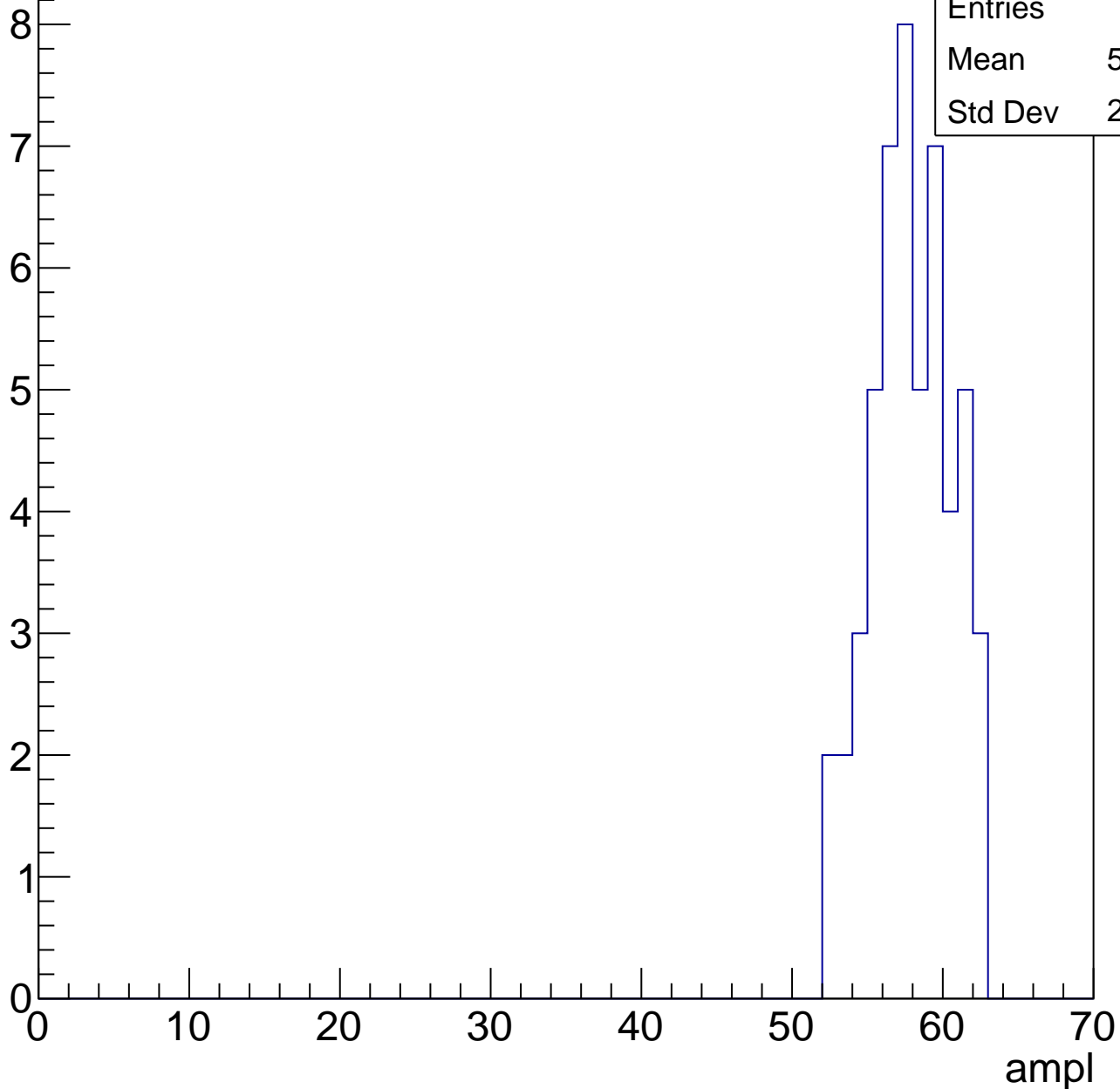
Entries	65
Mean	51.49
Std Dev	2.774



# B0L002S, U8-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



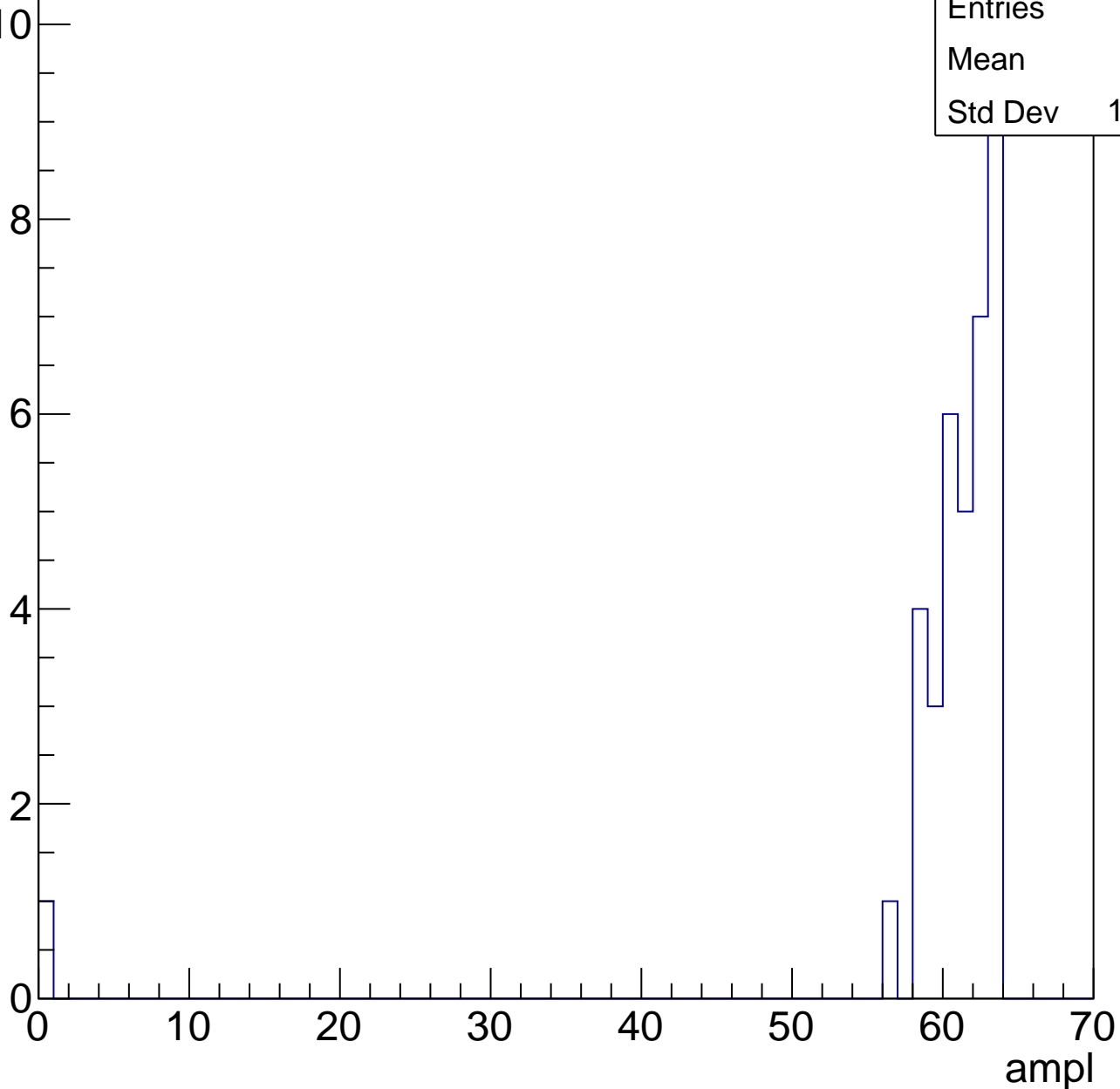
Entries	51
Mean	57.43
Std Dev	2.622

# B0L002S, U8-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.3
Std Dev	10.05



# B0L002S, U8-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B0L002S, U8-ch37, adc0

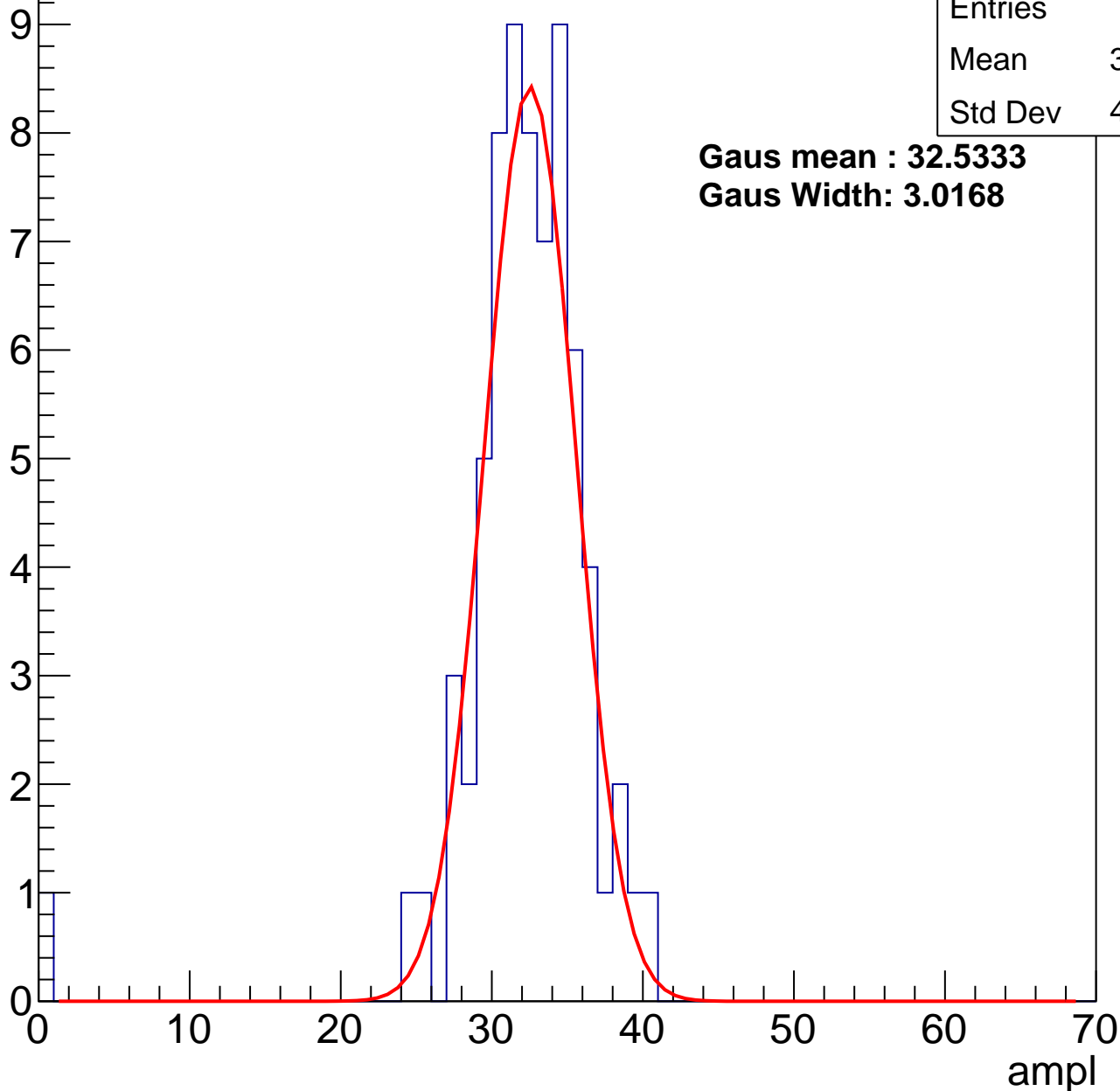
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.72
Std Dev	4.957

**Gaus mean : 32.5333**

**Gaus Width: 3.0168**



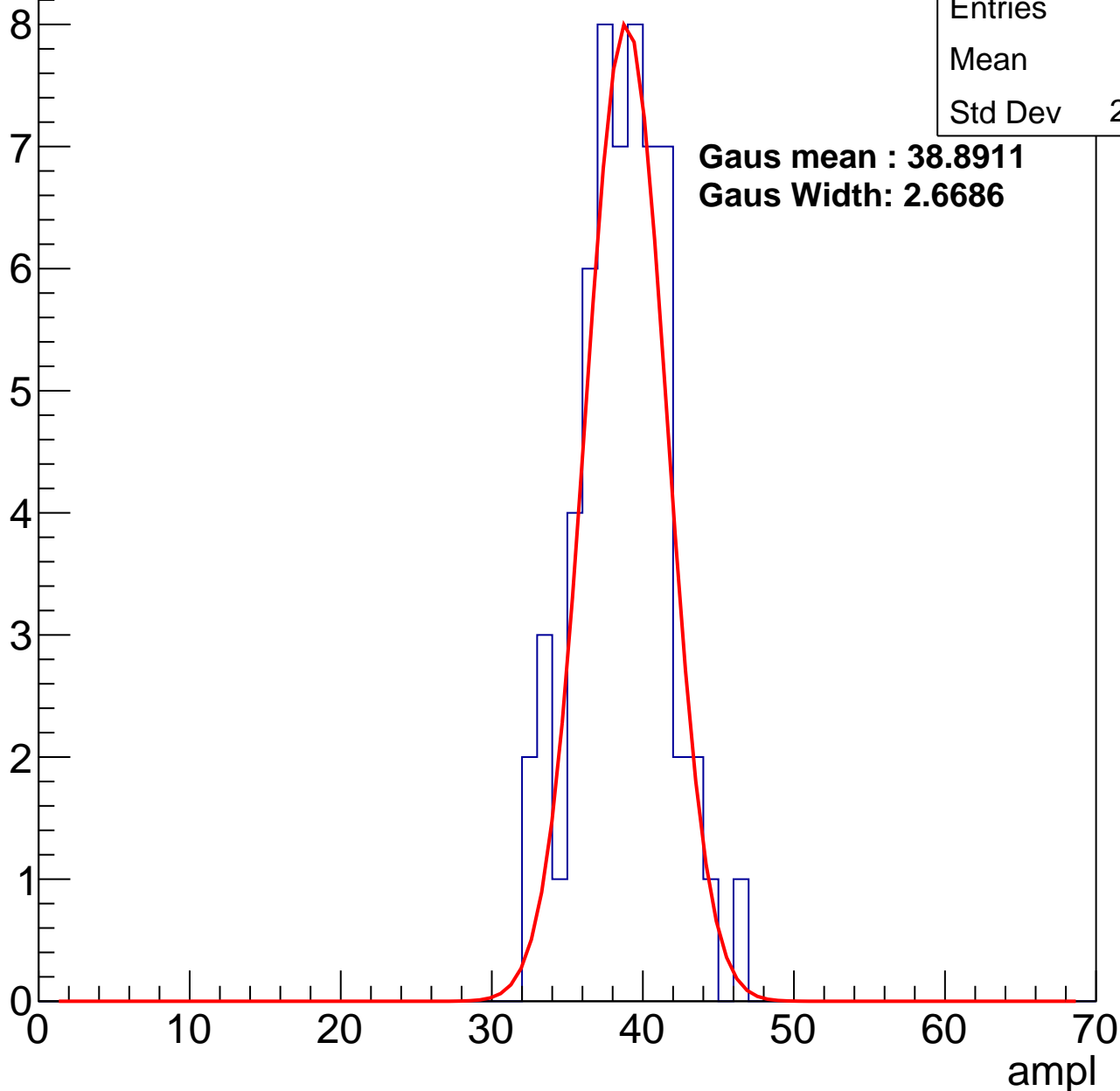
# B0L002S, U8-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.2
Std Dev	2.939

**Gaus mean : 38.8911**  
**Gaus Width: 2.6686**



# B0L002S, U8-ch37, adc2

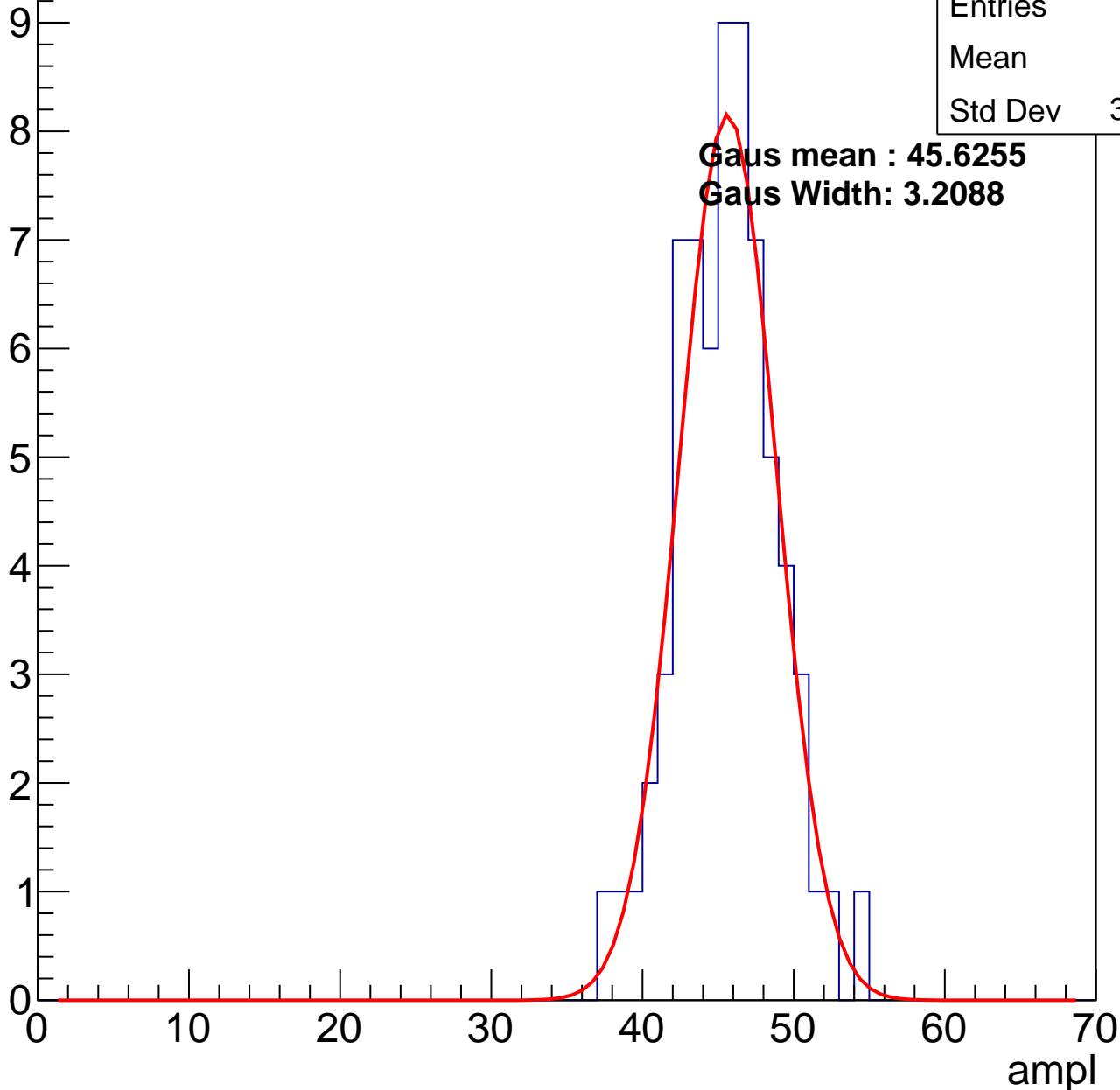
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.1
Std Dev	3.277

**Gaus mean : 45.6255**

**Gaus Width: 3.2088**

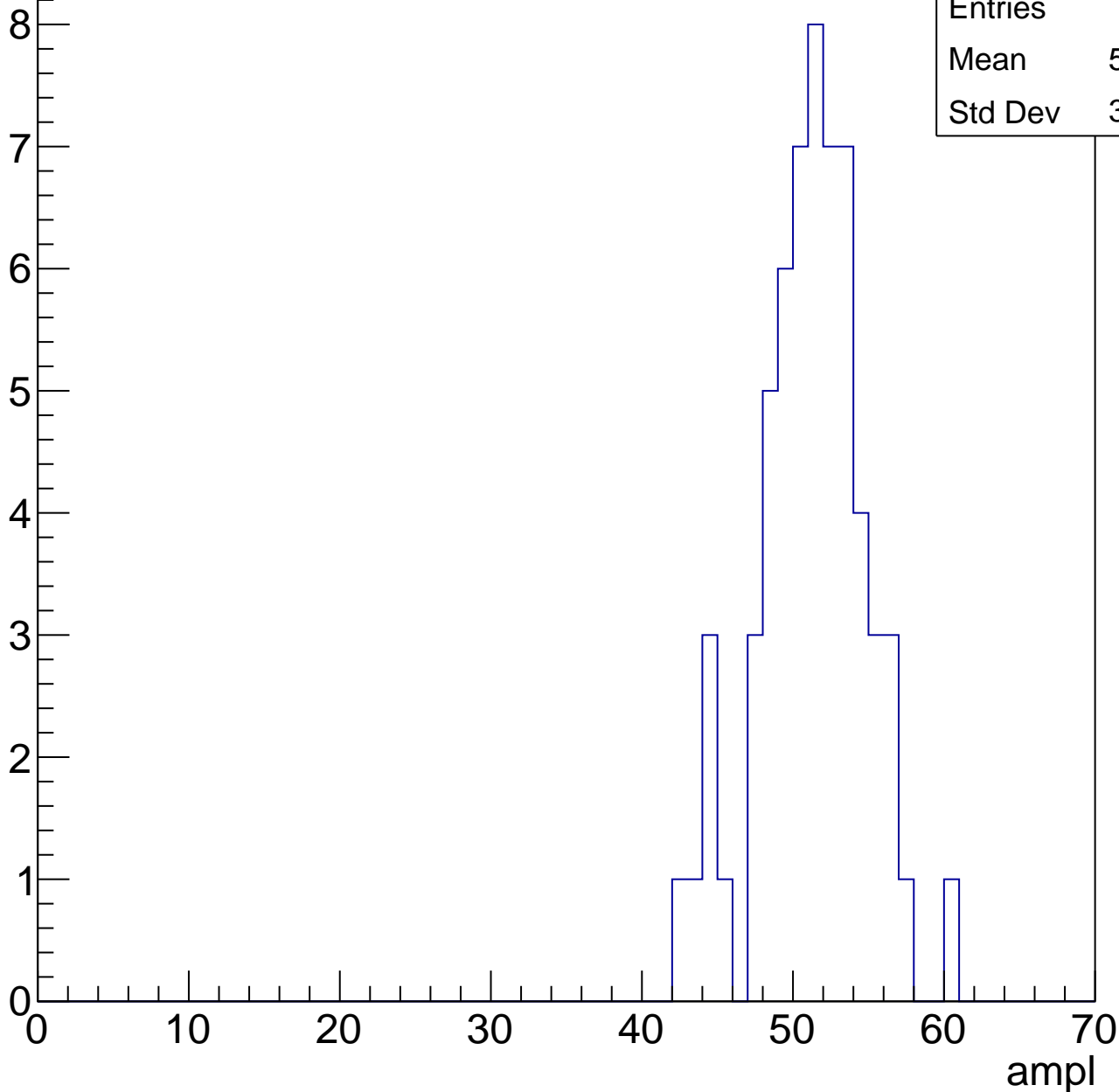


# B0L002S, U8-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.75
Std Dev	3.528

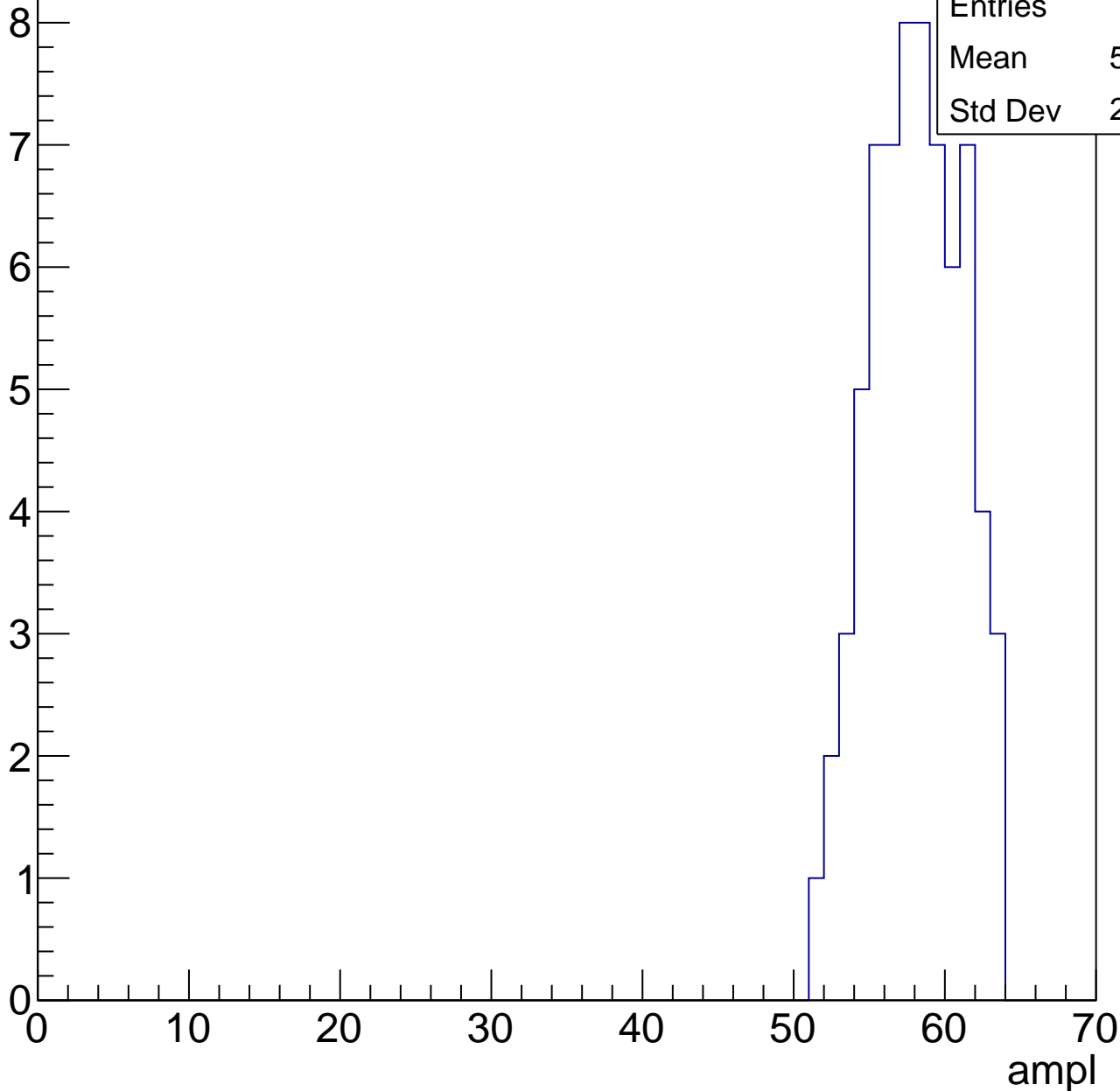


# B0L002S, U8-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	57.62
Std Dev	2.966

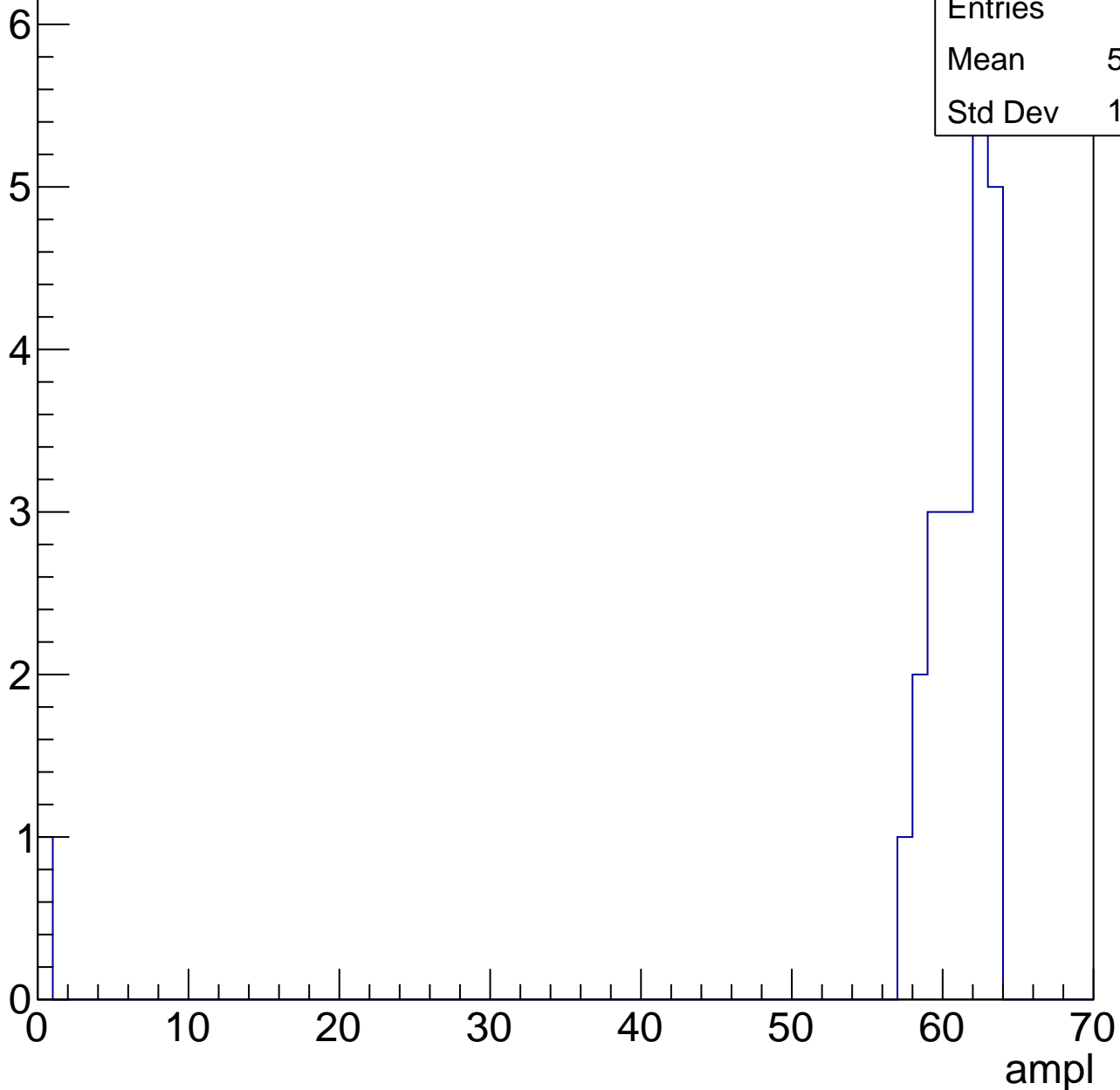


# B0L002S, U8-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.33
Std Dev	12.29



# B0L002S, U8-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch38, adc0

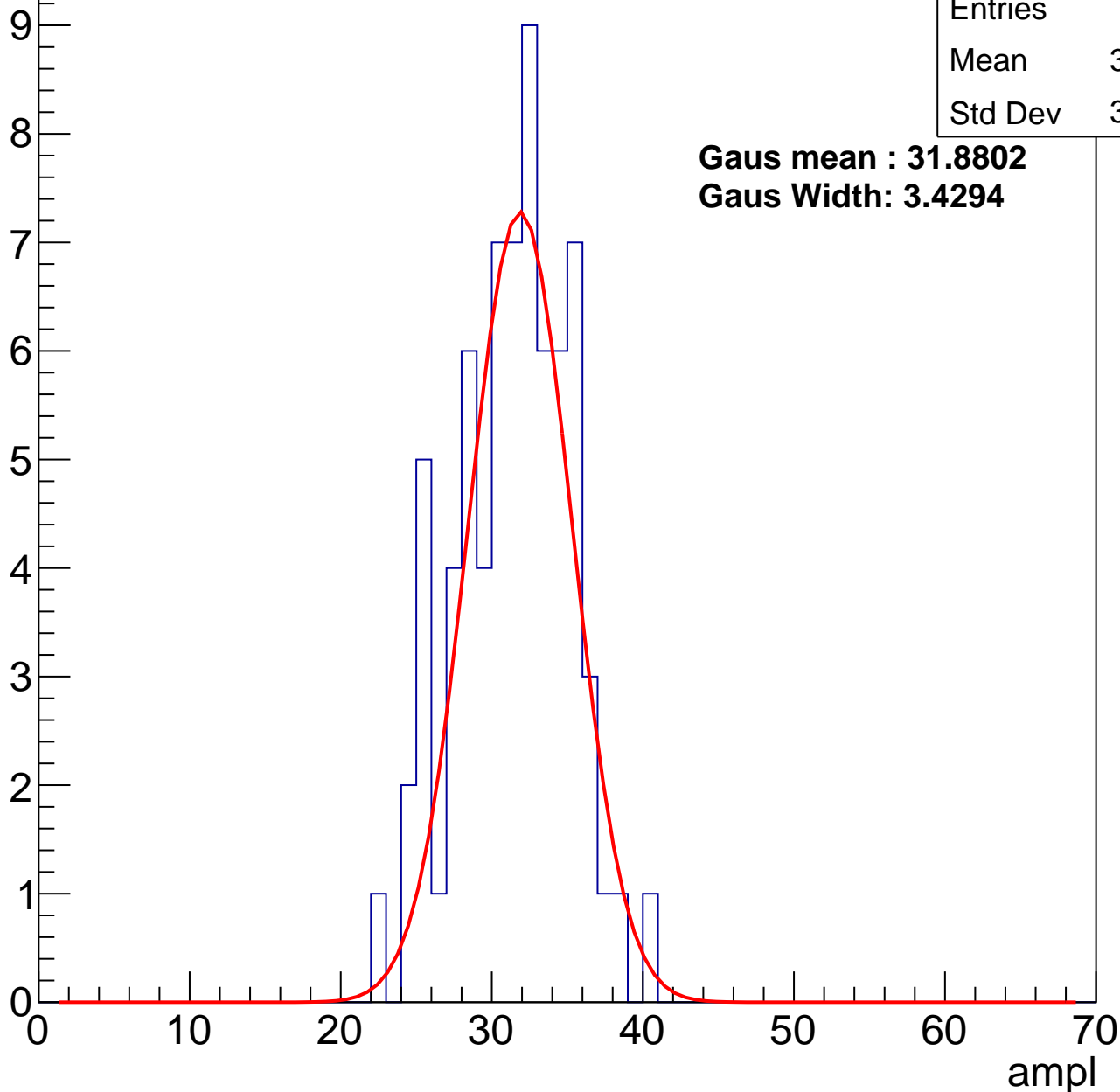
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.96
Std Dev	3.686

**Gaus mean : 31.8802**

**Gaus Width: 3.4294**



# B0L002S, U8-ch38, adc1

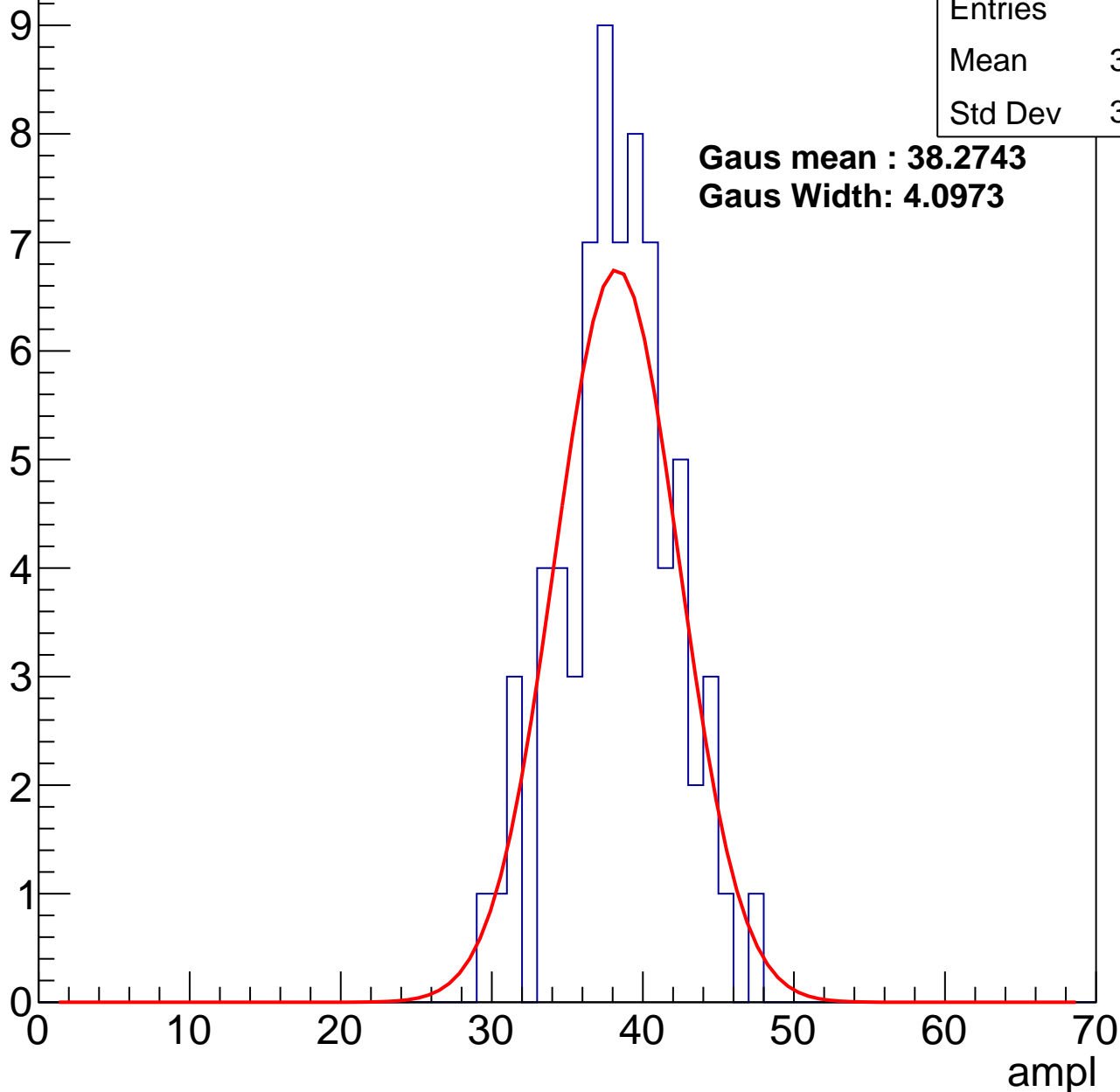
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.89
Std Dev	3.702

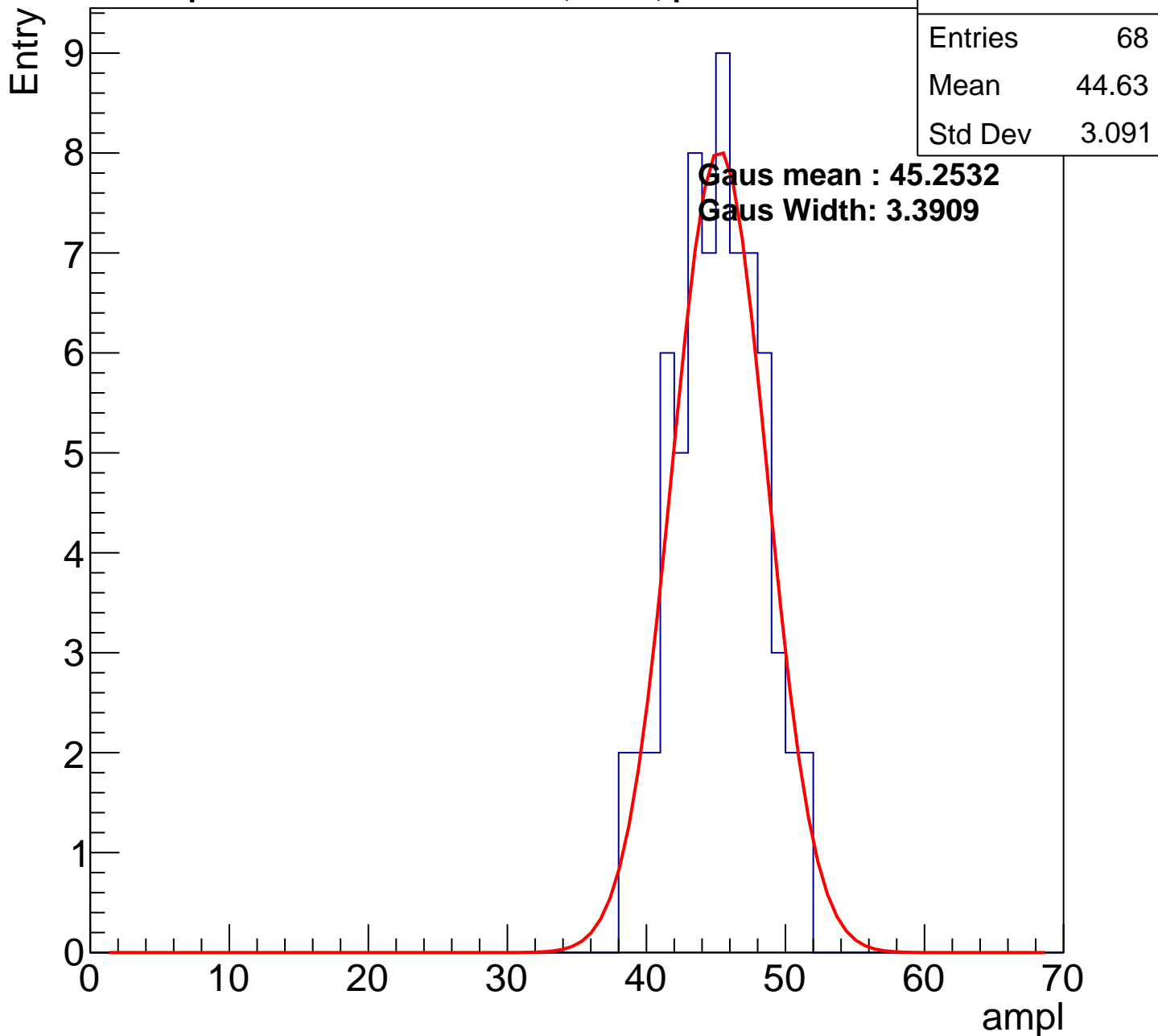
**Gaus mean : 38.2743**

**Gaus Width: 4.0973**



# B0L002S, U8-ch38, adc2

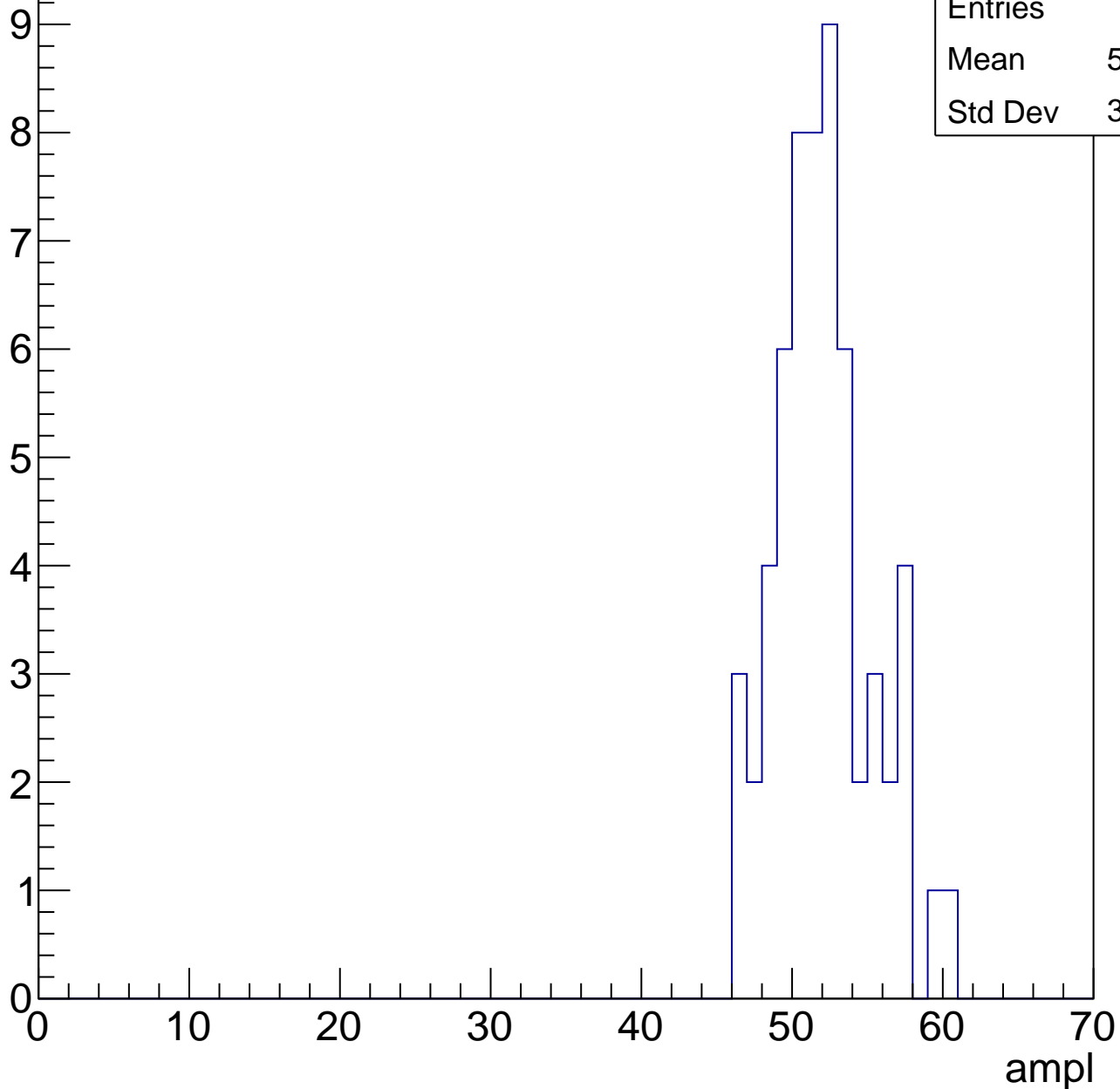
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch38, adc3

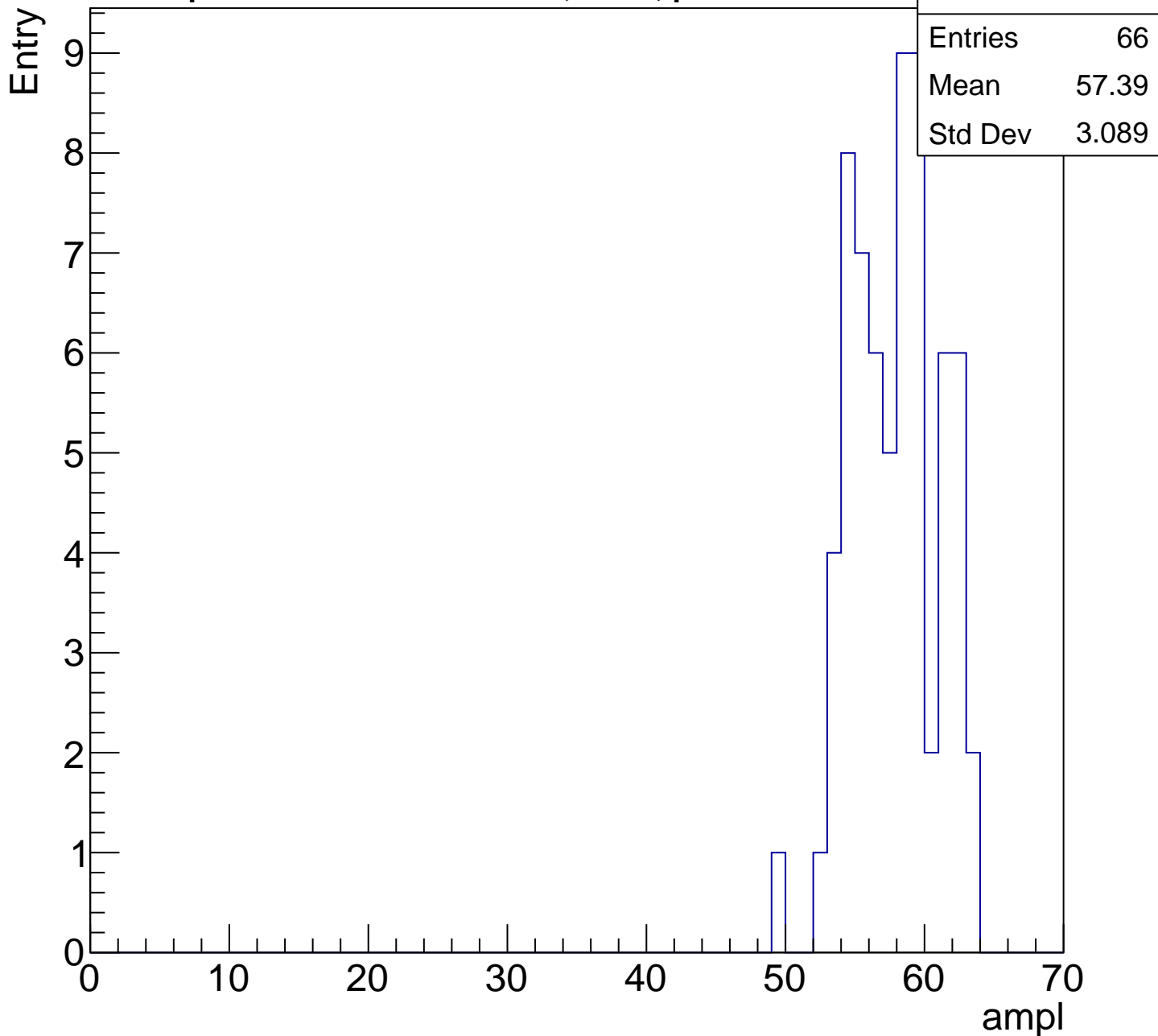
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

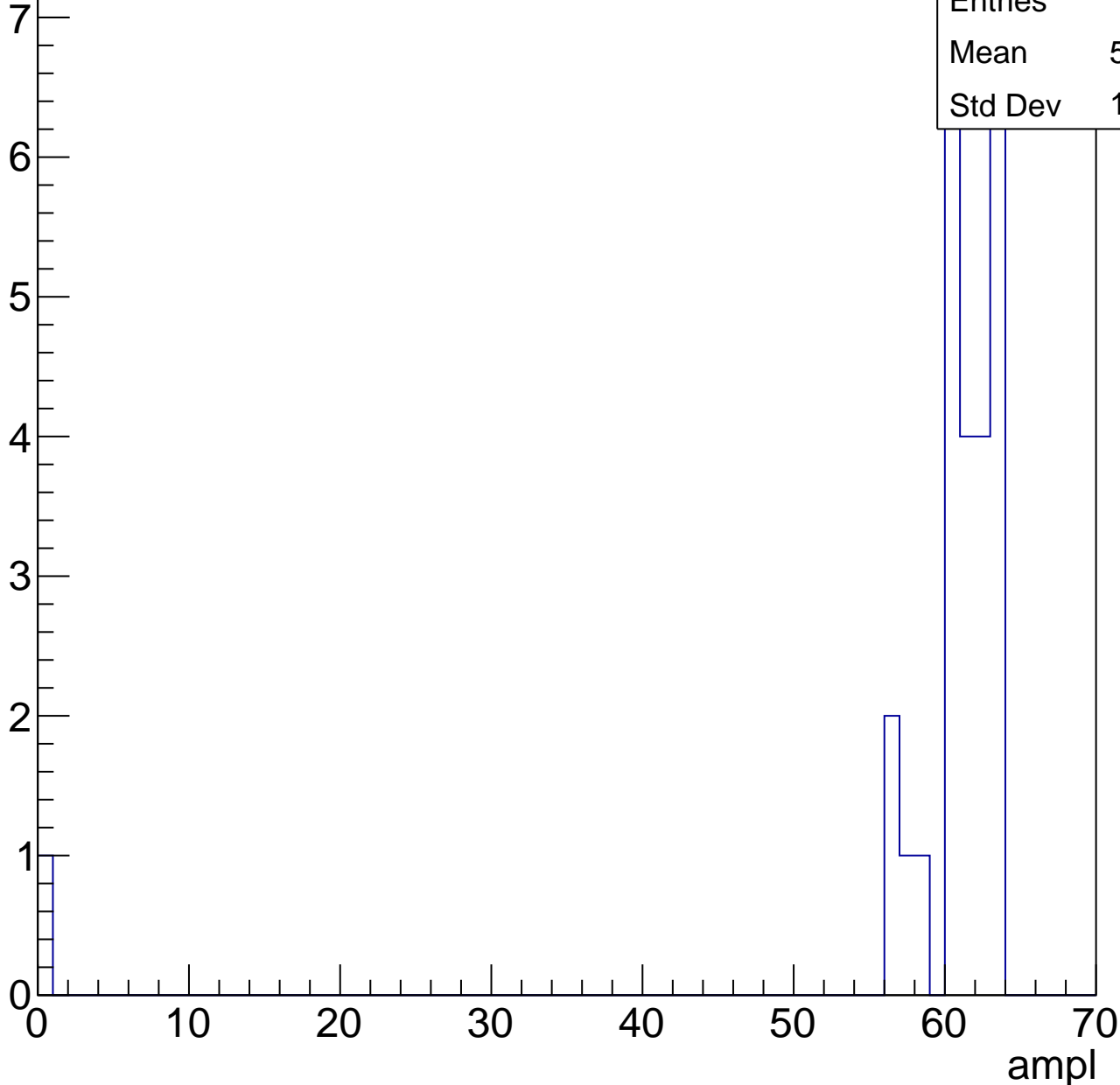


# B0L002S, U8-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.52
Std Dev	11.66



# B0L002S, U8-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L002S, U8-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	29.82
Std Dev	3.088

**Gaus mean : 30.2766**

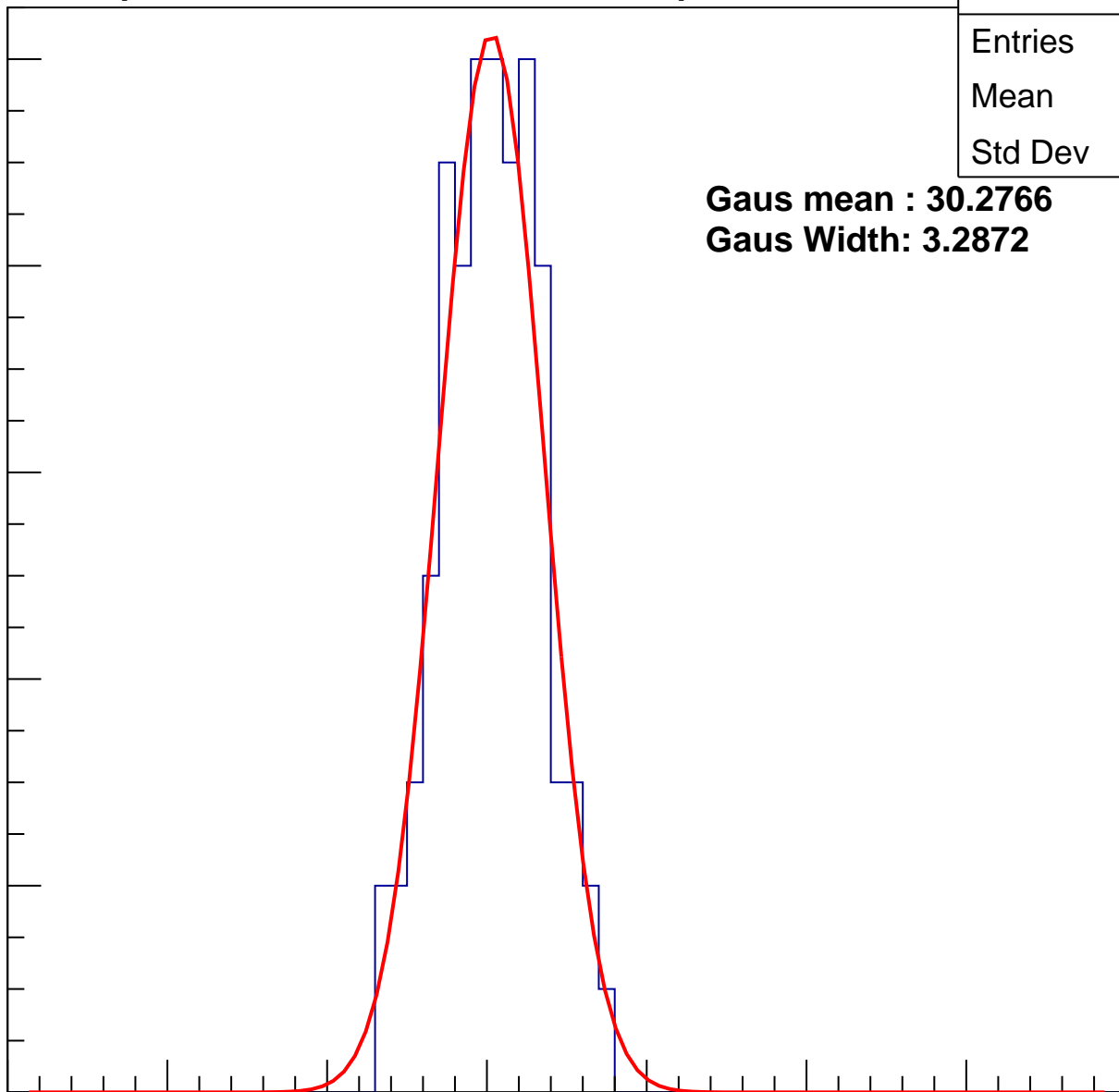
**Gaus Width: 3.2872**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



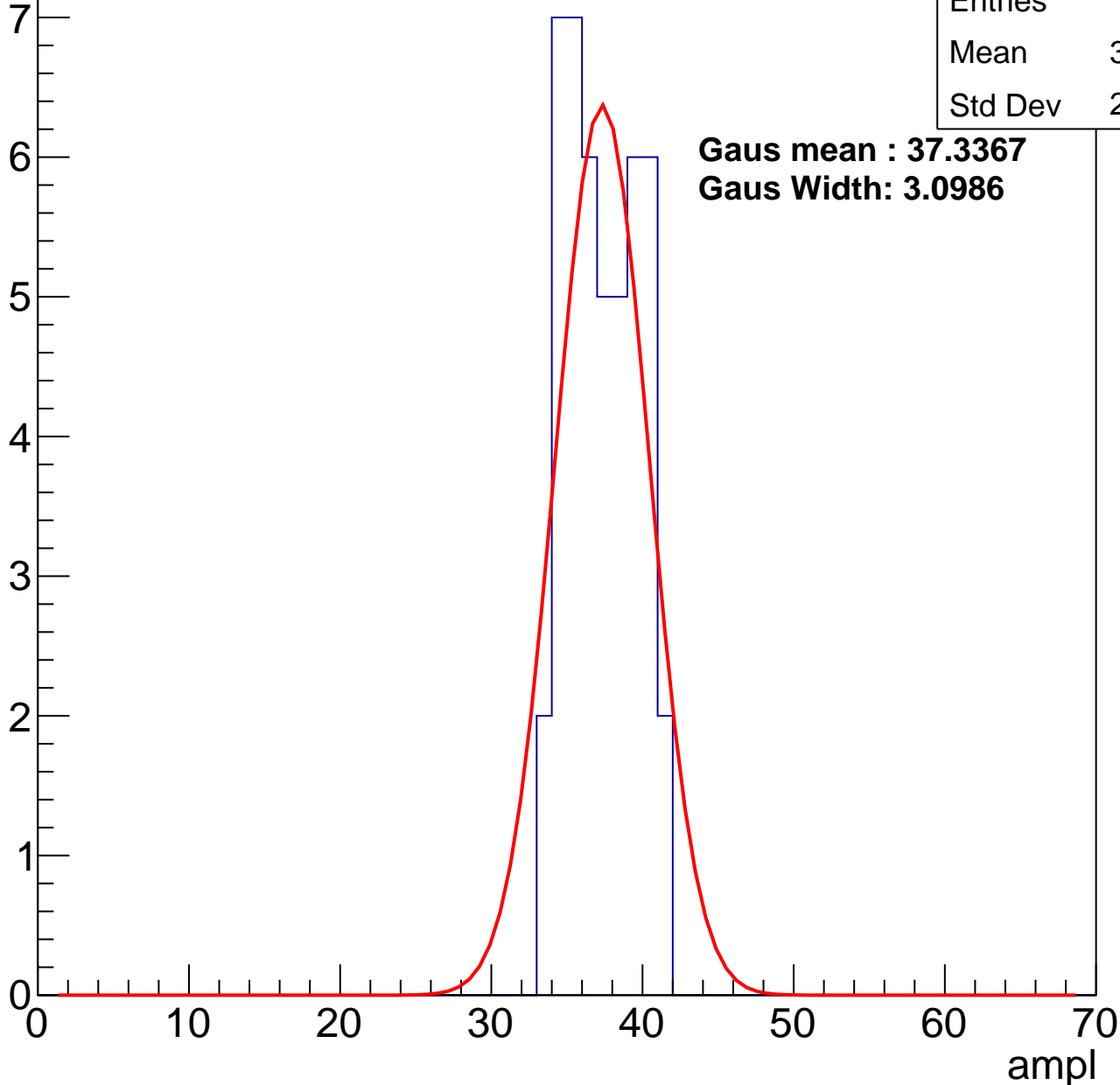
# B0L002S, U8-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	36.87
Std Dev	2.299

**Gaus mean : 37.3367**  
**Gaus Width: 3.0986**



# B0L002S, U8-ch39, adc2

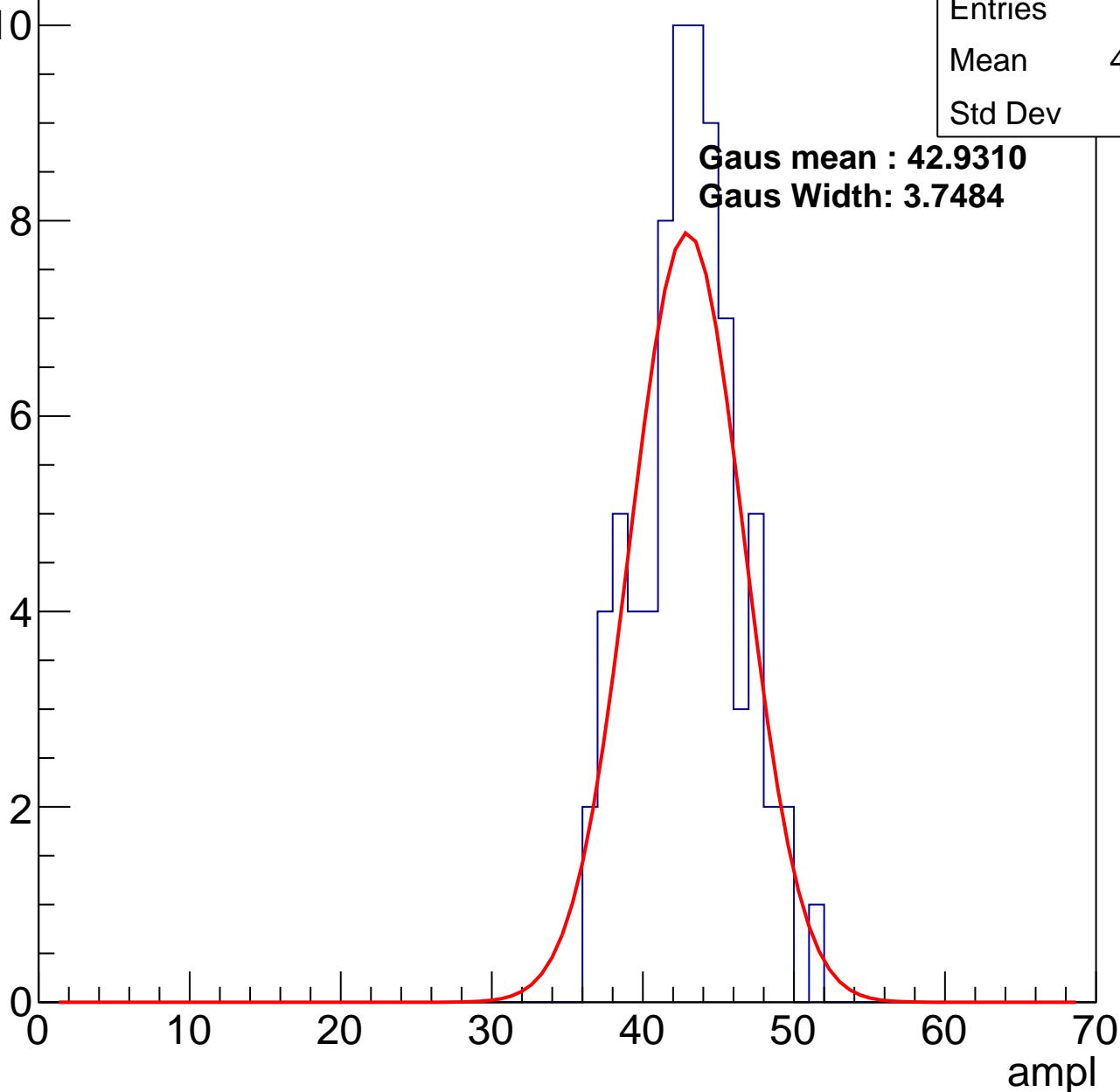
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	42.54
Std Dev	3.29

**Gaus mean : 42.9310**

**Gaus Width: 3.7484**

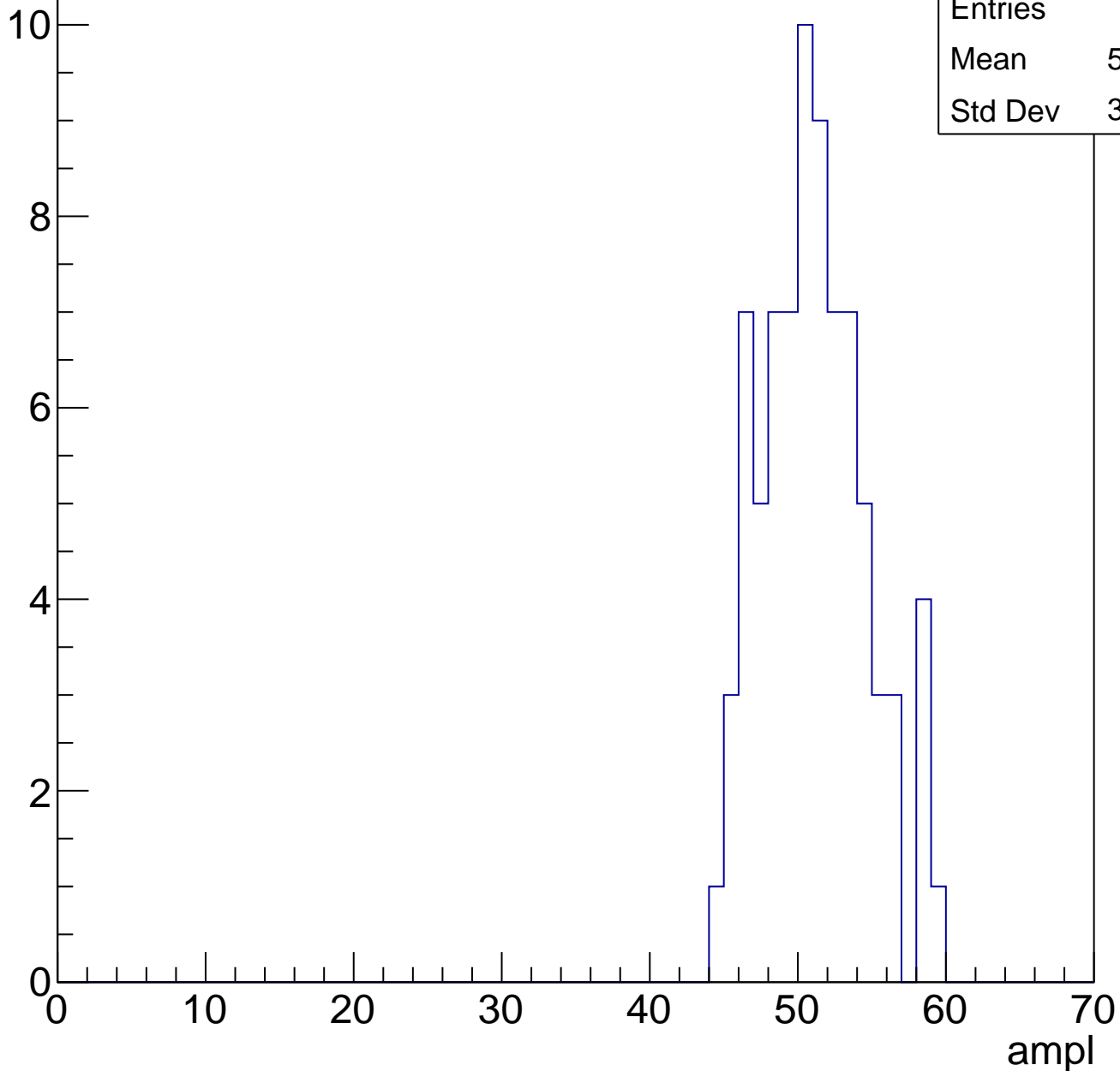


# B0L002S, U8-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	50.67
Std Dev	3.499

Entry

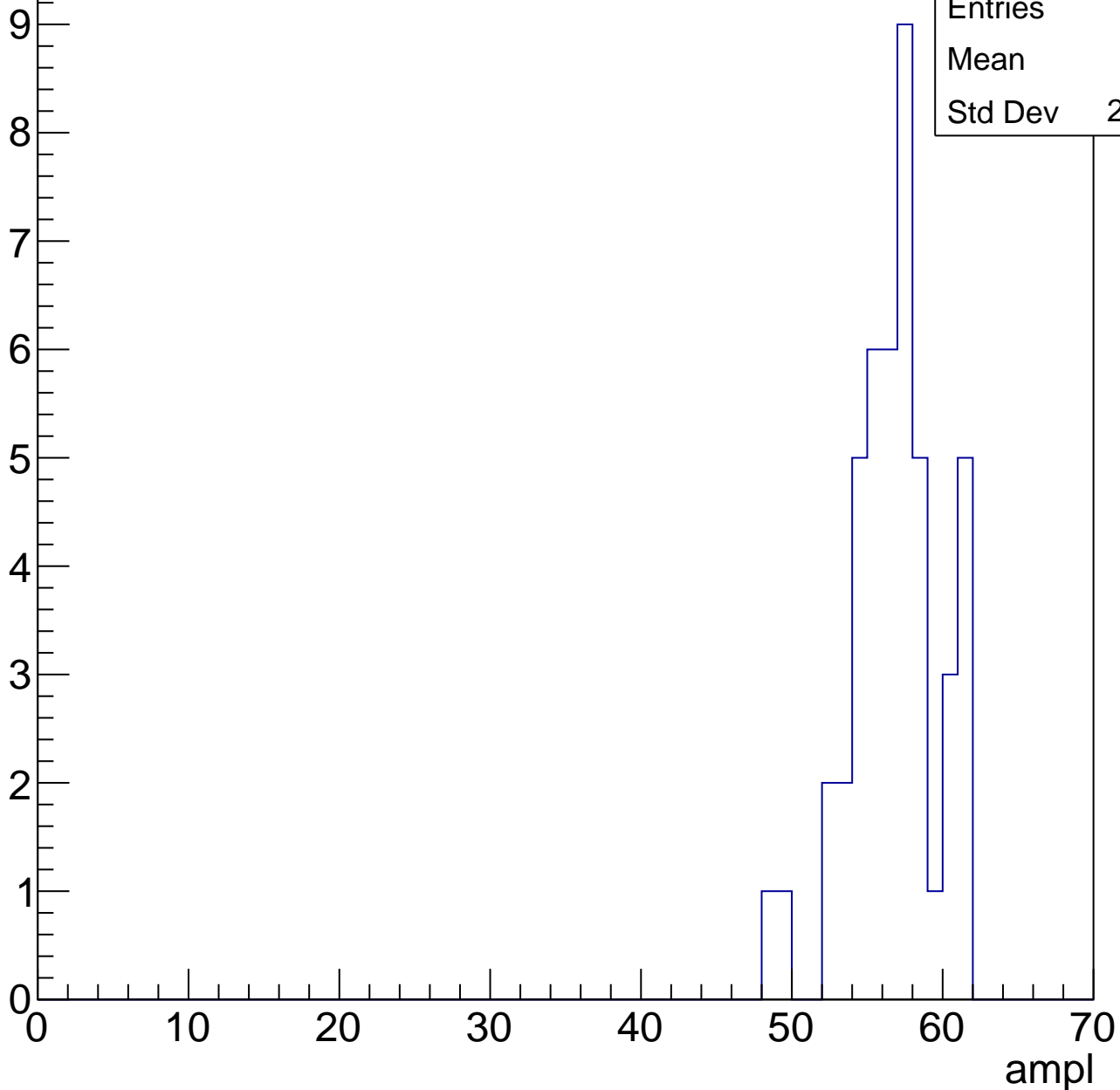


# B0L002S, U8-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	56.3
Std Dev	2.933

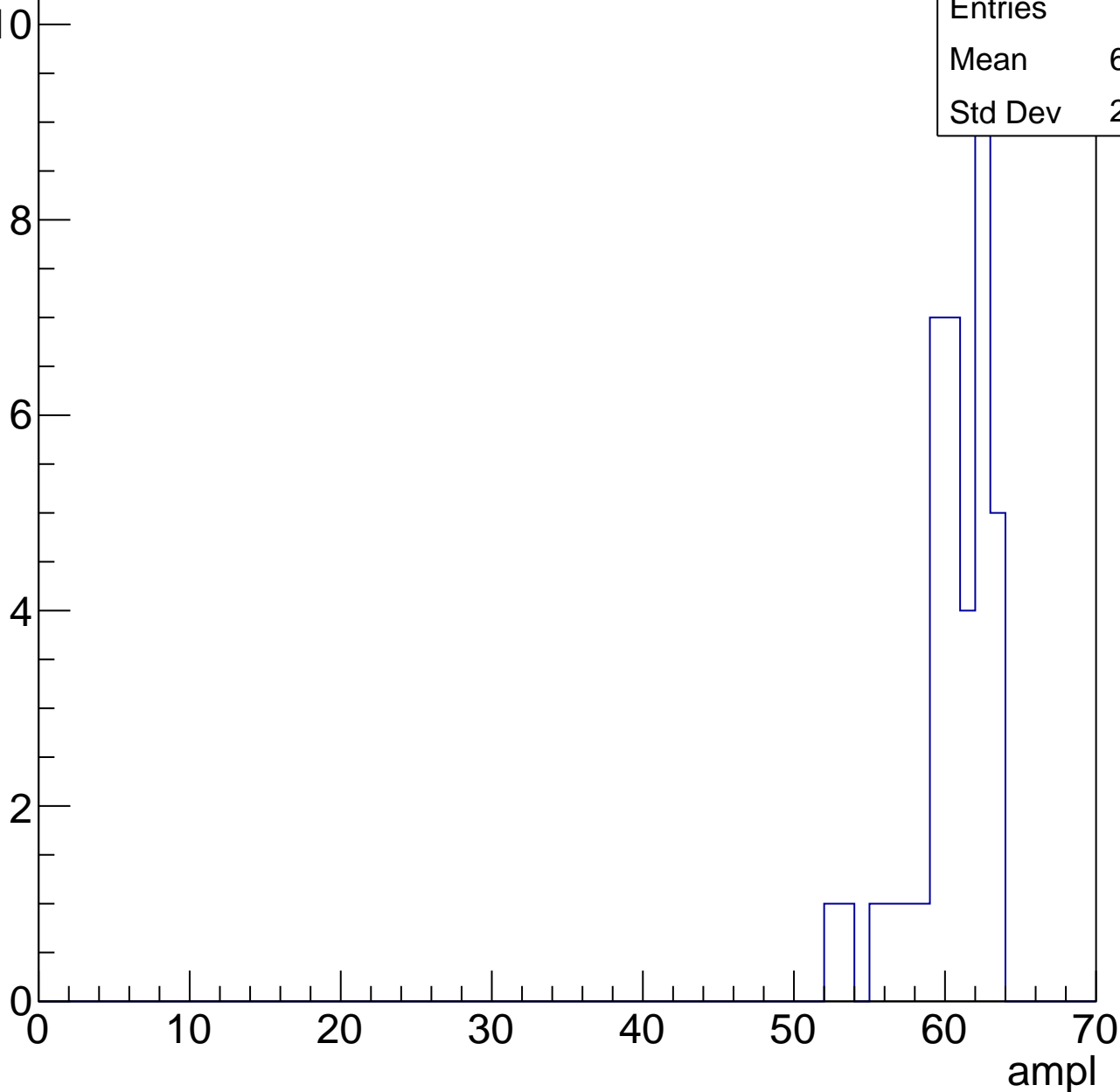


# B0L002S, U8-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	60.08
Std Dev	2.596

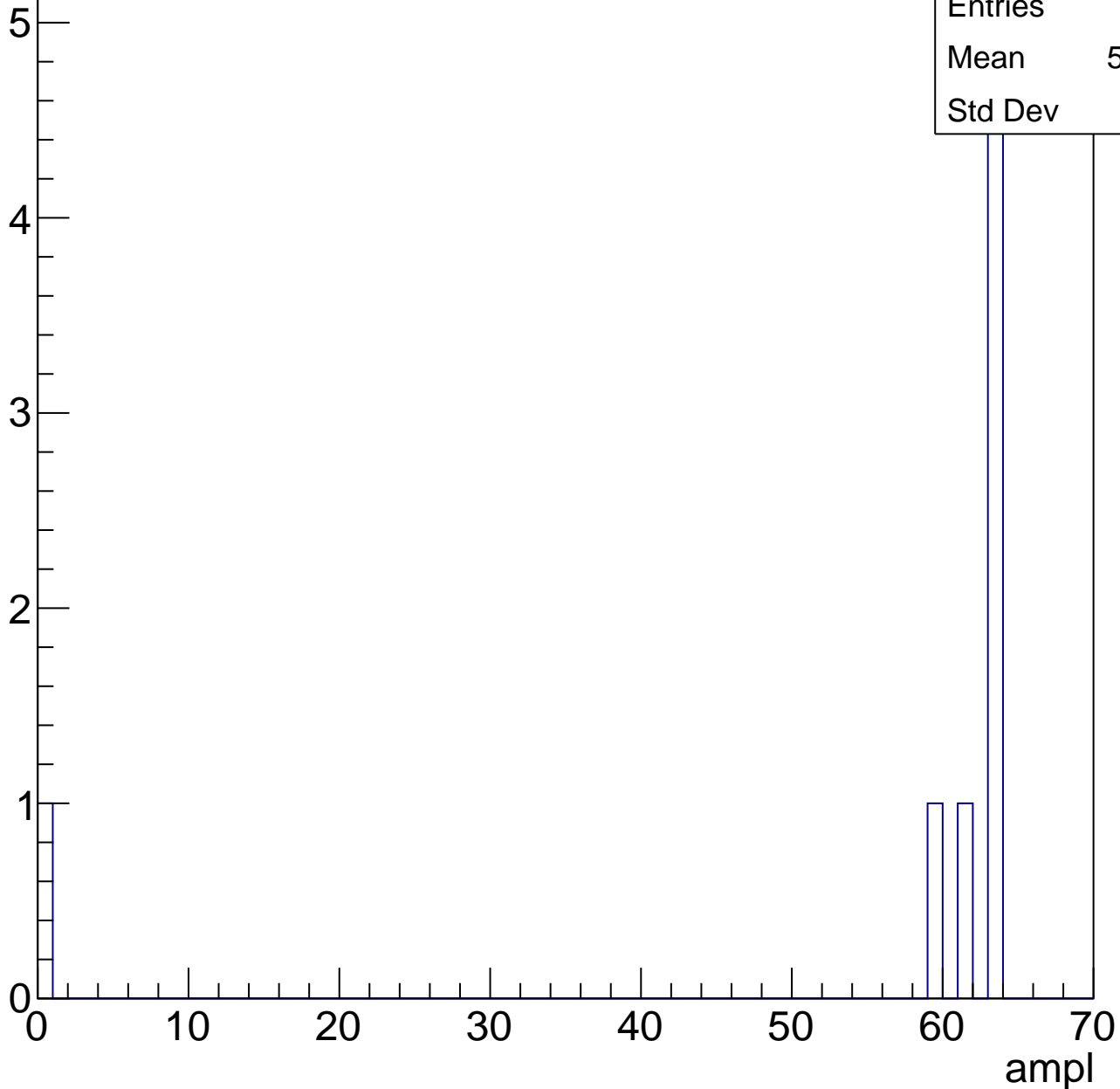


# B0L002S, U8-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	8
Mean	54.38
Std Dev	20.6





# B0L002S, U8-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch40, adc0

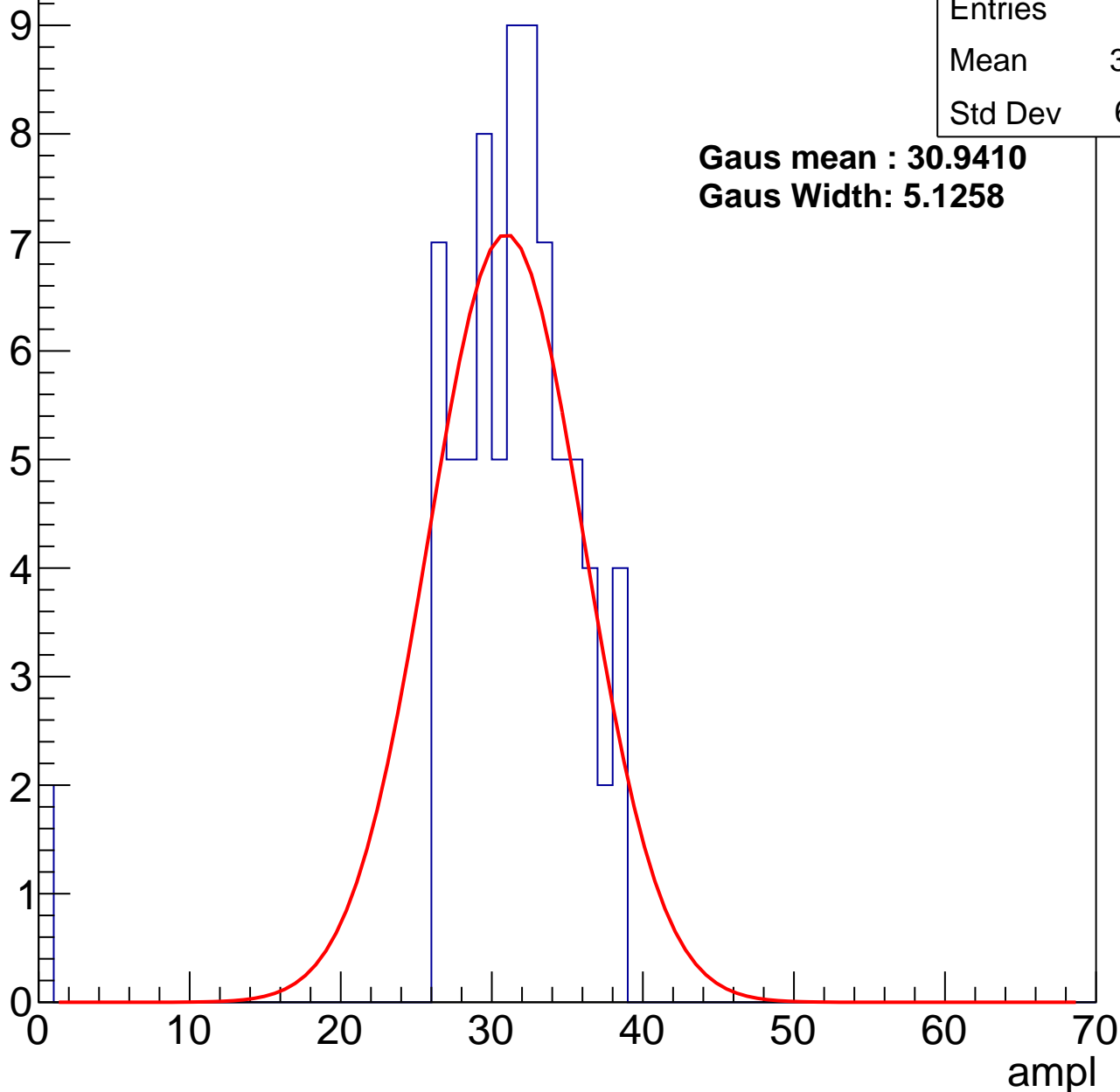
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.55
Std Dev	6.001

**Gaus mean : 30.9410**

**Gaus Width: 5.1258**



# B0L002S, U8-ch40, adc1

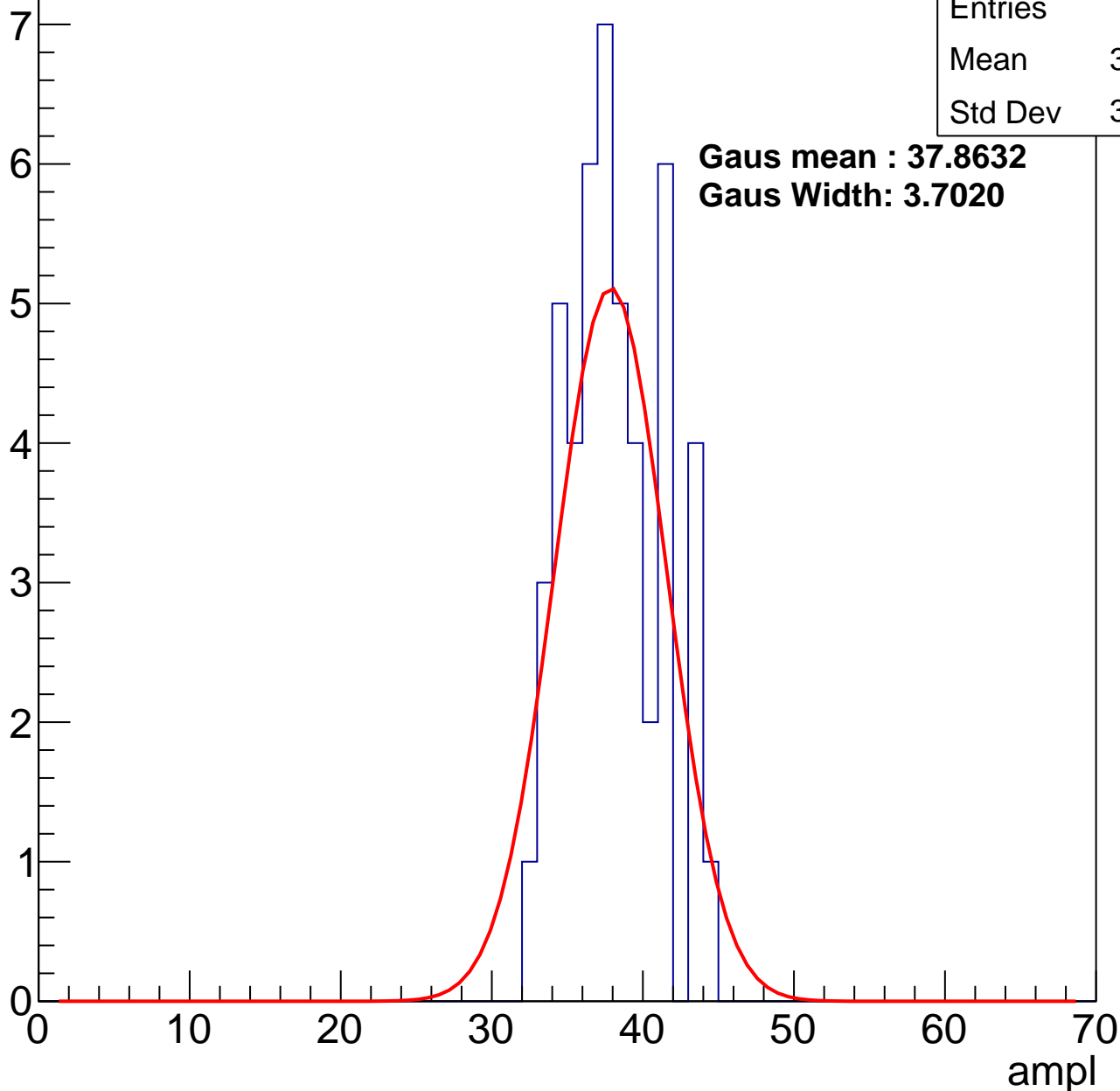
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	37.58
Std Dev	3.068

**Gaus mean : 37.8632**

**Gaus Width: 3.7020**



# B0L002S, U8-ch40, adc2

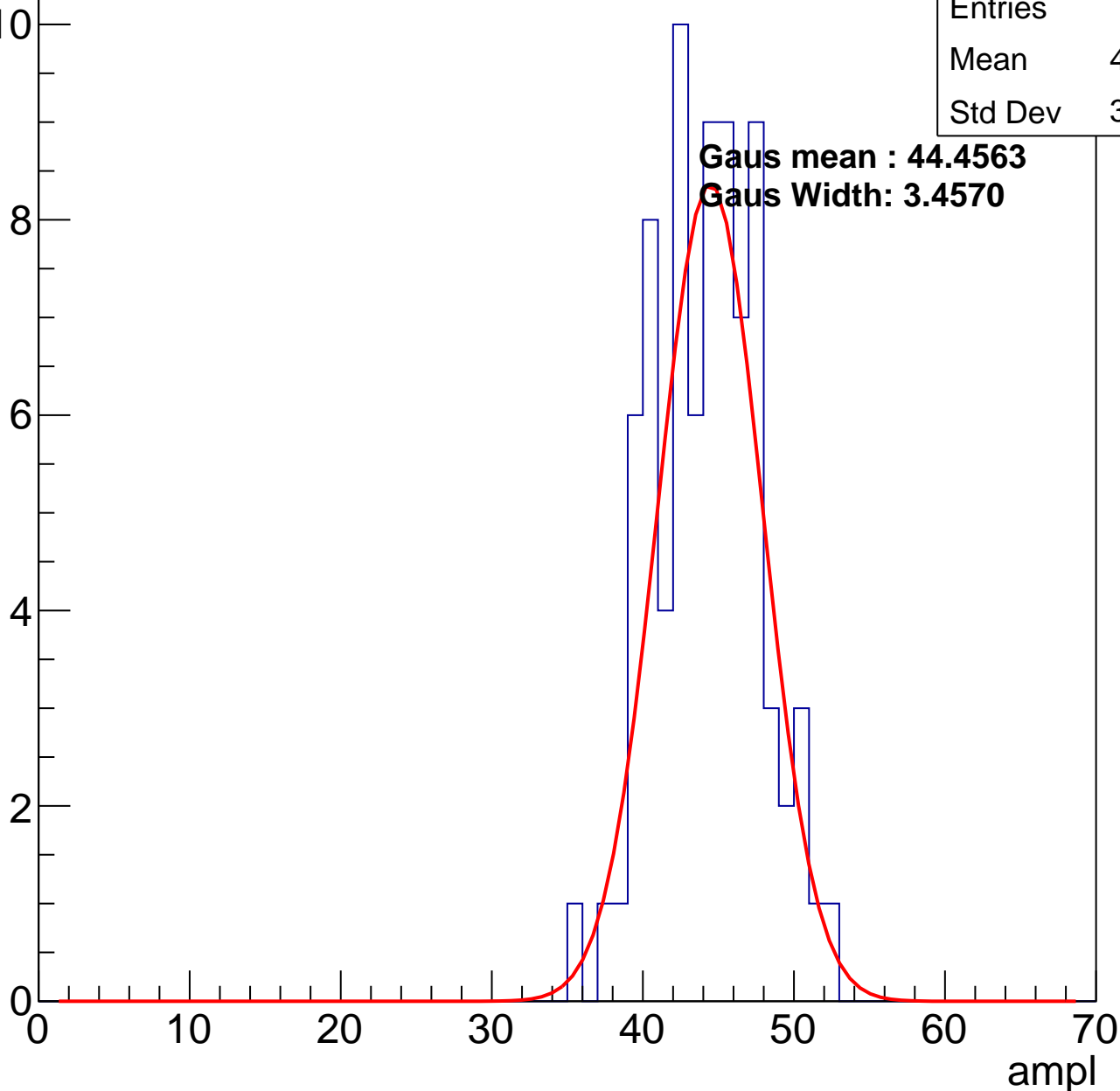
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	43.79
Std Dev	3.442

**Gaus mean : 44.4563**

**Gaus Width: 3.4570**

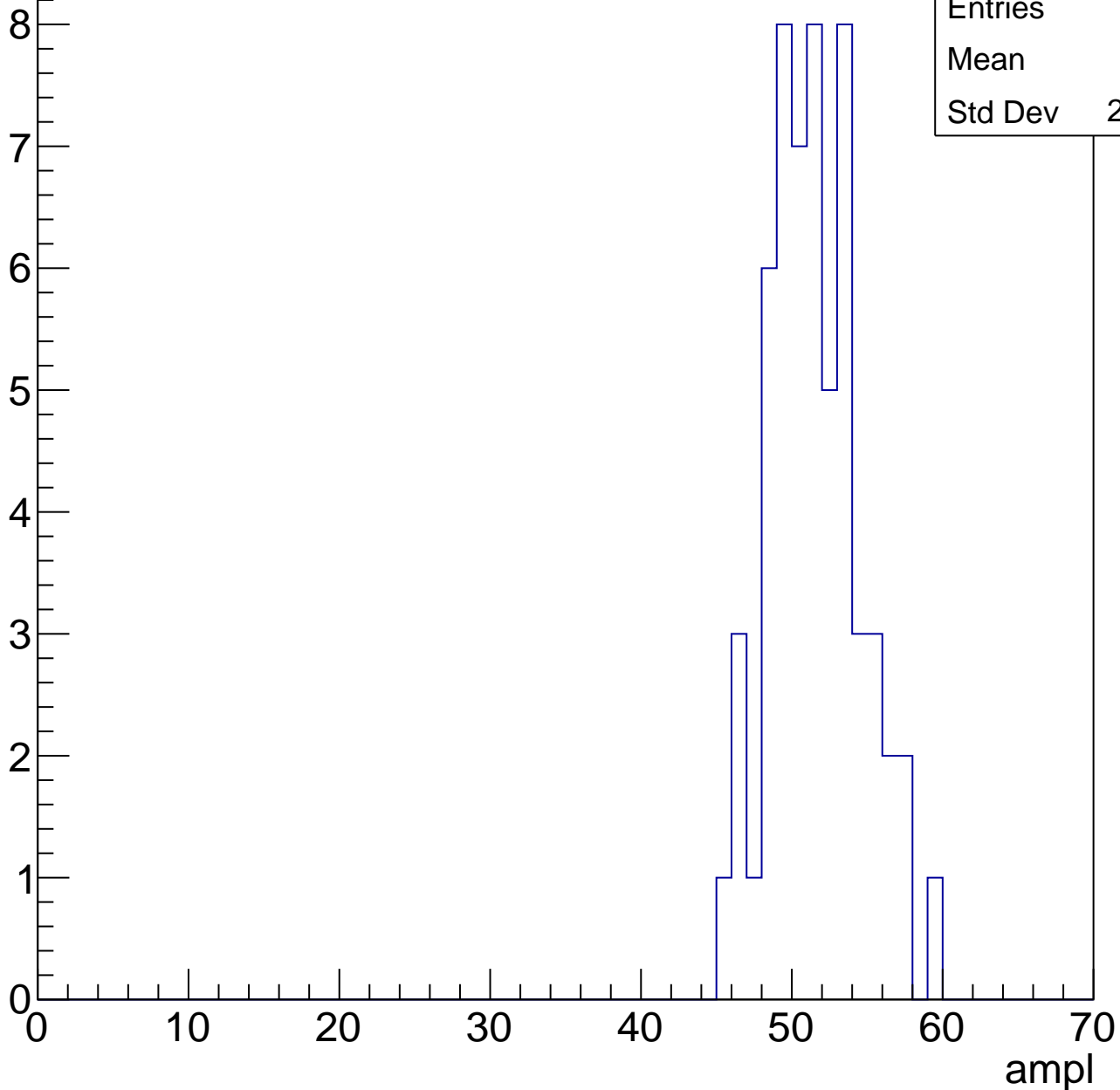


# B0L002S, U8-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

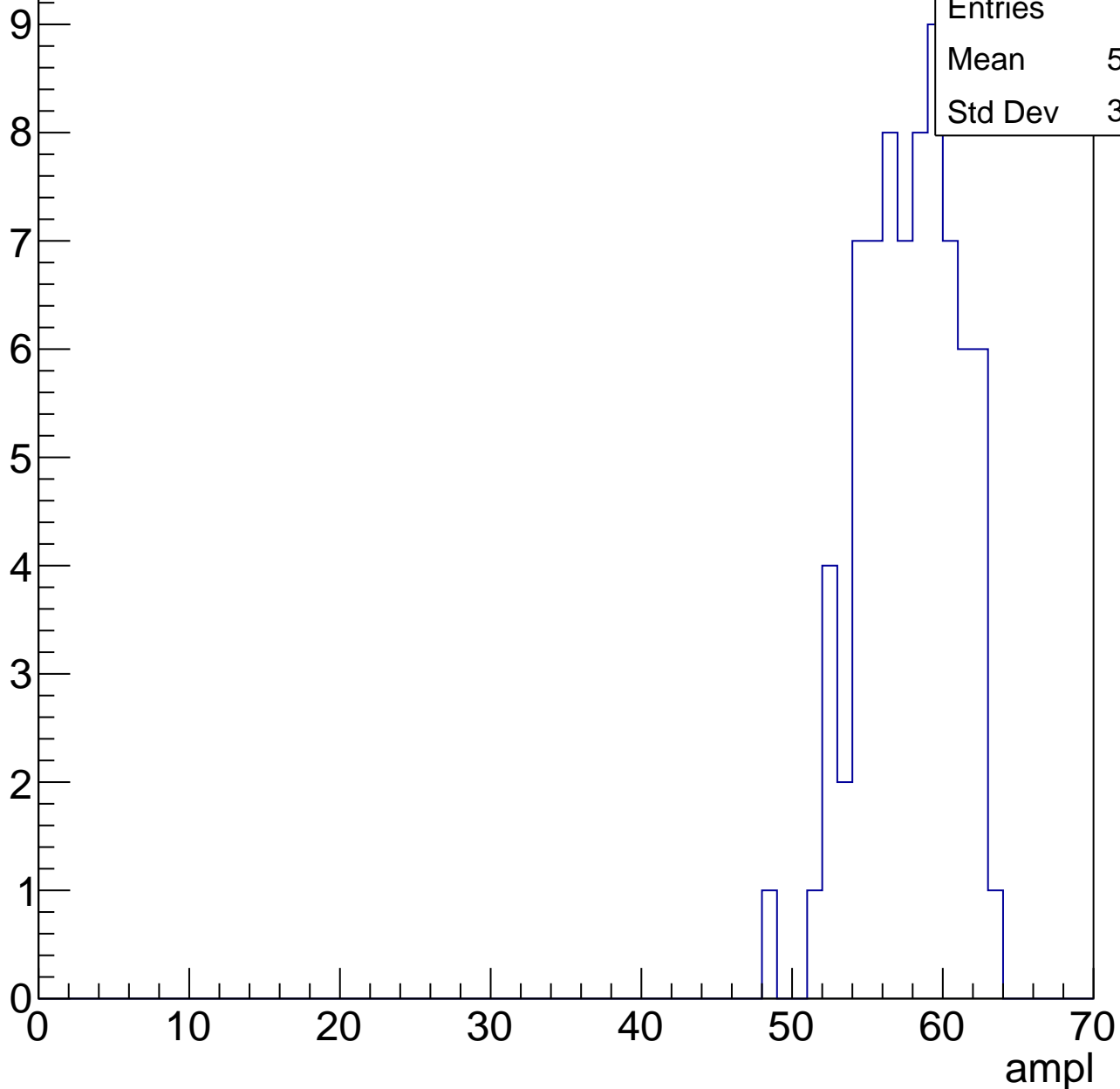
Entries	58
Mean	51.1
Std Dev	2.987



# B0L002S, U8-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



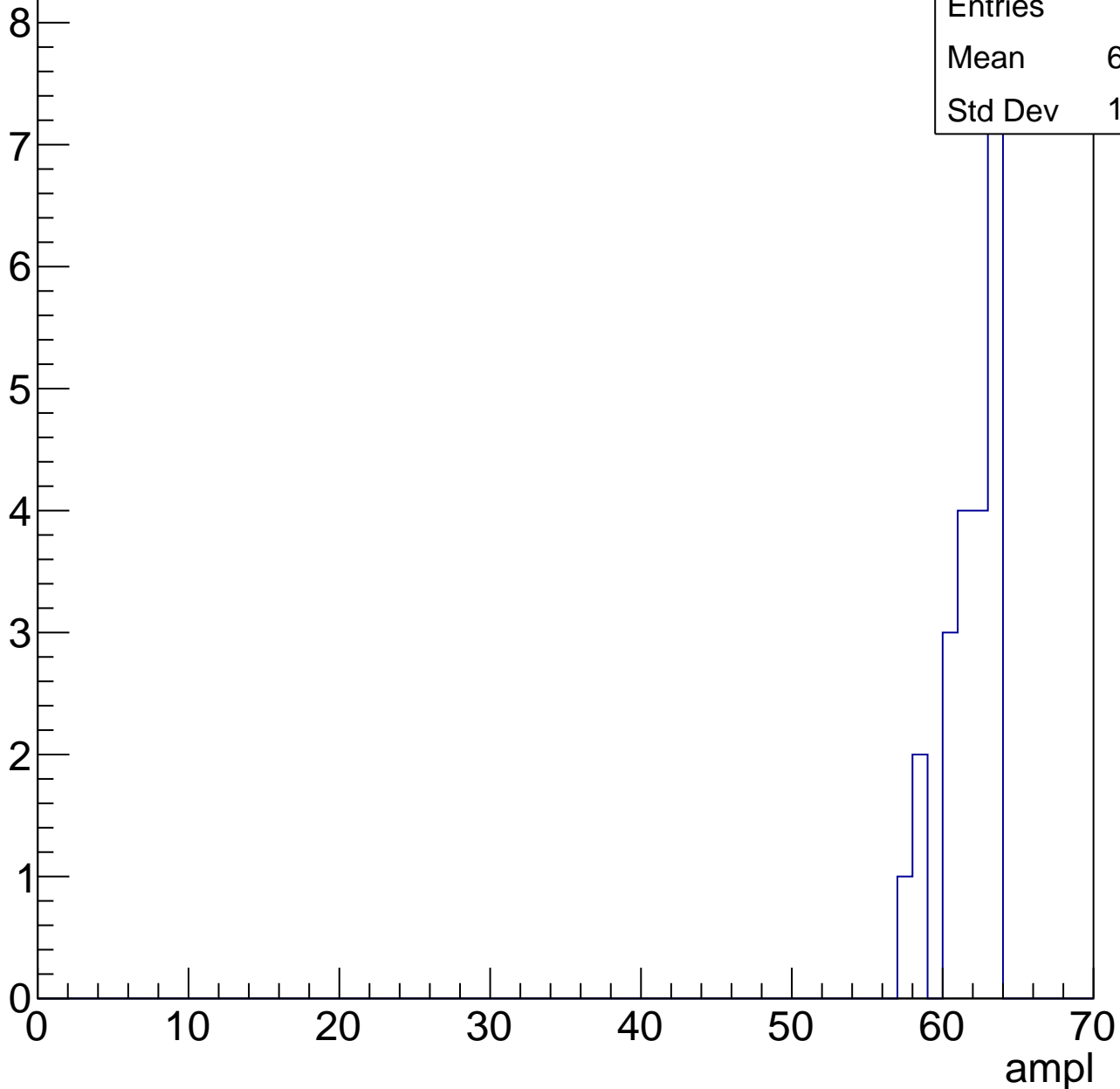
Entries	74
Mean	57.28
Std Dev	3.143

# B0L002S, U8-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61.32
Std Dev	1.794



# B0L002S, U8-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L002S, U8-ch41, adc0

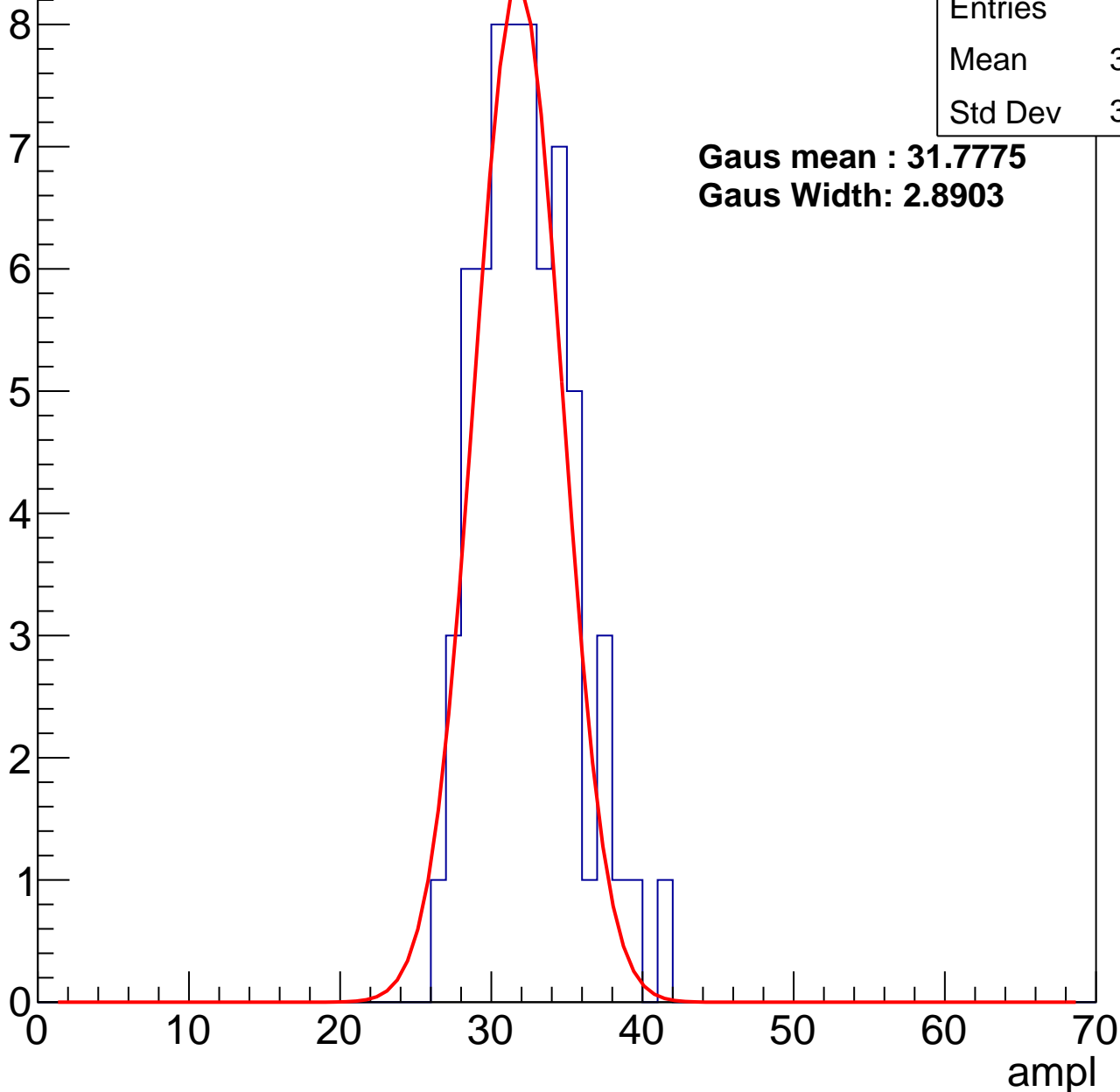
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.83
Std Dev	3.126

**Gaus mean : 31.7775**

**Gaus Width: 2.8903**



# B0L002S, U8-ch41, adc1

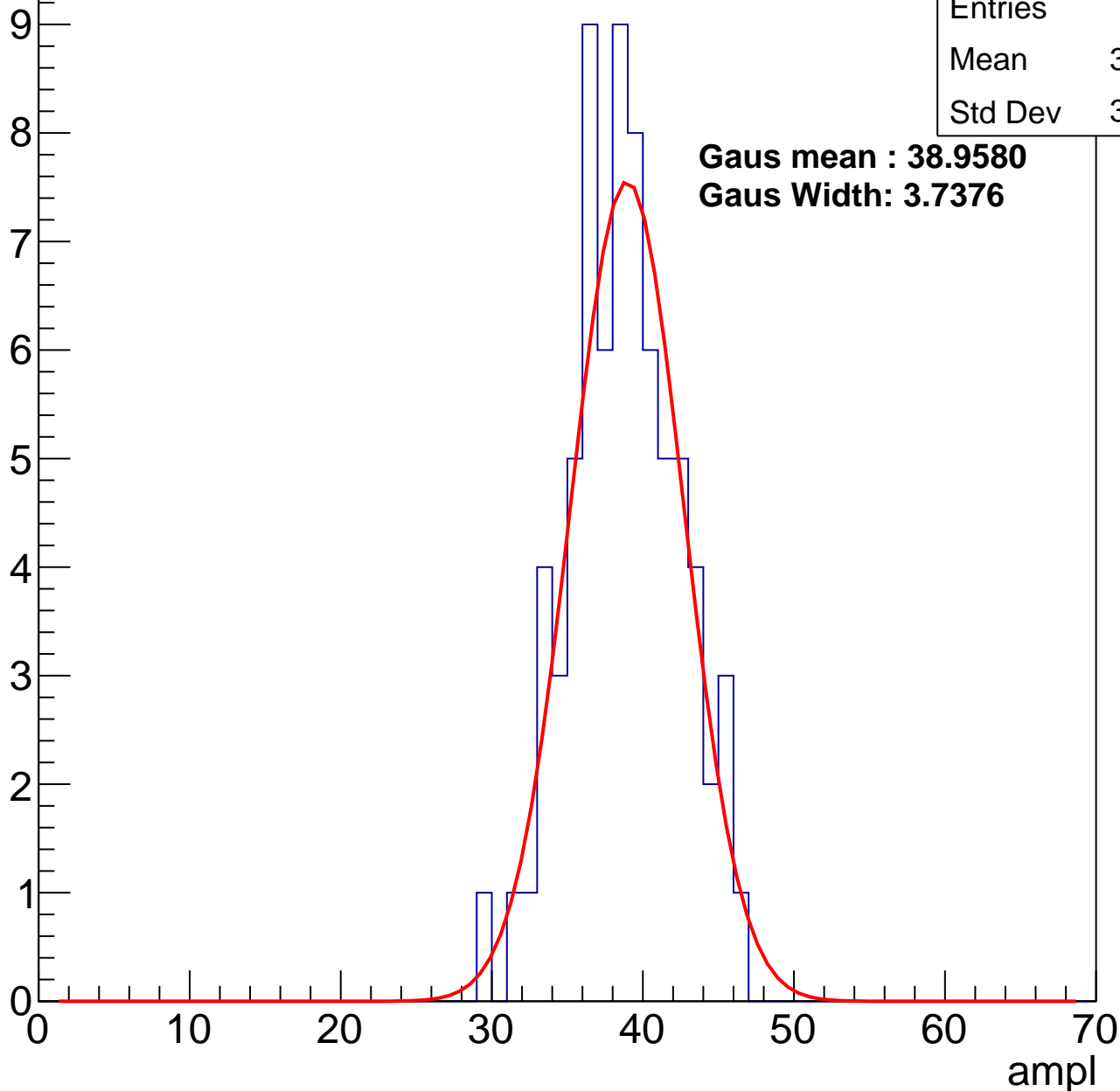
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	38.32
Std Dev	3.576

**Gaus mean : 38.9580**

**Gaus Width: 3.7376**



# B0L002S, U8-ch41, adc2

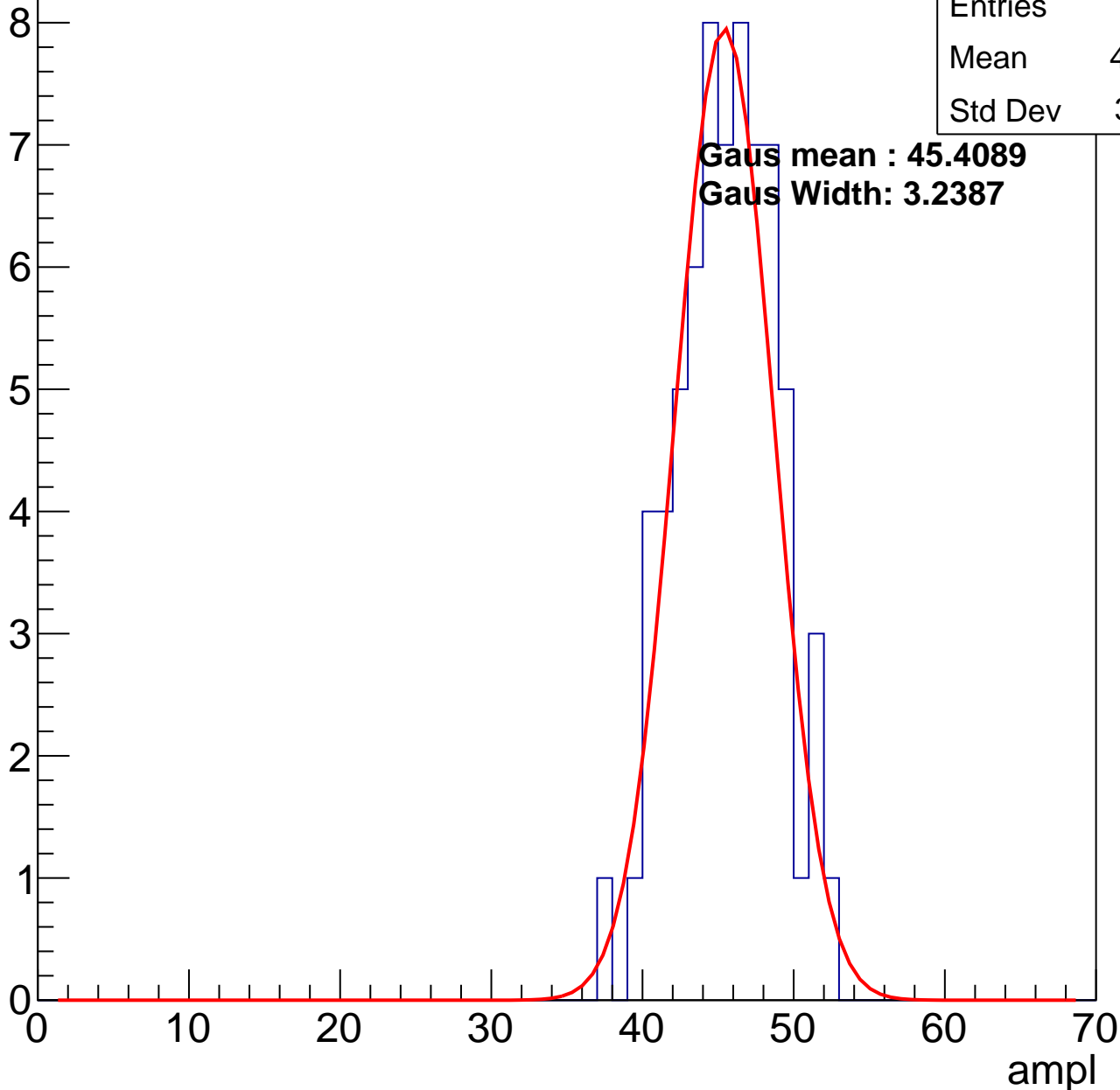
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.12
Std Dev	3.211

**Gaus mean : 45.4089**

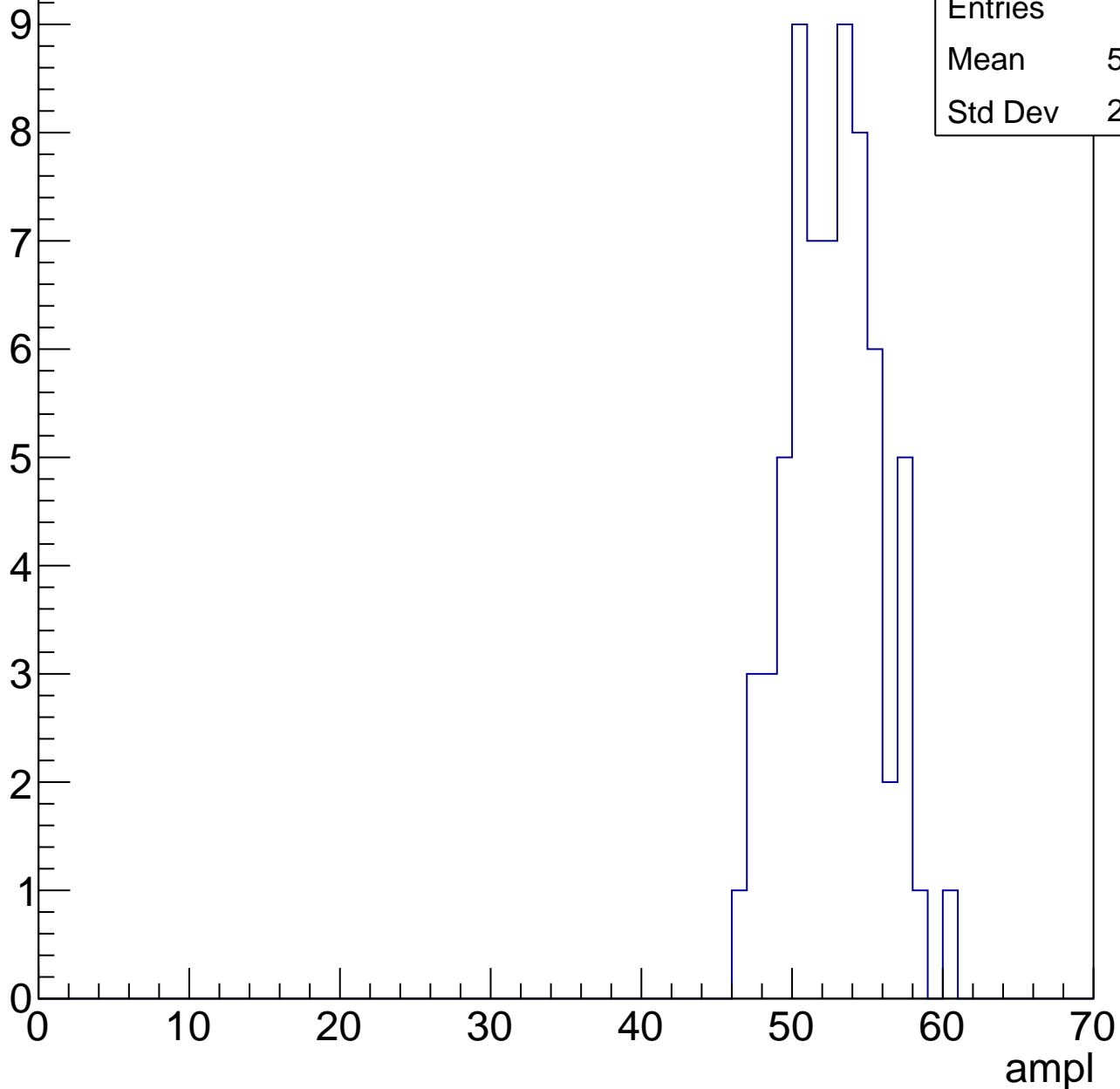
**Gaus Width: 3.2387**



# B0L002S, U8-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

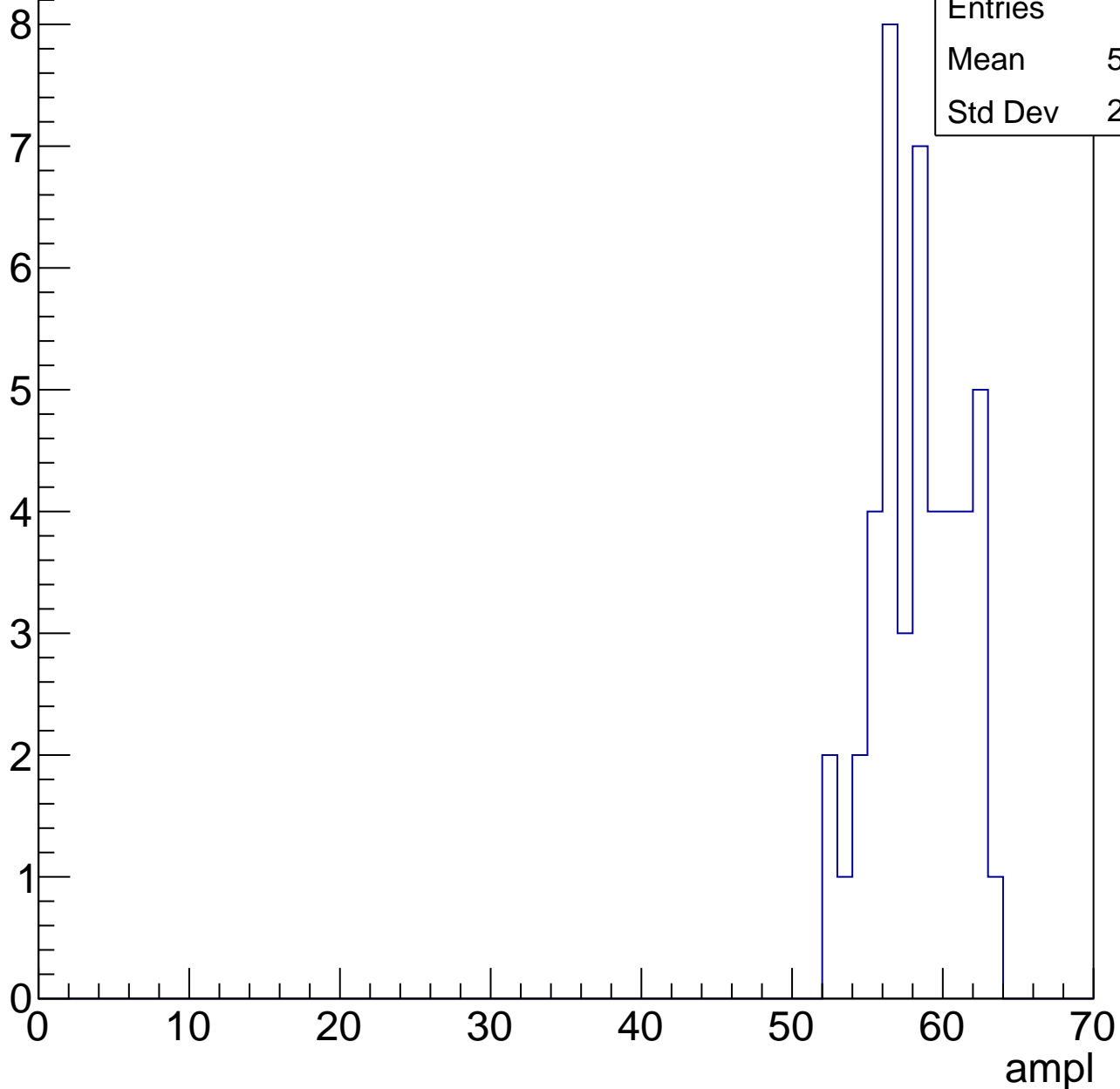


Entries	67
Mean	52.25
Std Dev	2.979

# B0L002S, U8-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

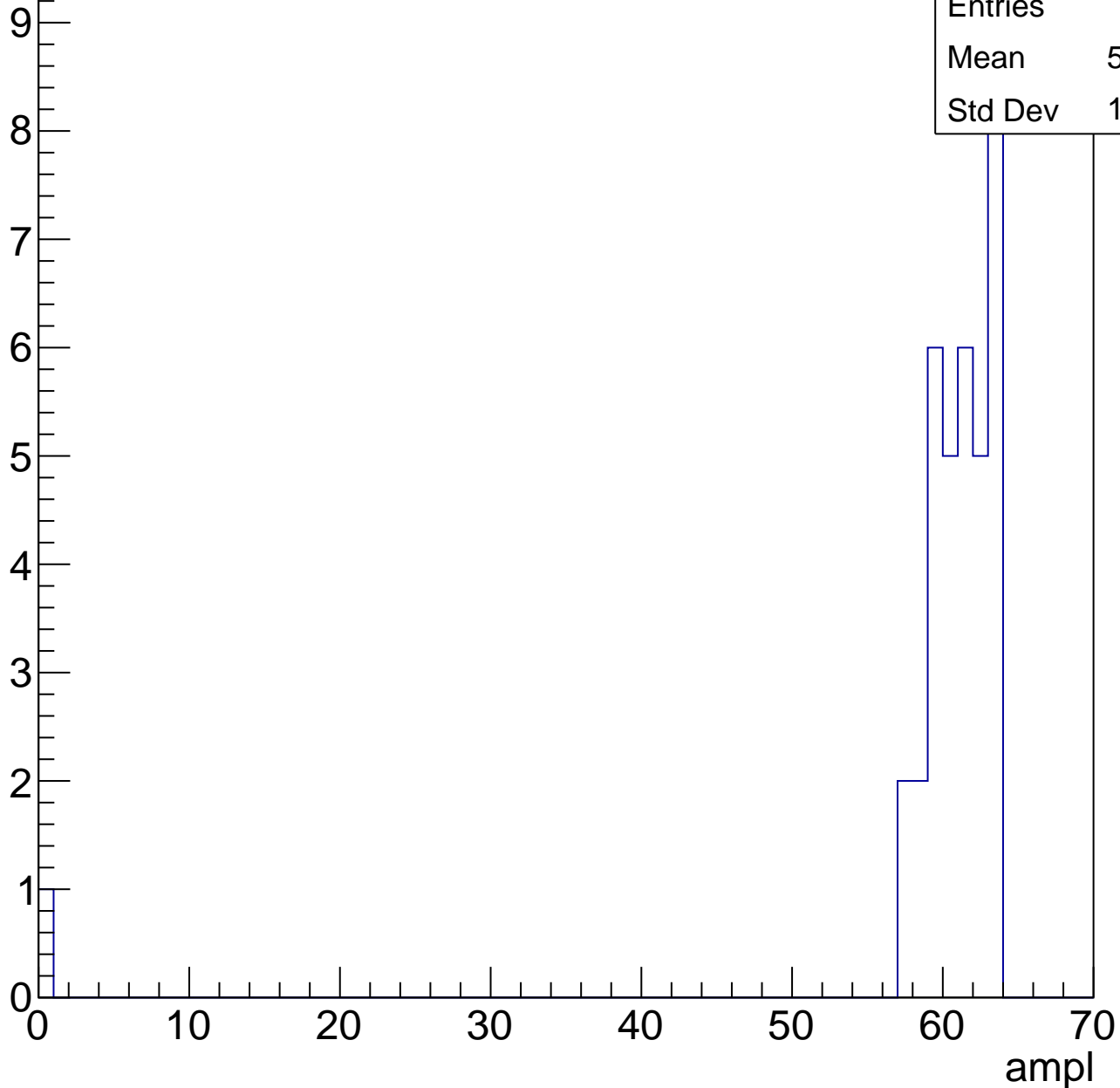
Entry



# B0L002S, U8-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch42, adc0

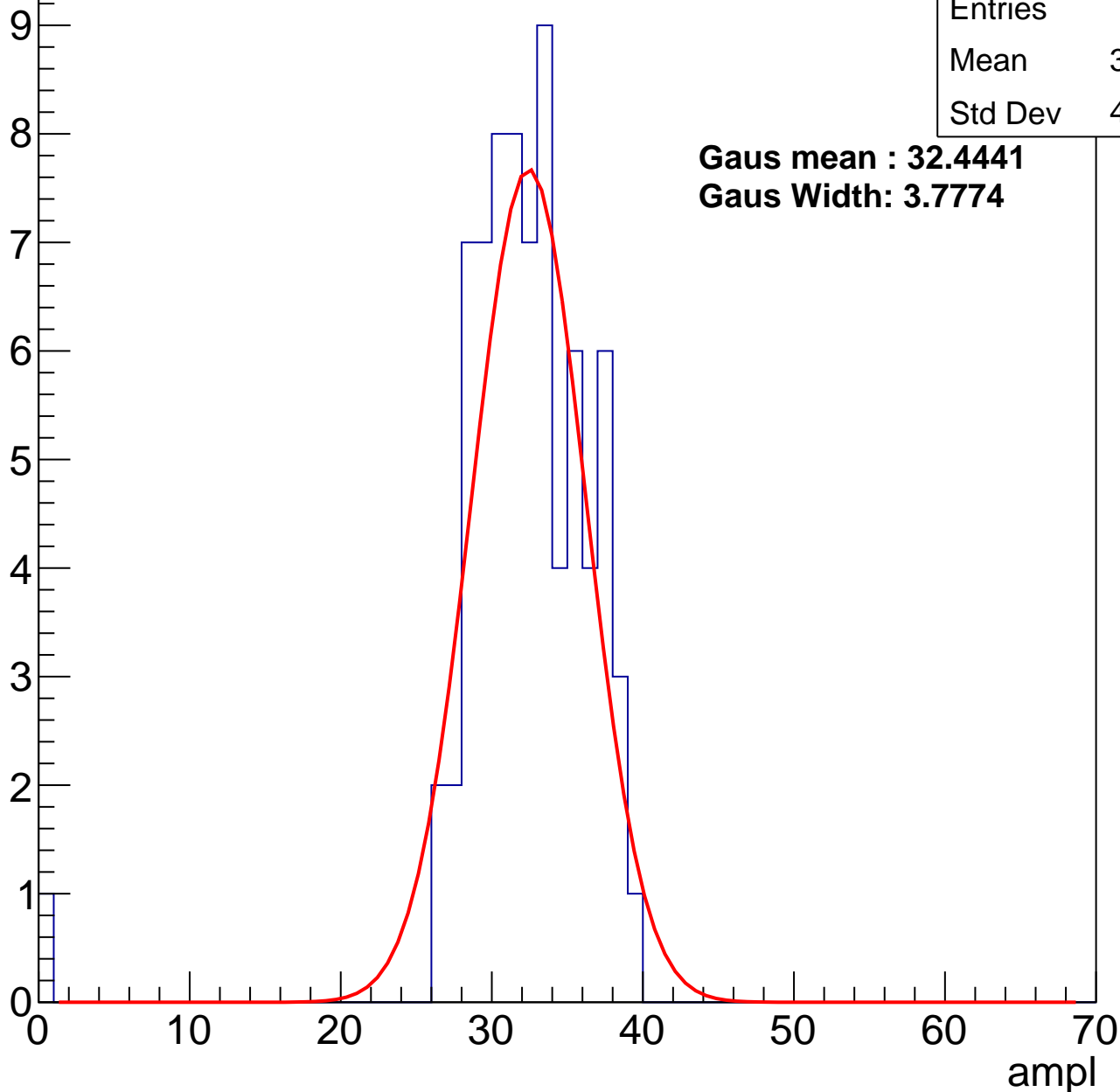
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.72
Std Dev	4.906

**Gaus mean : 32.4441**

**Gaus Width: 3.7774**



# B0L002S, U8-ch42, adc1

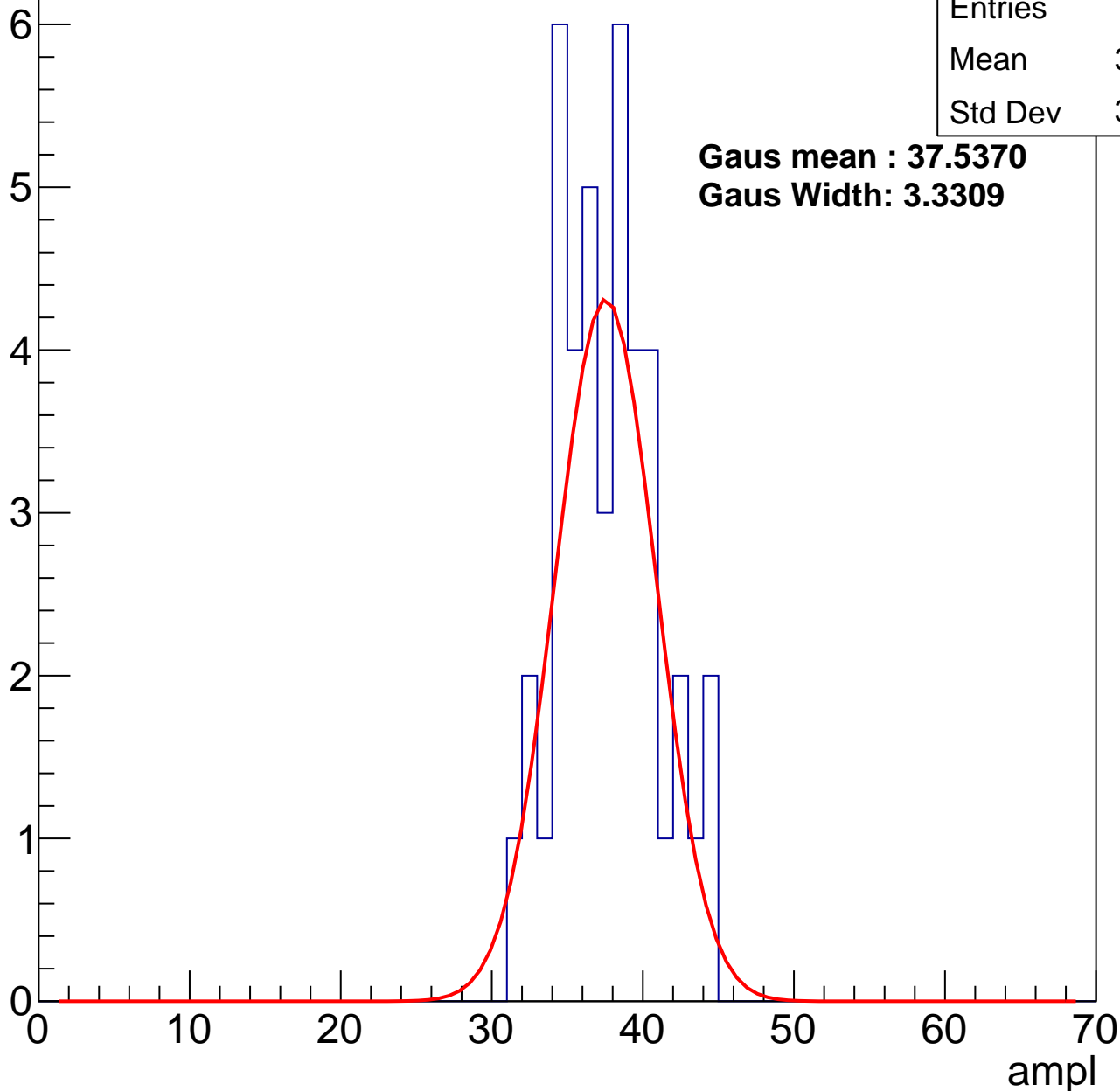
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	42
Mean	37.21
Std Dev	3.211

**Gaus mean : 37.5370**

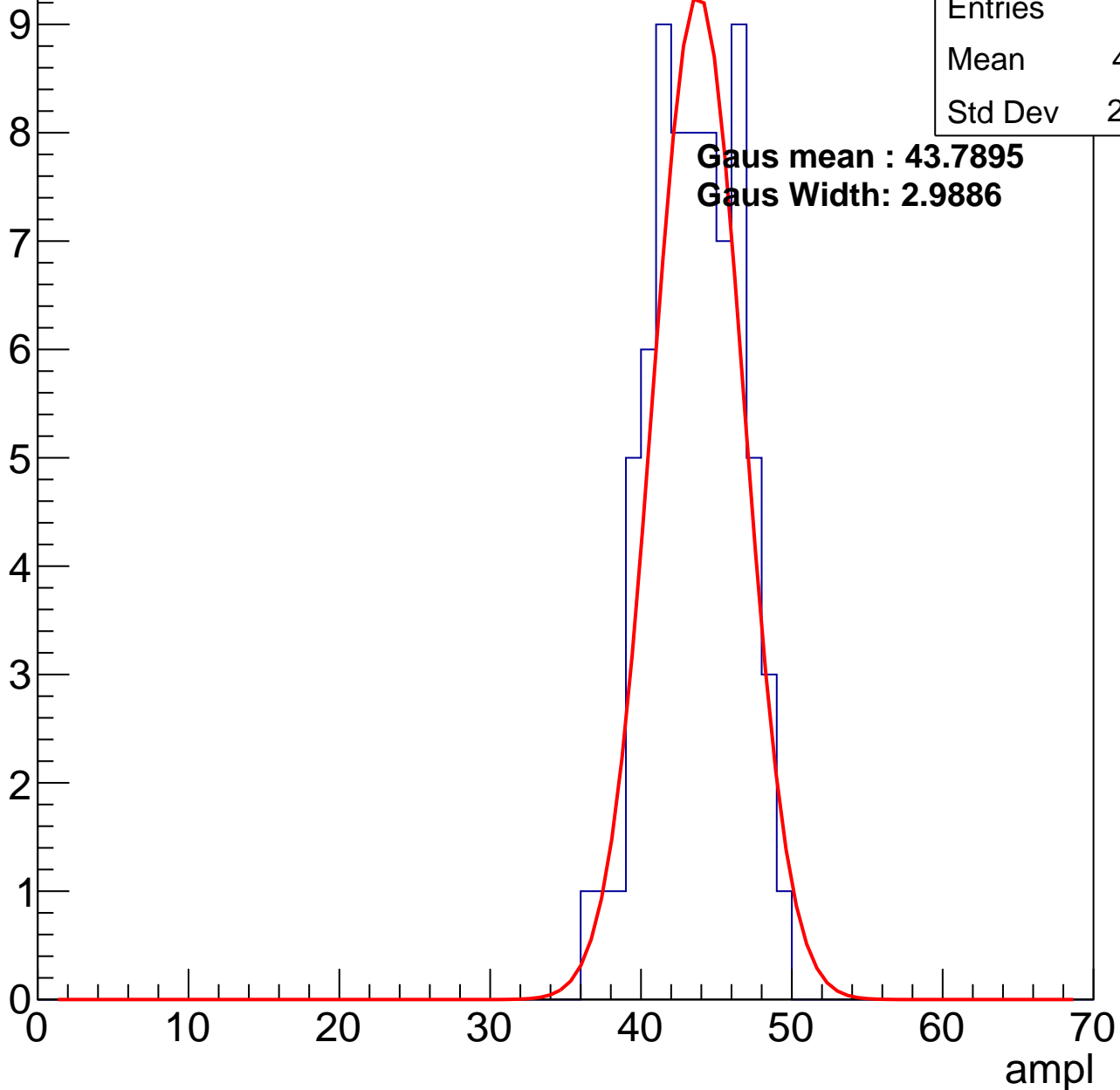
**Gaus Width: 3.3309**



# B0L002S, U8-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



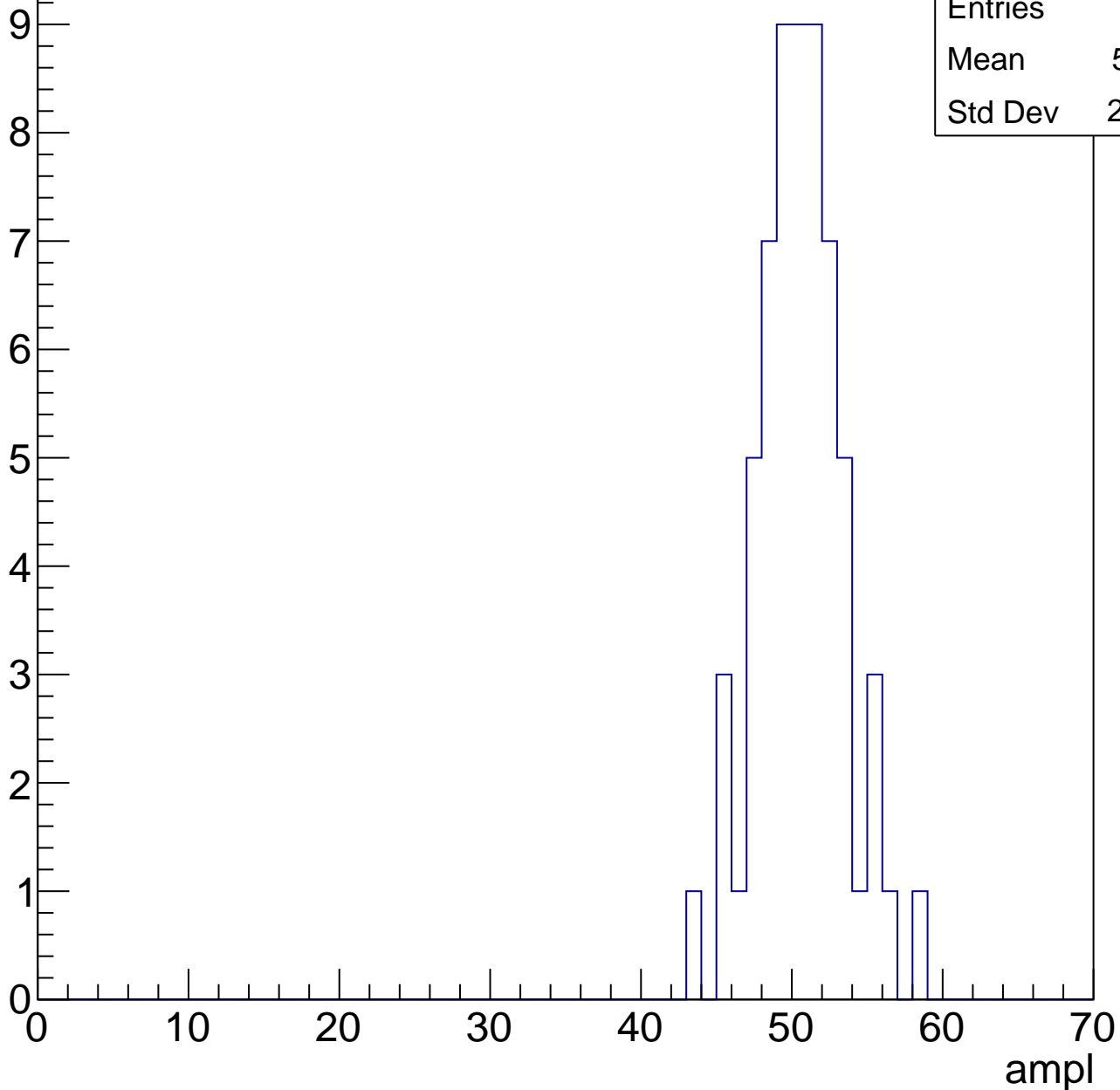
Entries	72
Mean	43.11
Std Dev	2.875

# B0L002S, U8-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

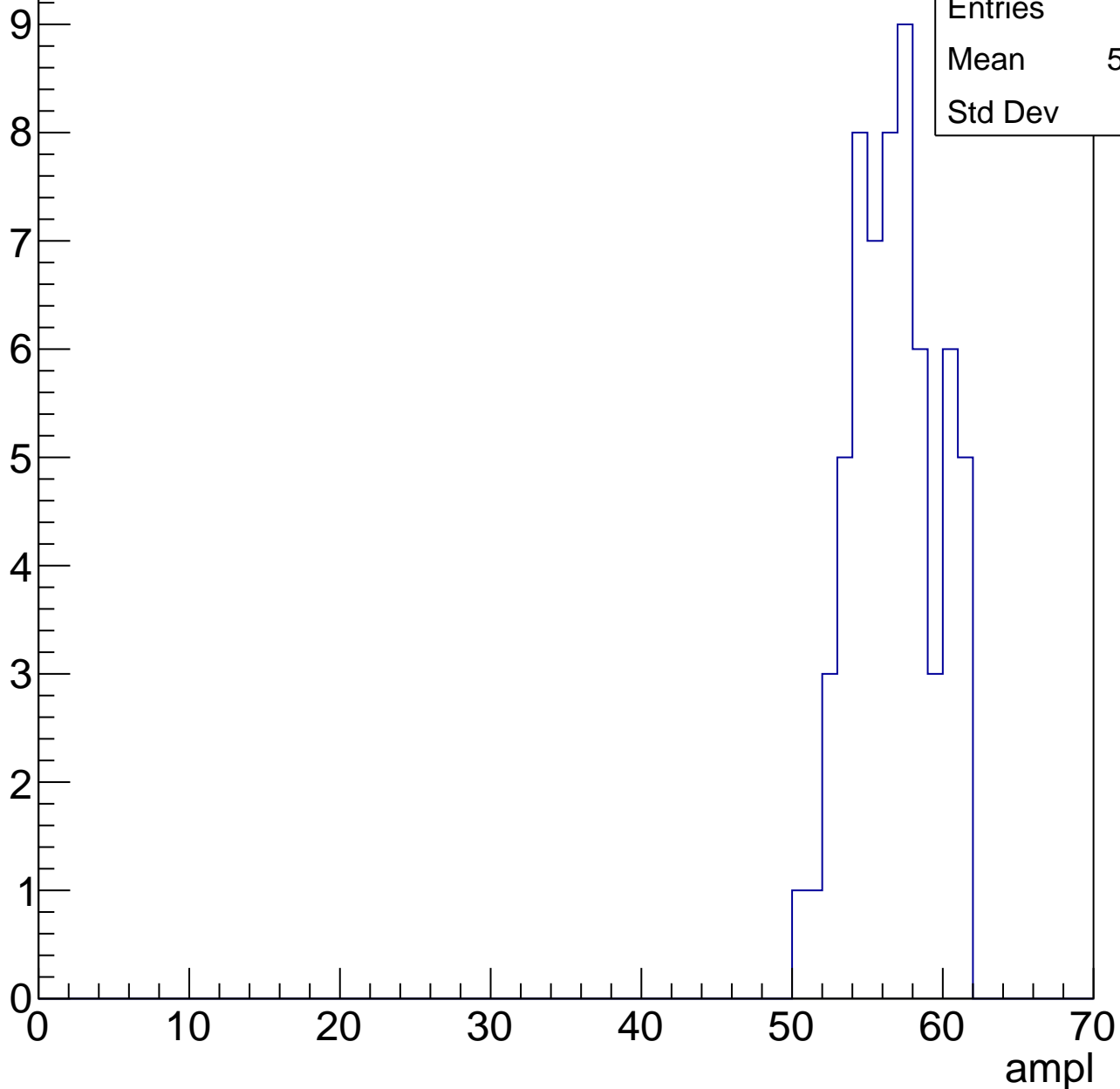
Entries	62
Mean	50.11
Std Dev	2.823



# B0L002S, U8-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	62
Mean	56.29
Std Dev	2.75

# B0L002S, U8-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	37
Mean	60.51
Std Dev	2.201

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

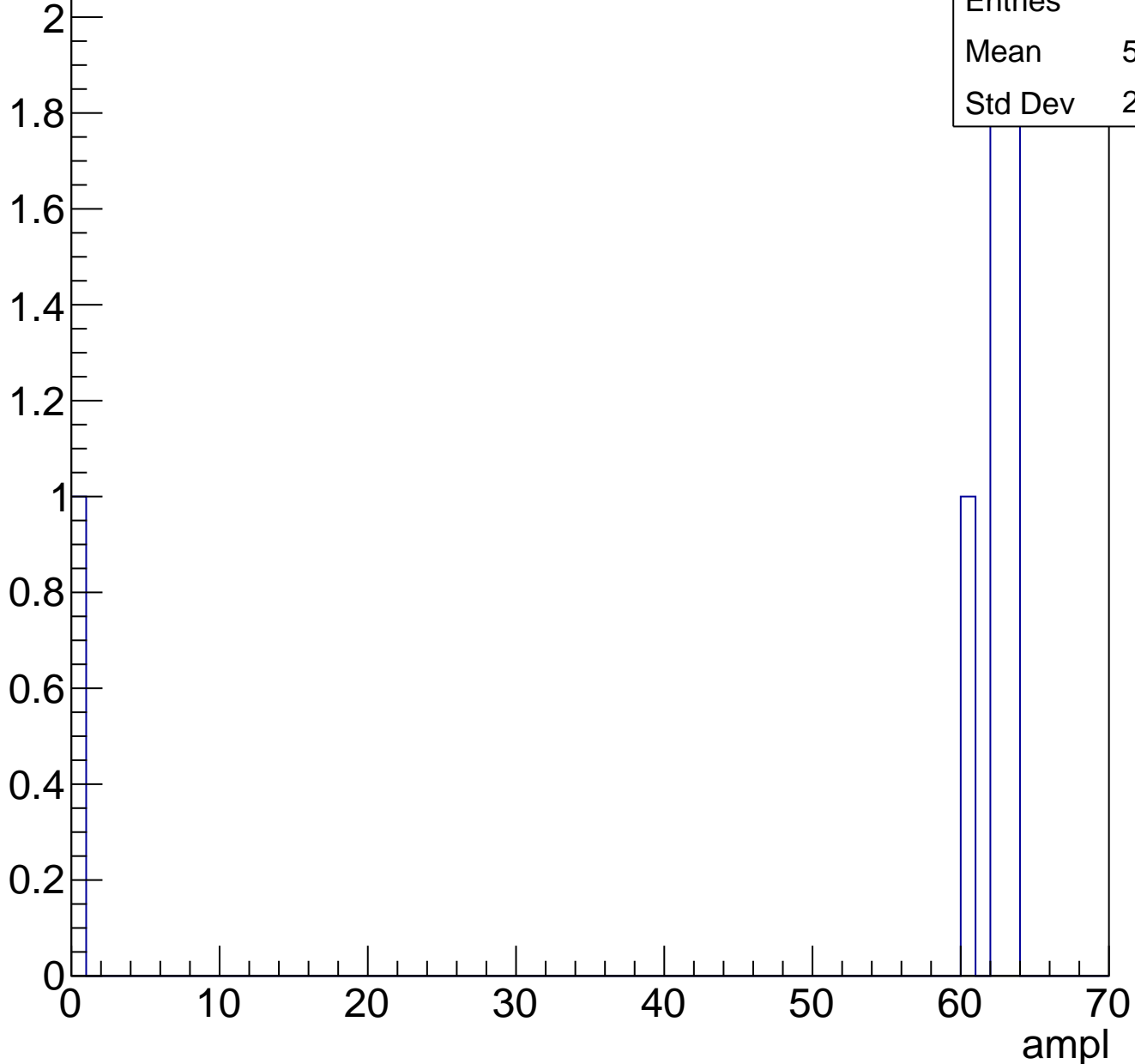
7

8

# B0L002S, U8-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L002S, U8-ch43, adc0

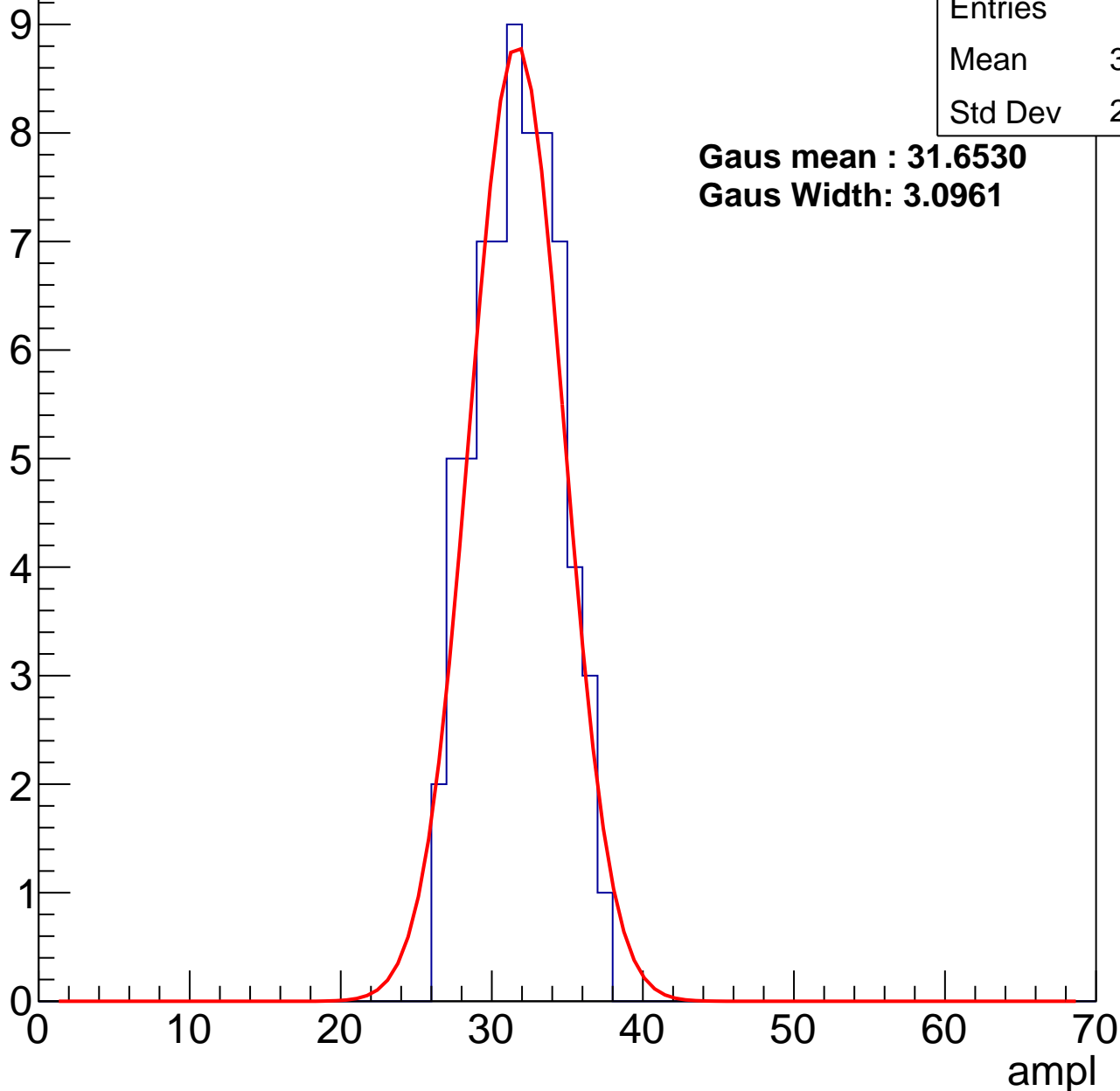
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.24
Std Dev	2.708

**Gaus mean : 31.6530**

**Gaus Width: 3.0961**



# B0L002S, U8-ch43, adc1

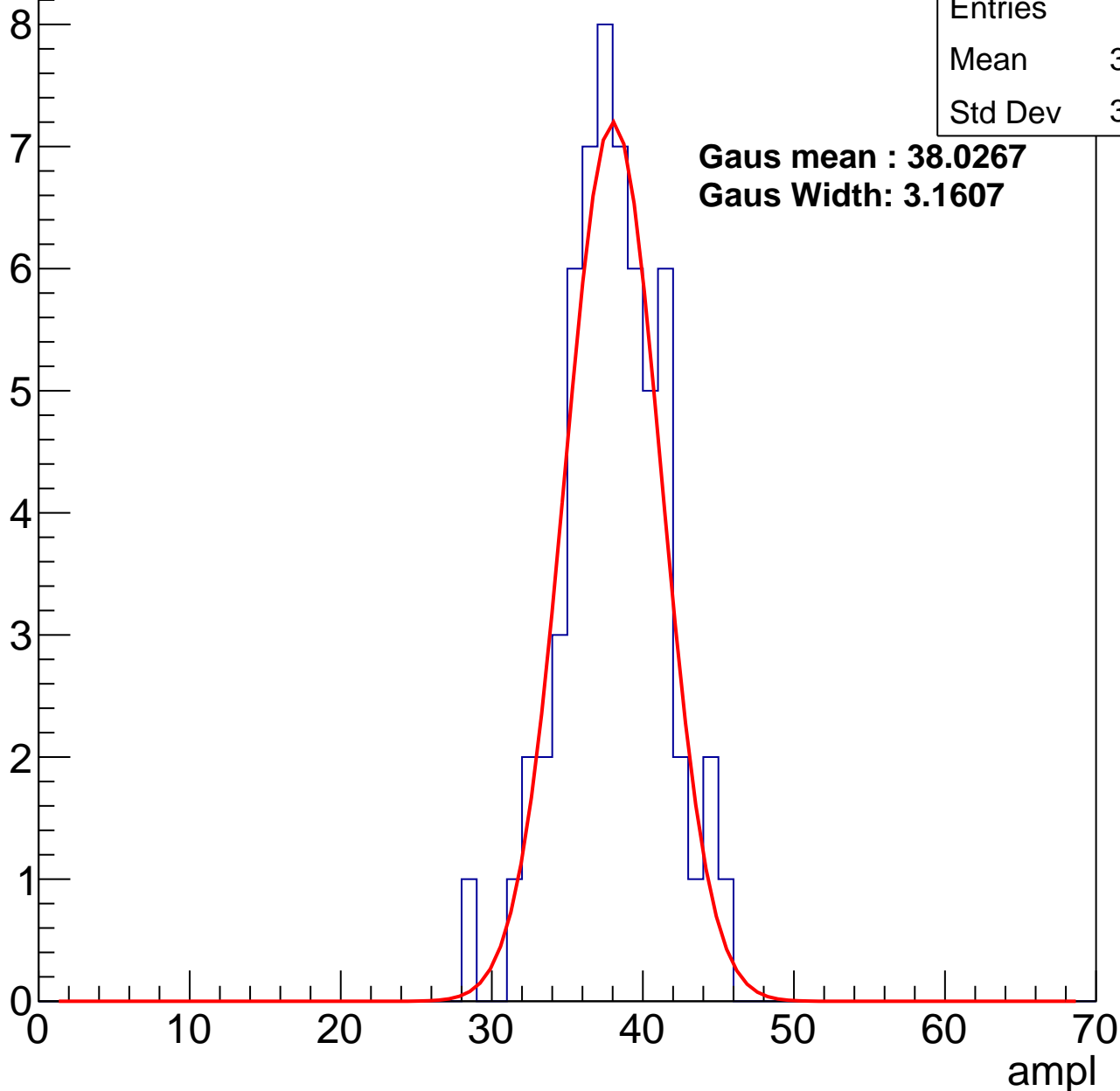
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.58
Std Dev	3.308

**Gaus mean : 38.0267**

**Gaus Width: 3.1607**



# B0L002S, U8-ch43, adc2

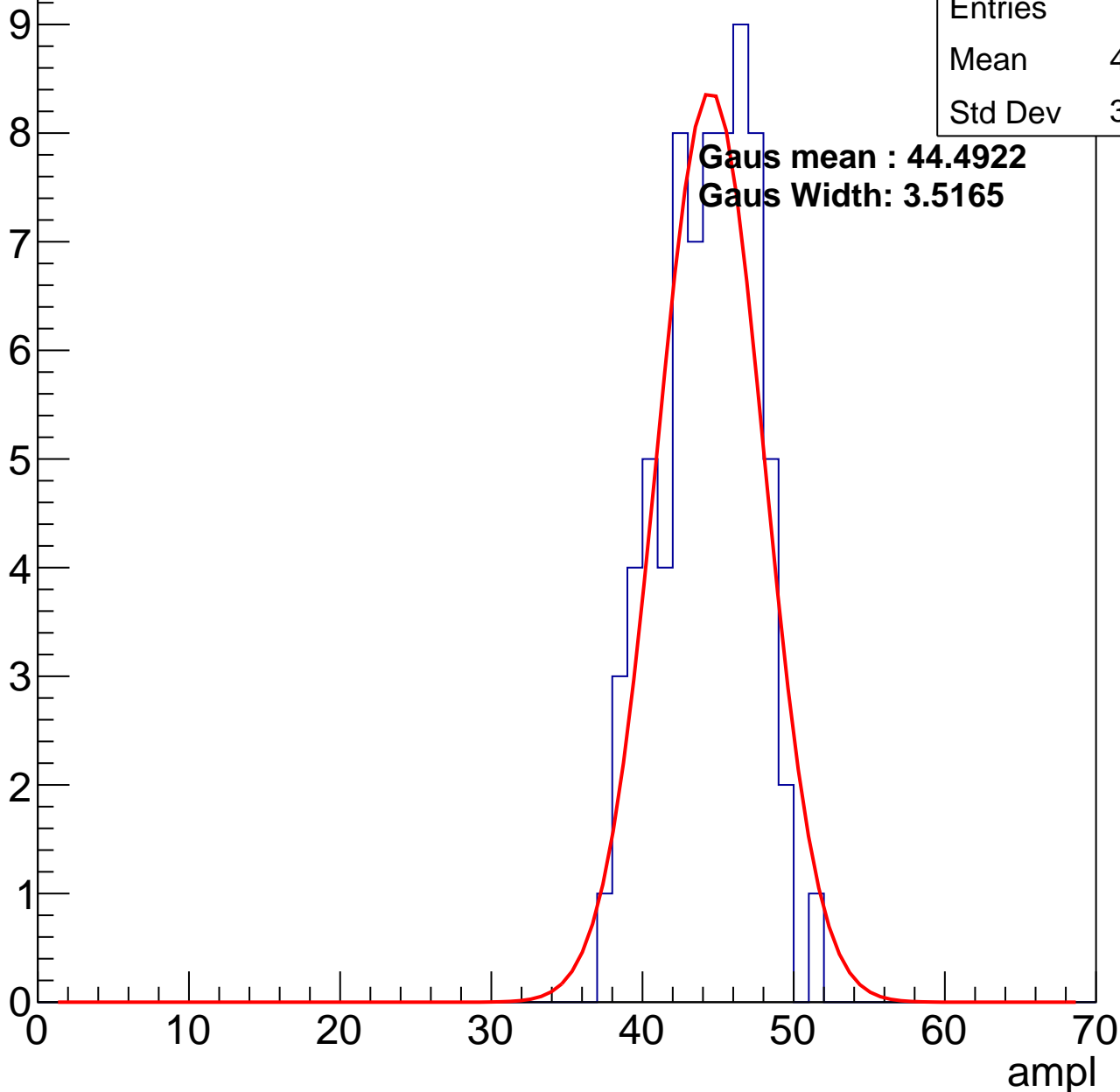
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	43.82
Std Dev	3.107

**Gaus mean : 44.4922**

**Gaus Width: 3.5165**

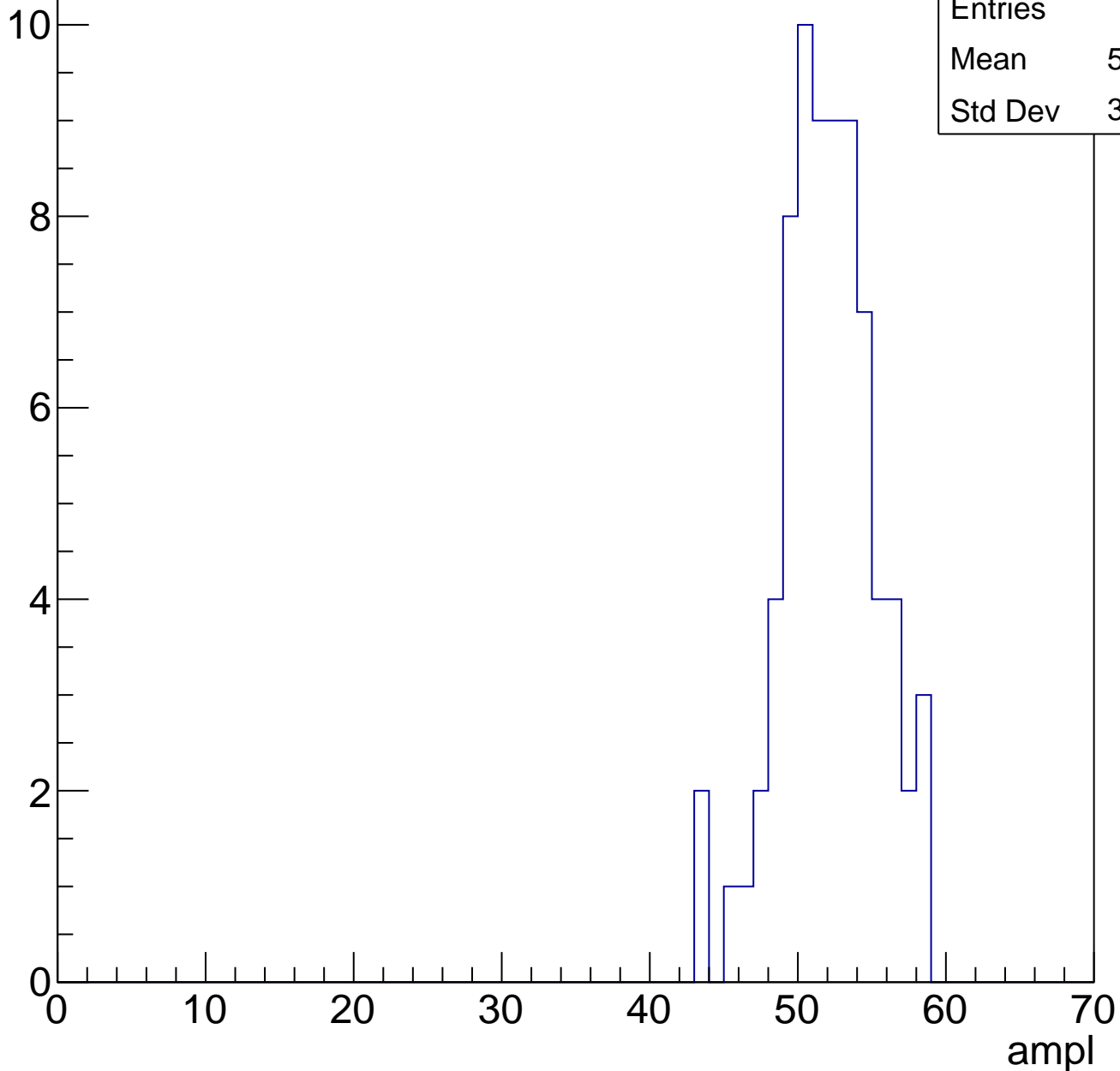


# B0L002S, U8-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	51.59
Std Dev	3.192

Entry

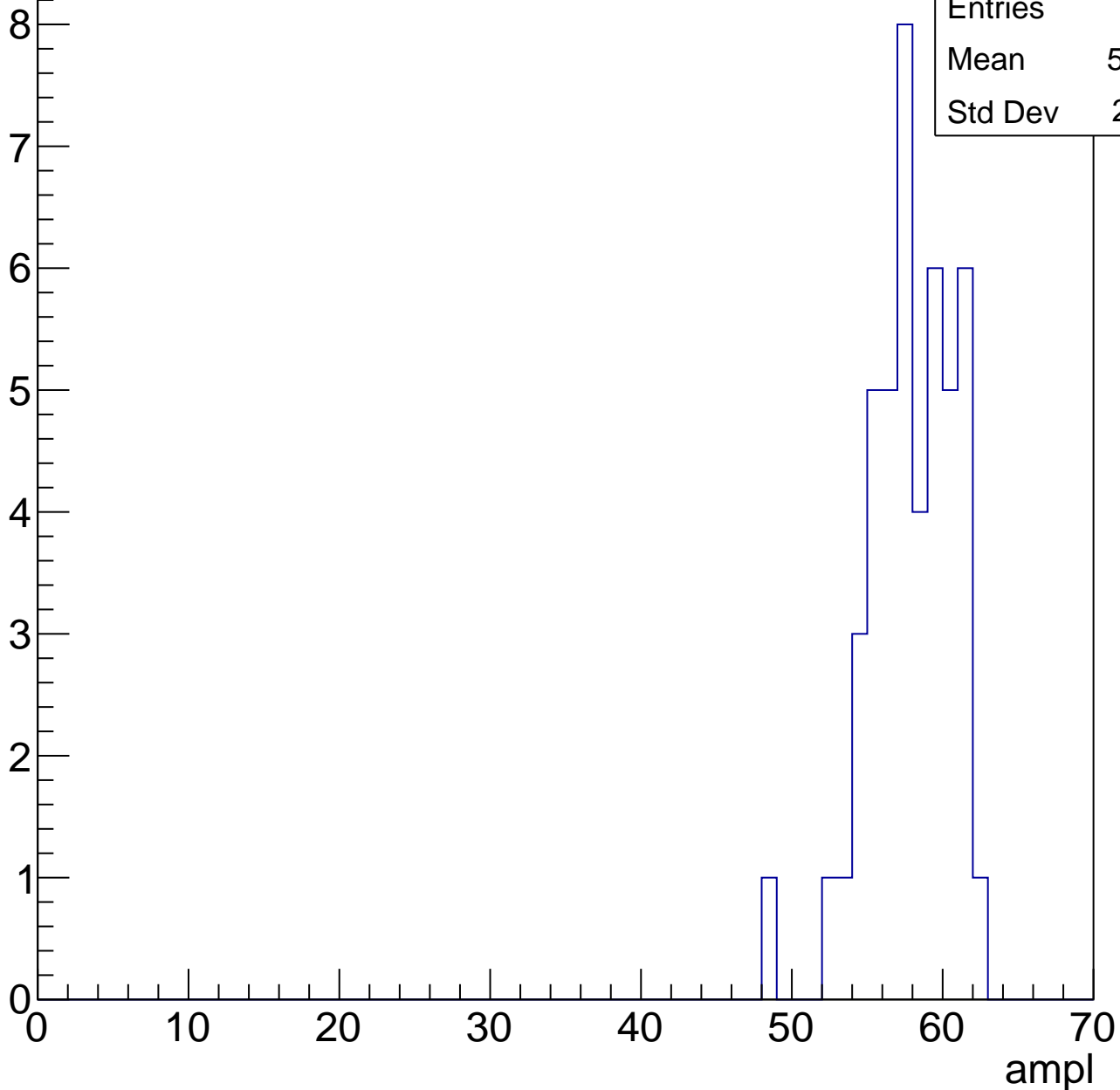


# B0L002S, U8-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.39
Std Dev	2.801

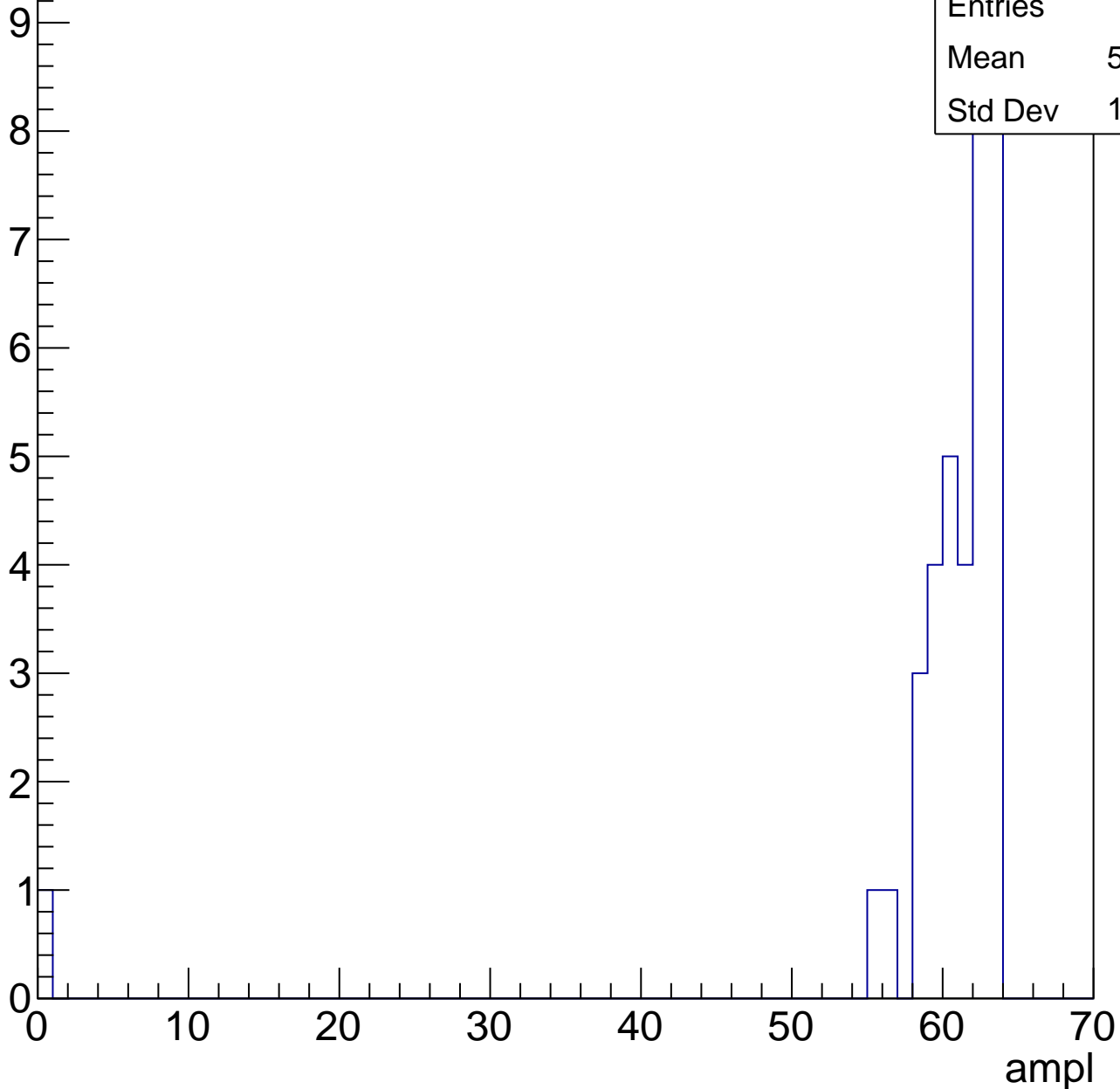


# B0L002S, U8-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.19
Std Dev	10.07



# B0L002S, U8-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

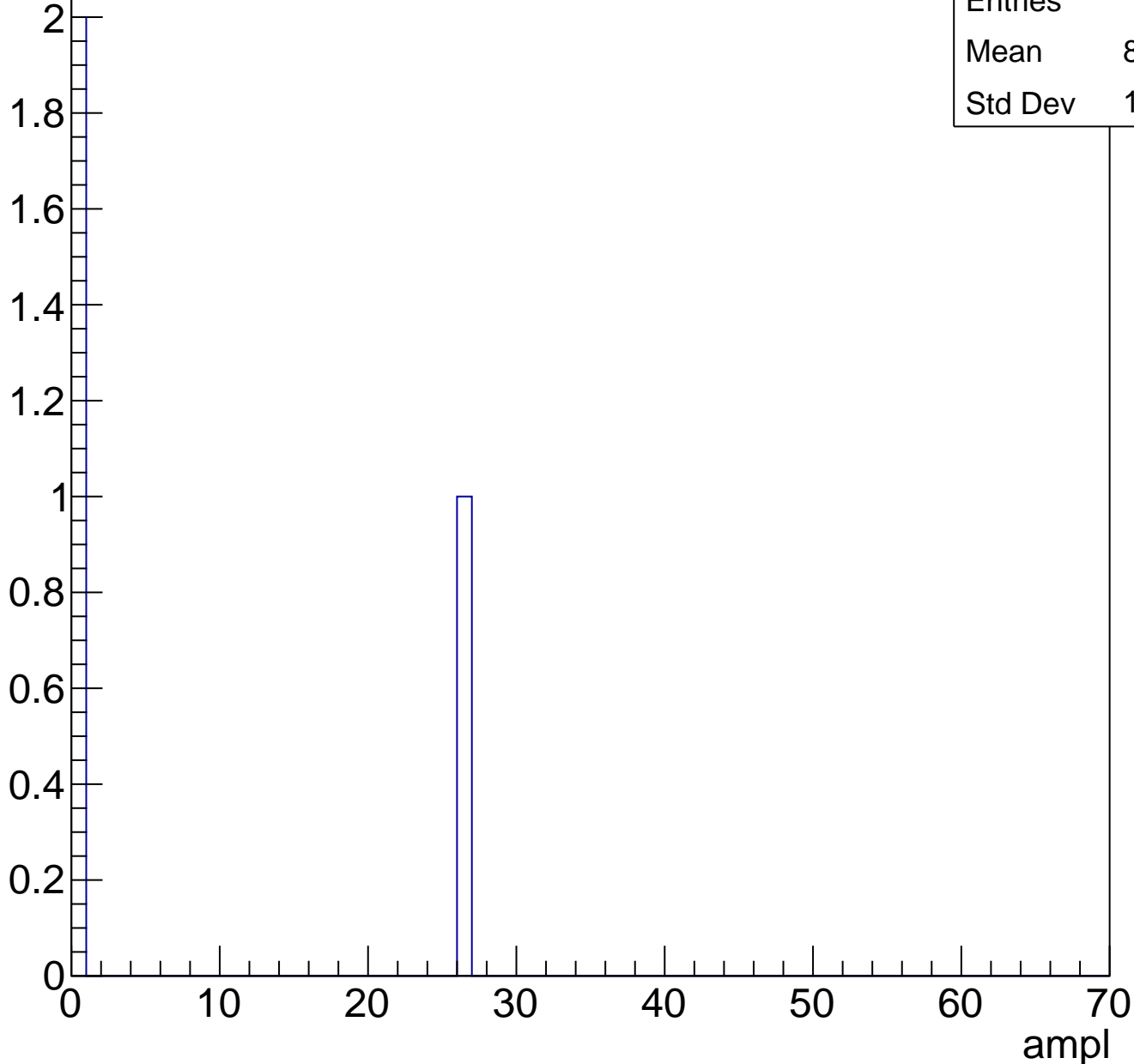




# B0L002S, U8-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.667
Std Dev	12.26

# B0L002S, U8-ch44, adc0

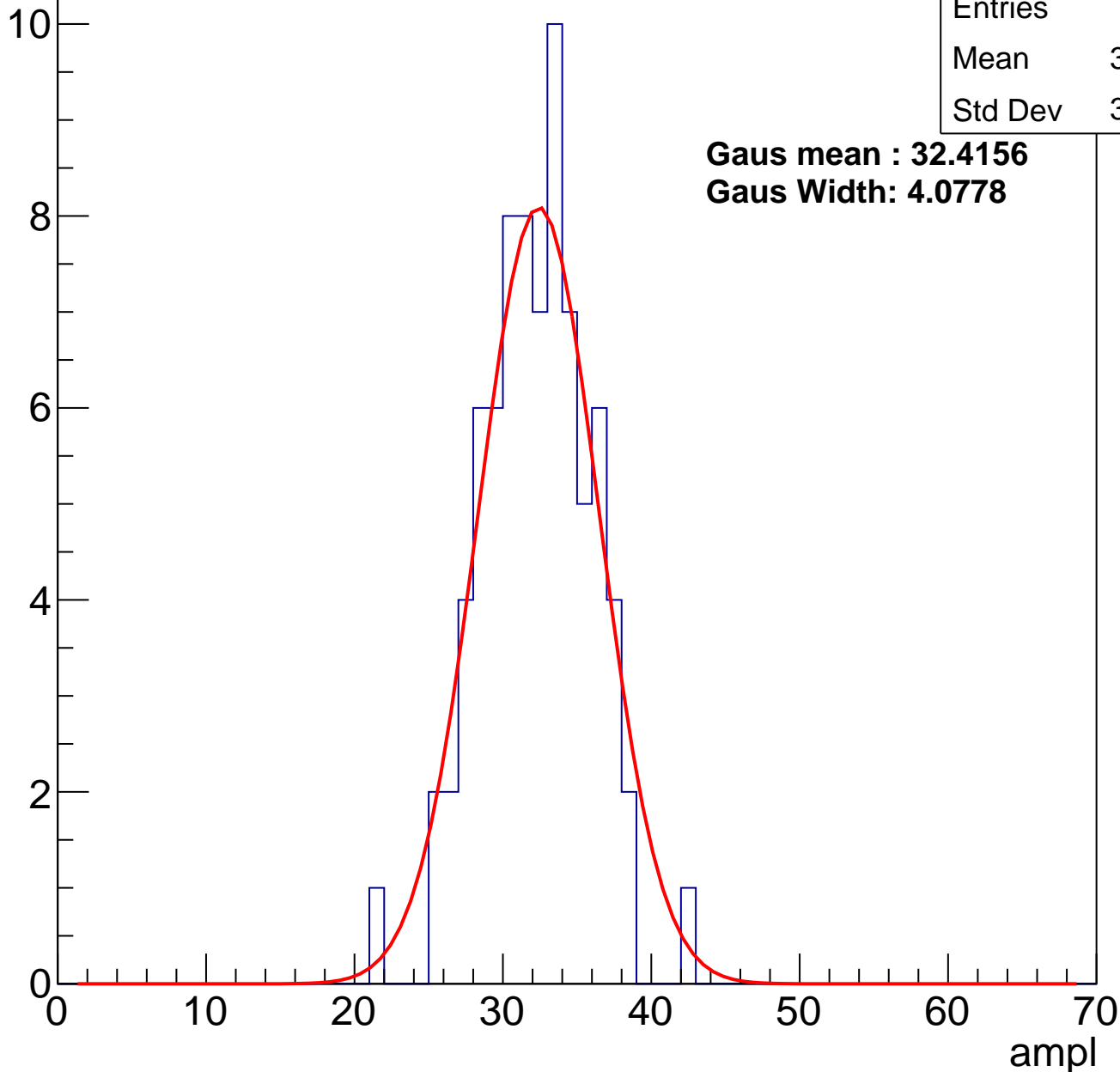
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	31.77
Std Dev	3.607

**Gaus mean : 32.4156**

**Gaus Width: 4.0778**

Entry



# B0L002S, U8-ch44, adc1

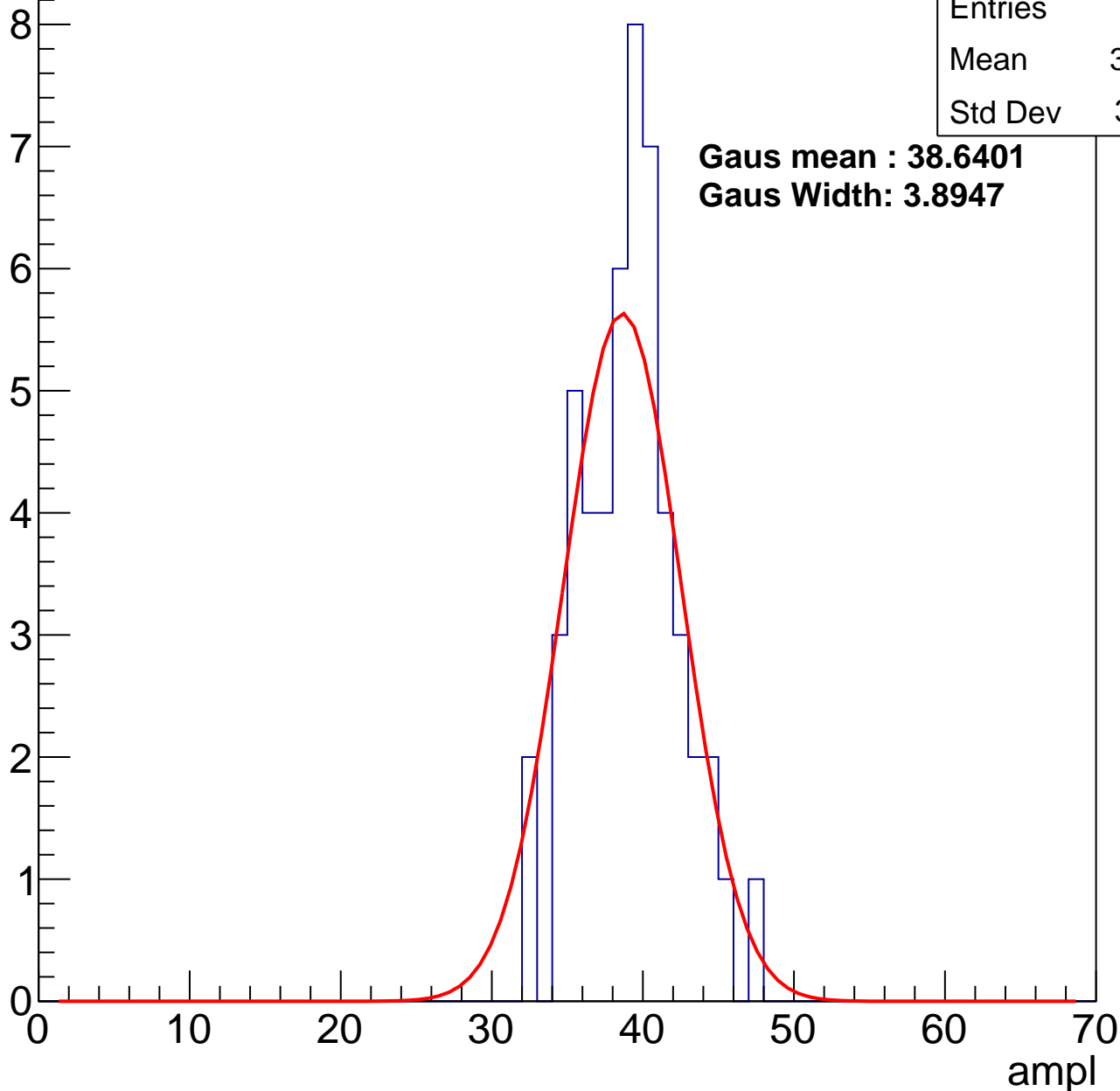
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	38.63
Std Dev	3.211

**Gaus mean : 38.6401**

**Gaus Width: 3.8947**



# B0L002S, U8-ch44, adc2

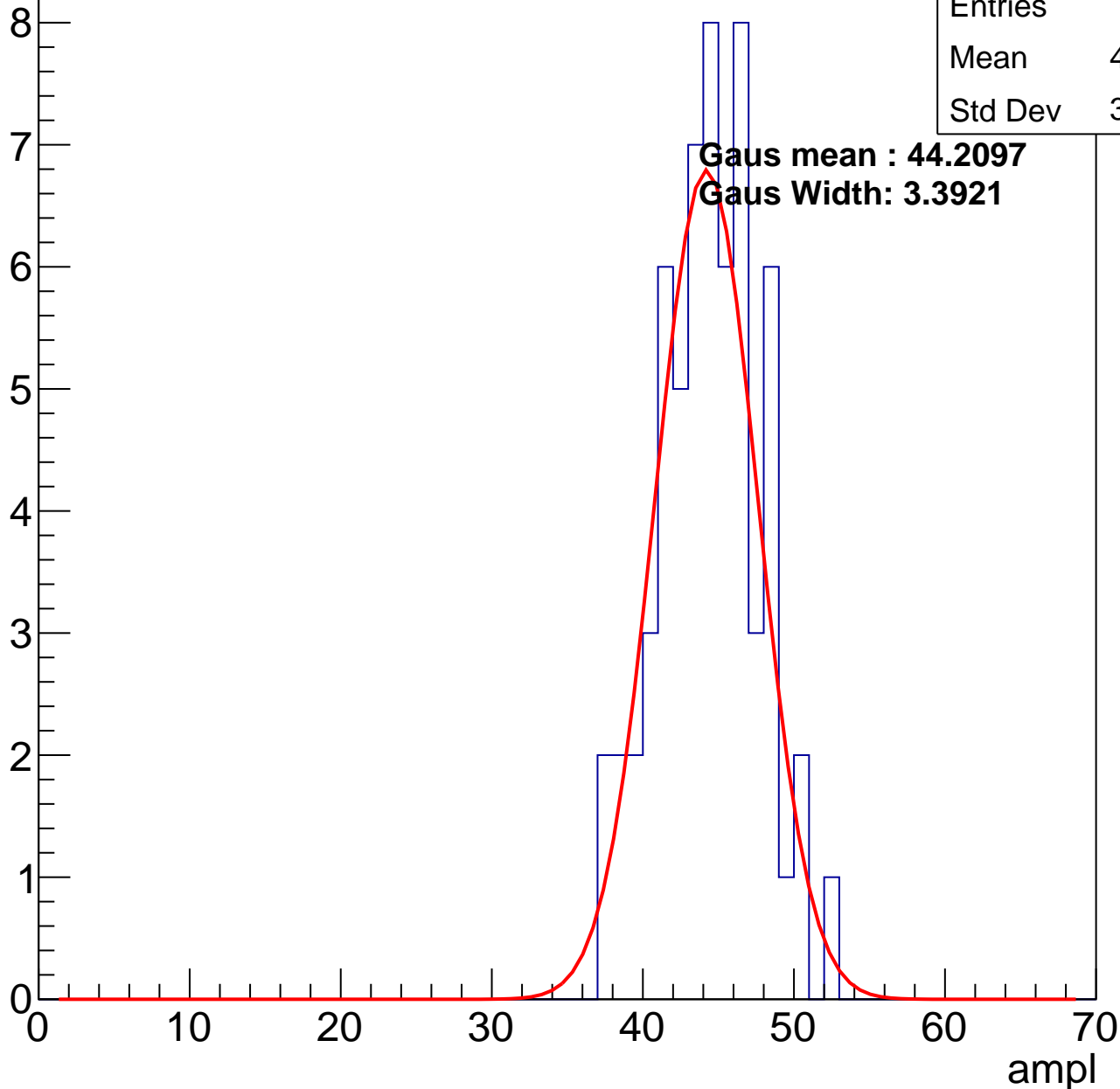
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.95
Std Dev	3.289

**Gaus mean : 44.2097**

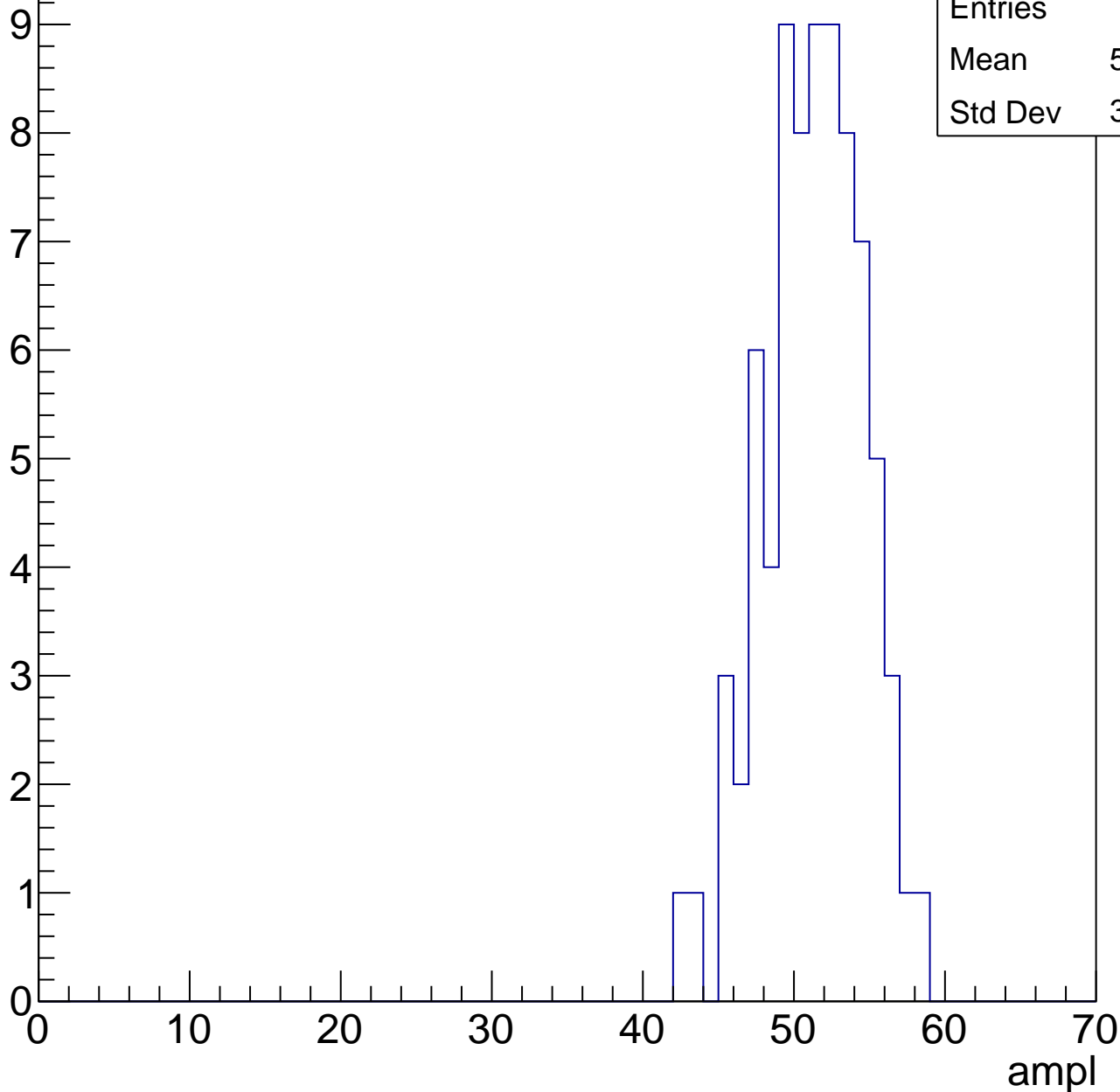
**Gaus Width: 3.3921**



# B0L002S, U8-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

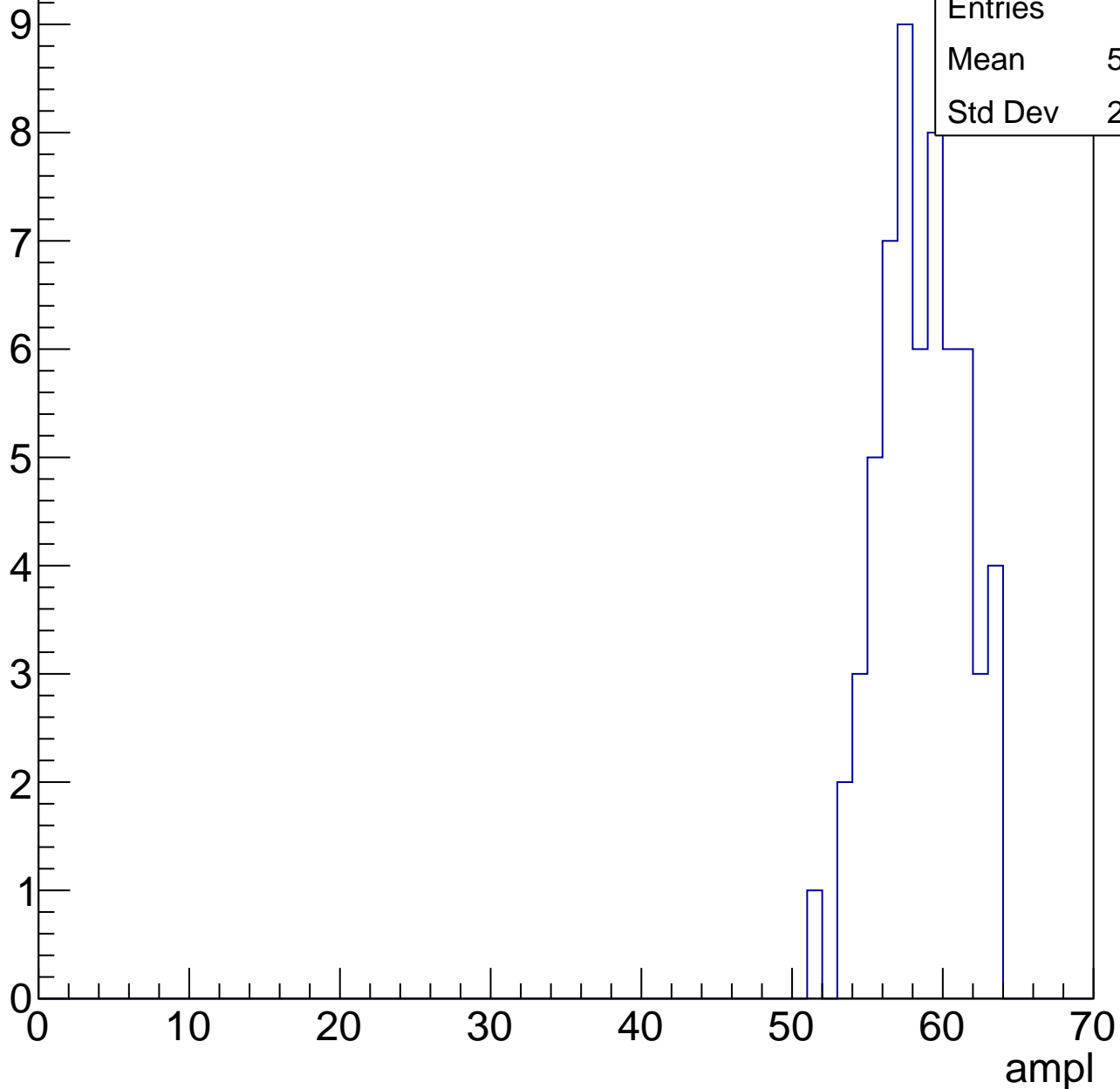


Entries	77
Mean	50.83
Std Dev	3.265

# B0L002S, U8-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

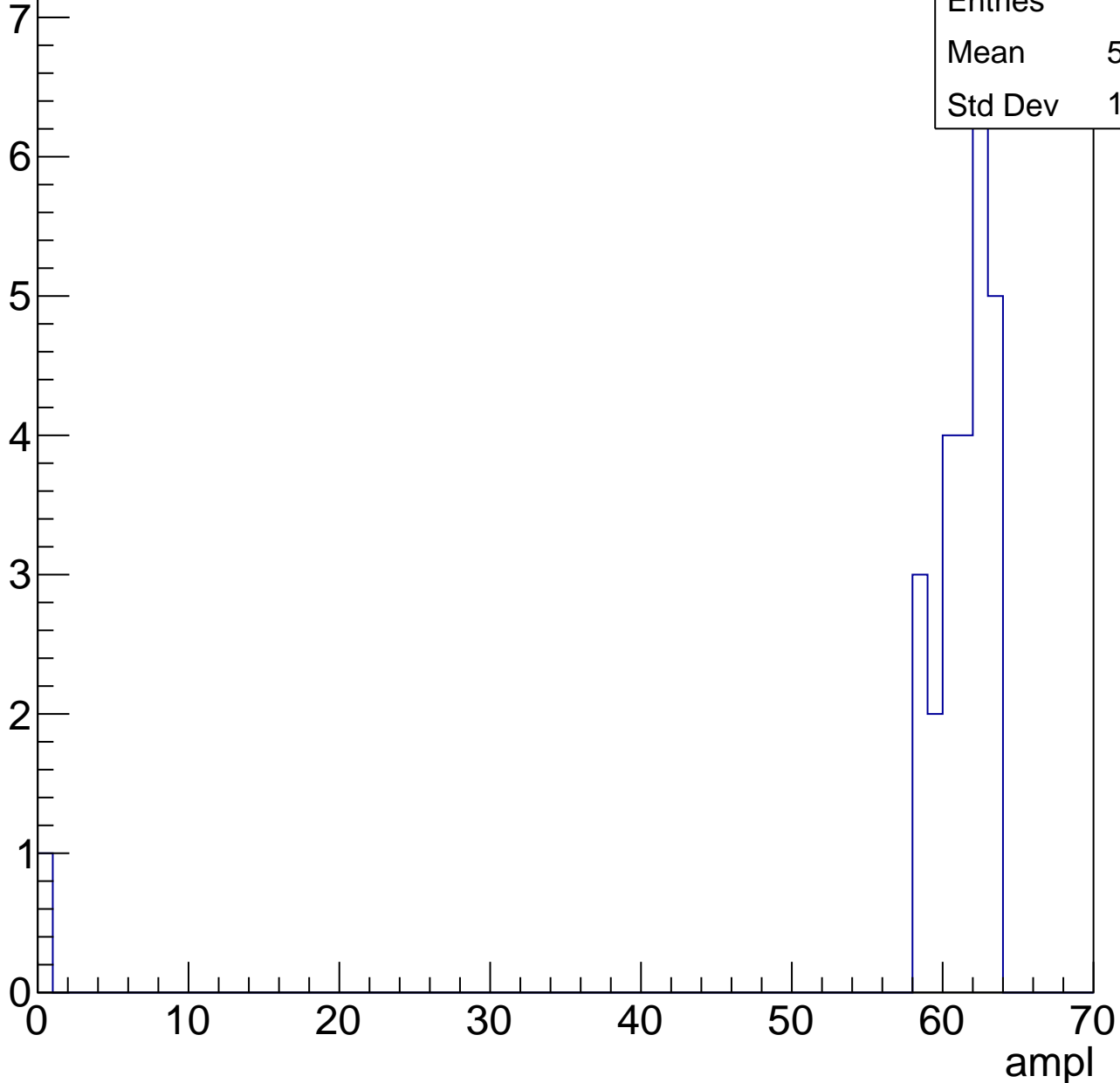


# B0L002S, U8-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

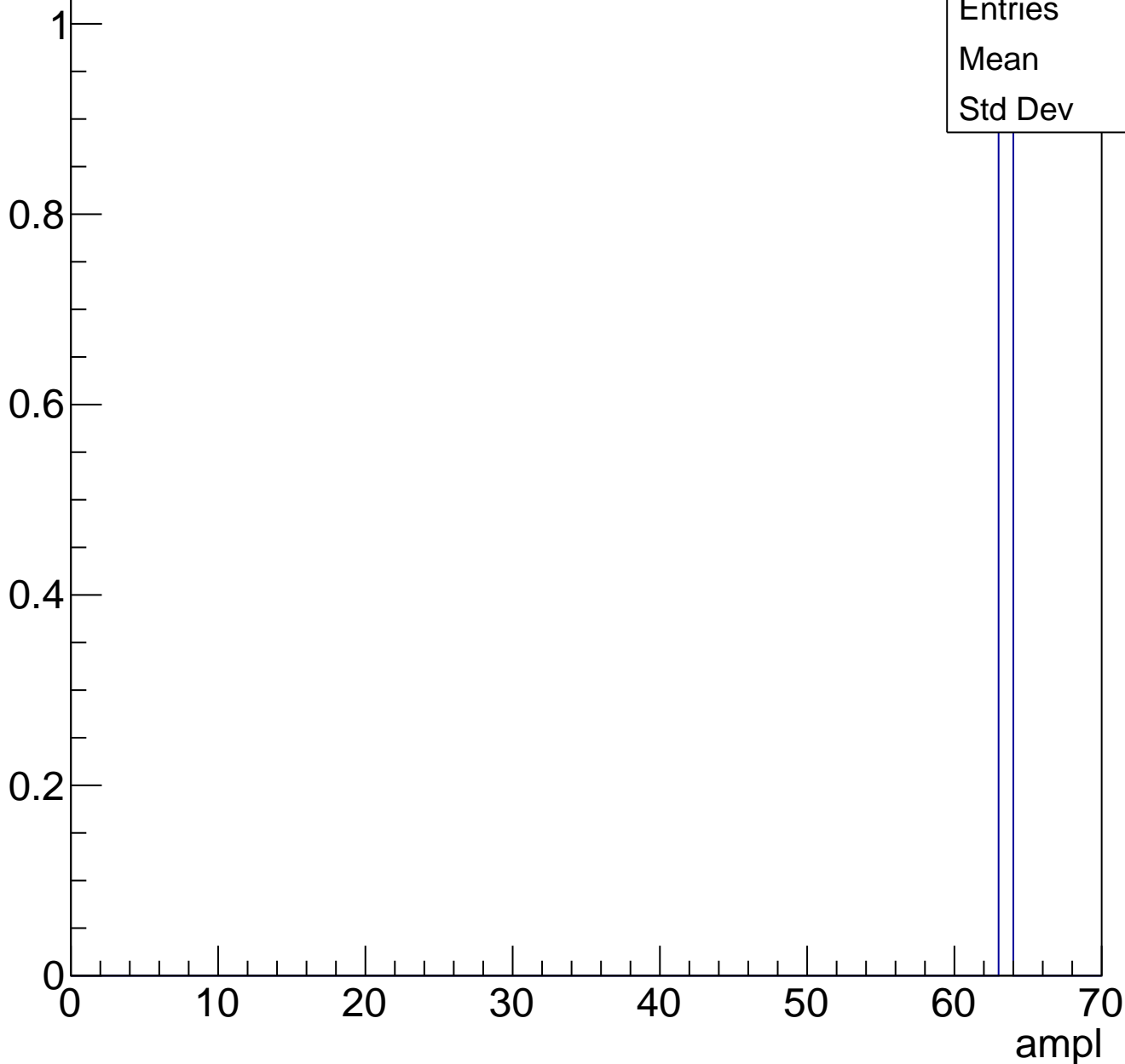
Entries	26
Mean	58.65
Std Dev	11.84



# B0L002S, U8-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	76
Mean	29.41
Std Dev	3.261

**Gaus mean : 29.8074**

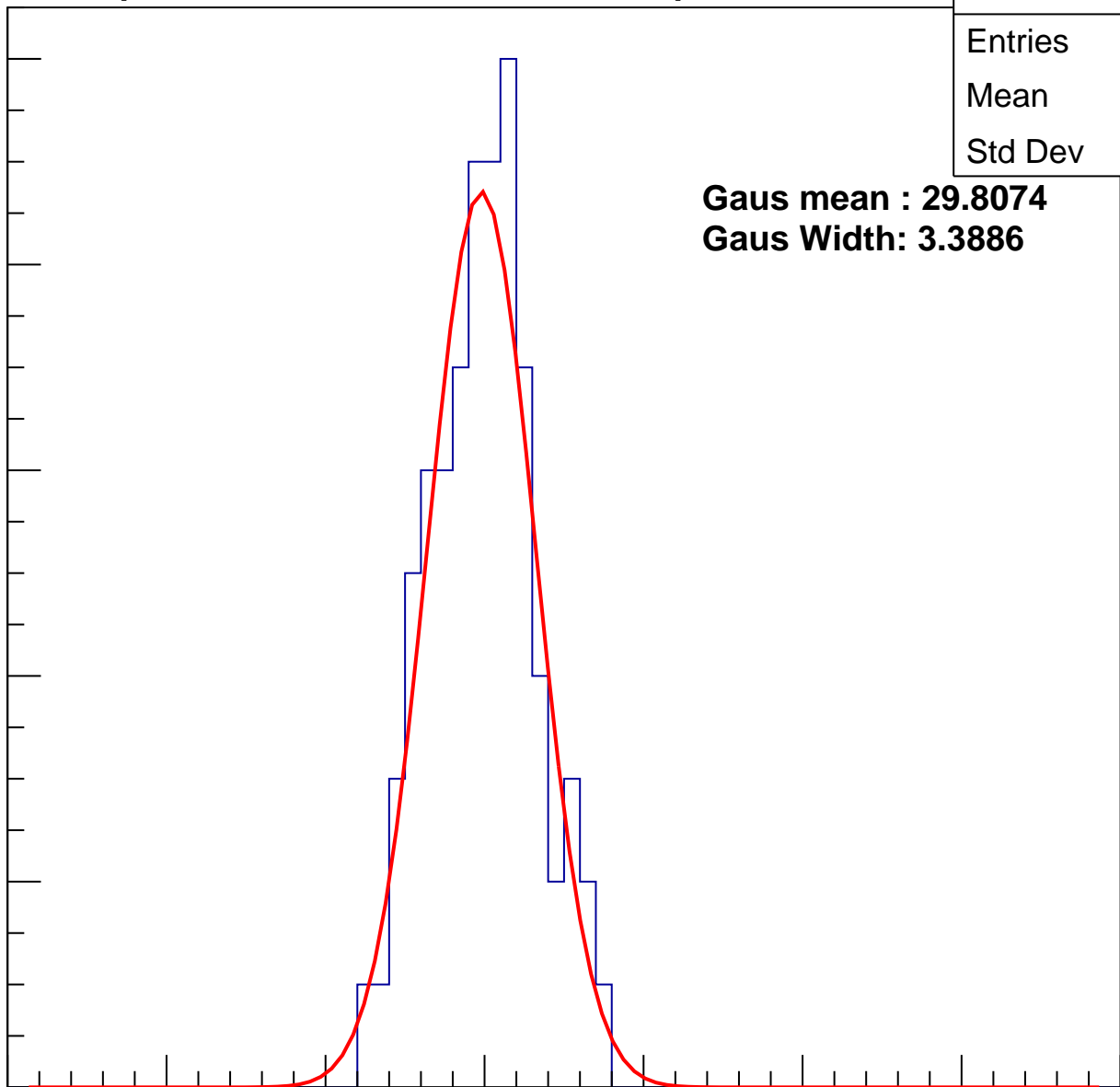
**Gaus Width: 3.3886**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch45, adc1

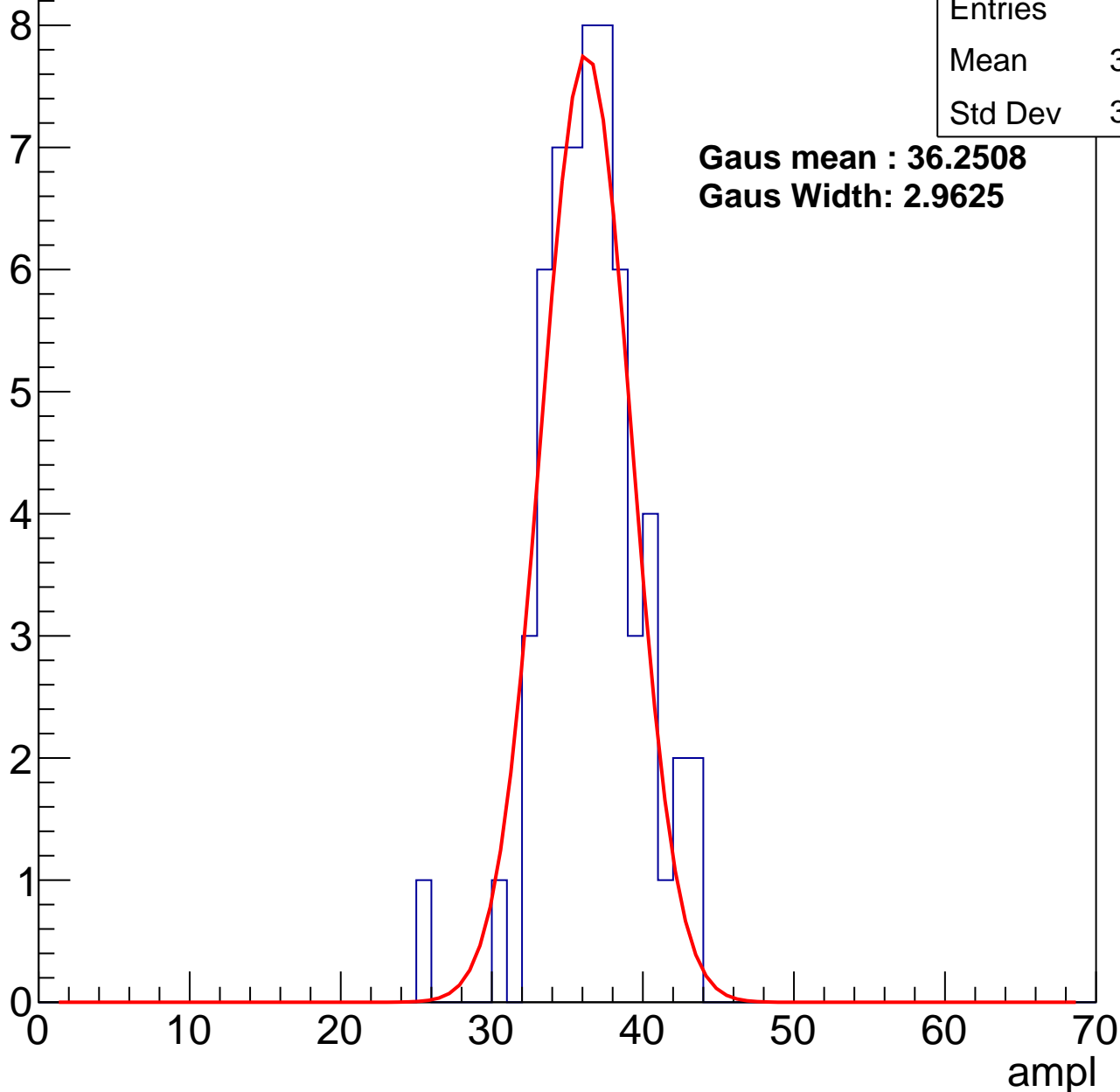
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.14
Std Dev	3.218

**Gaus mean : 36.2508**

**Gaus Width: 2.9625**



# B0L002S, U8-ch45, adc2

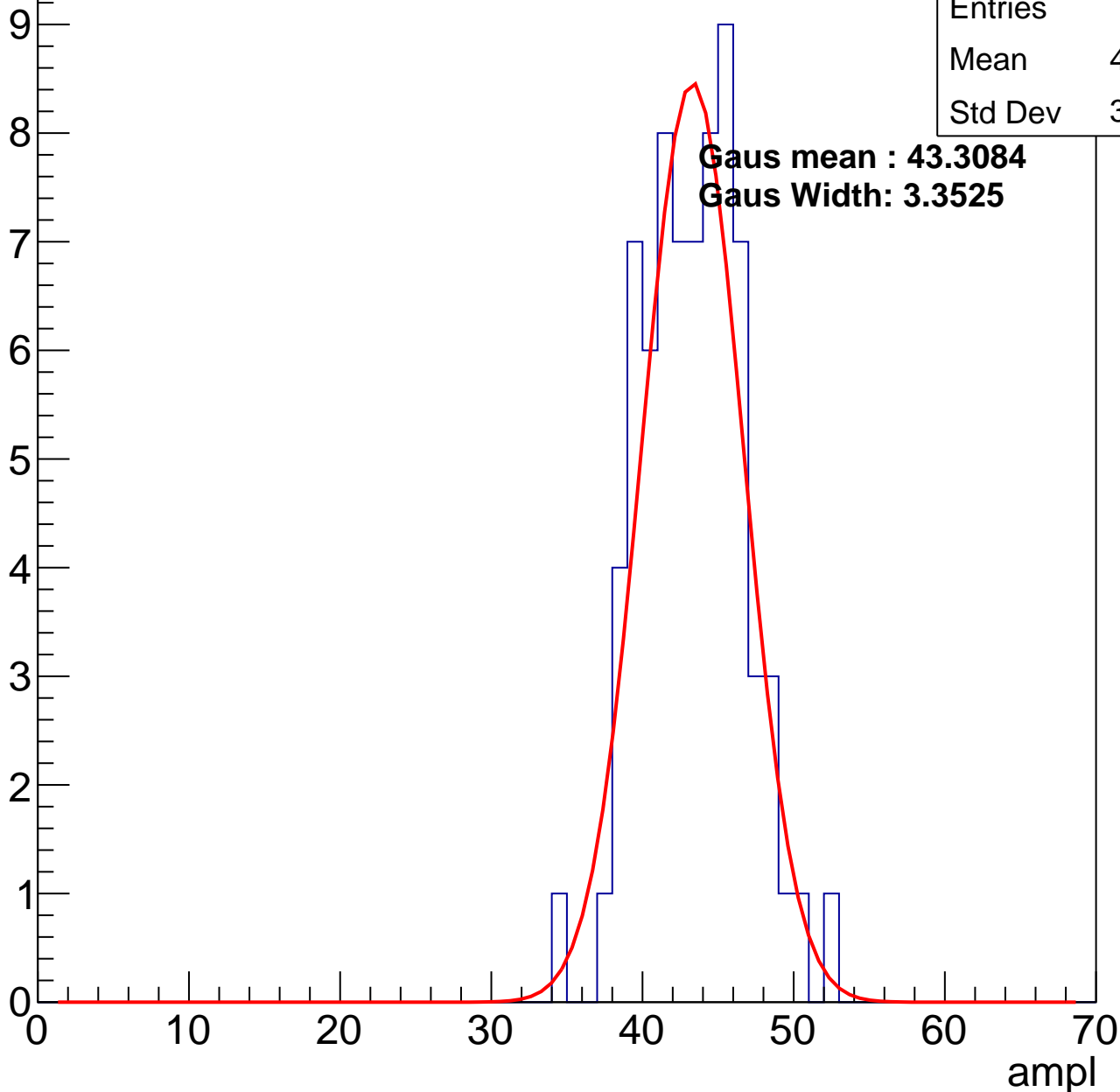
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	42.89
Std Dev	3.323

**Gaus mean : 43.3084**

**Gaus Width: 3.3525**



# B0L002S, U8-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

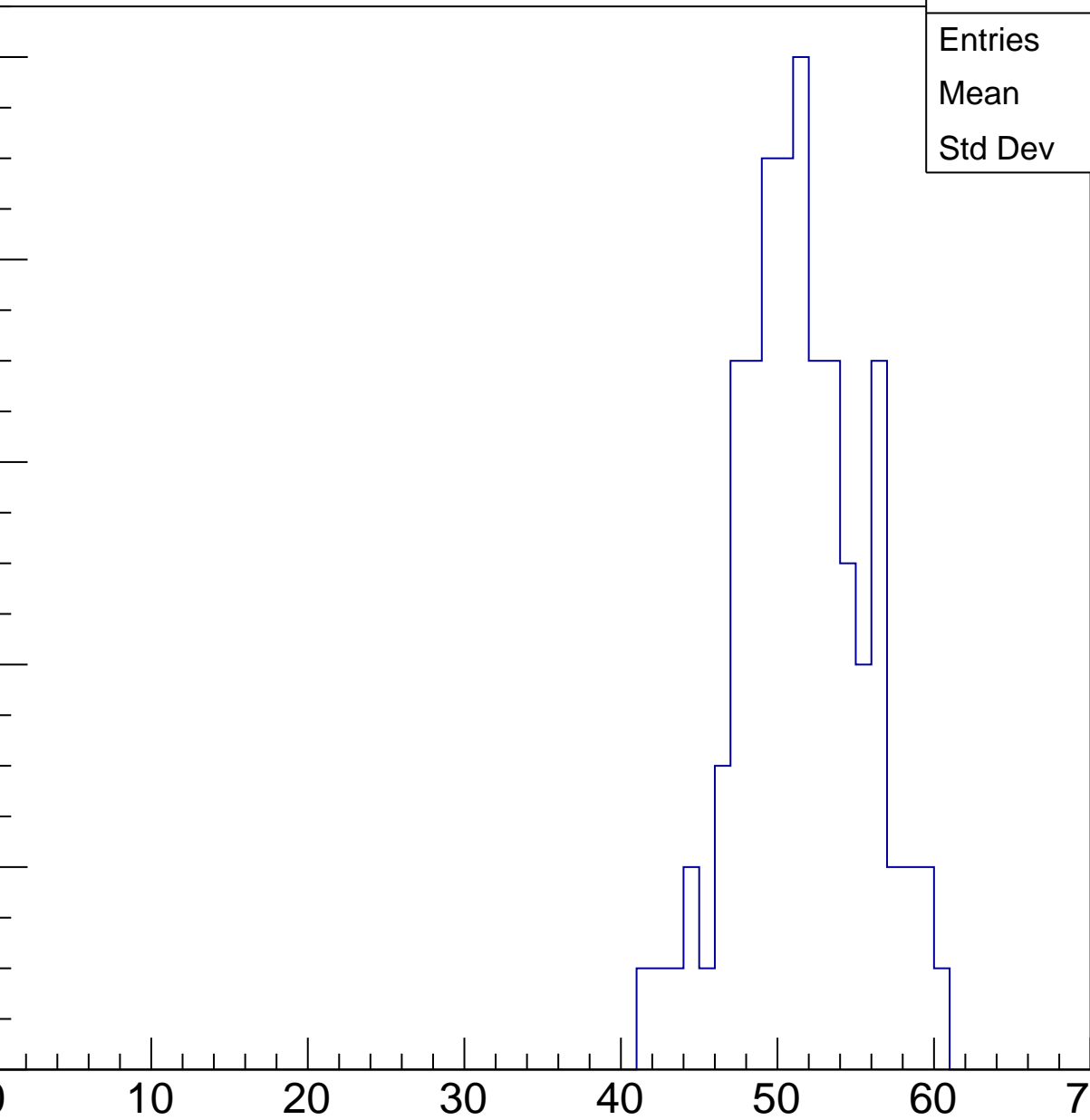
Entries	88
Mean	51
Std Dev	3.949

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

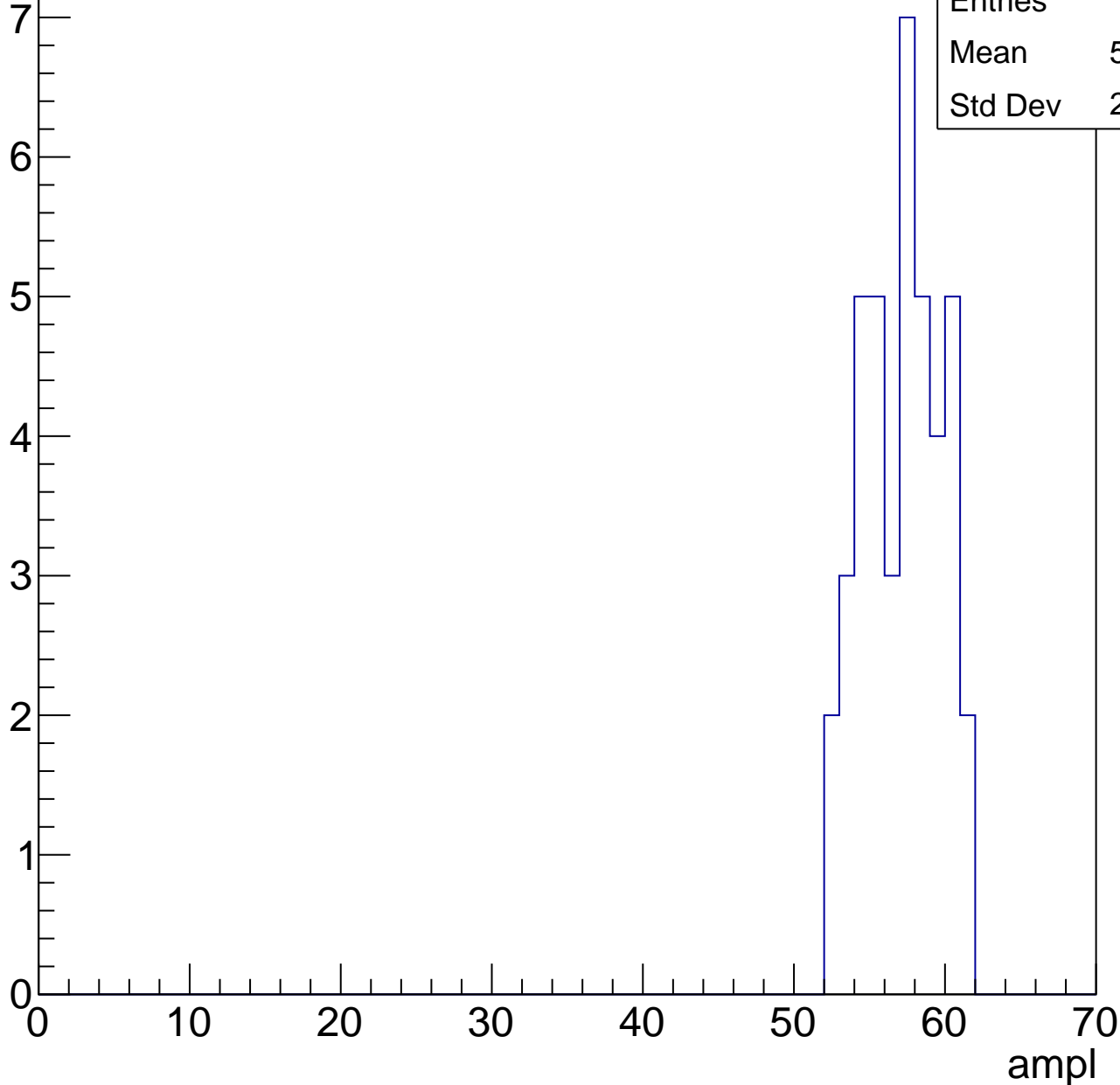
ampl



# B0L002S, U8-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

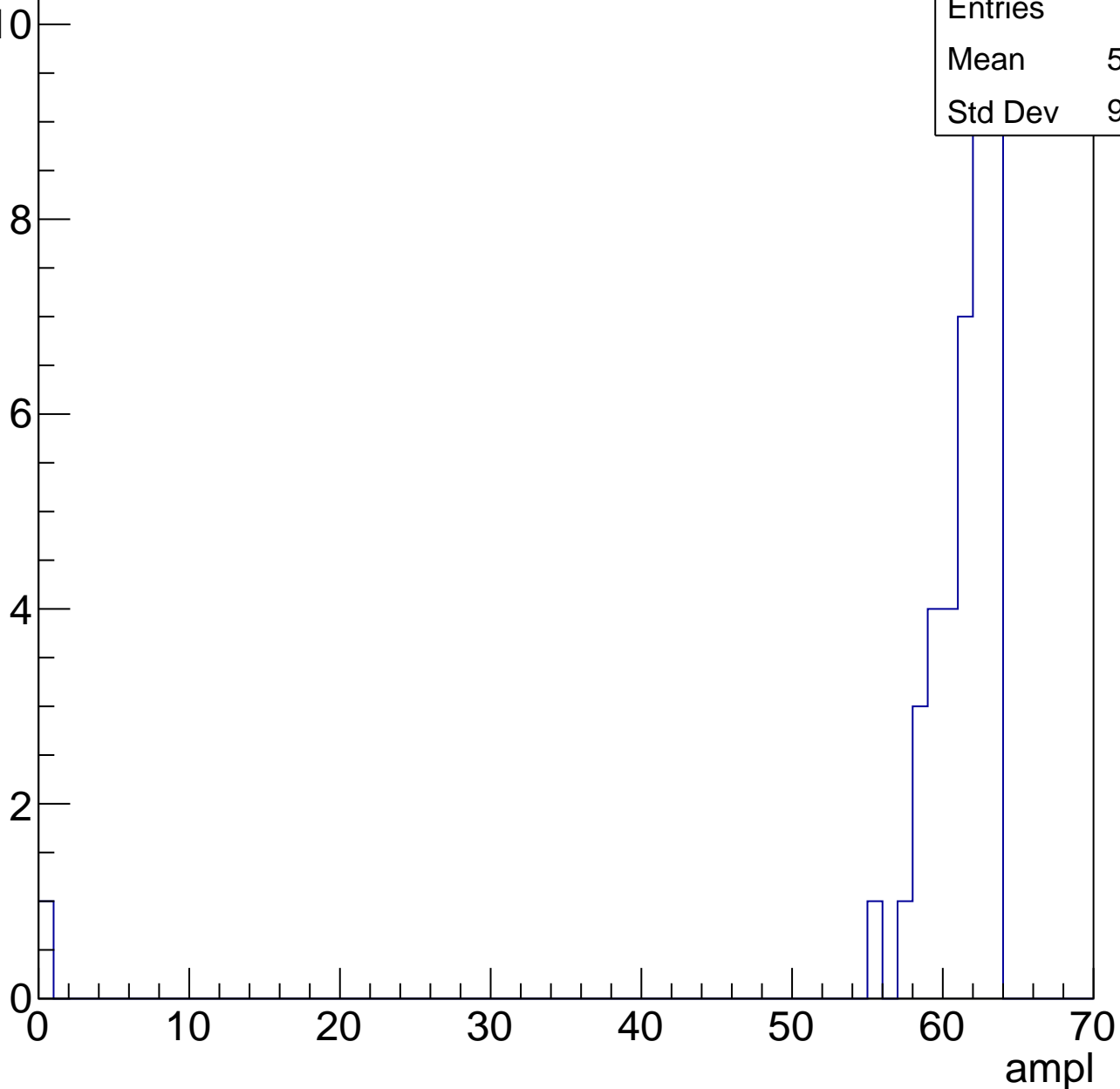


# B0L002S, U8-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.42
Std Dev	9.708



# B0L002S, U8-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch46, adc0

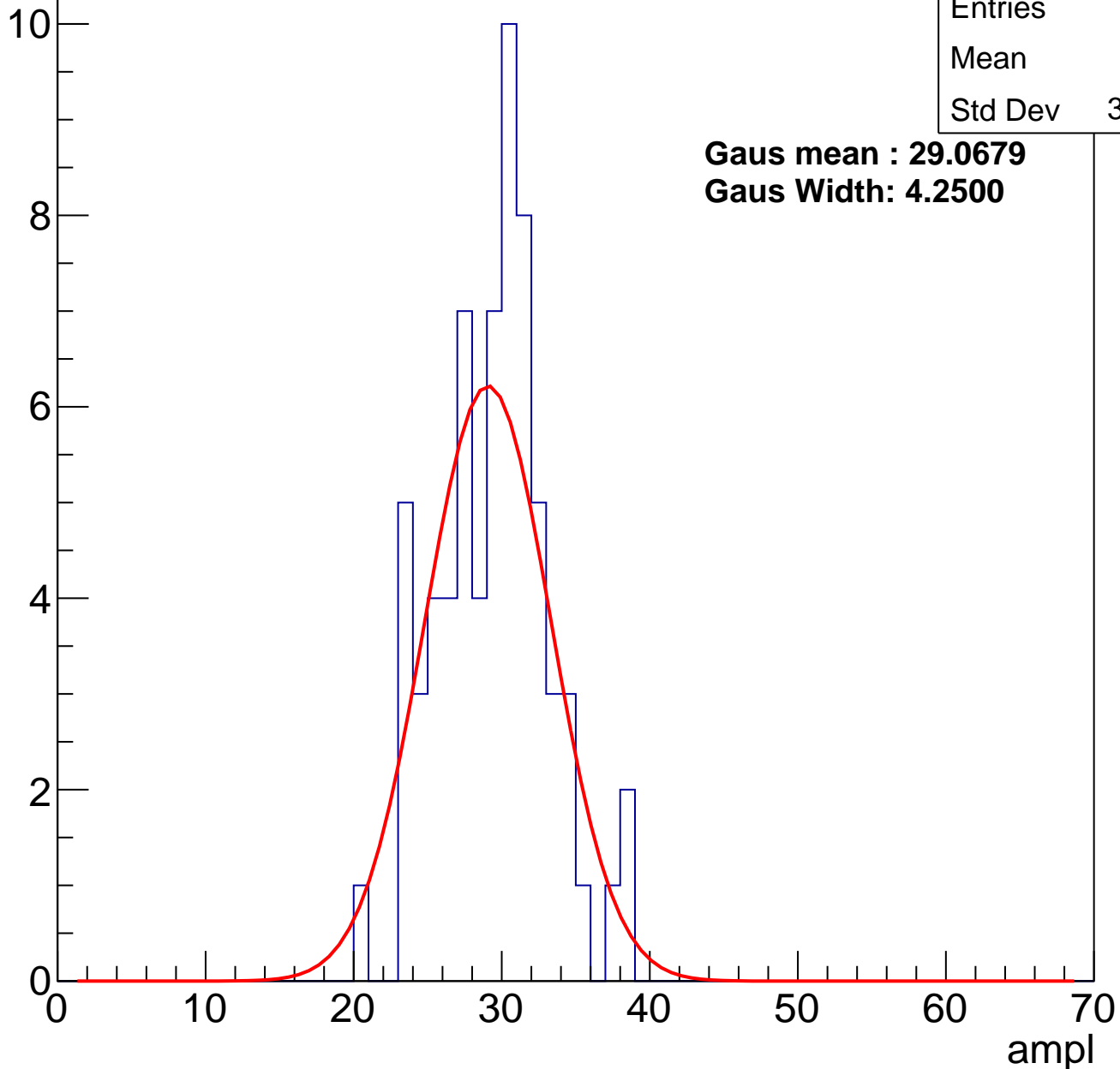
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	29
Std Dev	3.718

**Gaus mean : 29.0679**

**Gaus Width: 4.2500**

Entry



# B0L002S, U8-ch46, adc1

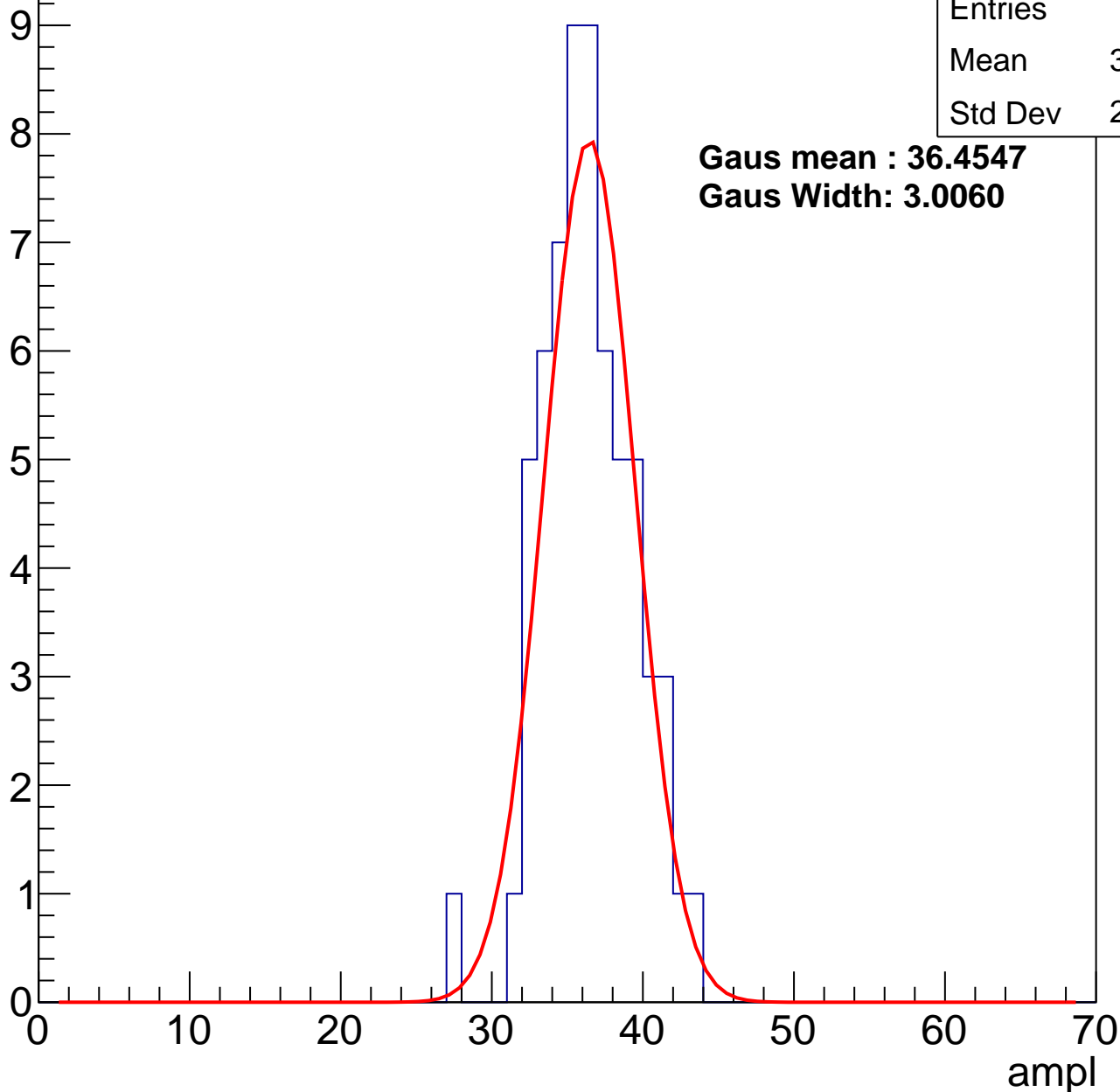
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	35.94
Std Dev	2.994

**Gaus mean : 36.4547**

**Gaus Width: 3.0060**



# B0L002S, U8-ch46, adc2

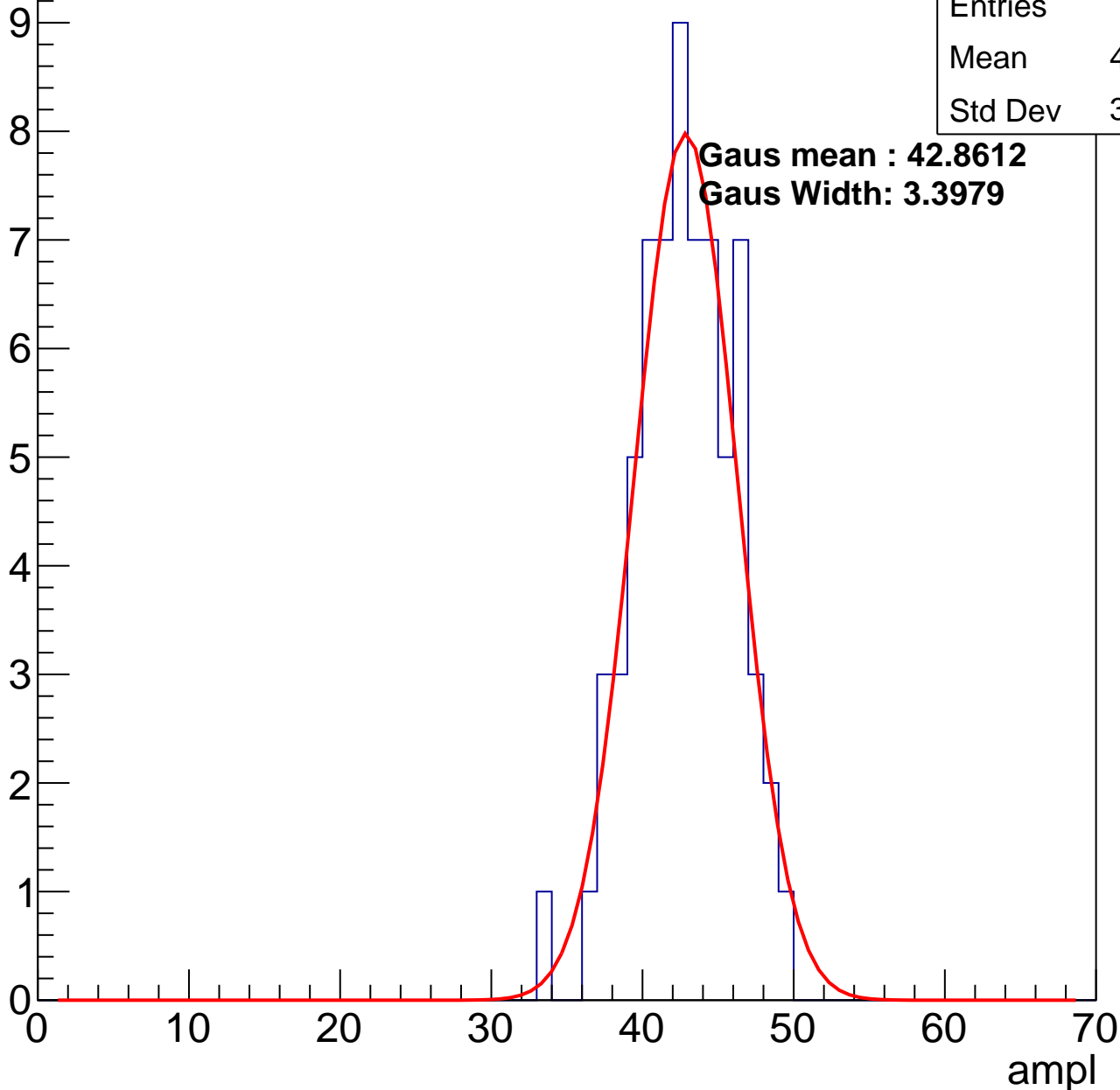
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.29
Std Dev	3.213

**Gaus mean : 42.8612**

**Gaus Width: 3.3979**

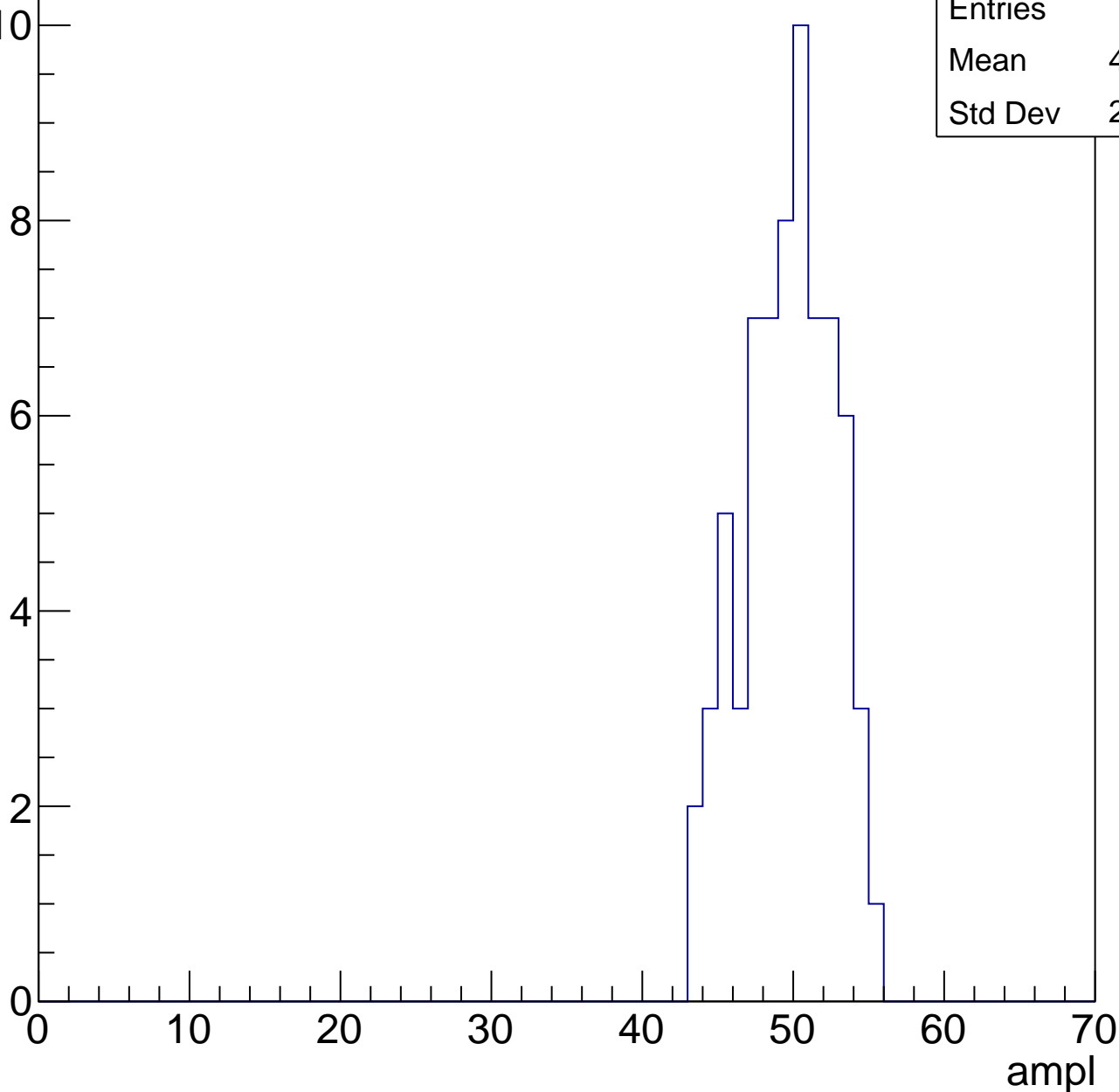


# B0L002S, U8-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

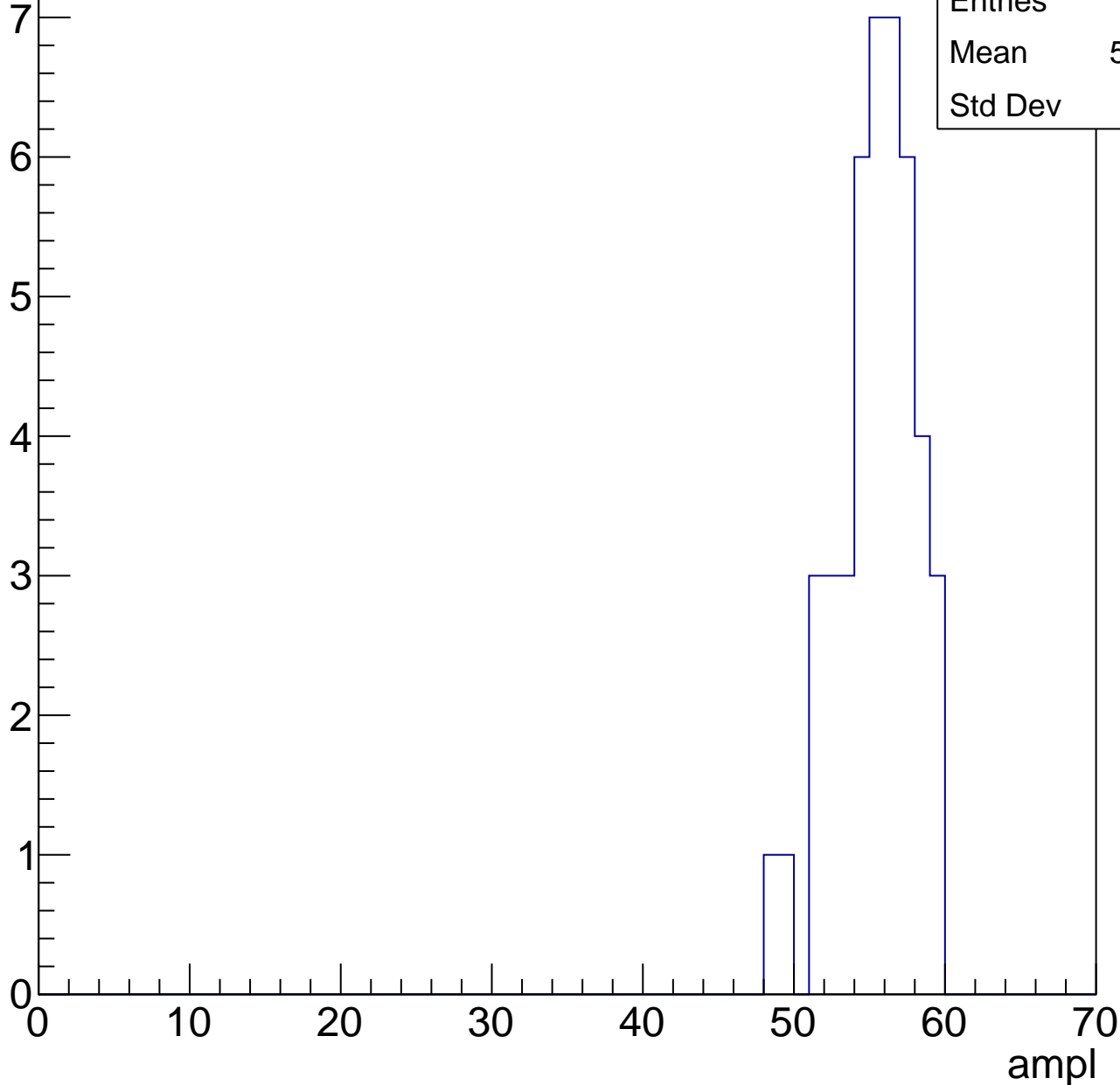
Entries	69
Mean	49.19
Std Dev	2.935



# B0L002S, U8-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

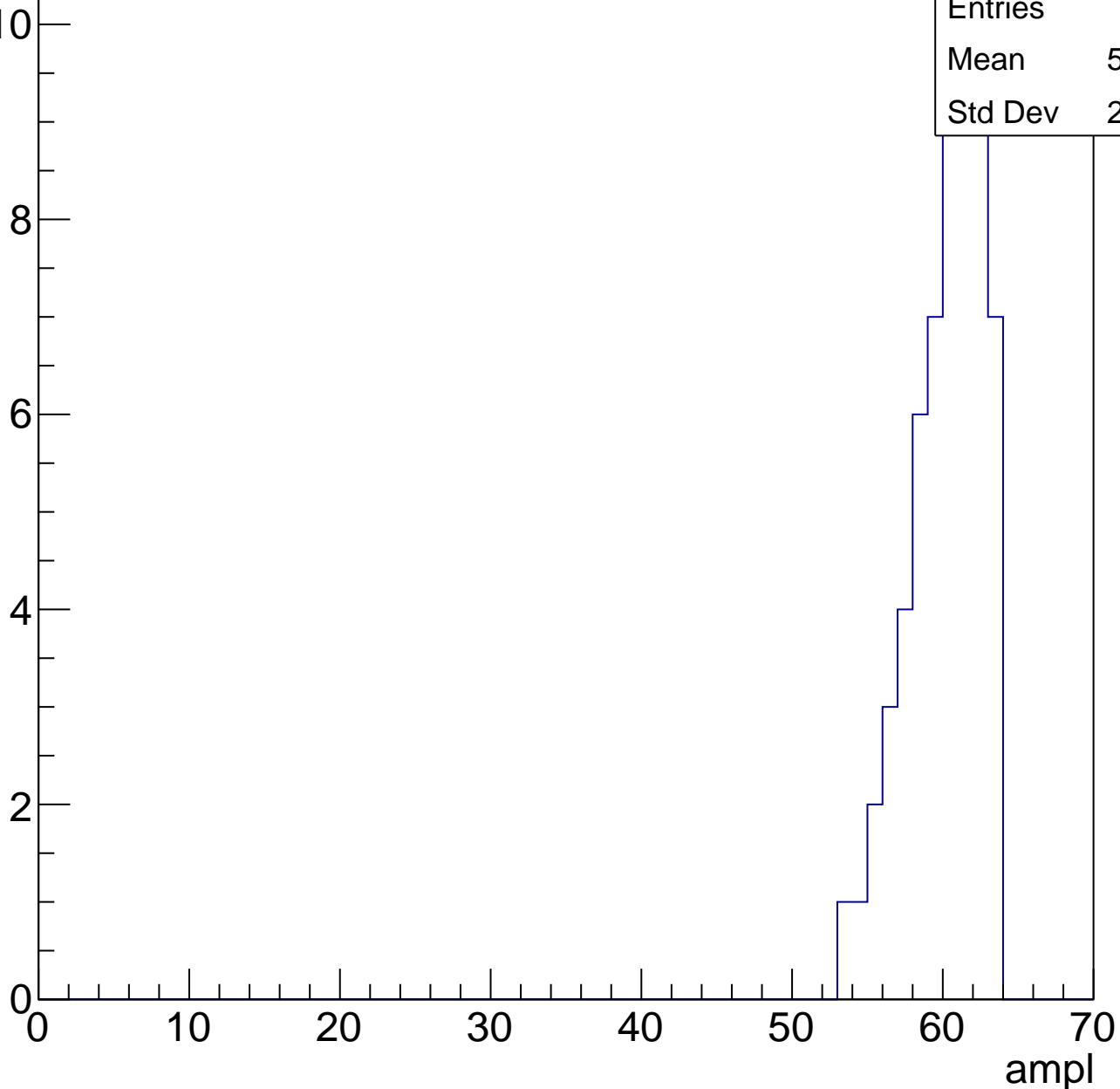


# B0L002S, U8-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

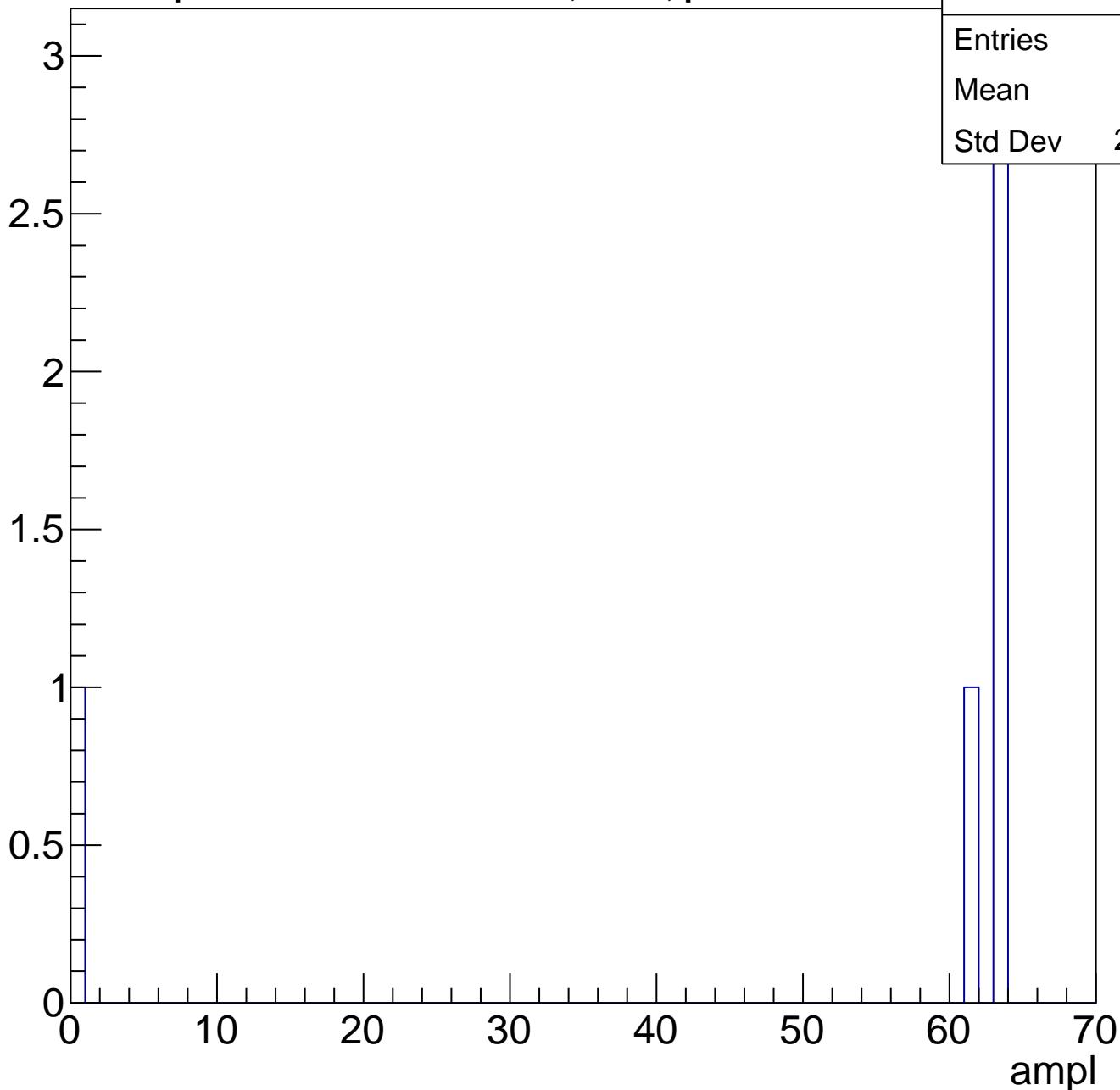
Entries	60
Mean	59.73
Std Dev	2.442



# B0L002S, U8-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L002S, U8-ch47, adc0

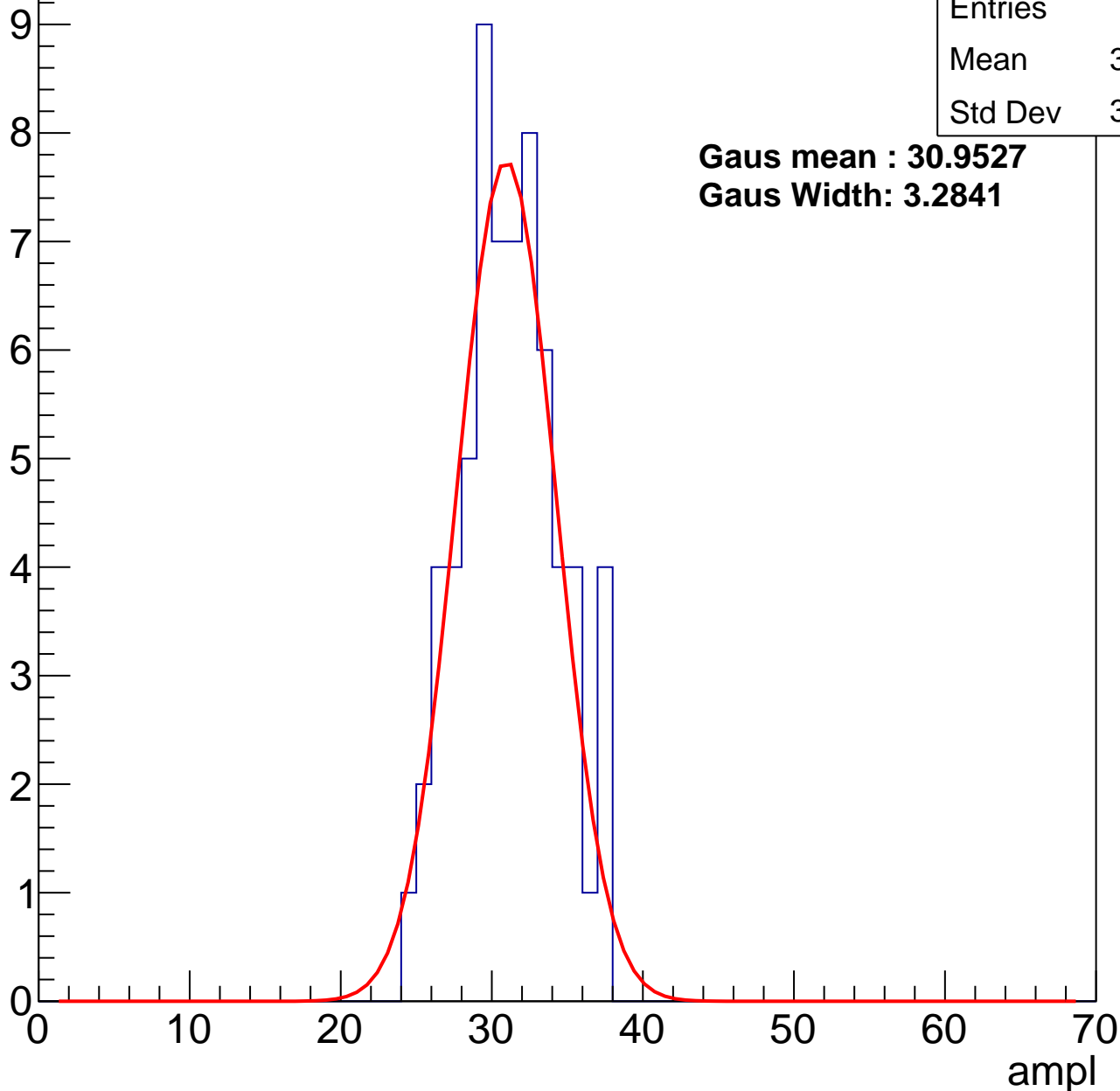
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.73
Std Dev	3.184

**Gaus mean : 30.9527**

**Gaus Width: 3.2841**



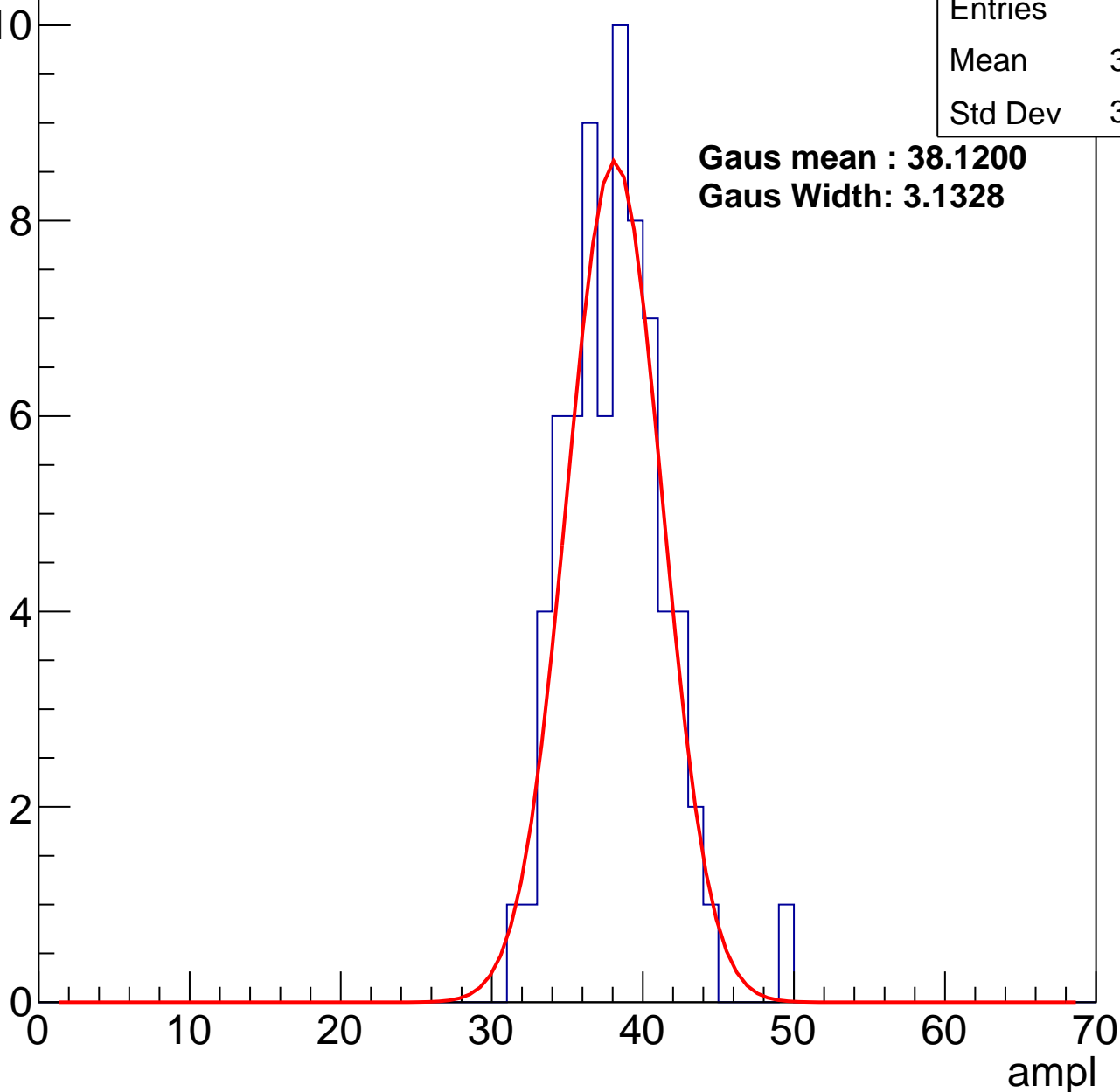
# B0L002S, U8-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.69
Std Dev	3.192

**Gaus mean : 38.1200**  
**Gaus Width: 3.1328**



# B0L002S, U8-ch47, adc2

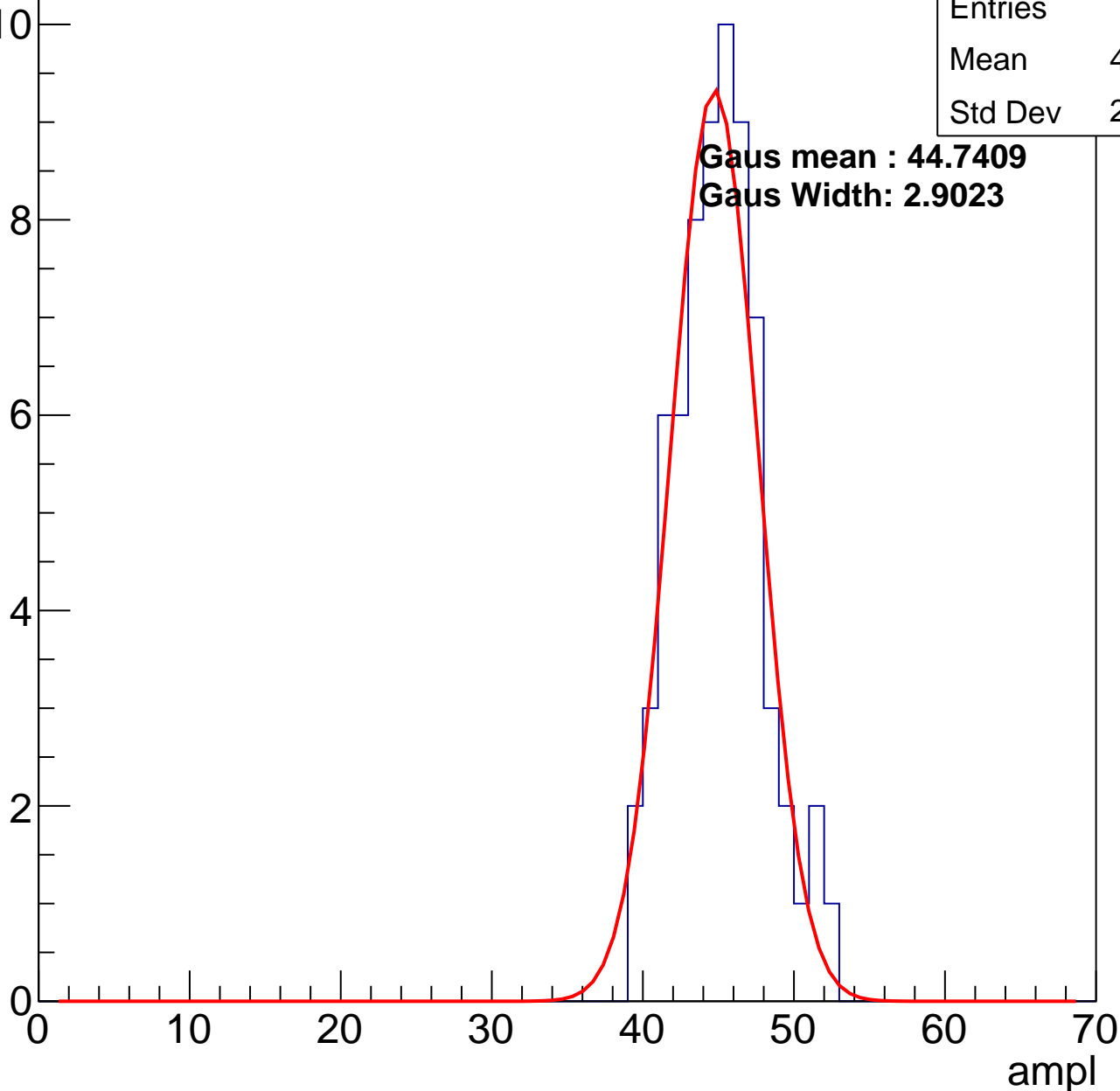
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.57
Std Dev	2.866

**Gaus mean : 44.7409**

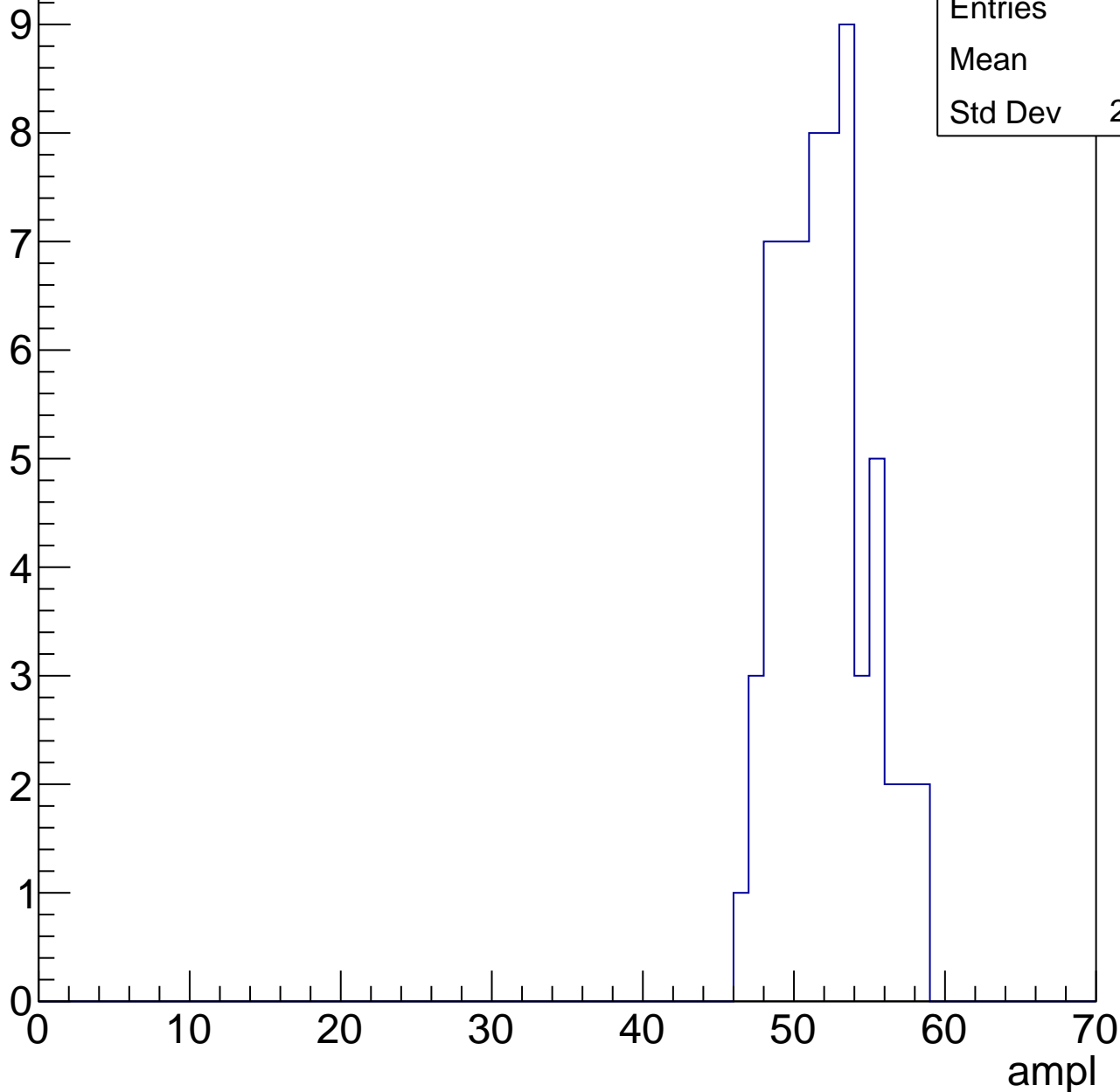
**Gaus Width: 2.9023**



# B0L002S, U8-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

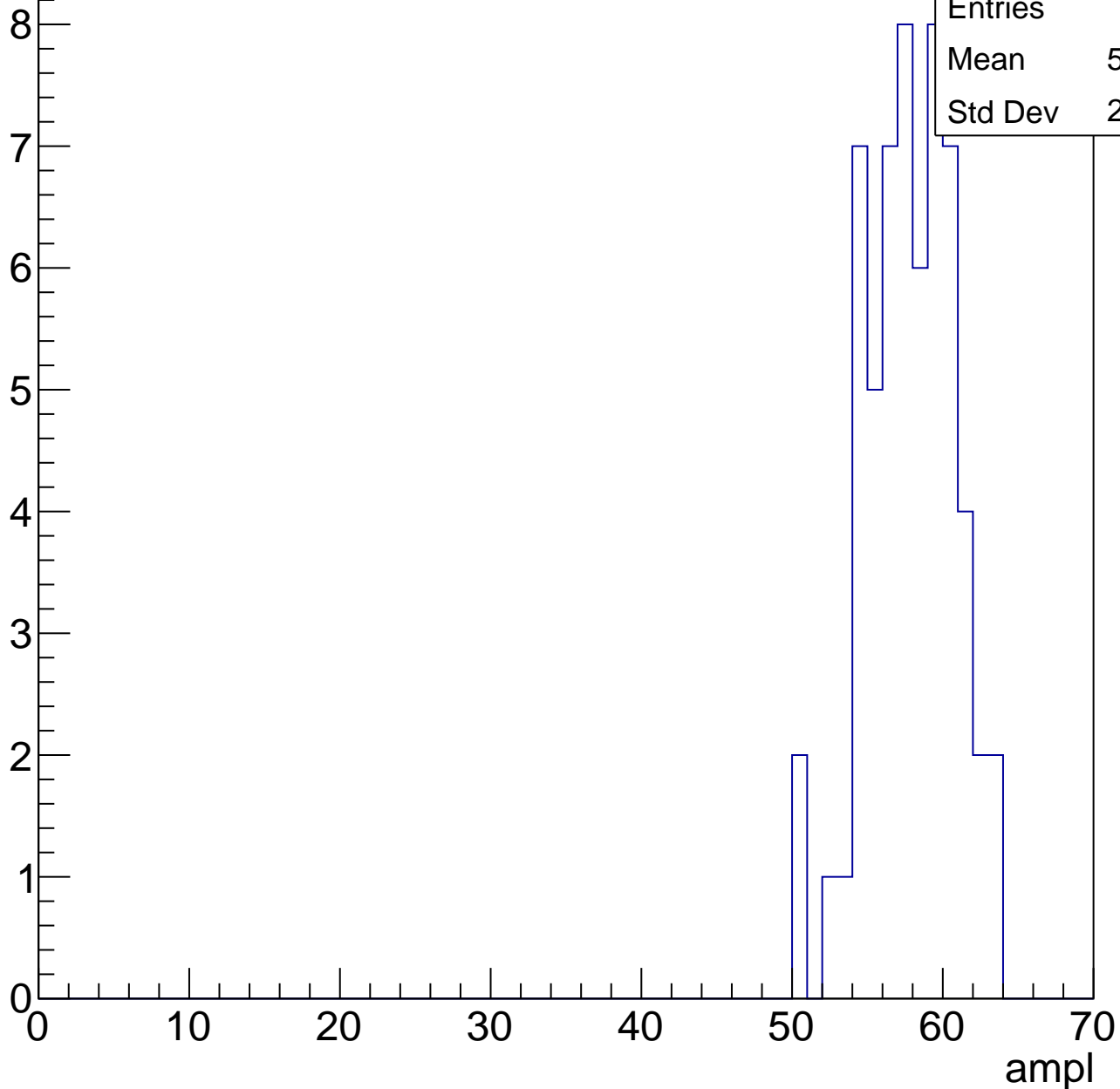


Entries	64
Mean	51.5
Std Dev	2.867

# B0L002S, U8-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

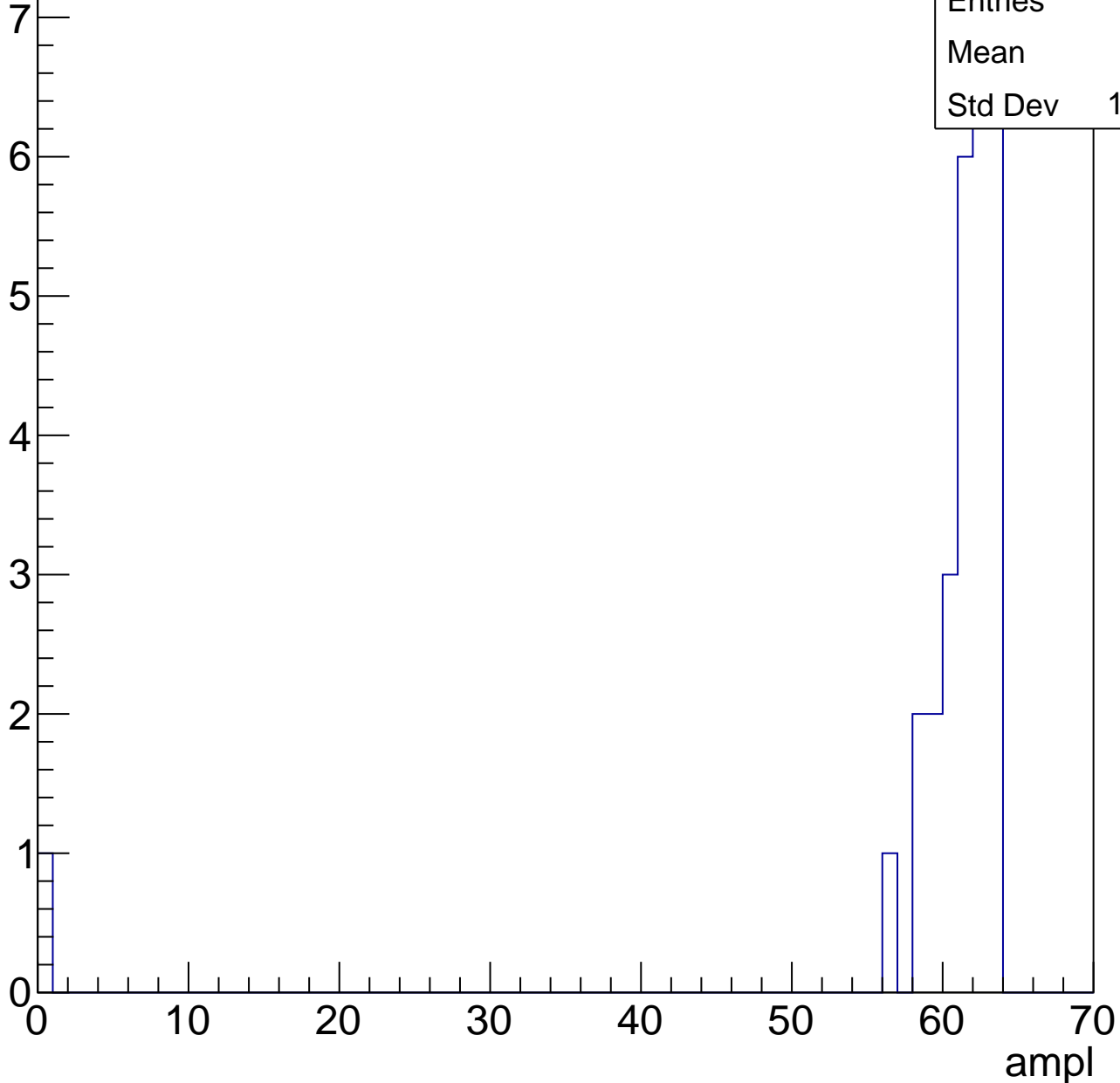


# B0L002S, U8-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

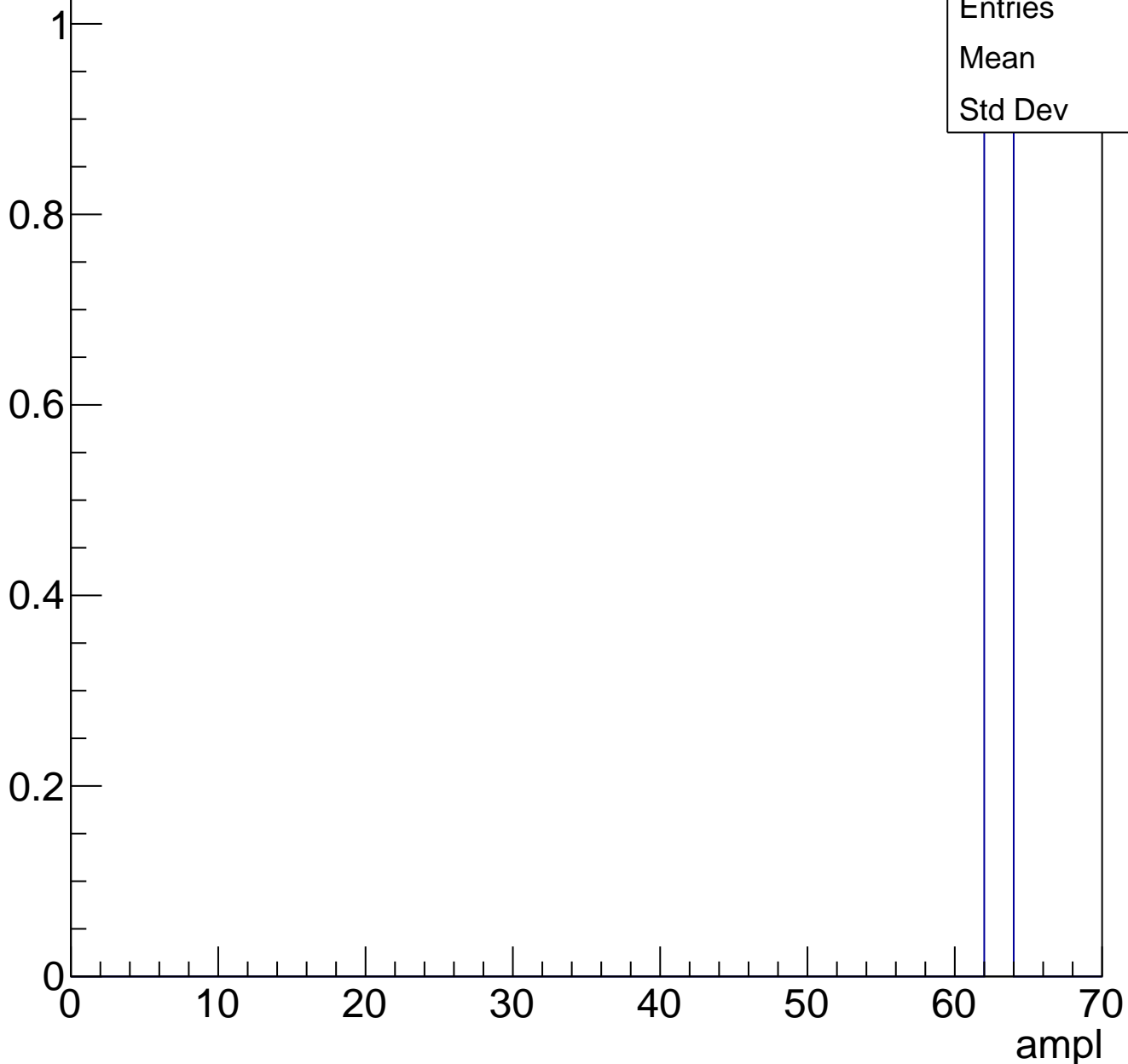
Entries	29
Mean	59
Std Dev	11.29



# B0L002S, U8-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	62.5
Std Dev	0.5



# B0L002S, U8-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch48, adc0

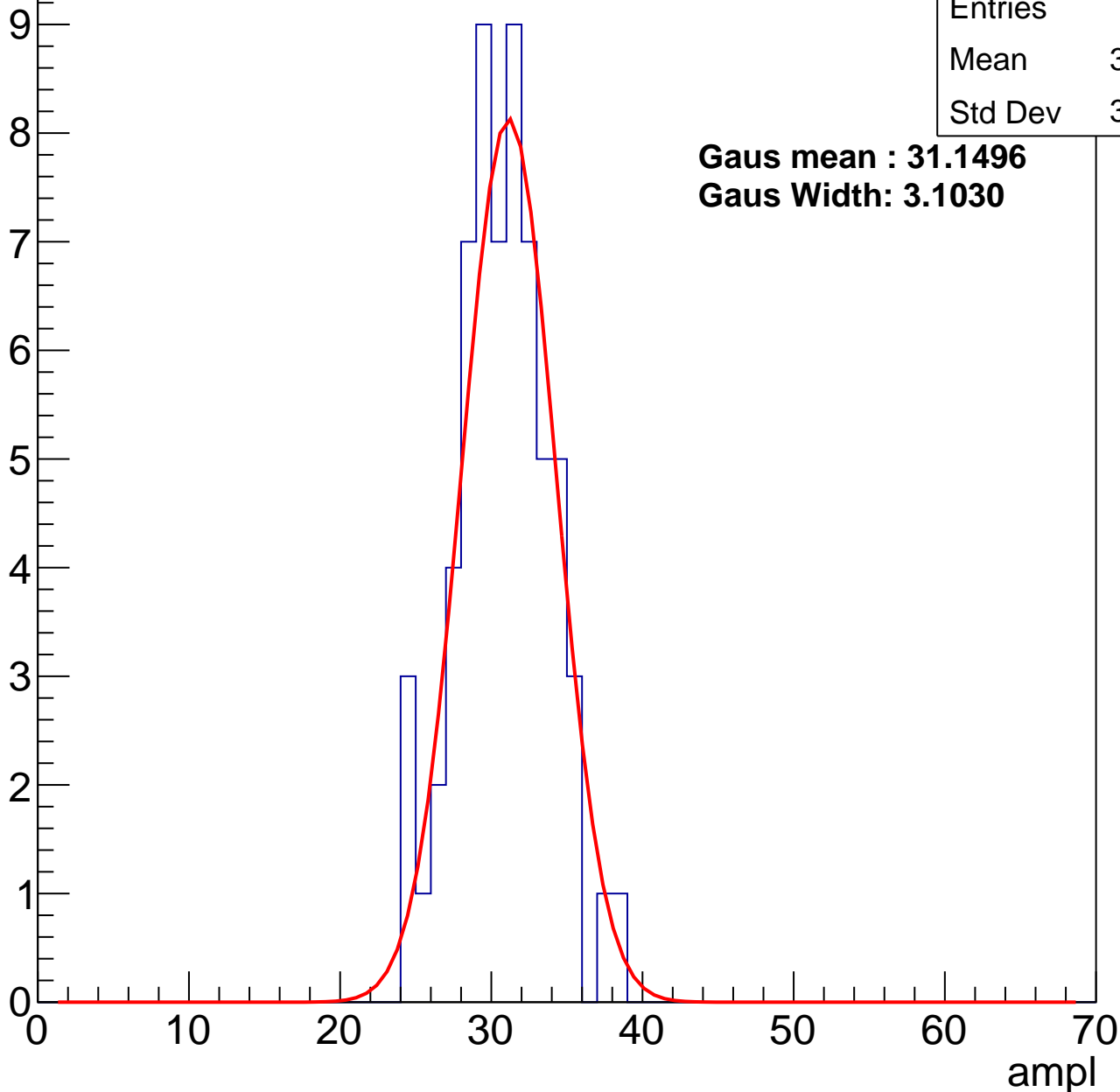
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.34
Std Dev	3.012

**Gaus mean : 31.1496**

**Gaus Width: 3.1030**



# B0L002S, U8-ch48, adc1

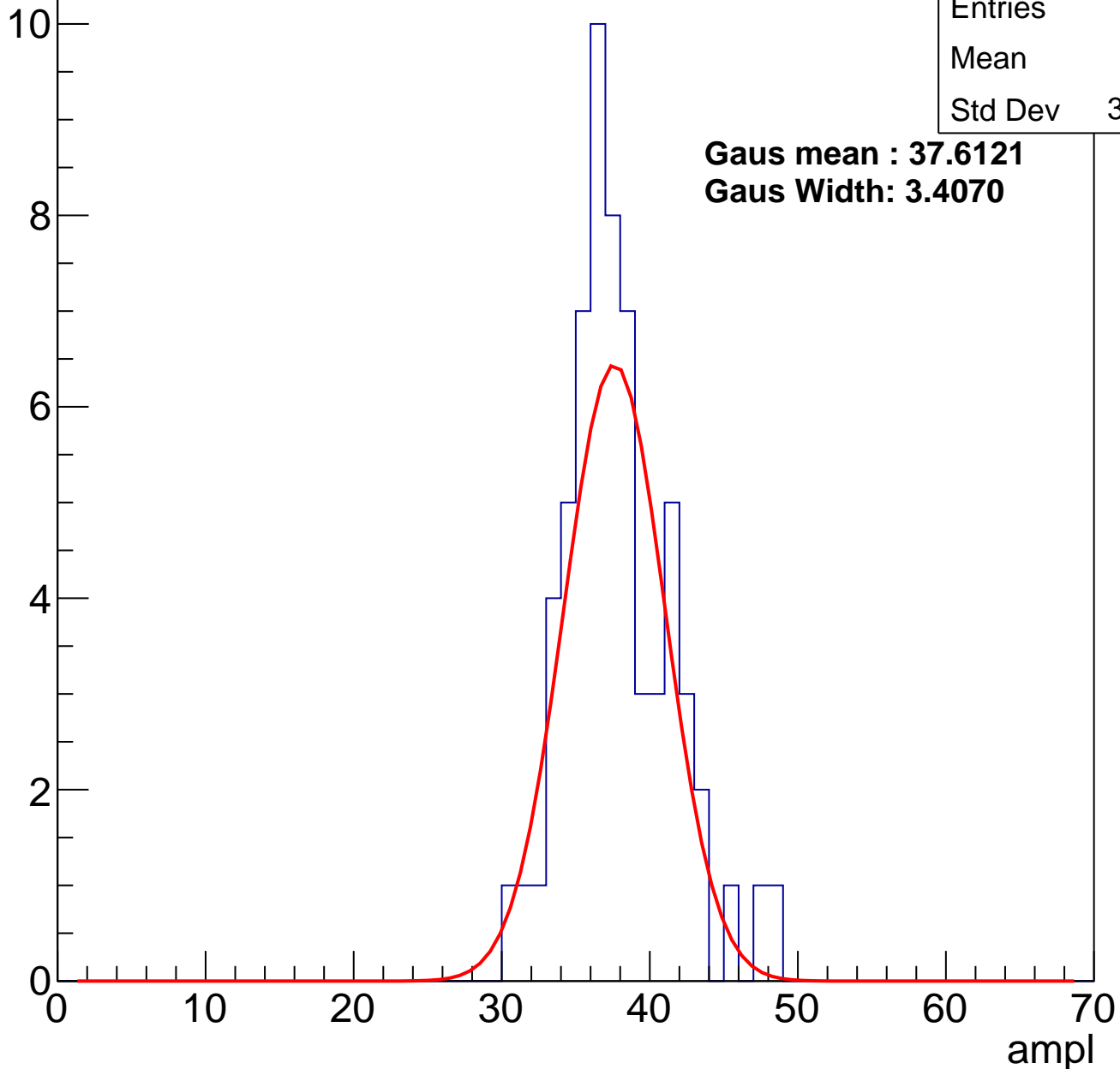
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	63
Mean	37.4
Std Dev	3.575

**Gaus mean : 37.6121**

**Gaus Width: 3.4070**

Entry



# B0L002S, U8-ch48, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	76
Mean	43.68
Std Dev	3.353

**Gaus mean : 44.2854**

**Gaus Width: 3.8332**

Entry

10

8

6

4

2

0

0

10

20

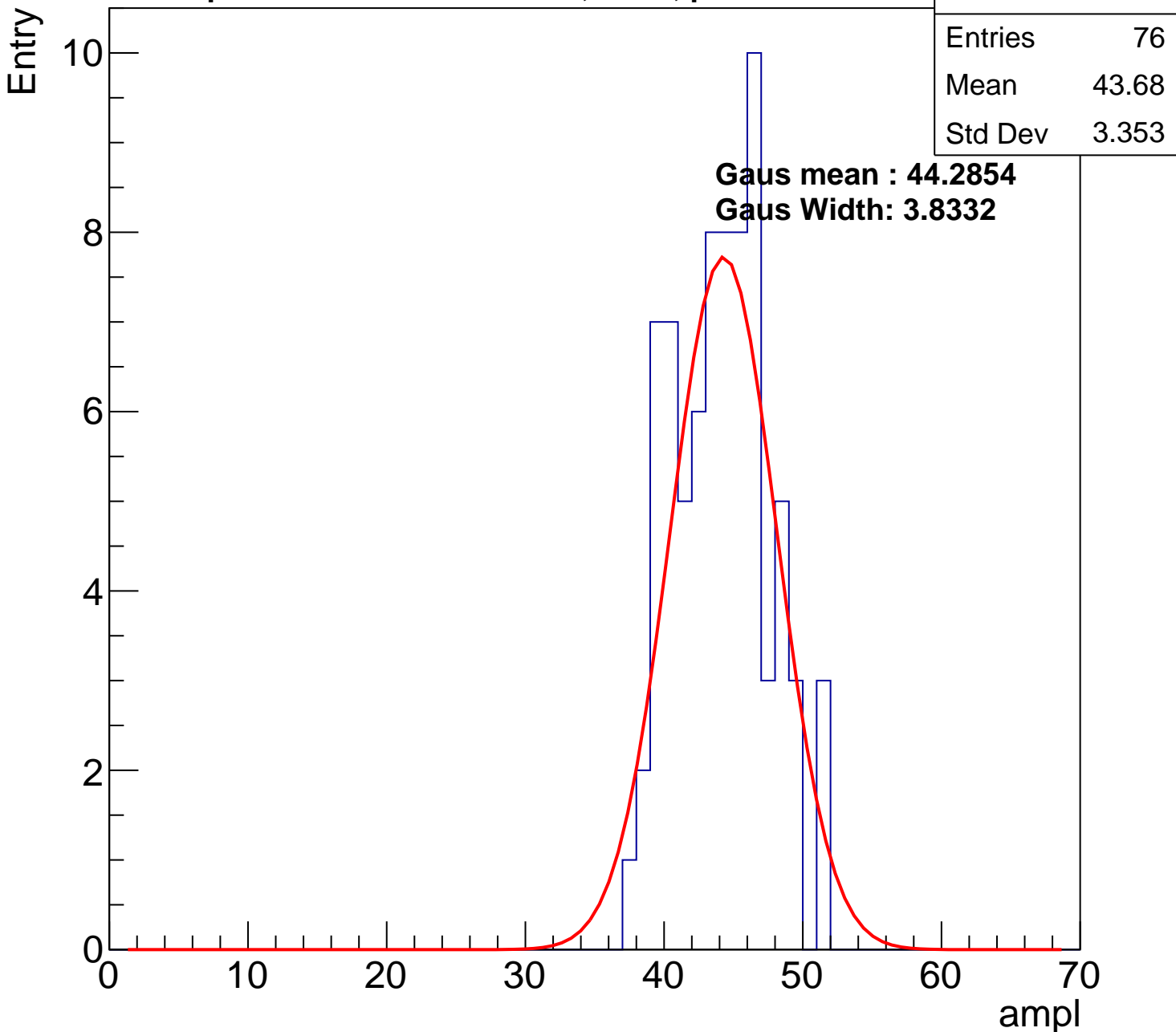
30

40

50

60

ampl

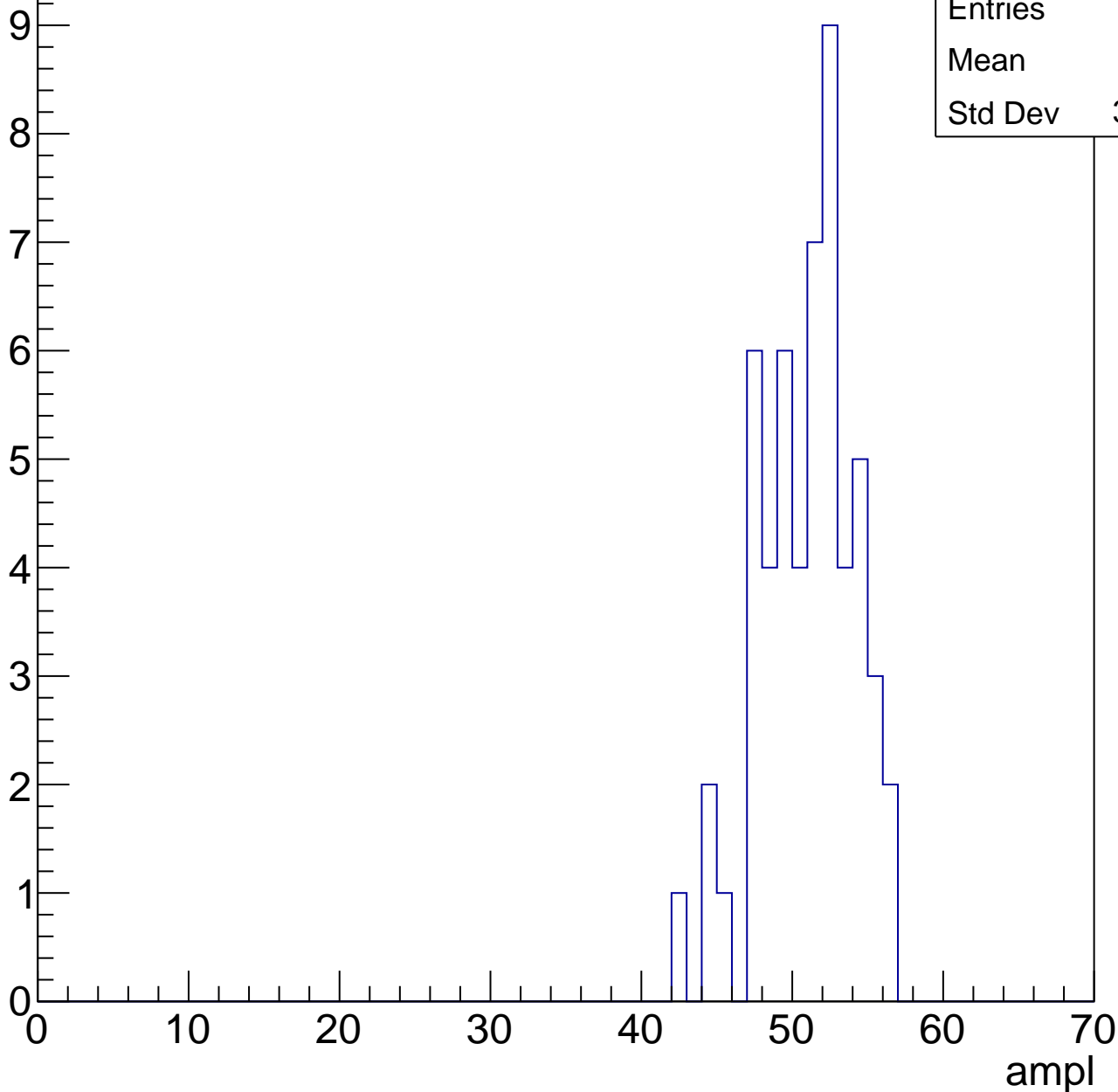


# B0L002S, U8-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

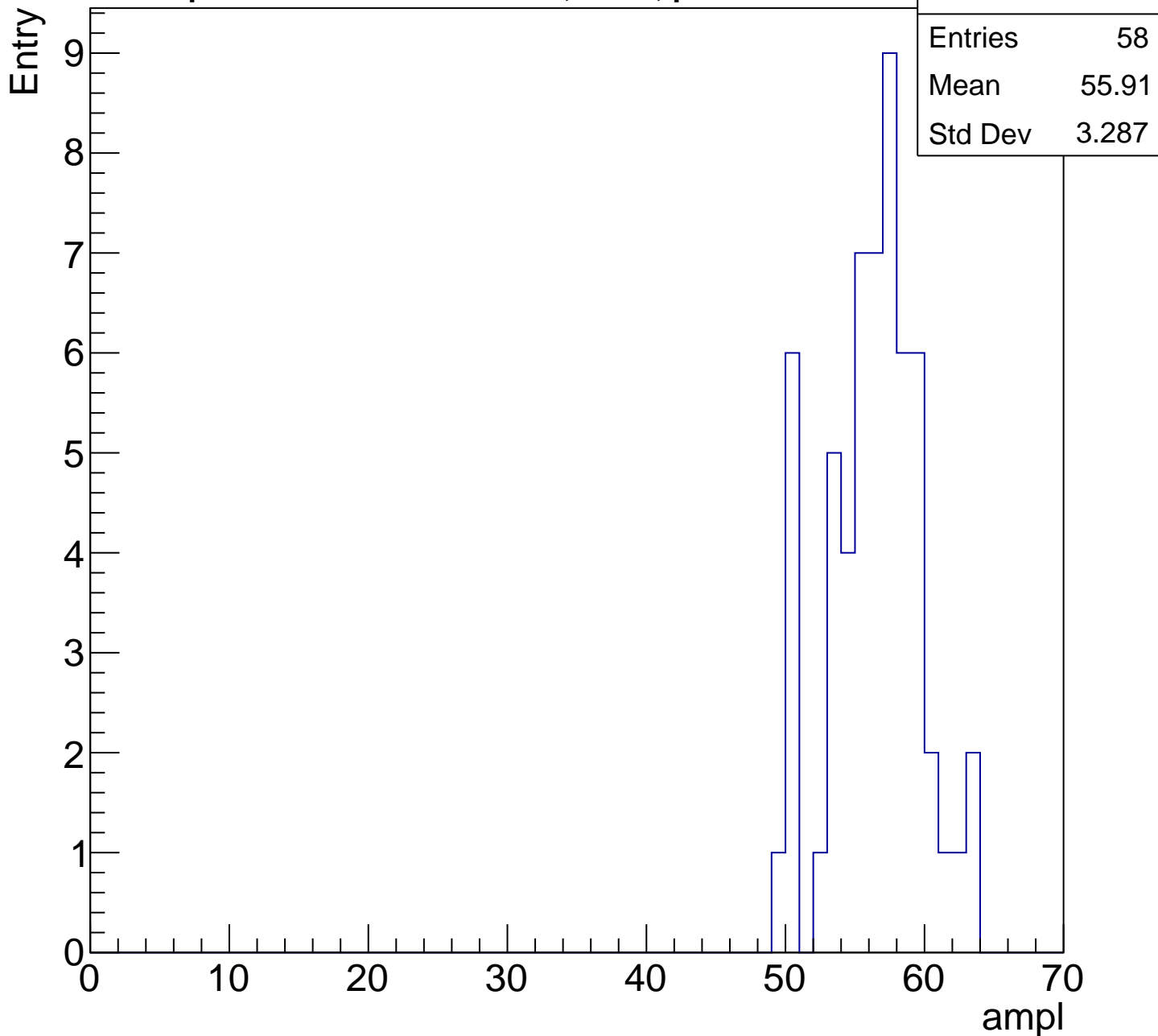
Entry

Entries	54
Mean	50.5
Std Dev	3.131



# B0L002S, U8-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

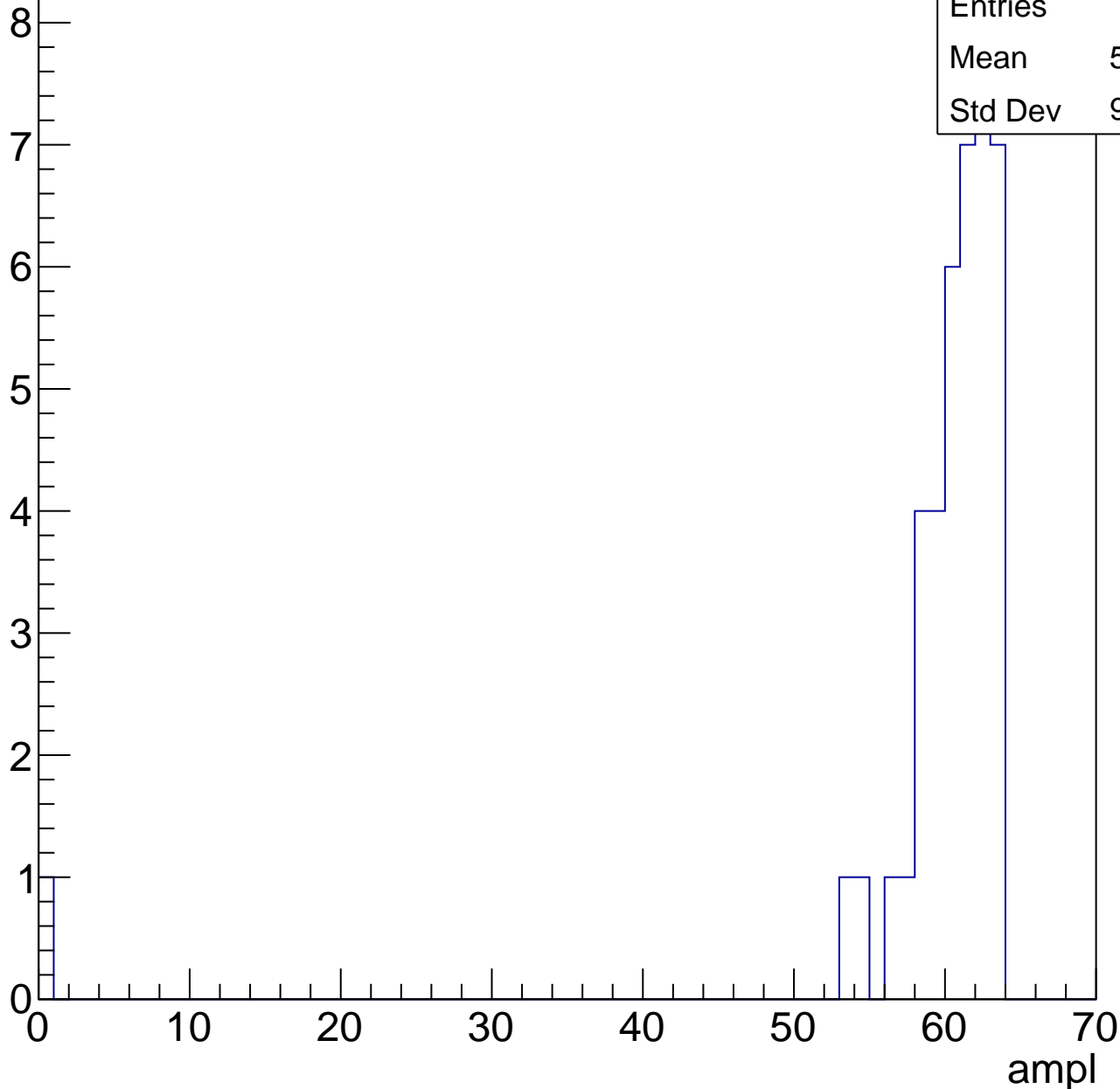


# B0L002S, U8-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.83
Std Dev	9.596



# B0L002S, U8-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

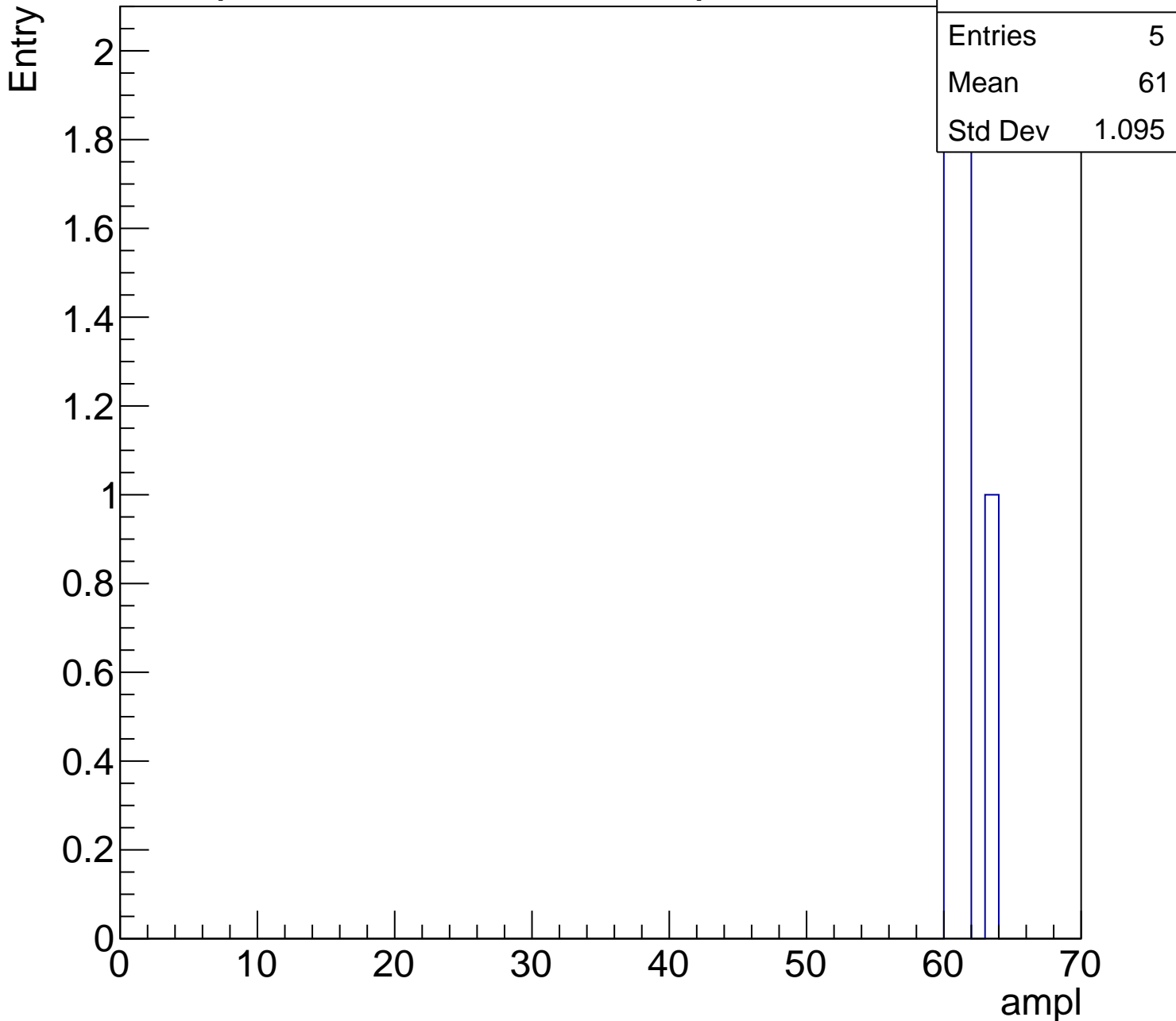
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61
Std Dev	1.095

0 10 20 30 40 50 60 70

ampl





# B0L002S, U8-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch49, adc0

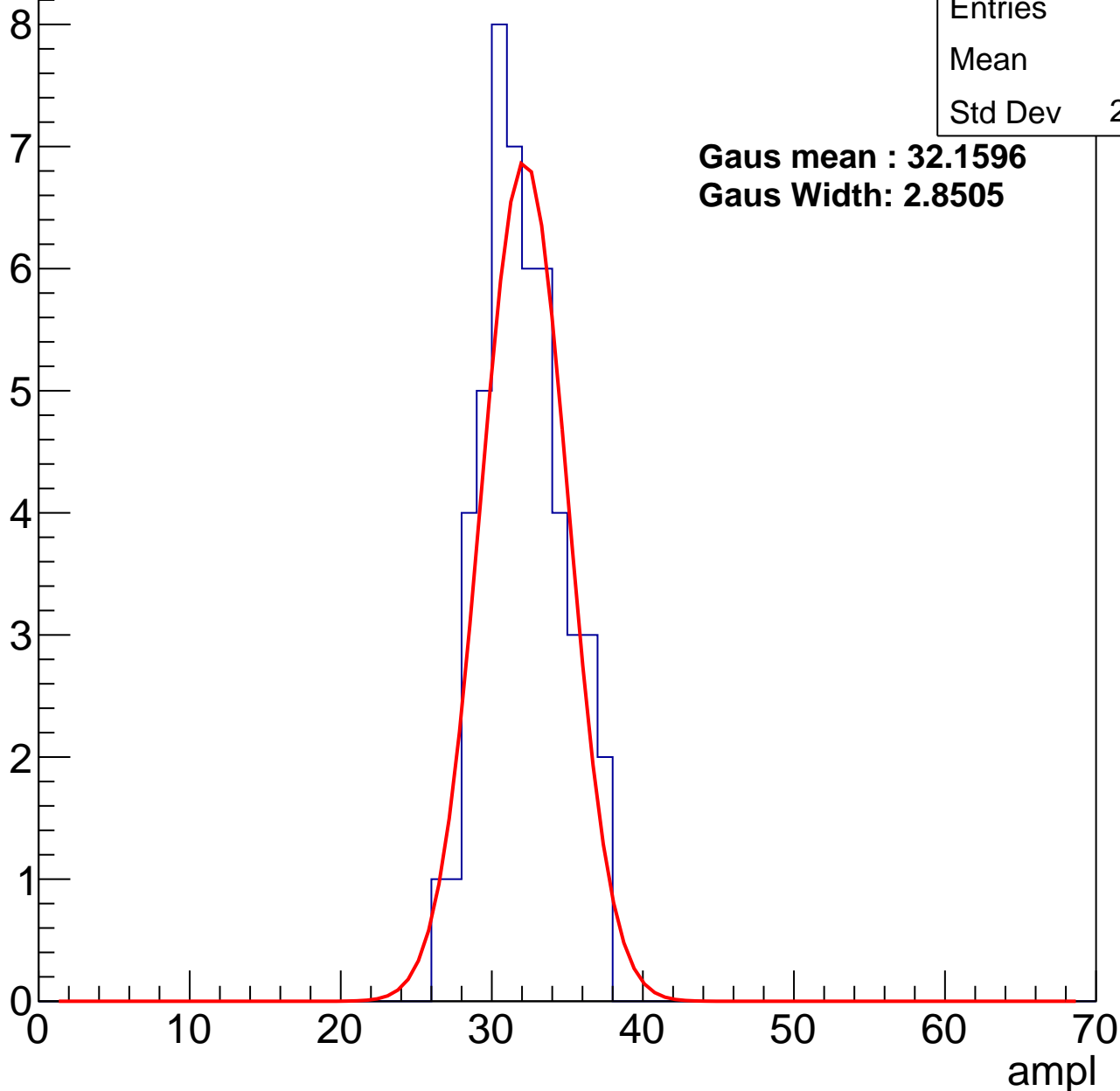
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	31.6
Std Dev	2.638

**Gaus mean : 32.1596**

**Gaus Width: 2.8505**



# B0L002S, U8-ch49, adc1

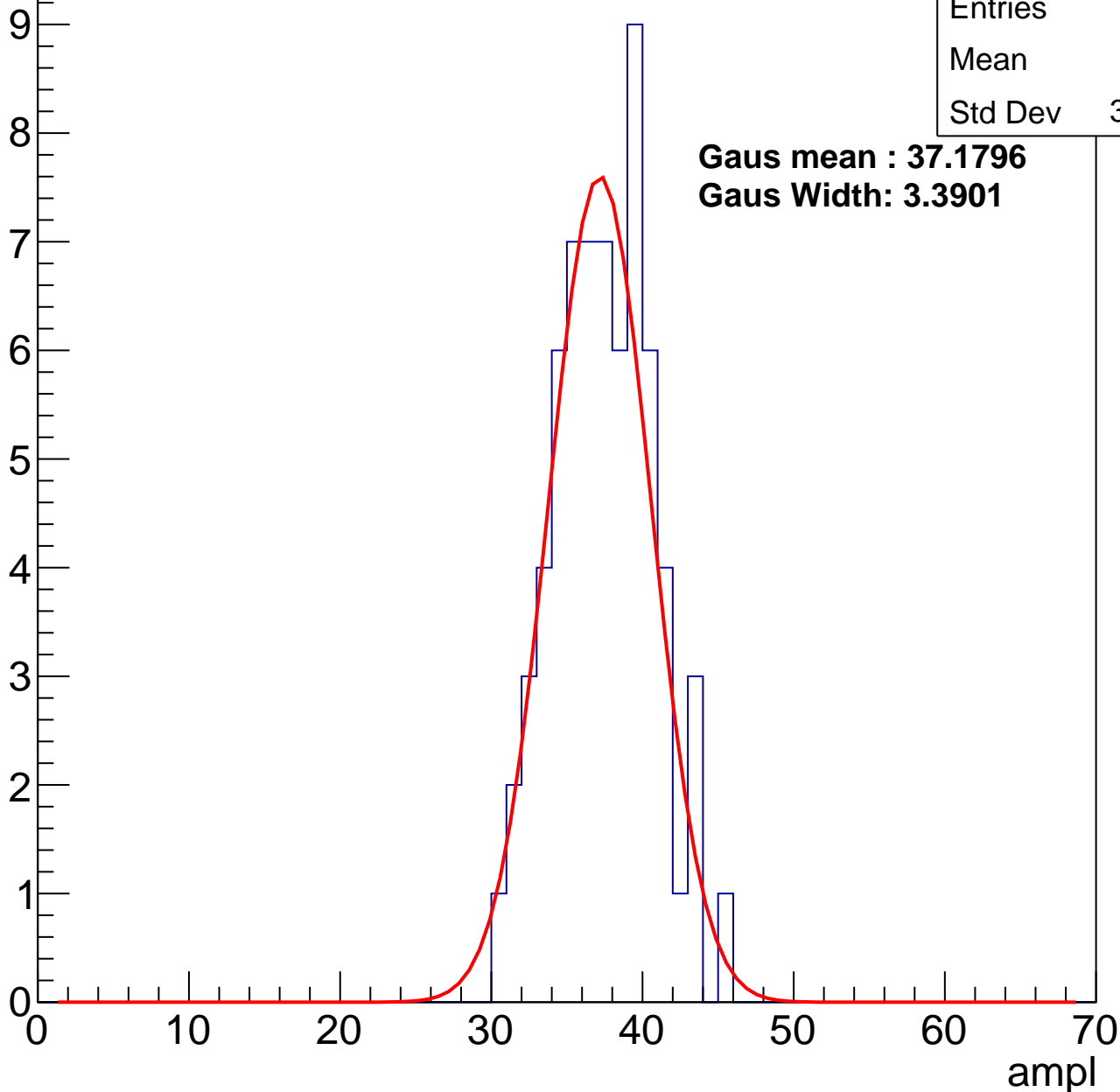
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37
Std Dev	3.246

**Gaus mean : 37.1796**

**Gaus Width: 3.3901**



# B0L002S, U8-ch49, adc2

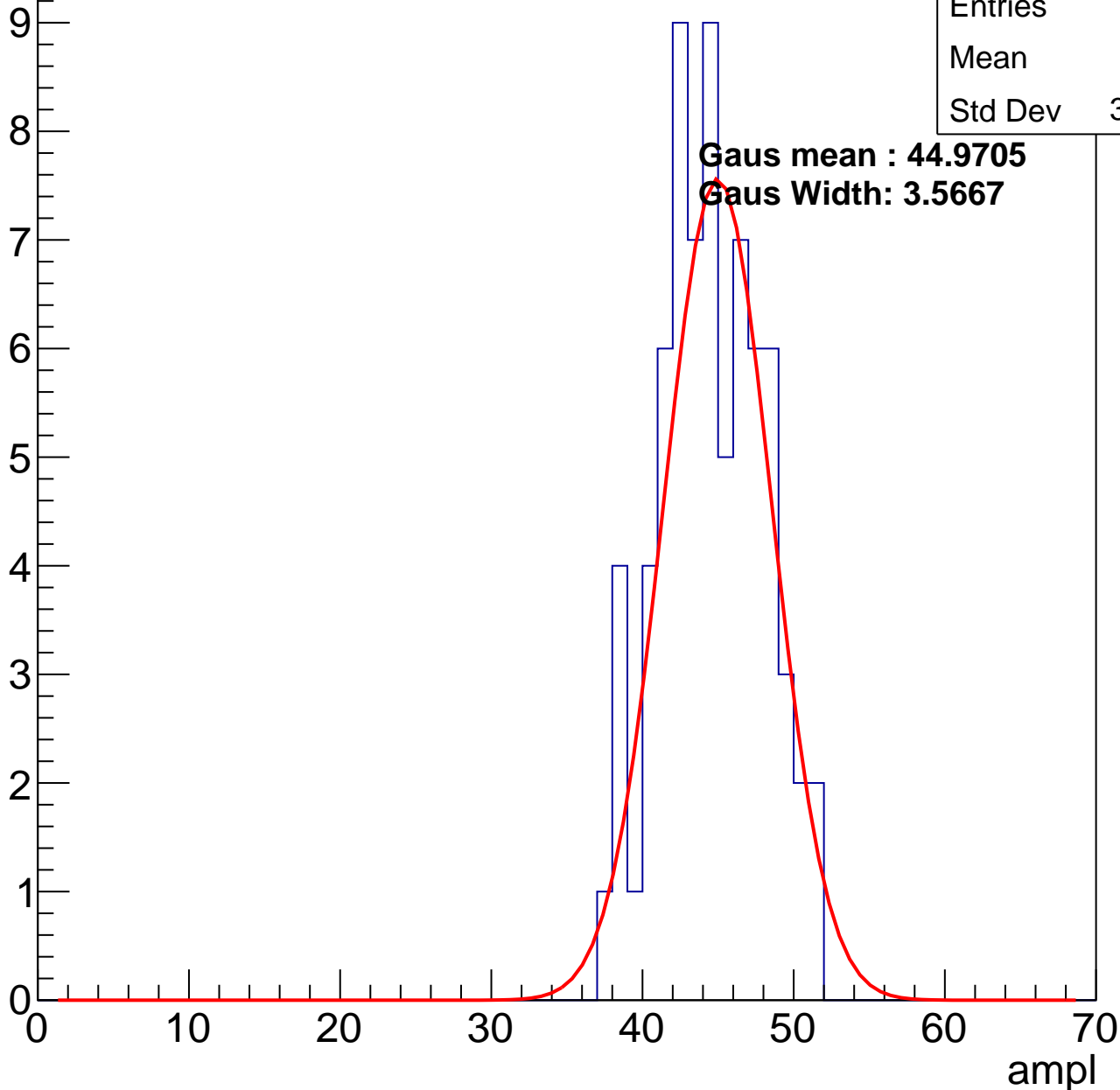
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.1
Std Dev	3.346

**Gaus mean : 44.9705**

**Gaus Width: 3.5667**

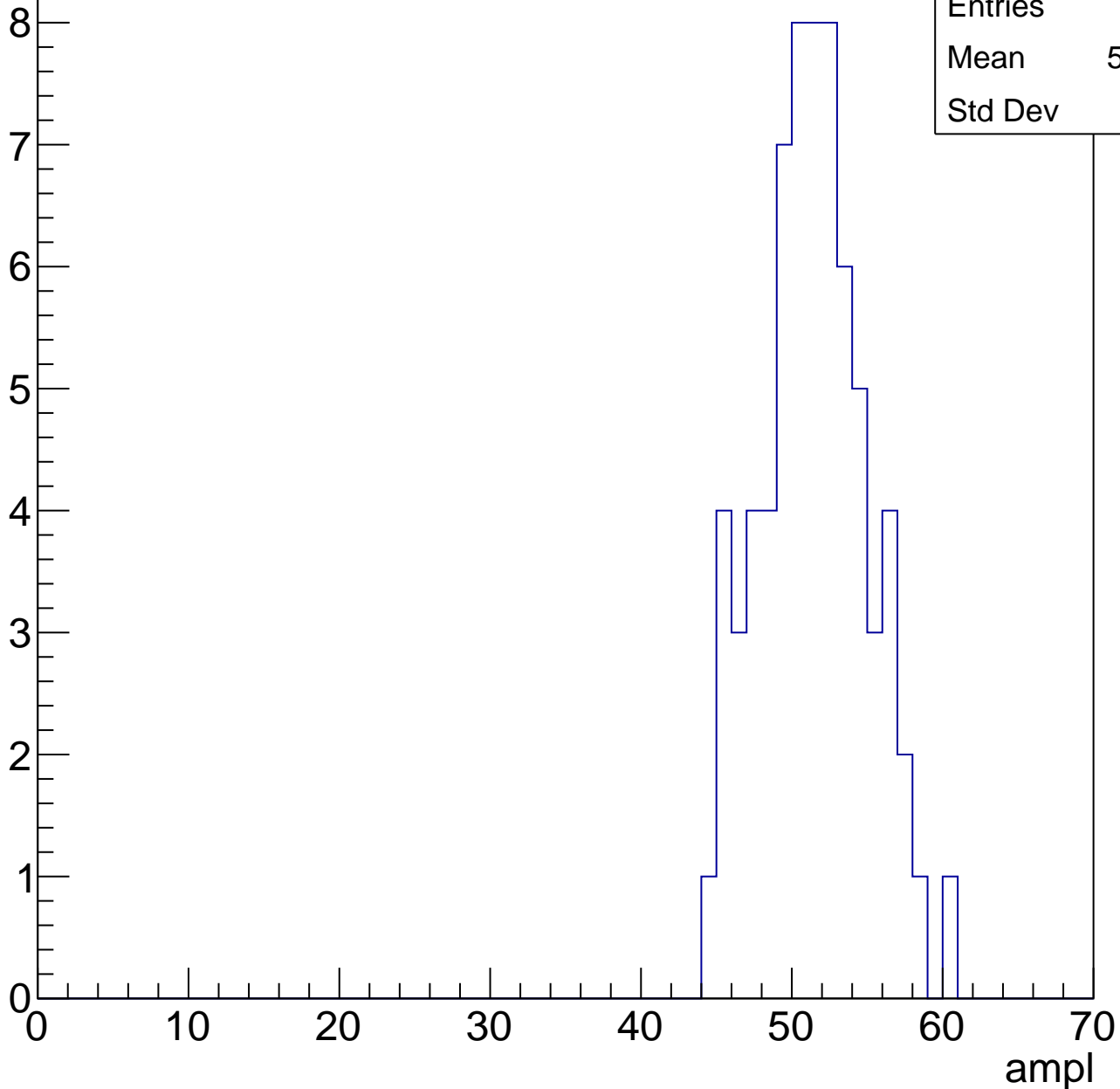


# B0L002S, U8-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.99
Std Dev	3.47



# B0L002S, U8-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 56.25

Std Dev 8.38

ampl

0

10

20

30

40

50

60

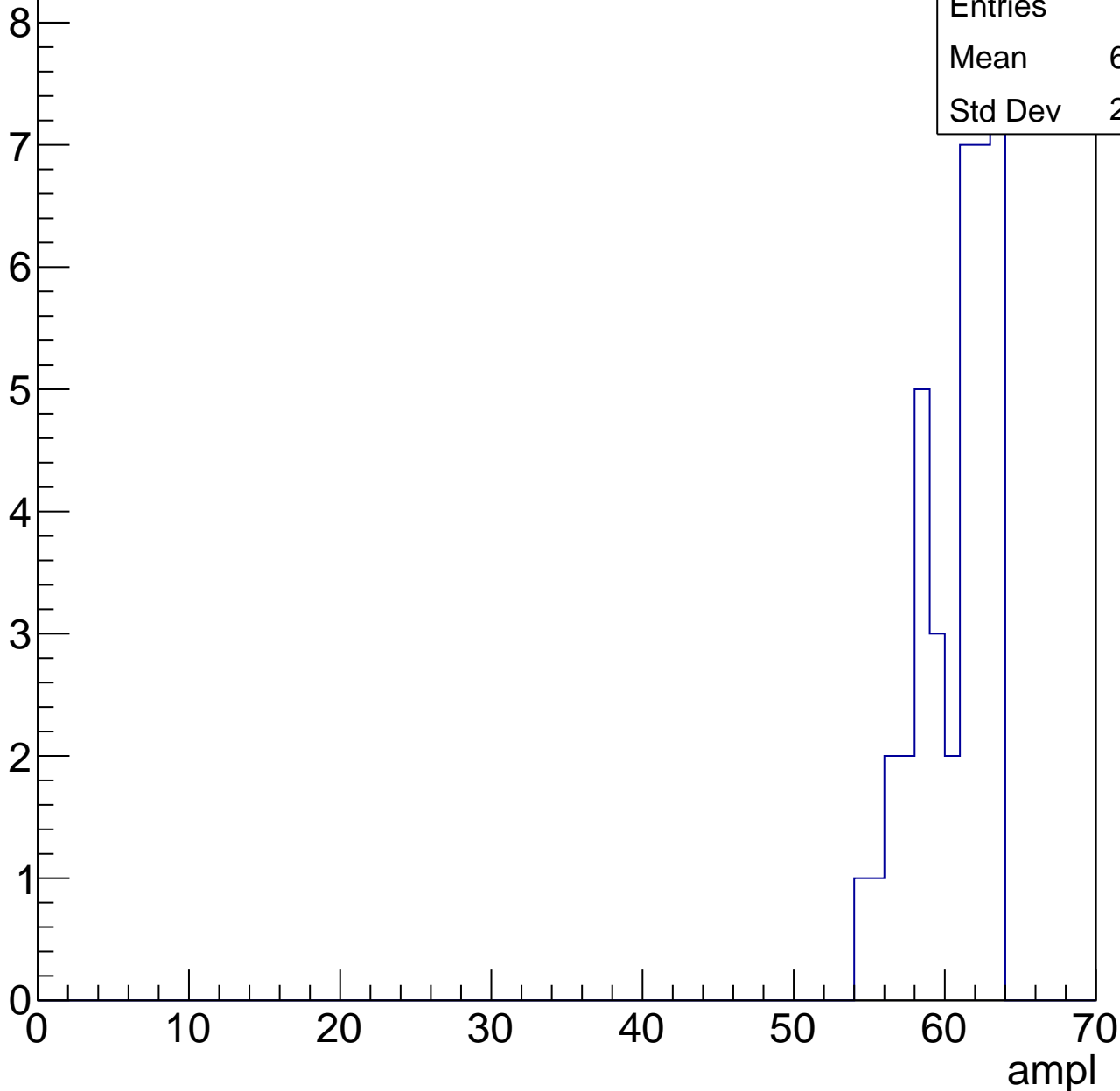
70

# B0L002S, U8-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	60.18
Std Dev	2.512



# B0L002S, U8-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L002S, U8-ch50, adc0

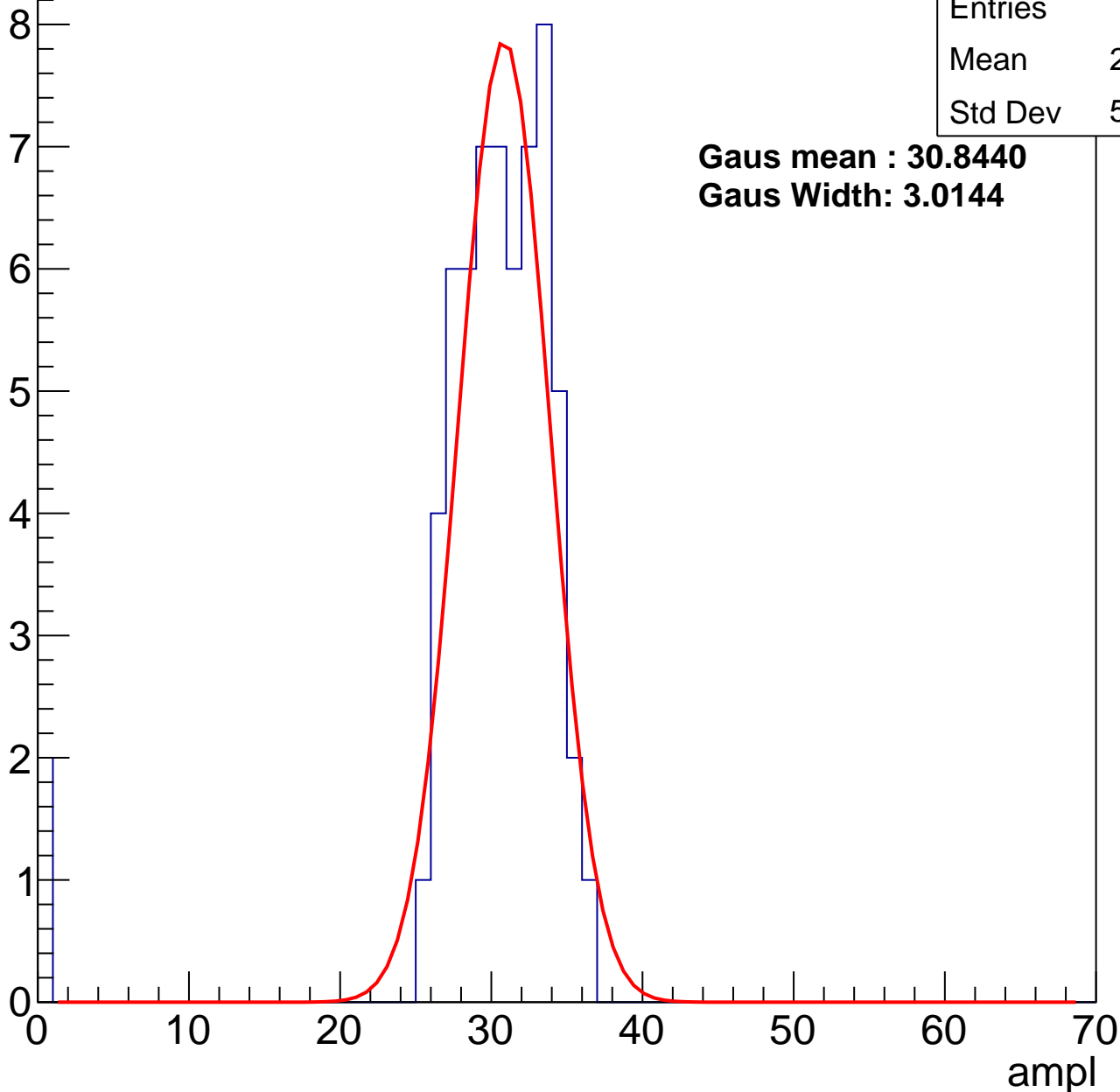
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.39
Std Dev	5.987

**Gaus mean : 30.8440**

**Gaus Width: 3.0144**



# B0L002S, U8-ch50, adc1

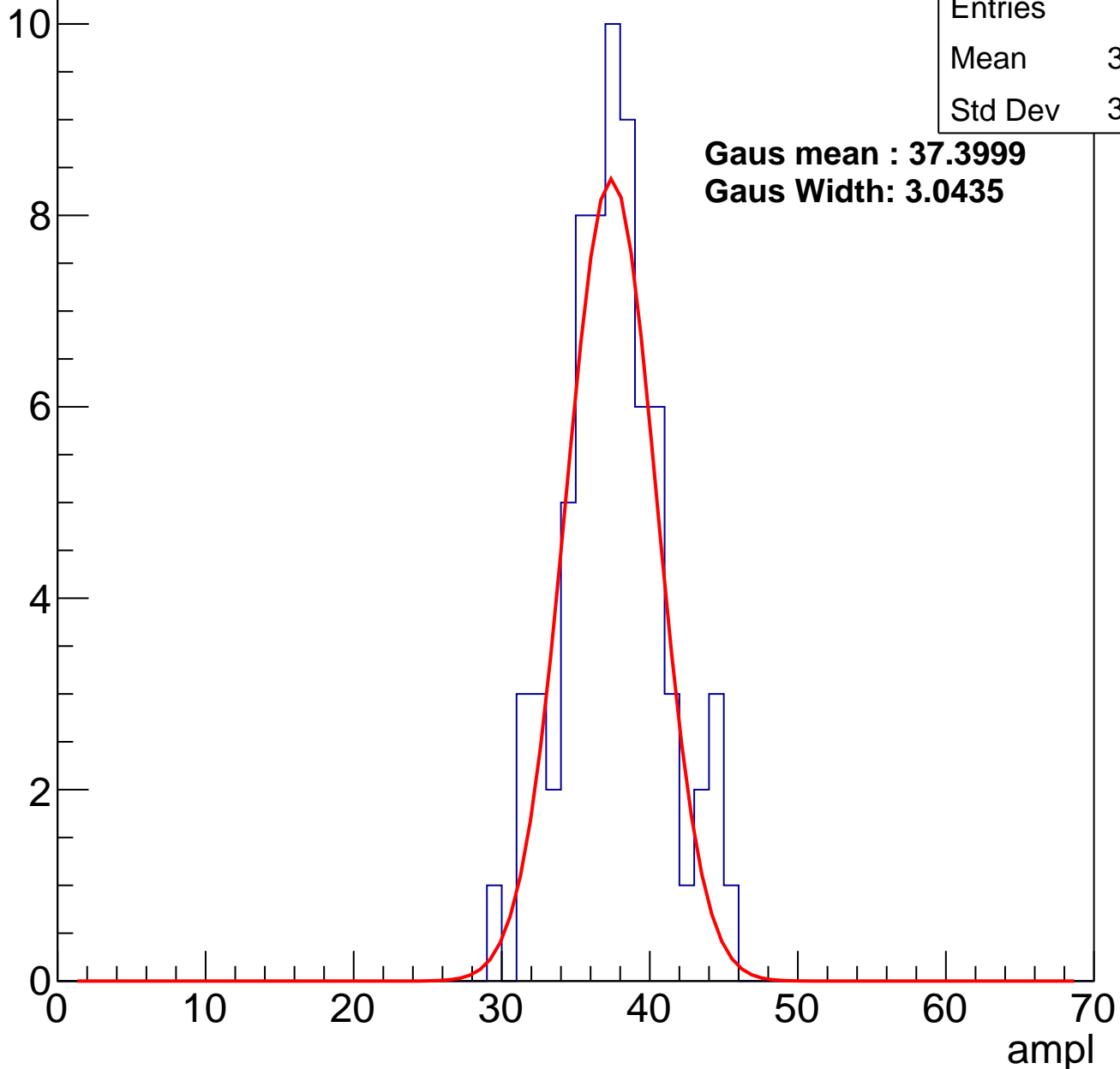
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	37.13
Std Dev	3.369

**Gaus mean : 37.3999**

**Gaus Width: 3.0435**

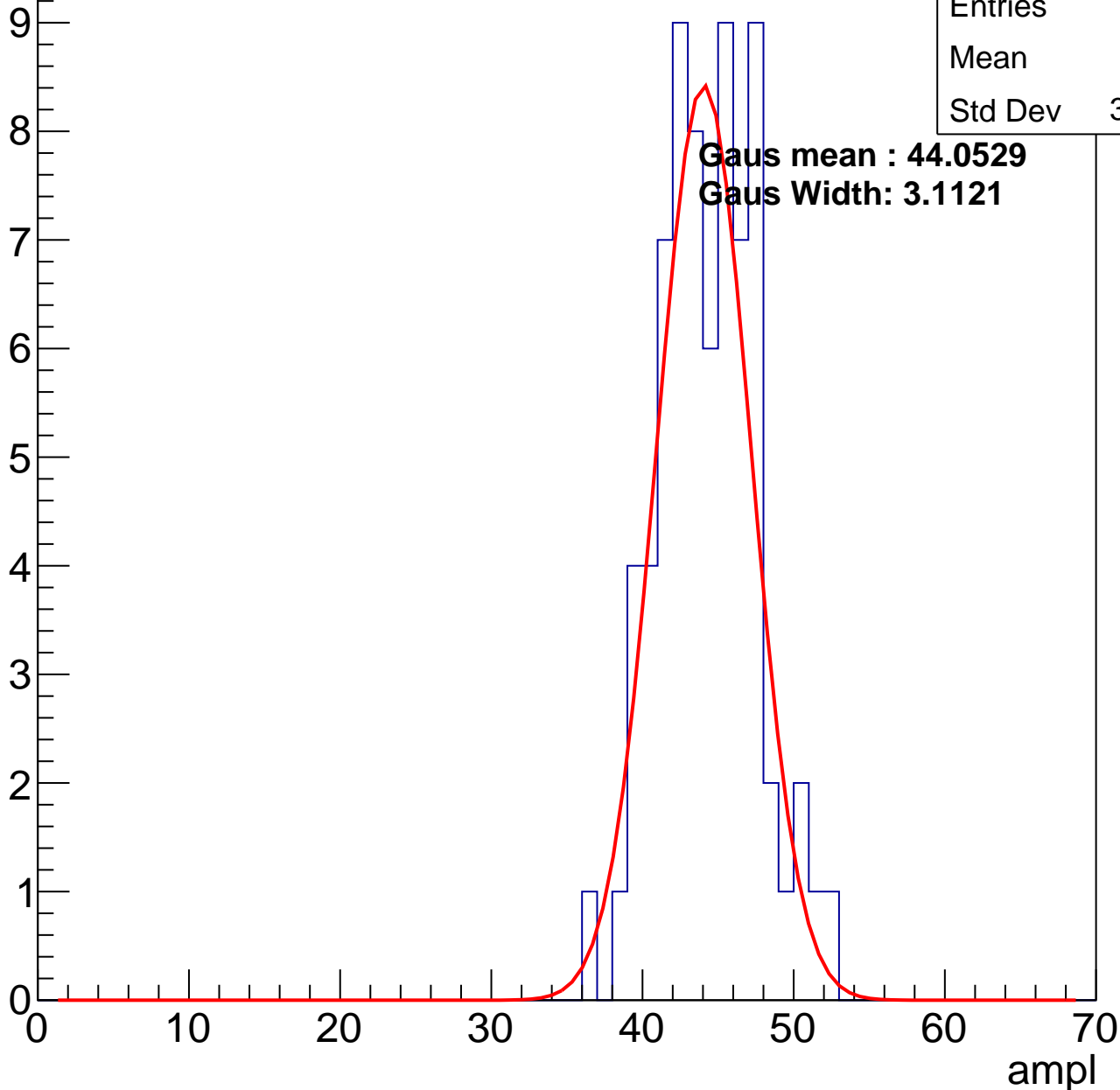
Entry



# B0L002S, U8-ch50, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

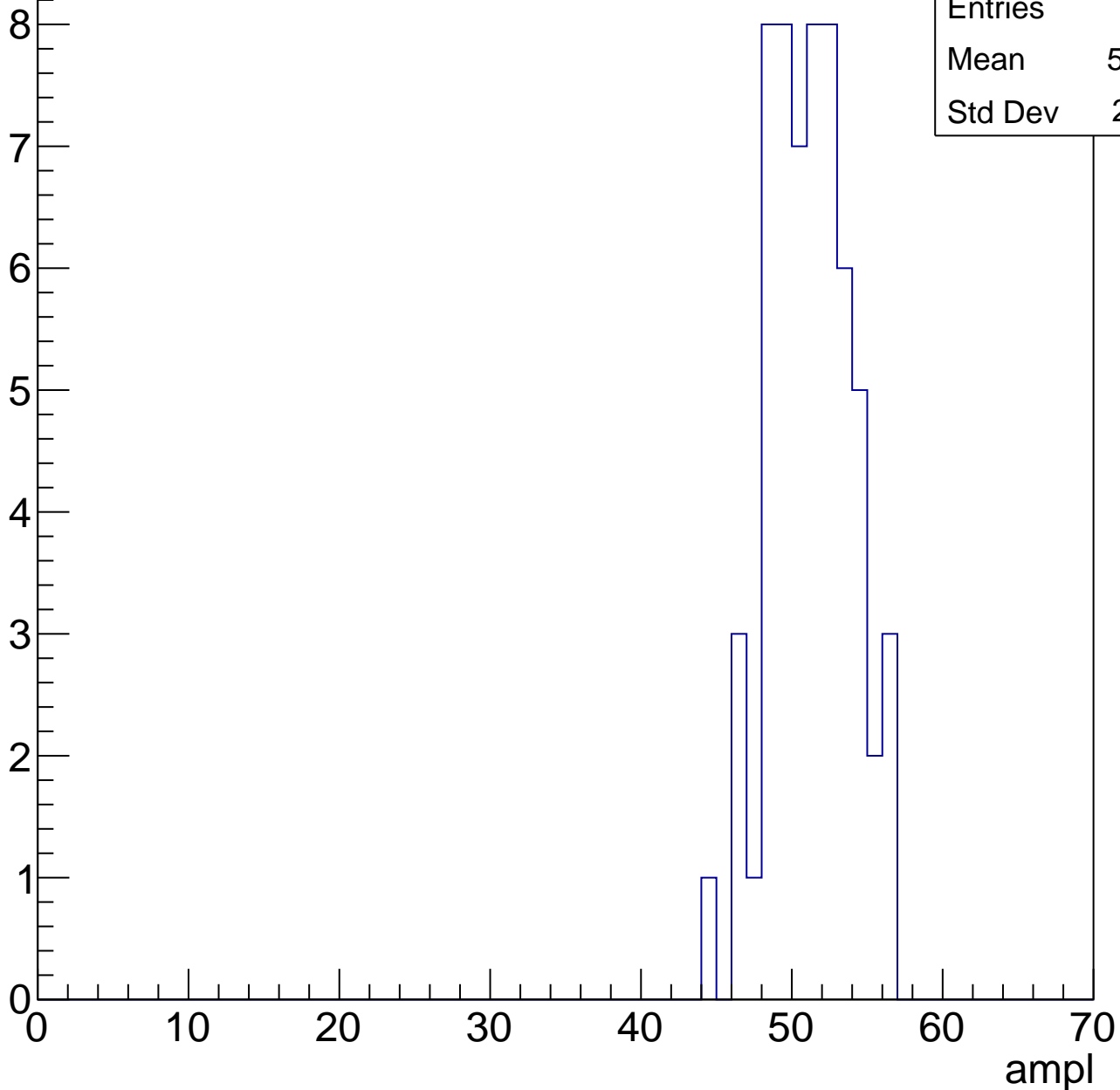


# B0L002S, U8-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.75
Std Dev	2.681

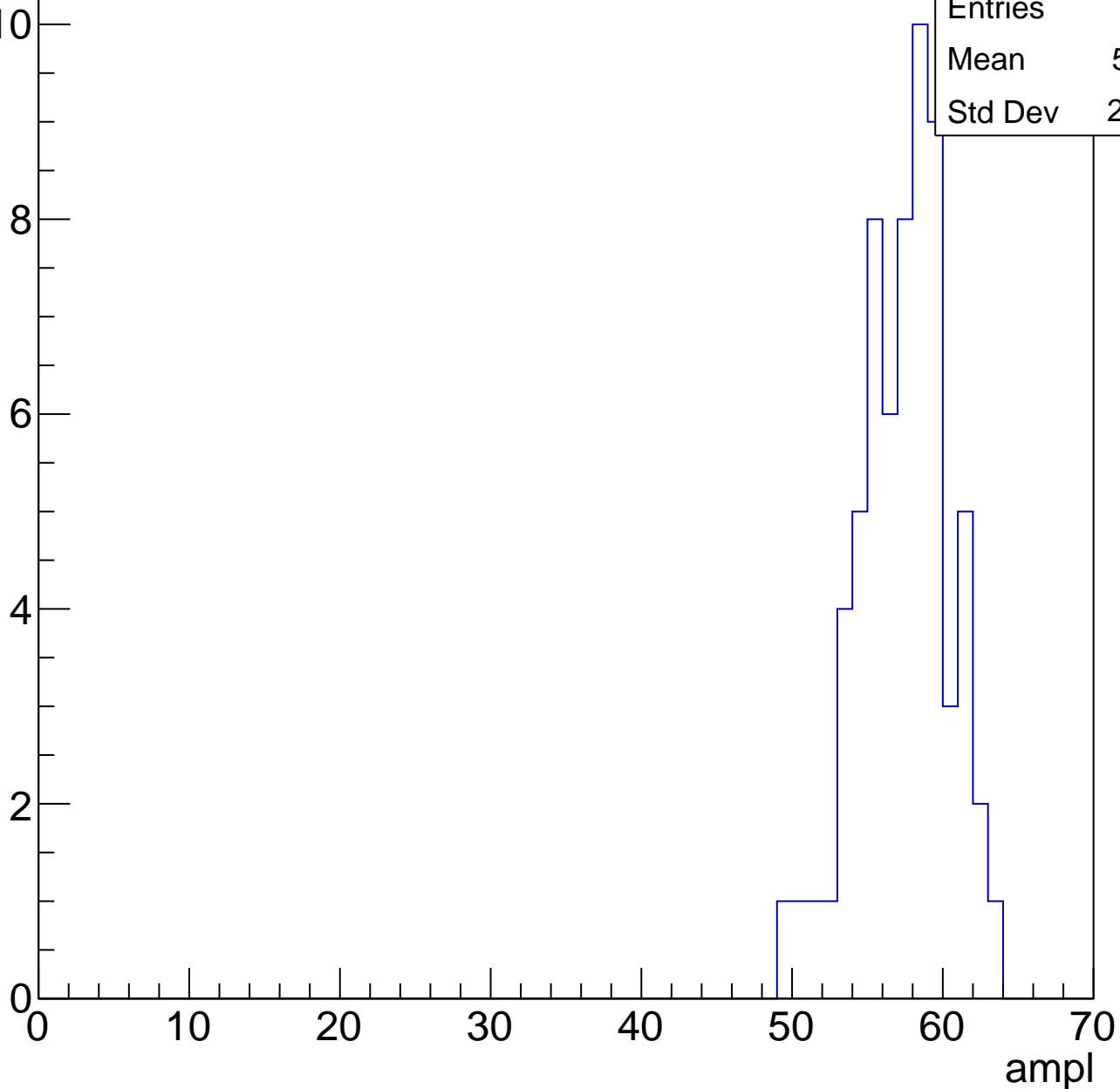


# B0L002S, U8-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.91
Std Dev	2.934

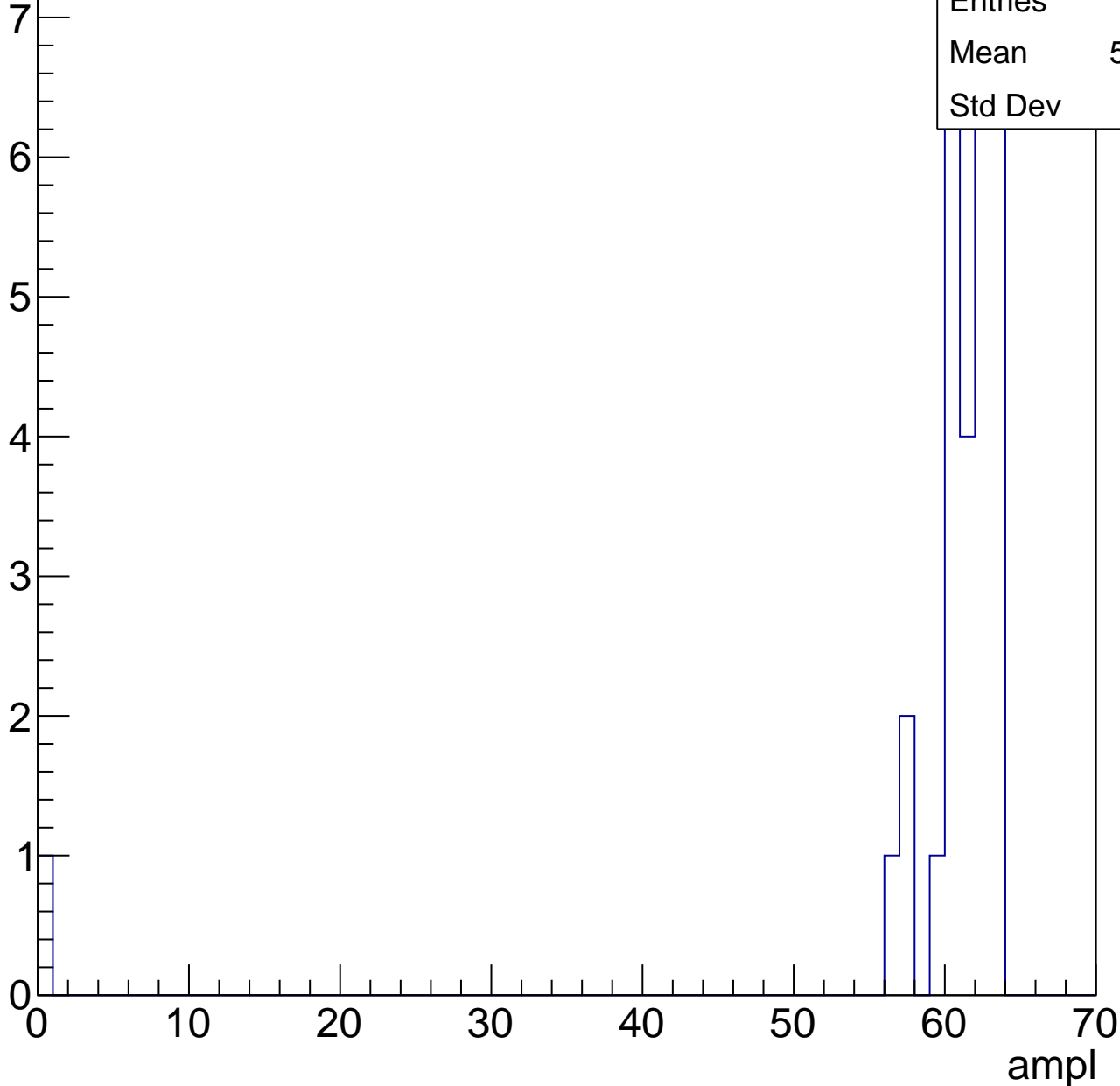


# B0L002S, U8-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.93
Std Dev	11.1



# B0L002S, U8-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch51, adc0

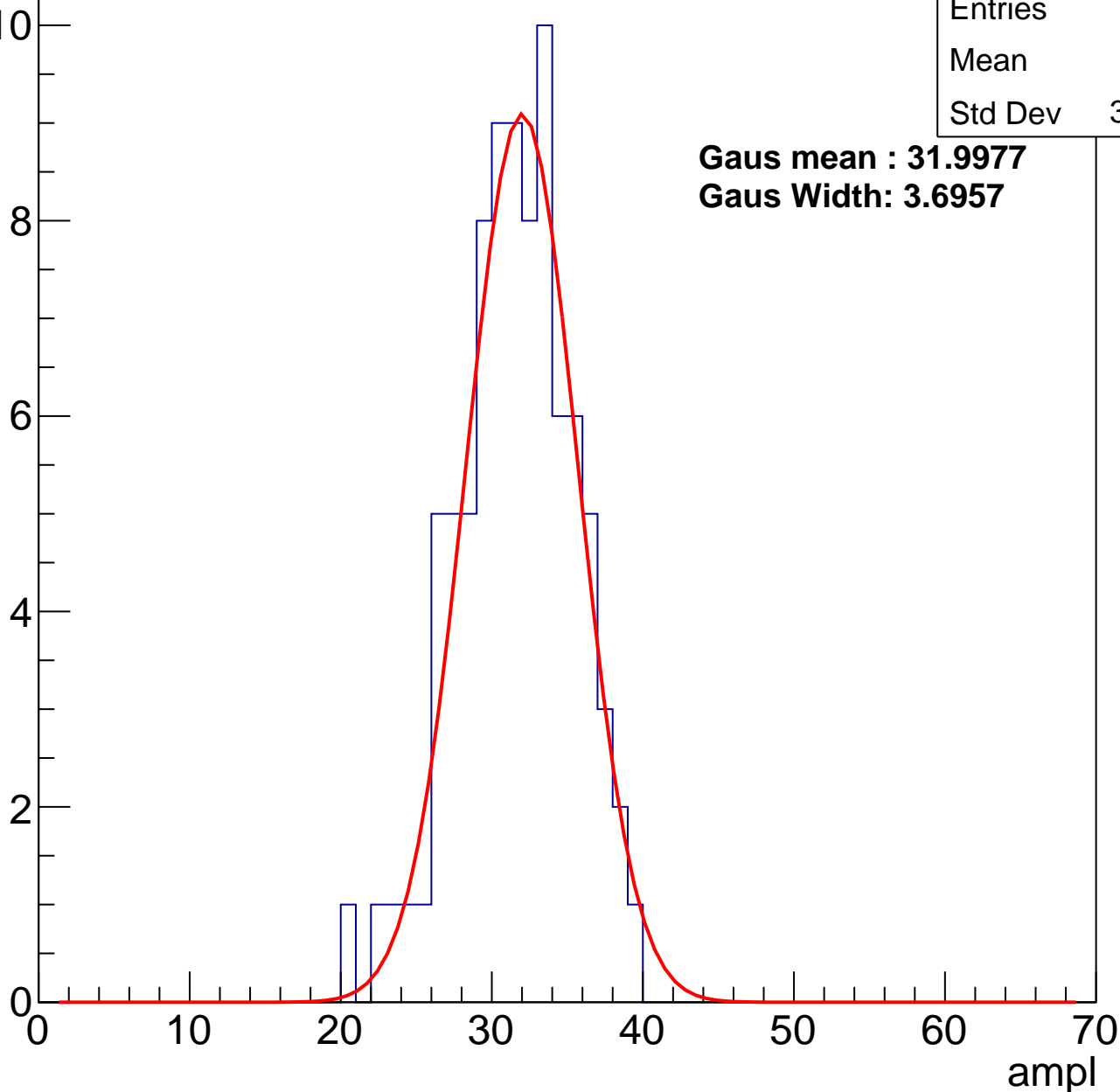
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	87
Mean	31.1
Std Dev	3.766

**Gaus mean : 31.9977**

**Gaus Width: 3.6957**



# B0L002S, U8-ch51, adc1

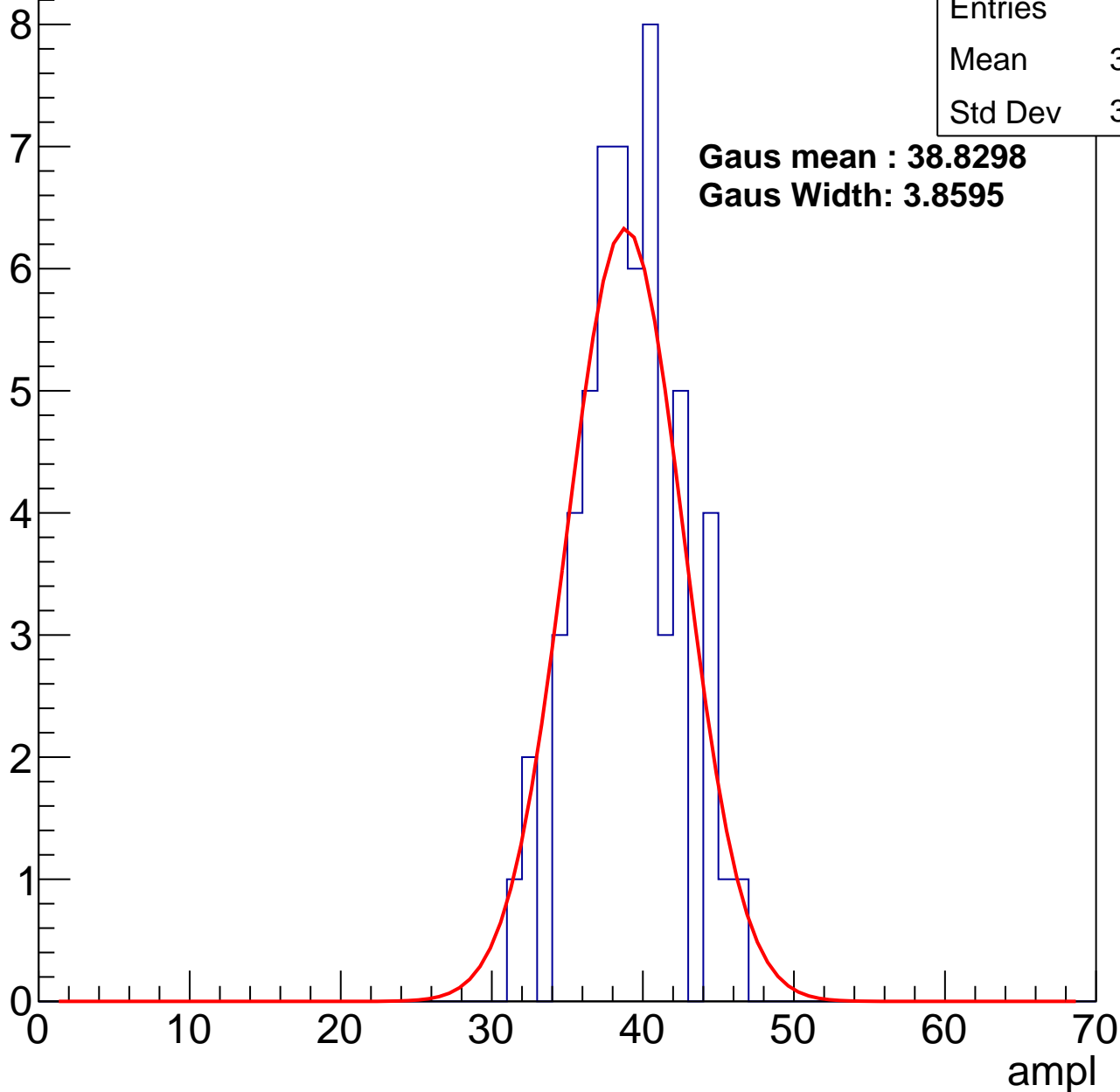
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	38.53
Std Dev	3.293

**Gaus mean : 38.8298**

**Gaus Width: 3.8595**



# B0L002S, U8-ch51, adc2

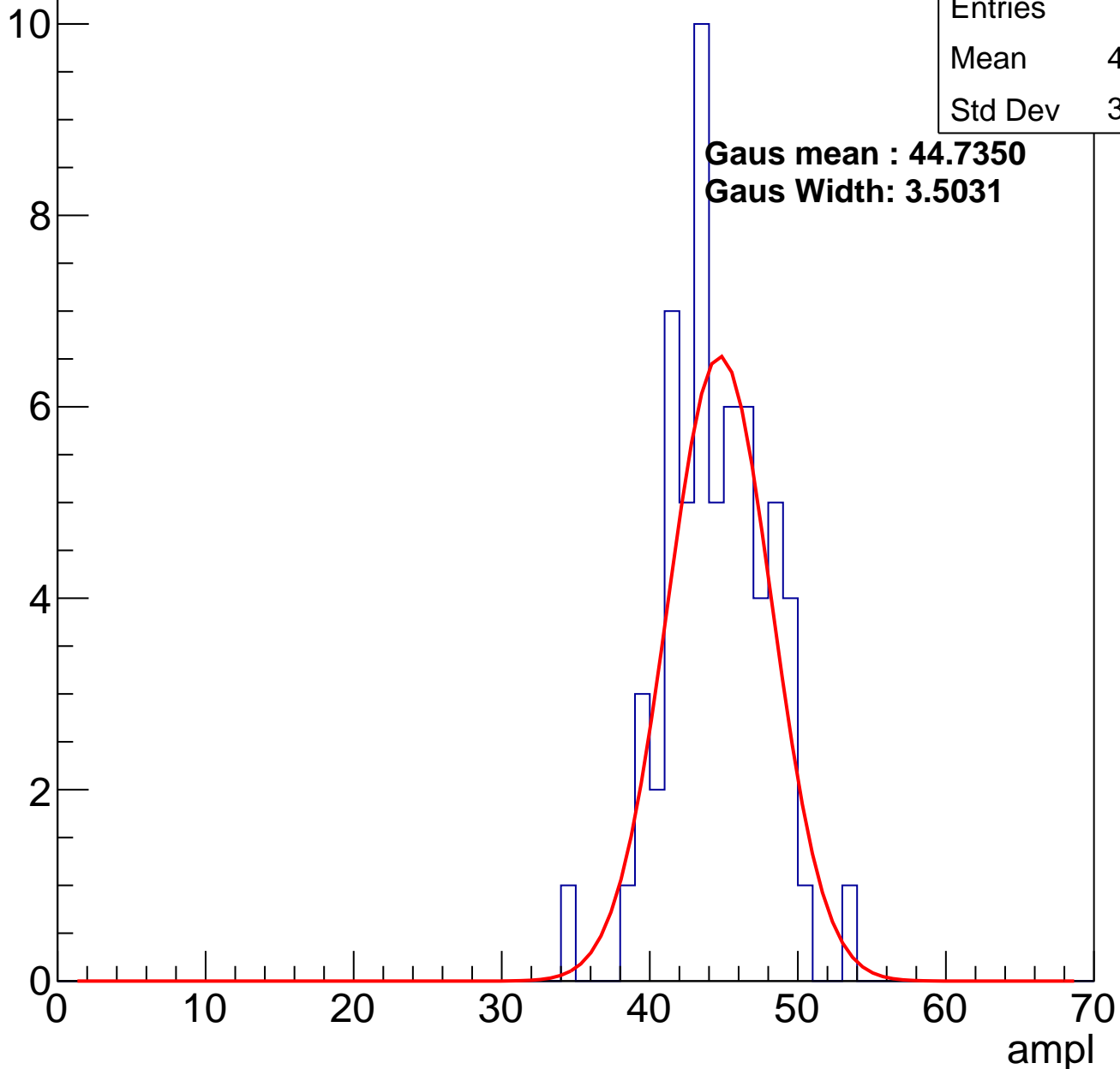
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	44.08
Std Dev	3.384

**Gaus mean : 44.7350**

**Gaus Width: 3.5031**

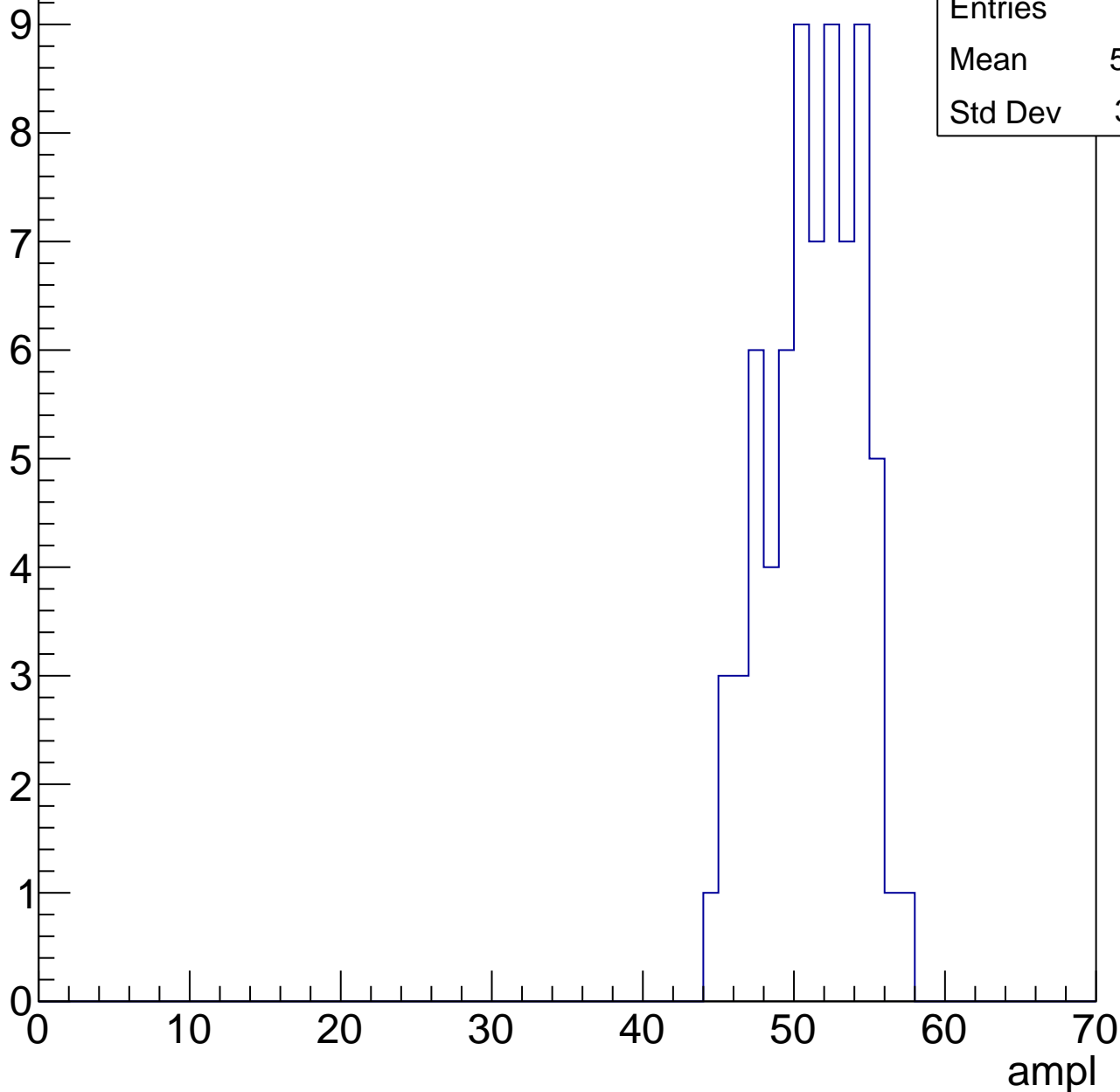
Entry



# B0L002S, U8-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	60
Mean	57.28
Std Dev	2.956

Entry

10

8

6

4

2

0

0

10

20

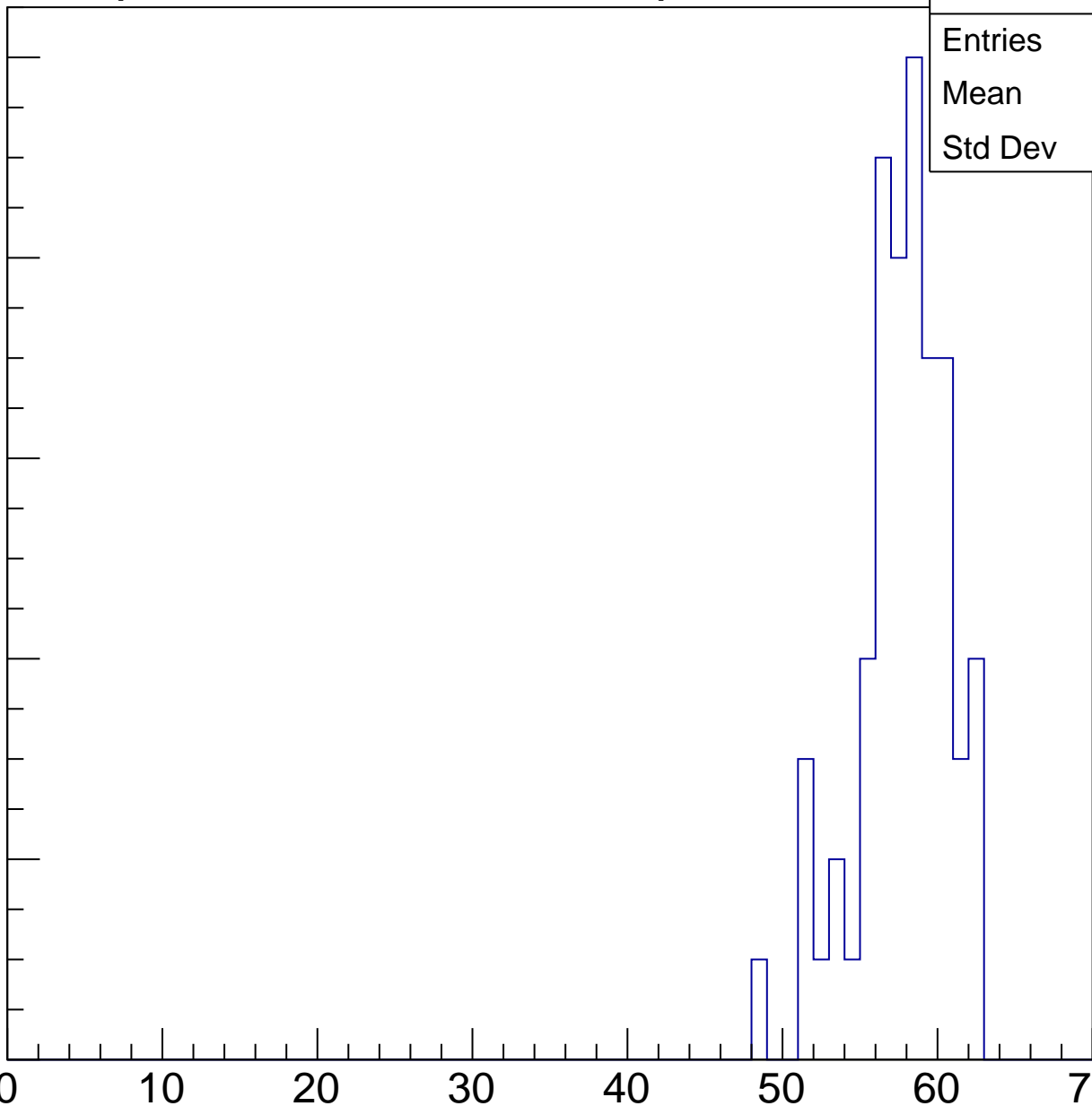
30

40

50

60

ampl

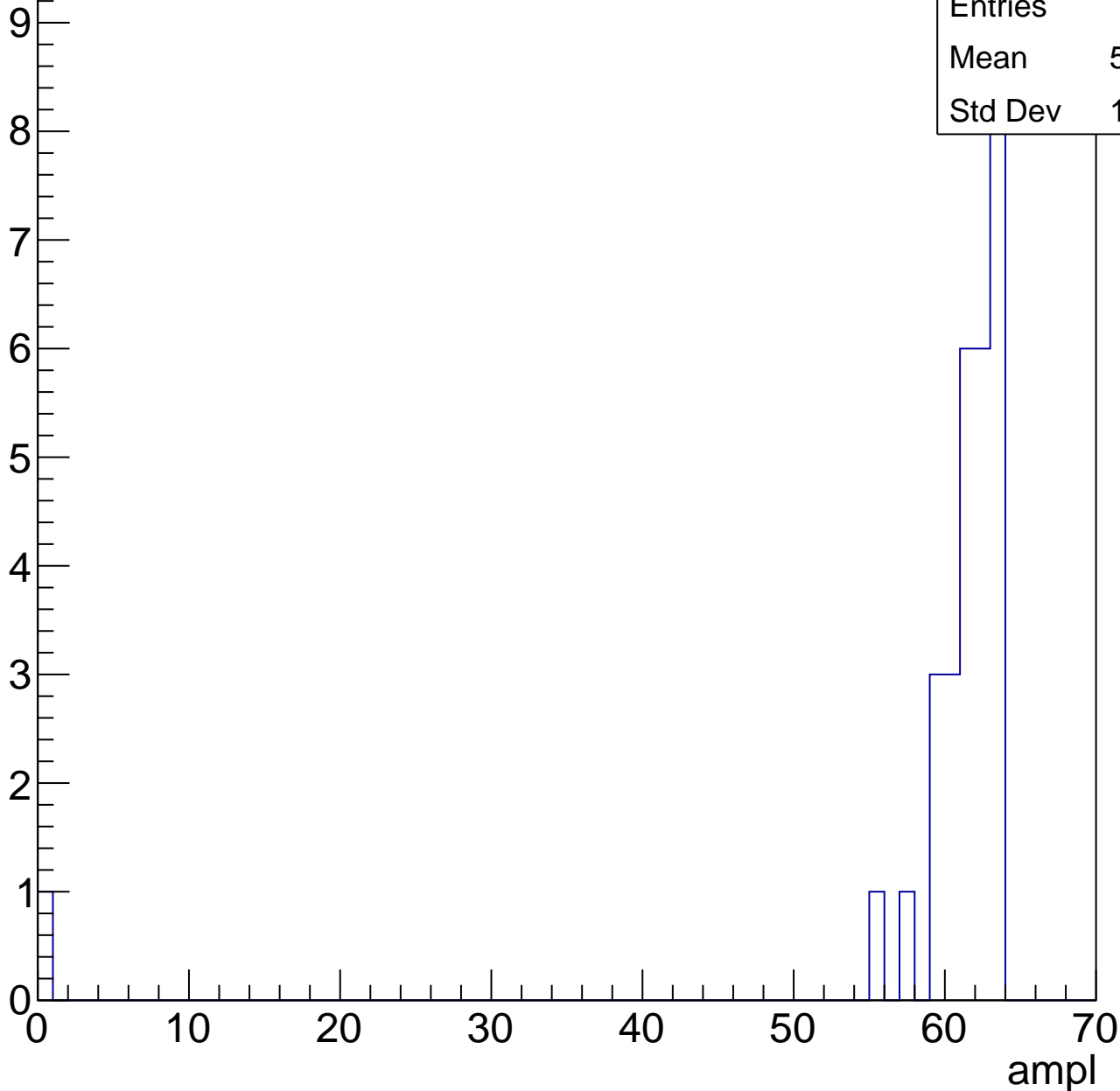


# B0L002S, U8-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.13
Std Dev	11.14



# B0L002S, U8-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	61
Std Dev	0



# B0L002S, U8-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch52, adc0

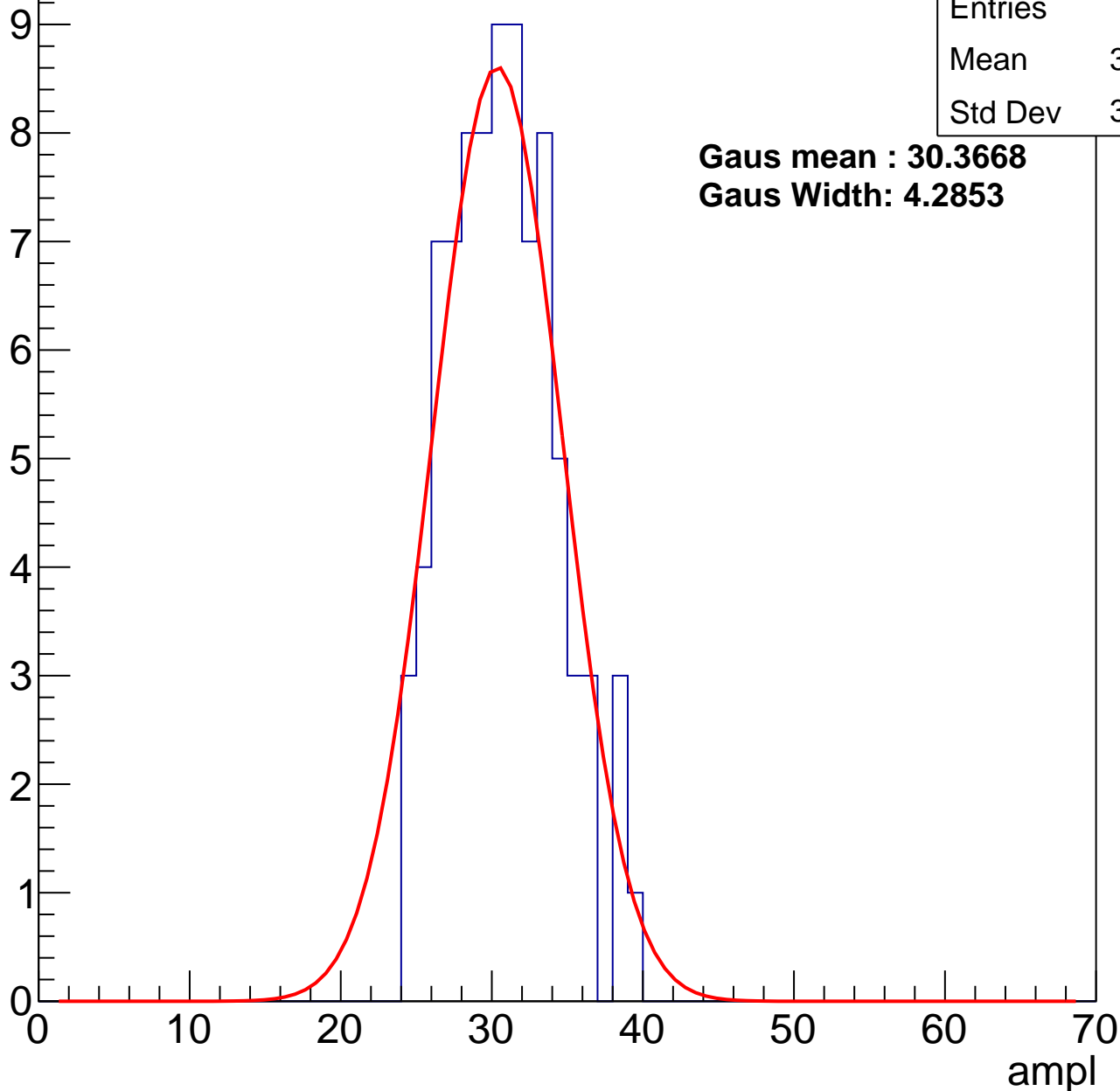
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	85
Mean	30.26
Std Dev	3.535

**Gaus mean : 30.3668**

**Gaus Width: 4.2853**



# B0L002S, U8-ch52, adc1

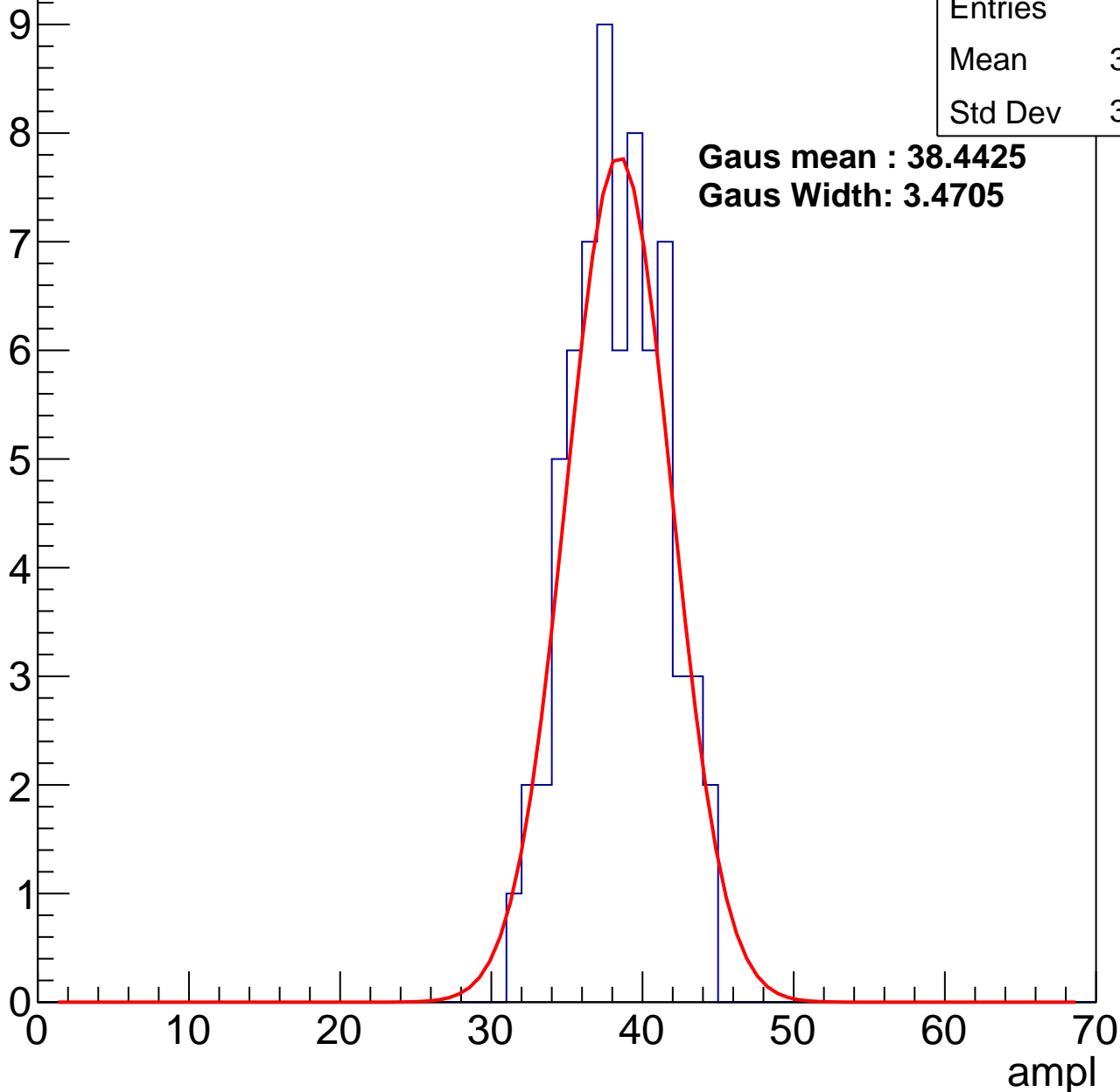
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.85
Std Dev	3.068

**Gaus mean : 38.4425**

**Gaus Width: 3.4705**



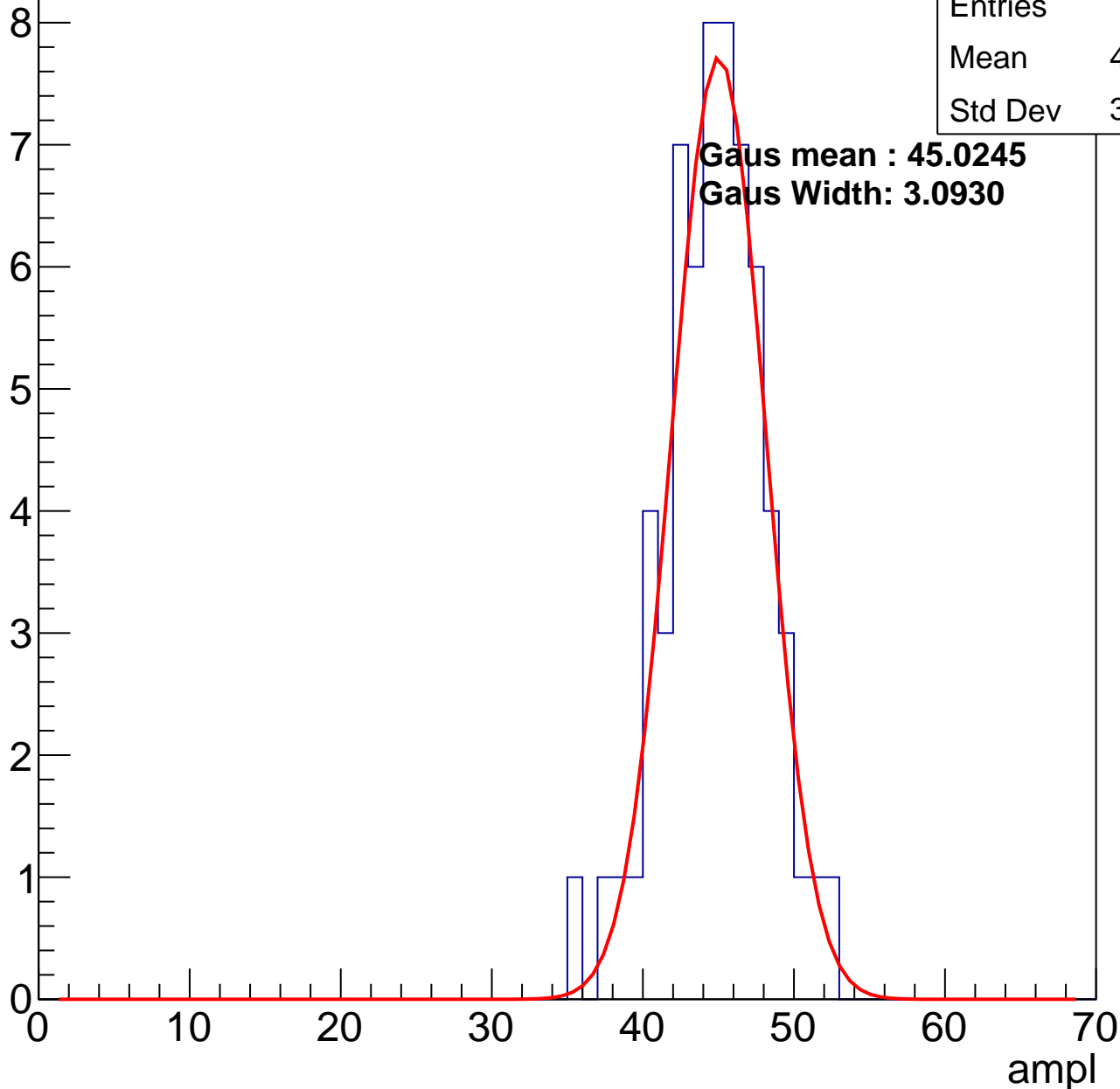
# B0L002S, U8-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

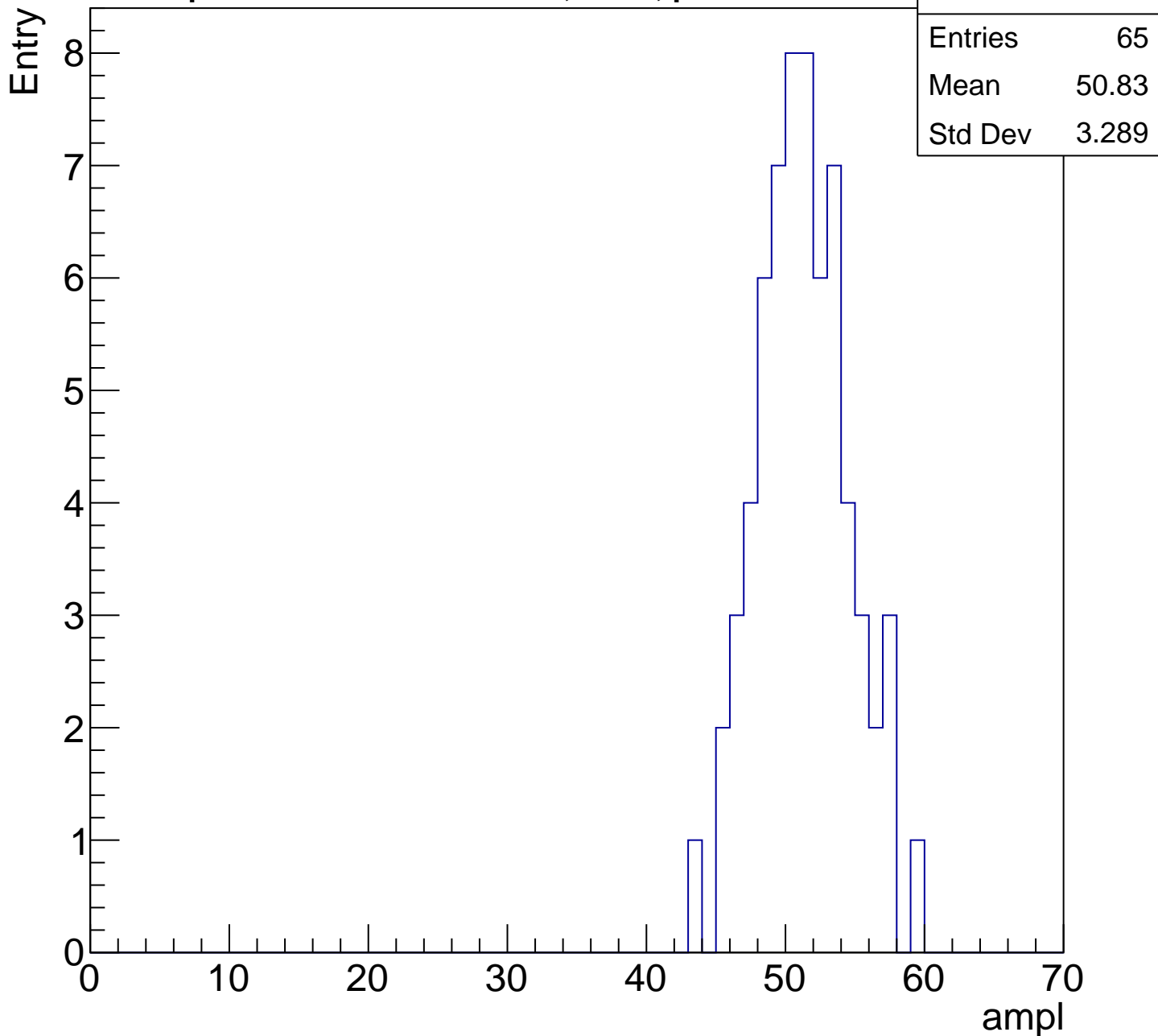
Entries	63
Mean	44.32
Std Dev	3.304

**Gaus mean : 45.0245**  
**Gaus Width: 3.0930**



# B0L002S, U8-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	56.26
Std Dev	2.935

ampl

0

10

20

30

40

50

60

70

# B0L002S, U8-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	59.04
Std Dev	8.961

ampl

0

10

20

30

40

50

60

70

# B0L002S, U8-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch53, adc0

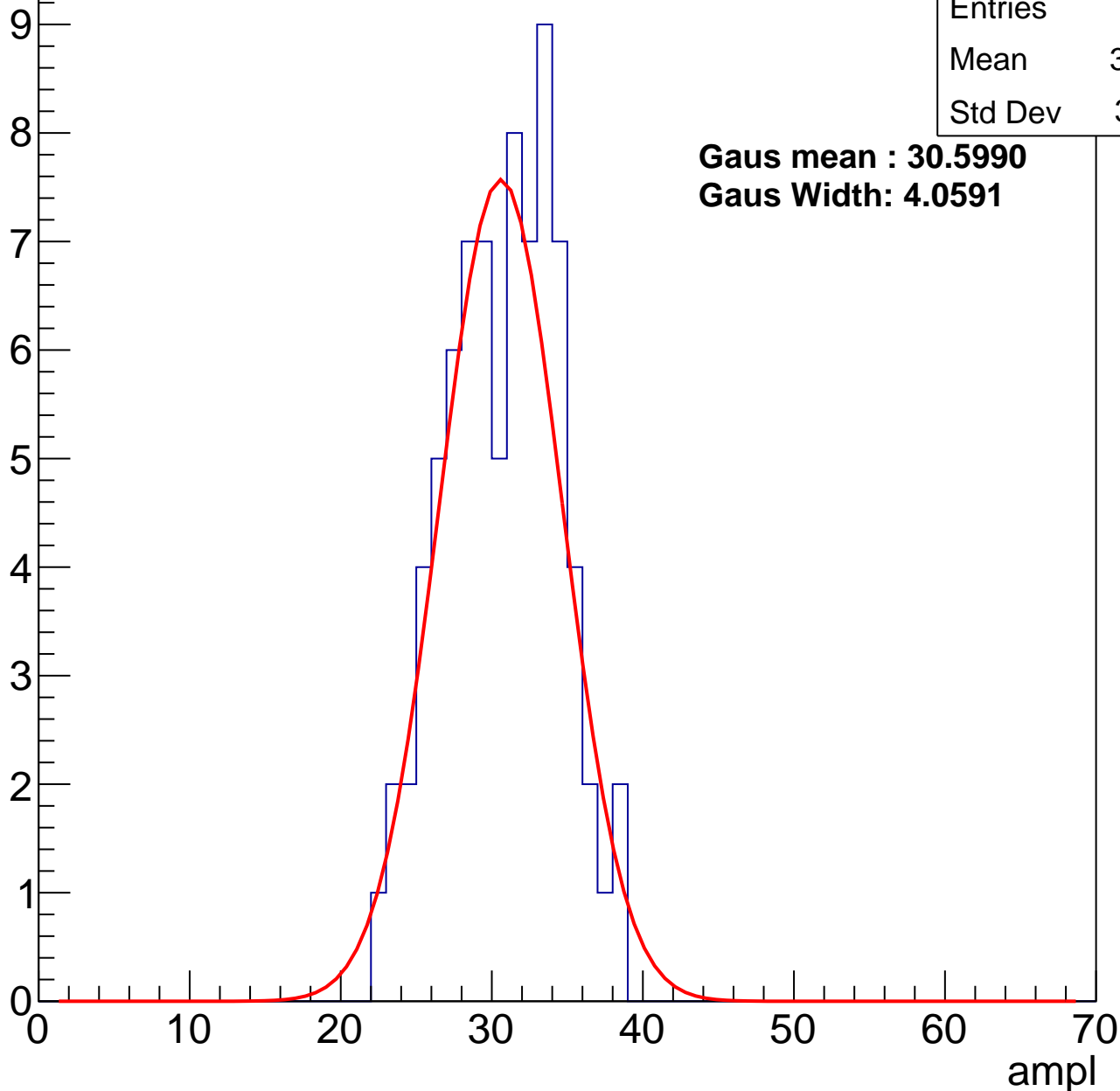
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.24
Std Dev	3.691

**Gaus mean : 30.5990**

**Gaus Width: 4.0591**



# B0L002S, U8-ch53, adc1

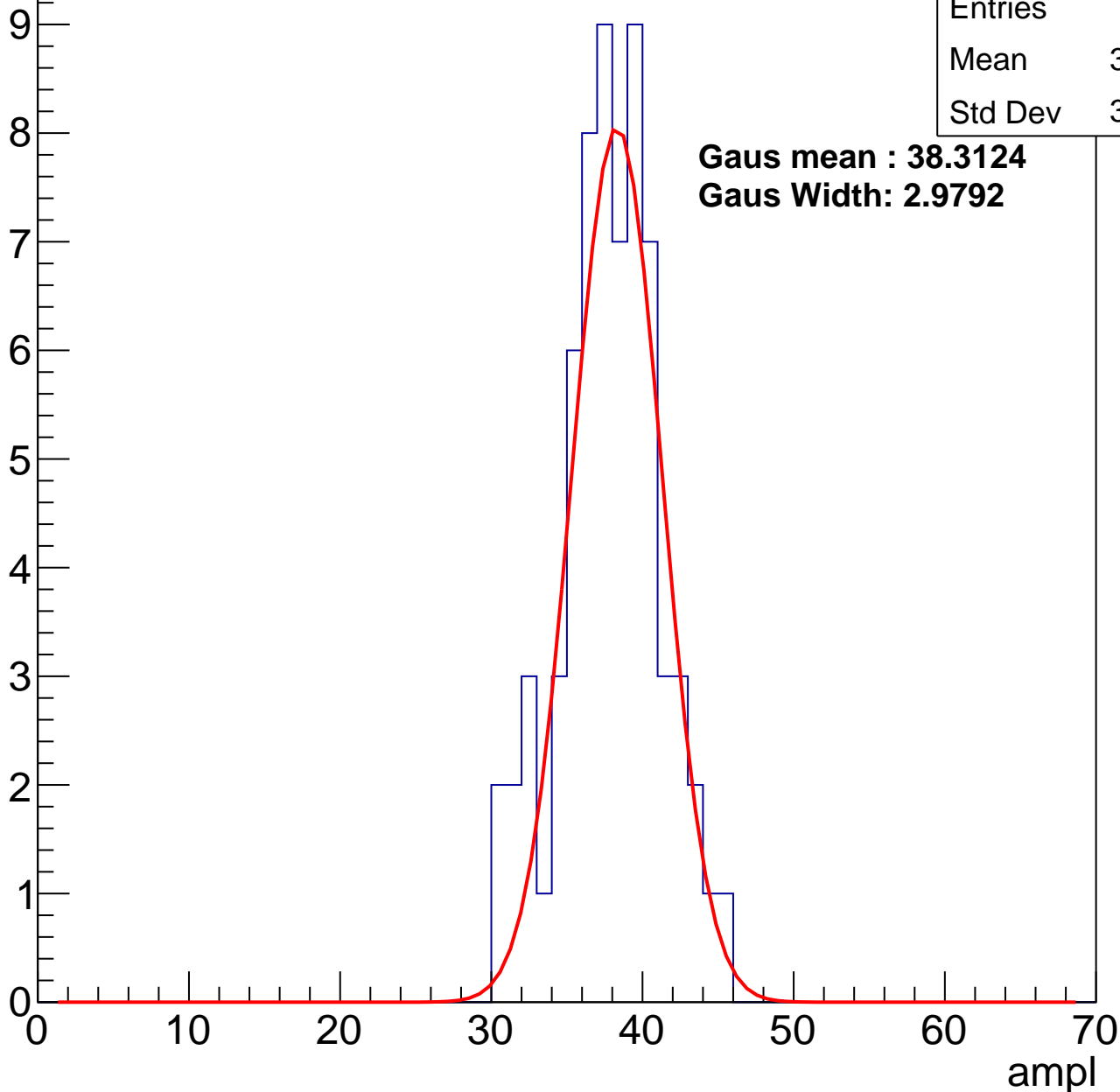
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.39
Std Dev	3.287

**Gaus mean : 38.3124**

**Gaus Width: 2.9792**



# B0L002S, U8-ch53, adc2

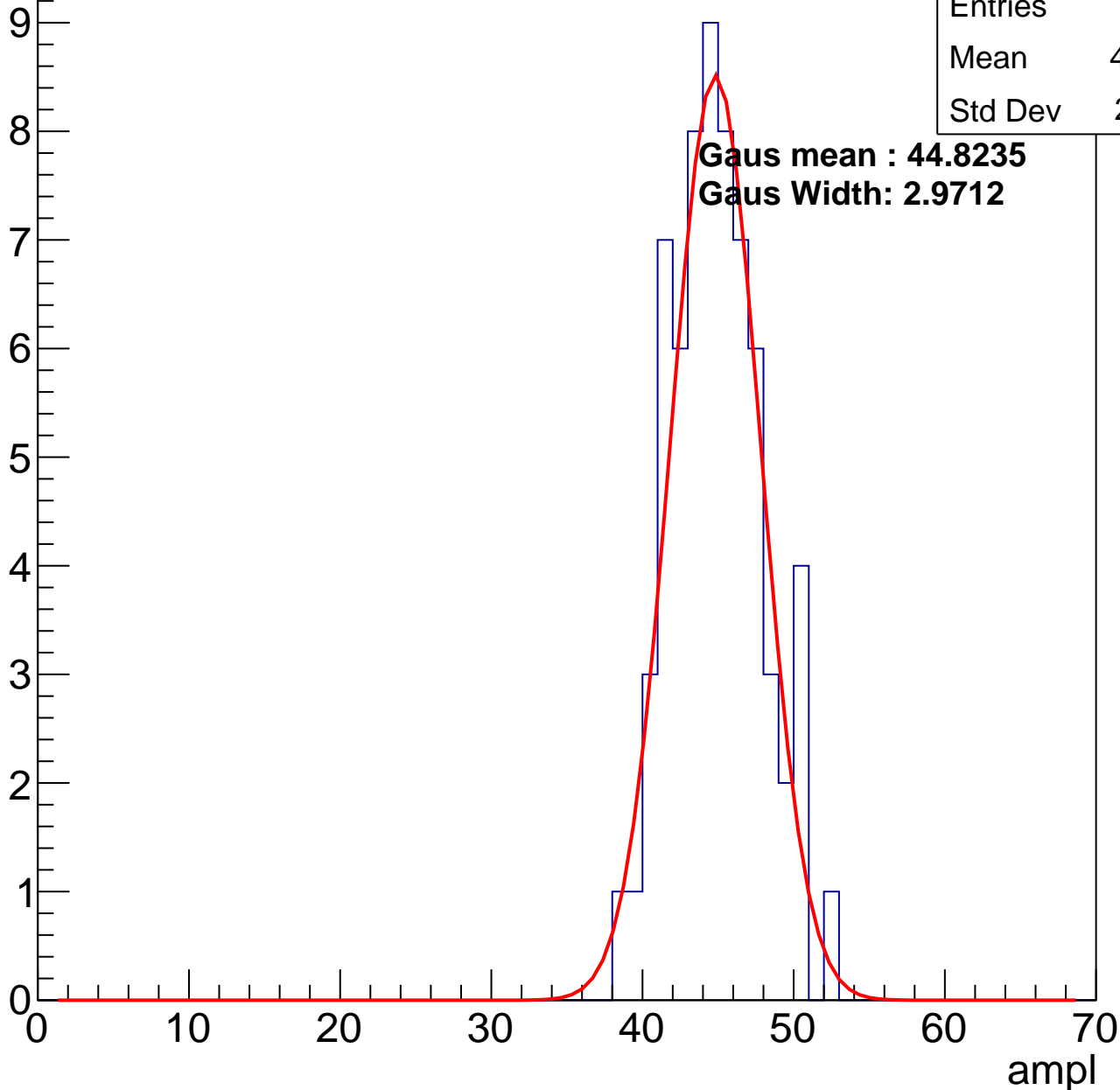
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.45
Std Dev	2.981

**Gaus mean : 44.8235**

**Gaus Width: 2.9712**

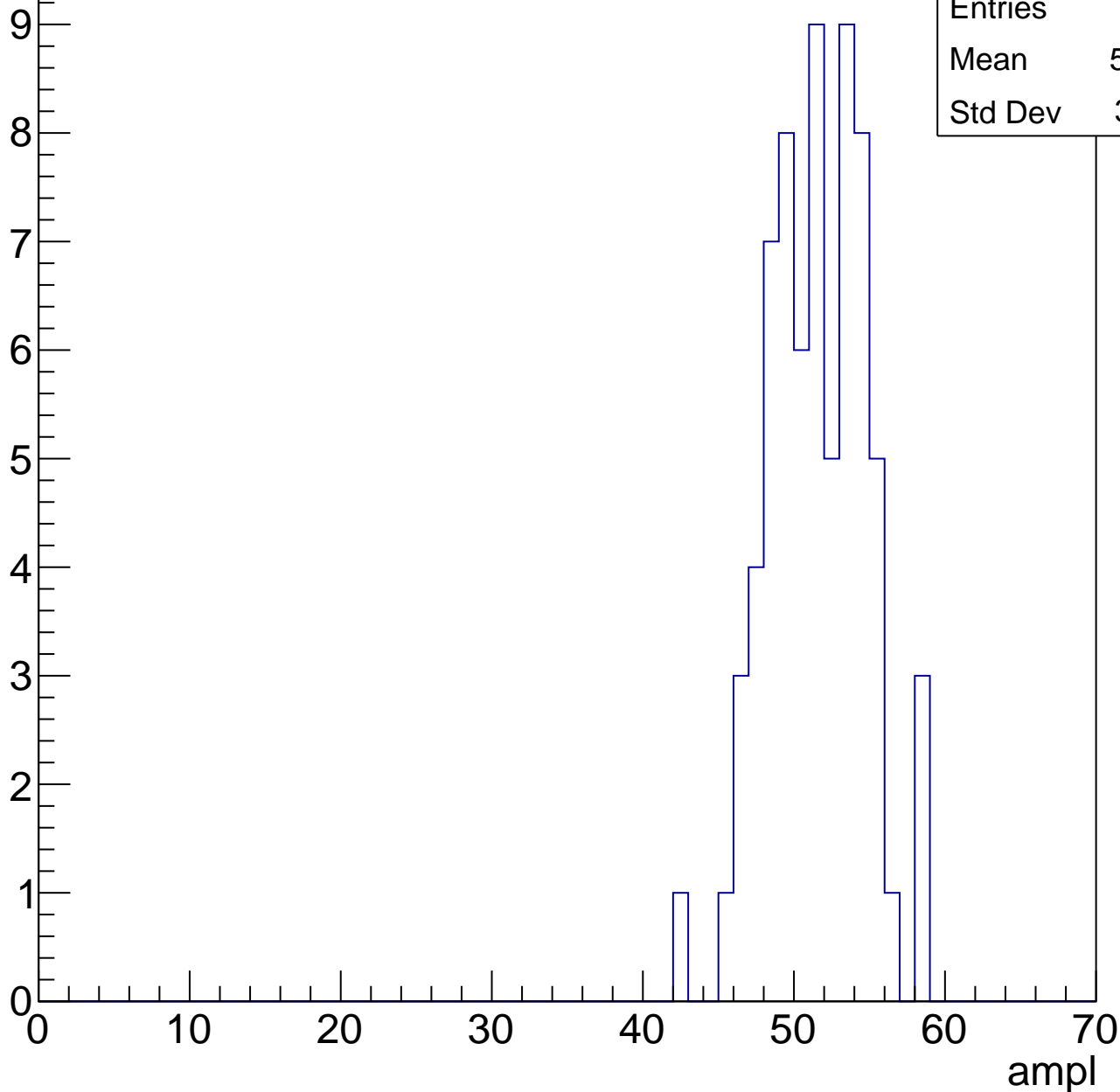


# B0L002S, U8-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	51.06
Std Dev	3.211



# B0L002S, U8-ch53, adc4

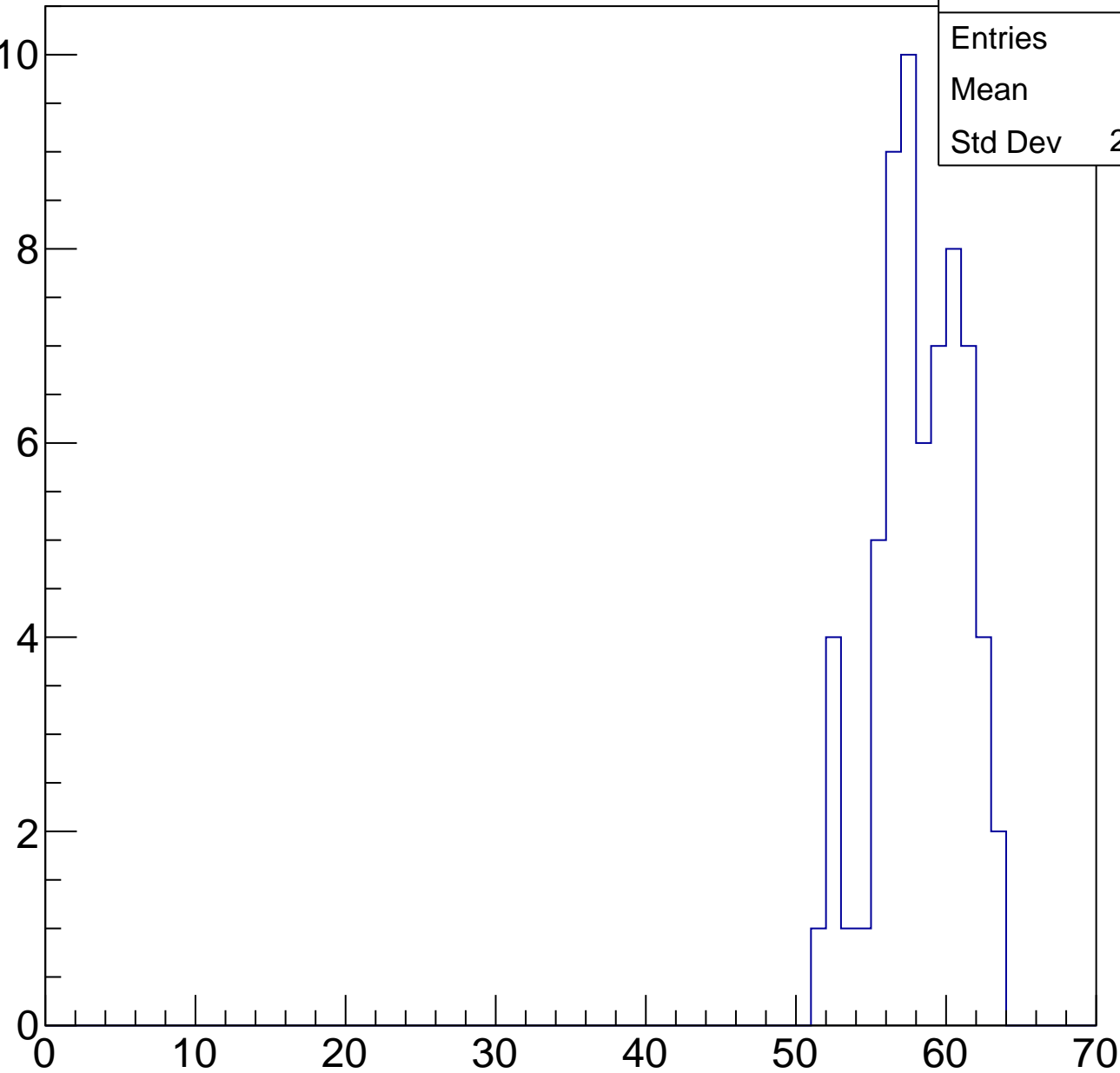
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10  
8  
6  
4  
2  
0

Entries	65
Mean	57.8
Std Dev	2.878

ampl

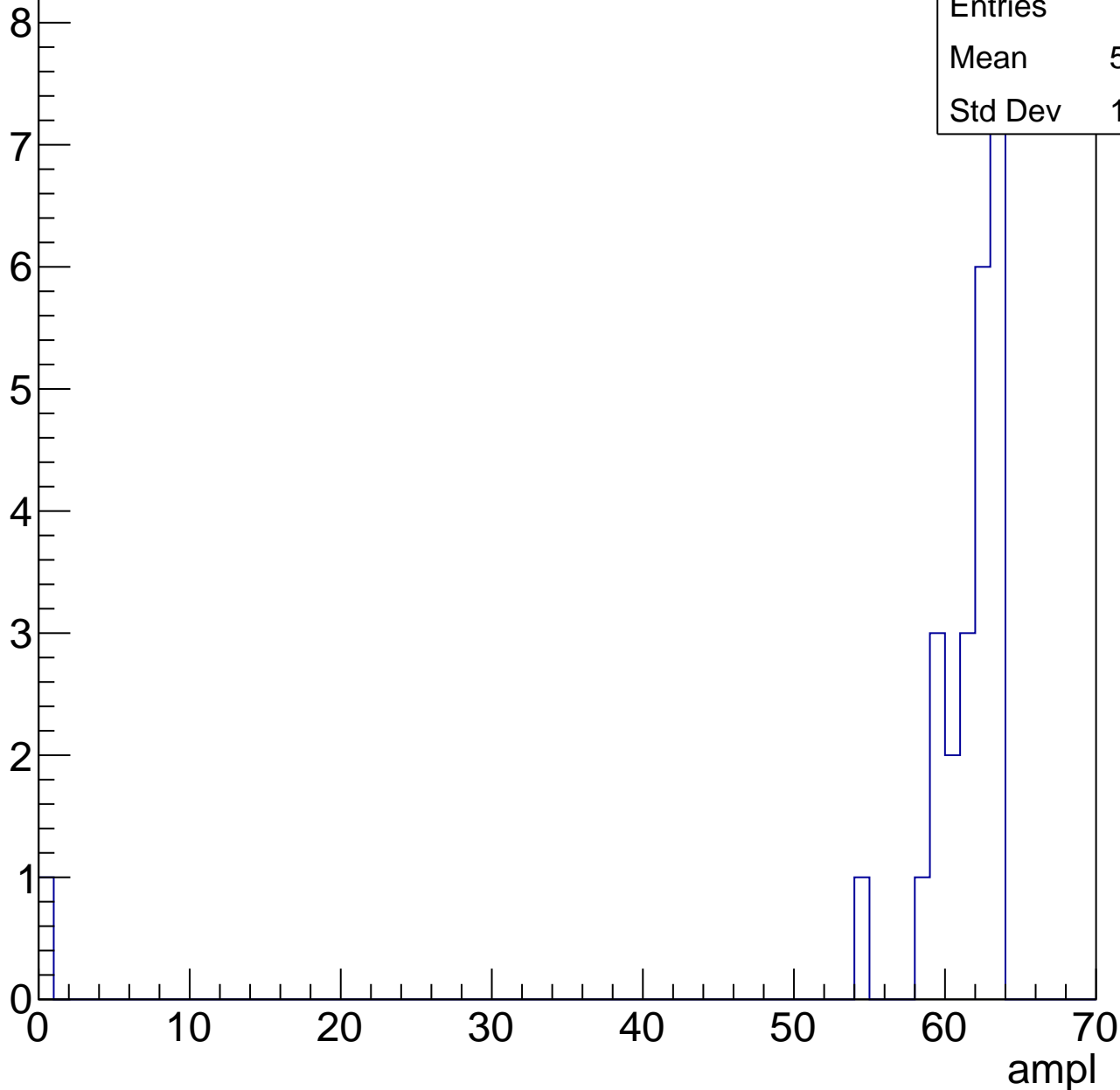


# B0L002S, U8-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.72
Std Dev	12.17



# B0L002S, U8-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U8-ch54, adc0

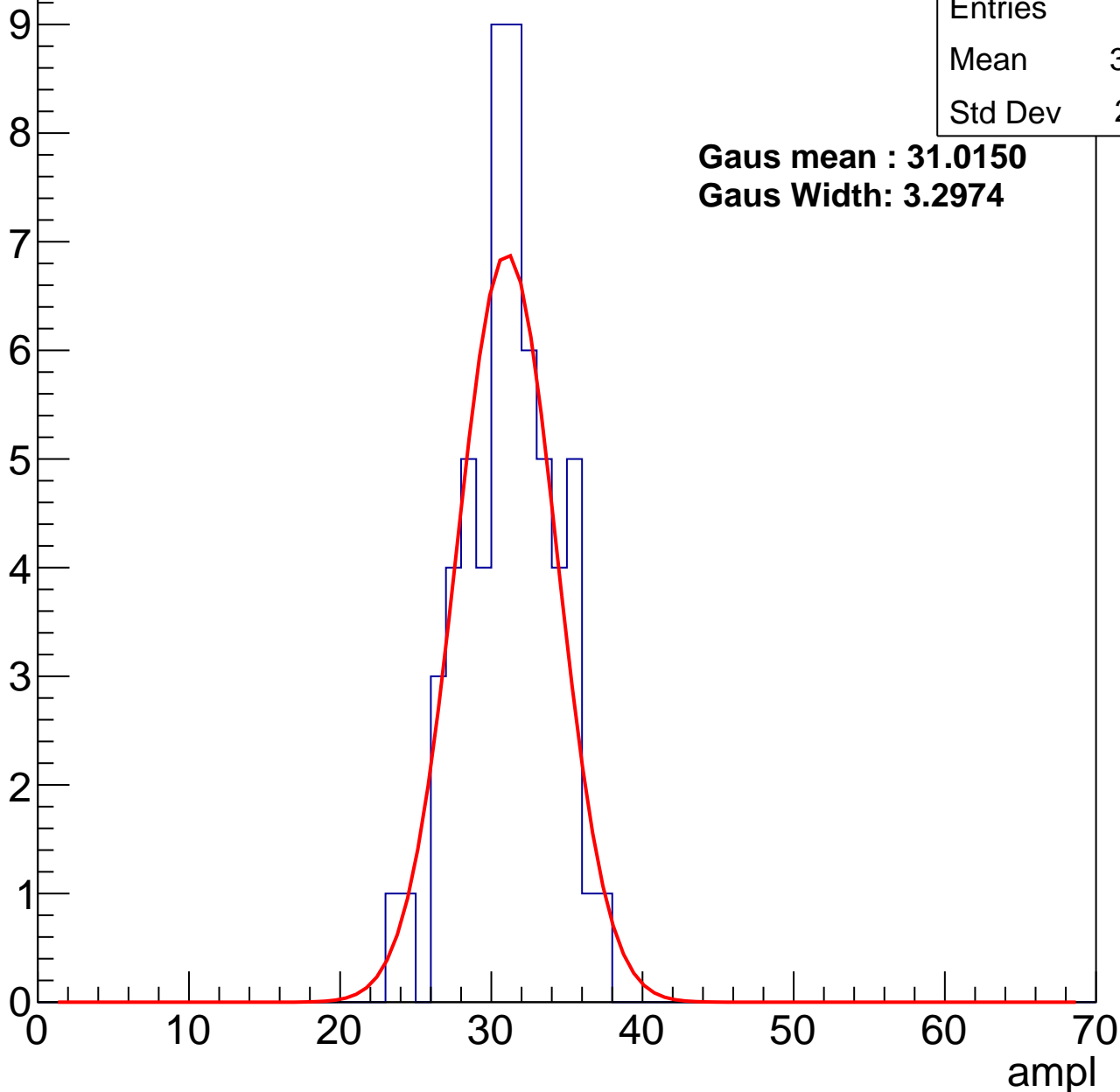
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.67
Std Dev	2.991

**Gaus mean : 31.0150**

**Gaus Width: 3.2974**



# B0L002S, U8-ch54, adc1

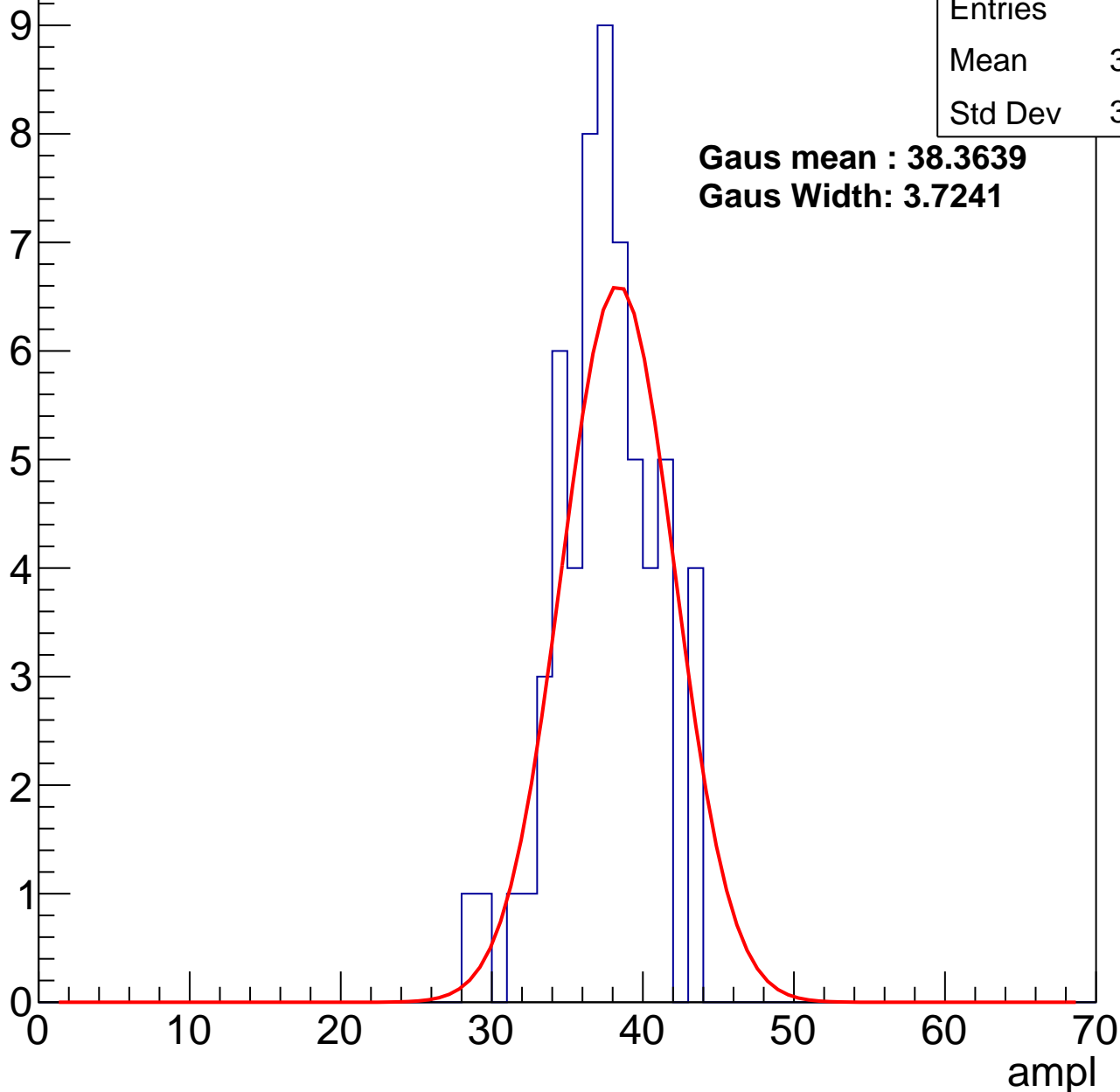
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.98
Std Dev	3.239

**Gaus mean : 38.3639**

**Gaus Width: 3.7241**



# B0L002S, U8-ch54, adc2

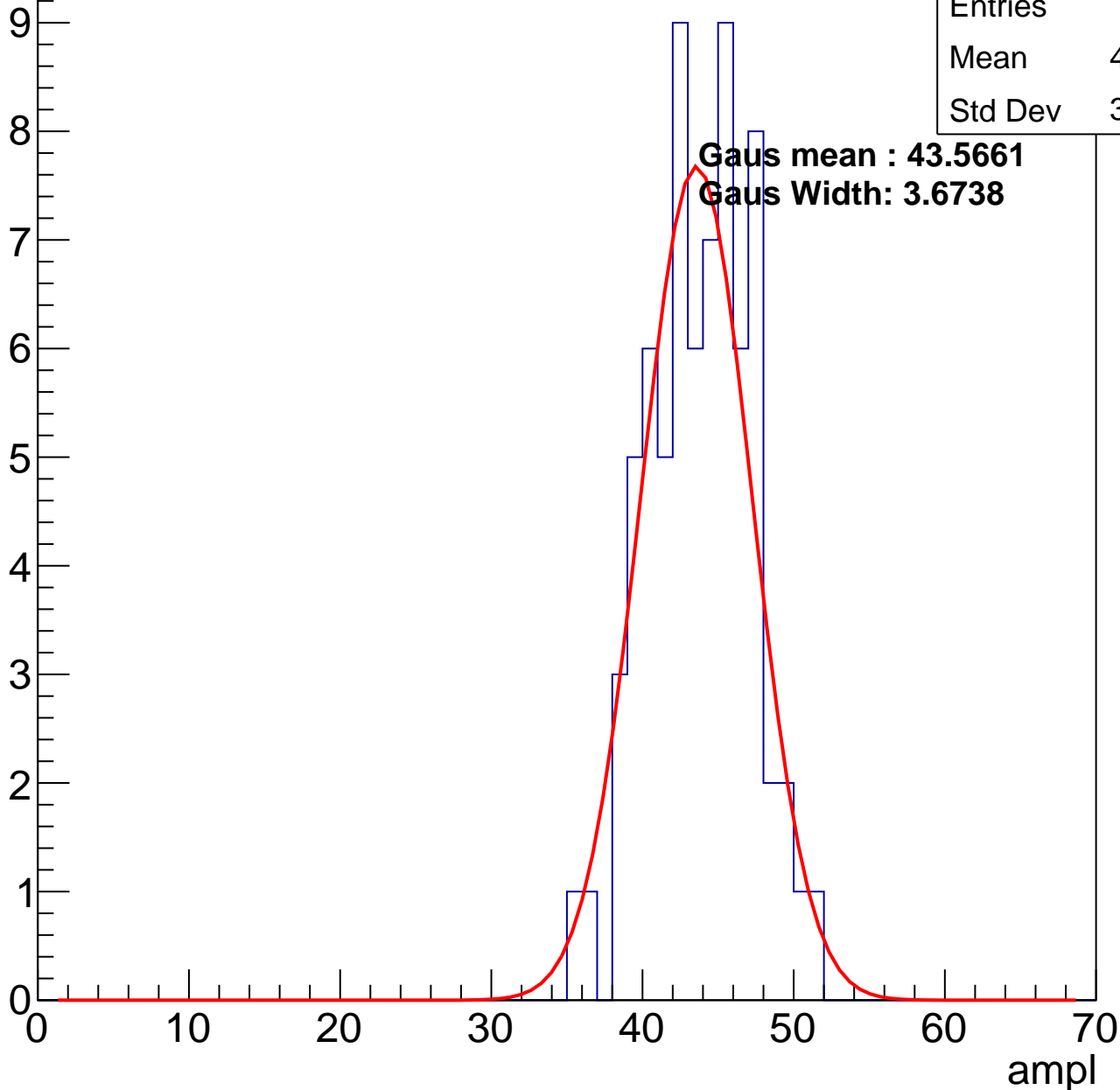
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.35
Std Dev	3.338

**Gaus mean : 43.5661**

**Gaus Width: 3.6738**

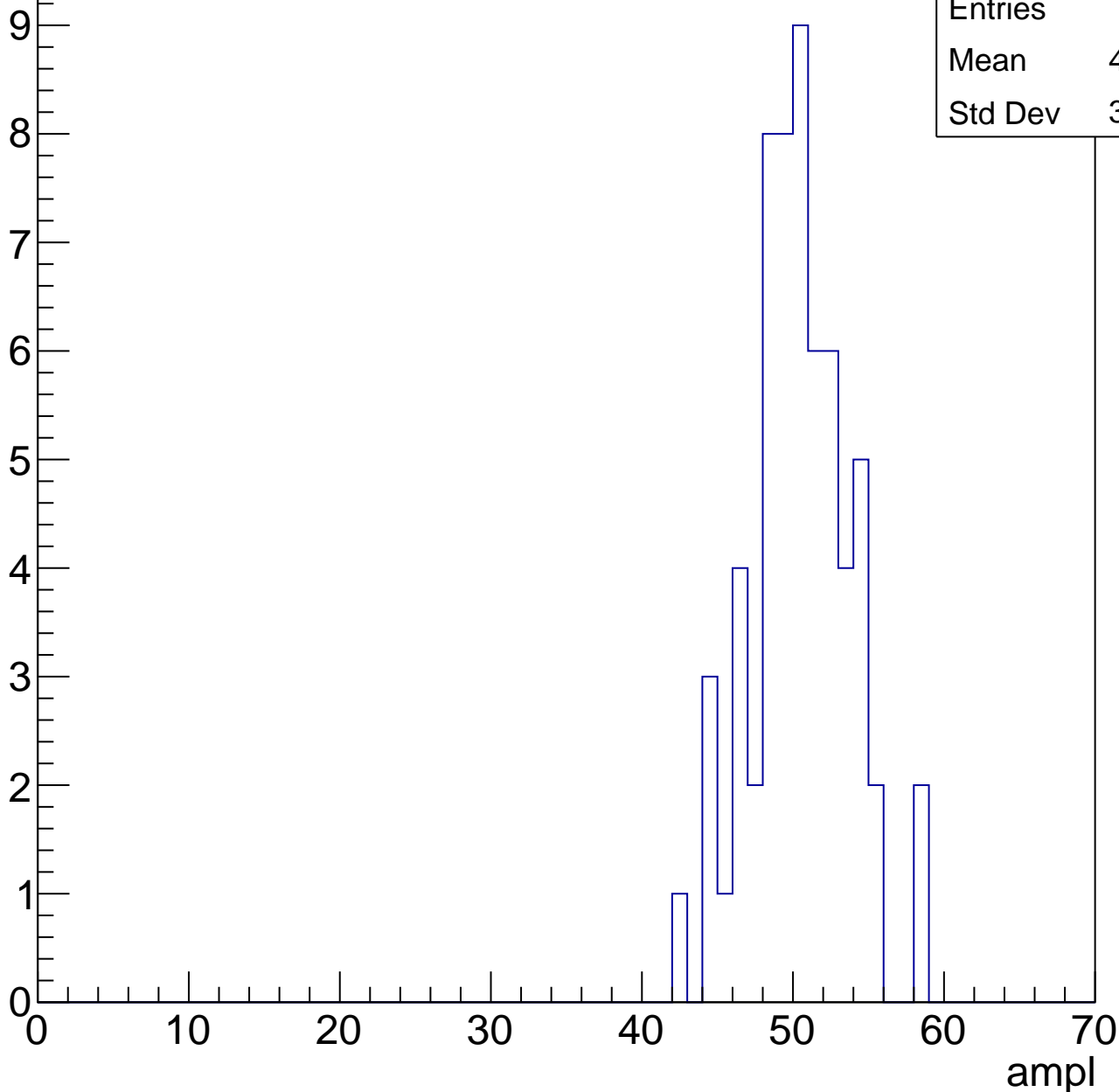


# B0L002S, U8-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.98
Std Dev	3.247



# B0L002S, U8-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

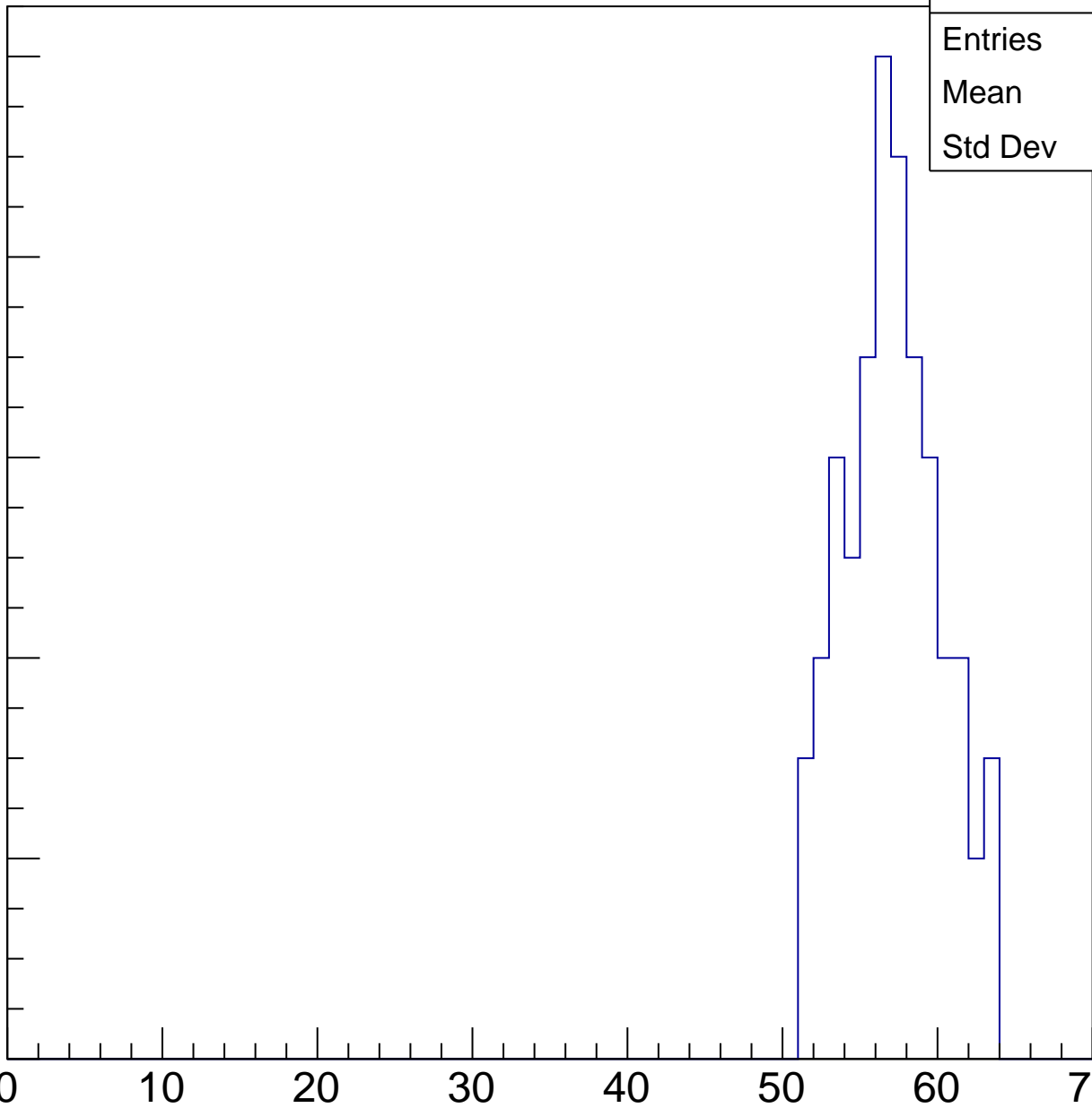
Entries	70
Mean	56.63
Std Dev	3.085

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

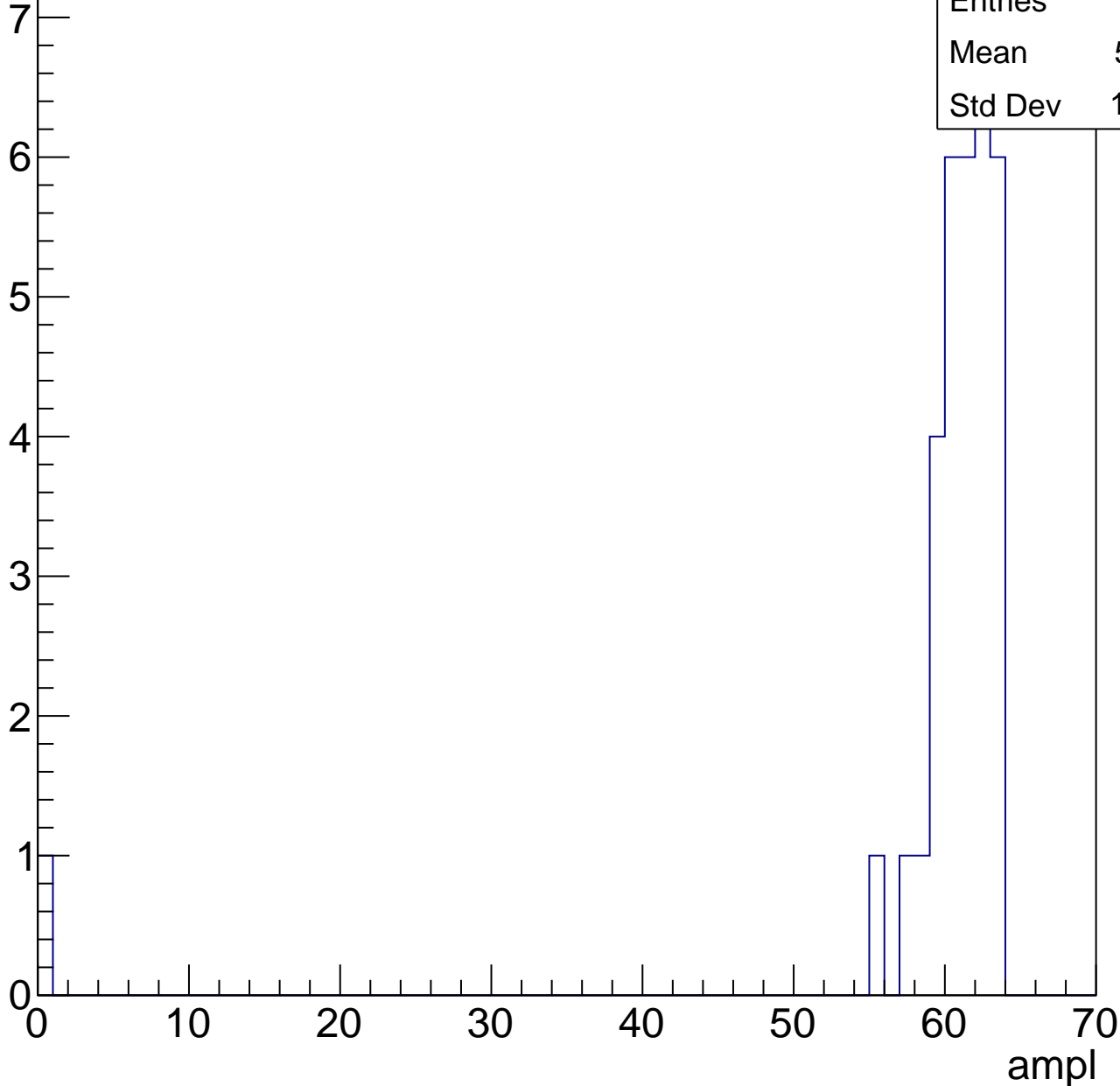


# B0L002S, U8-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.91
Std Dev	10.58



# B0L002S, U8-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch55, adc0

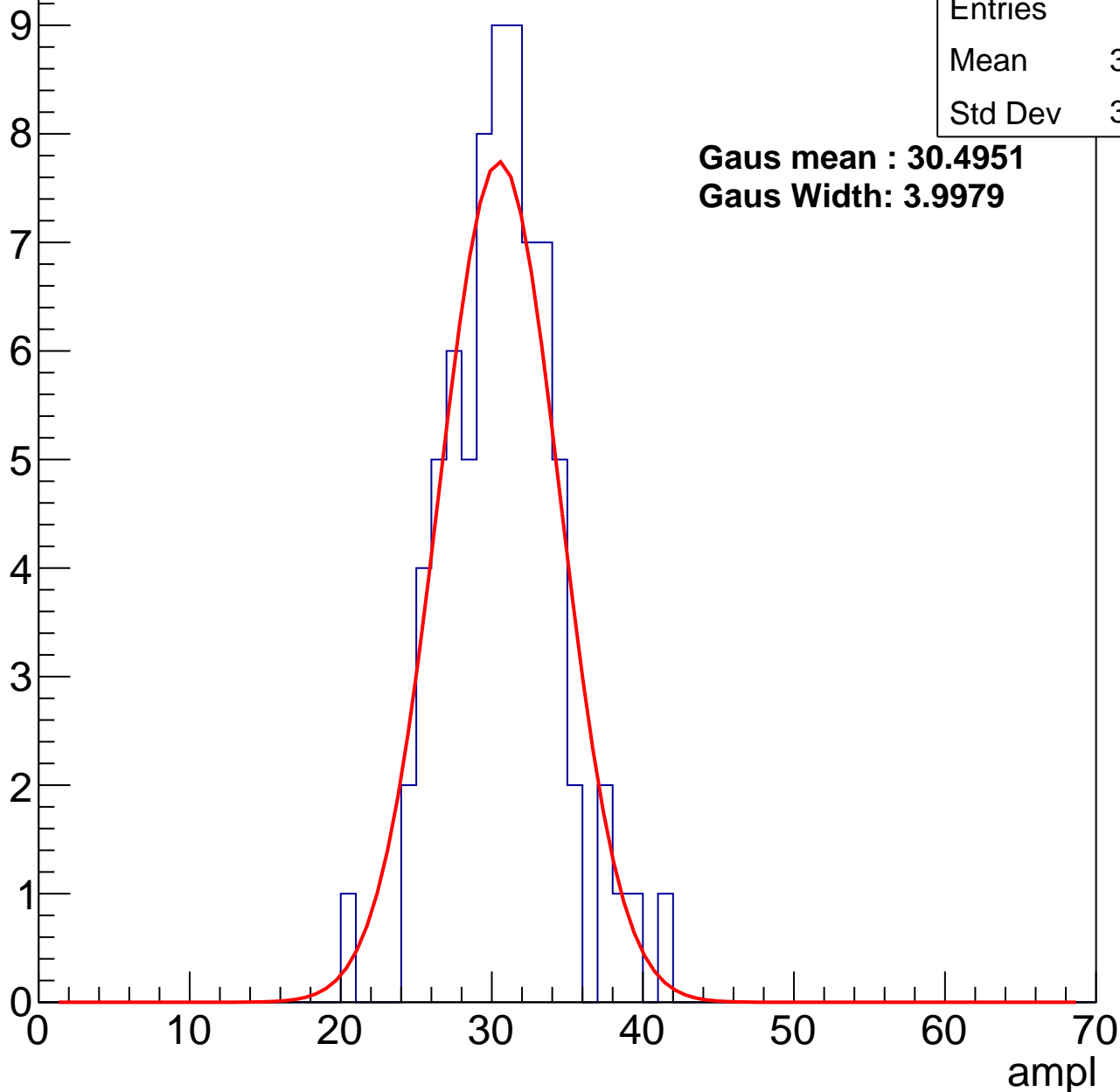
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.24
Std Dev	3.694

**Gaus mean : 30.4951**

**Gaus Width: 3.9979**



# B0L002S, U8-ch55, adc1

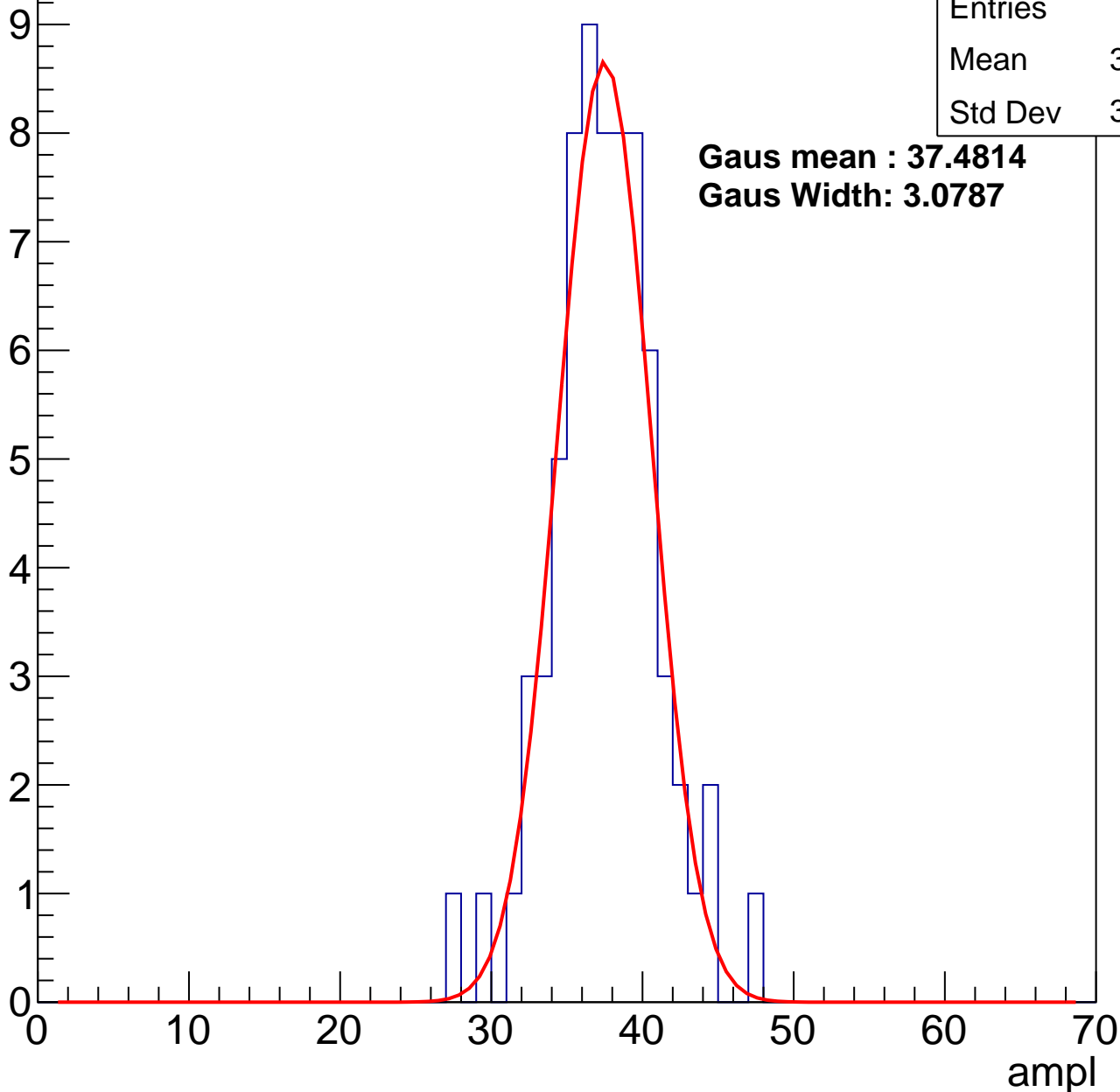
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.04
Std Dev	3.454

**Gaus mean : 37.4814**

**Gaus Width: 3.0787**



# B0L002S, U8-ch55, adc2

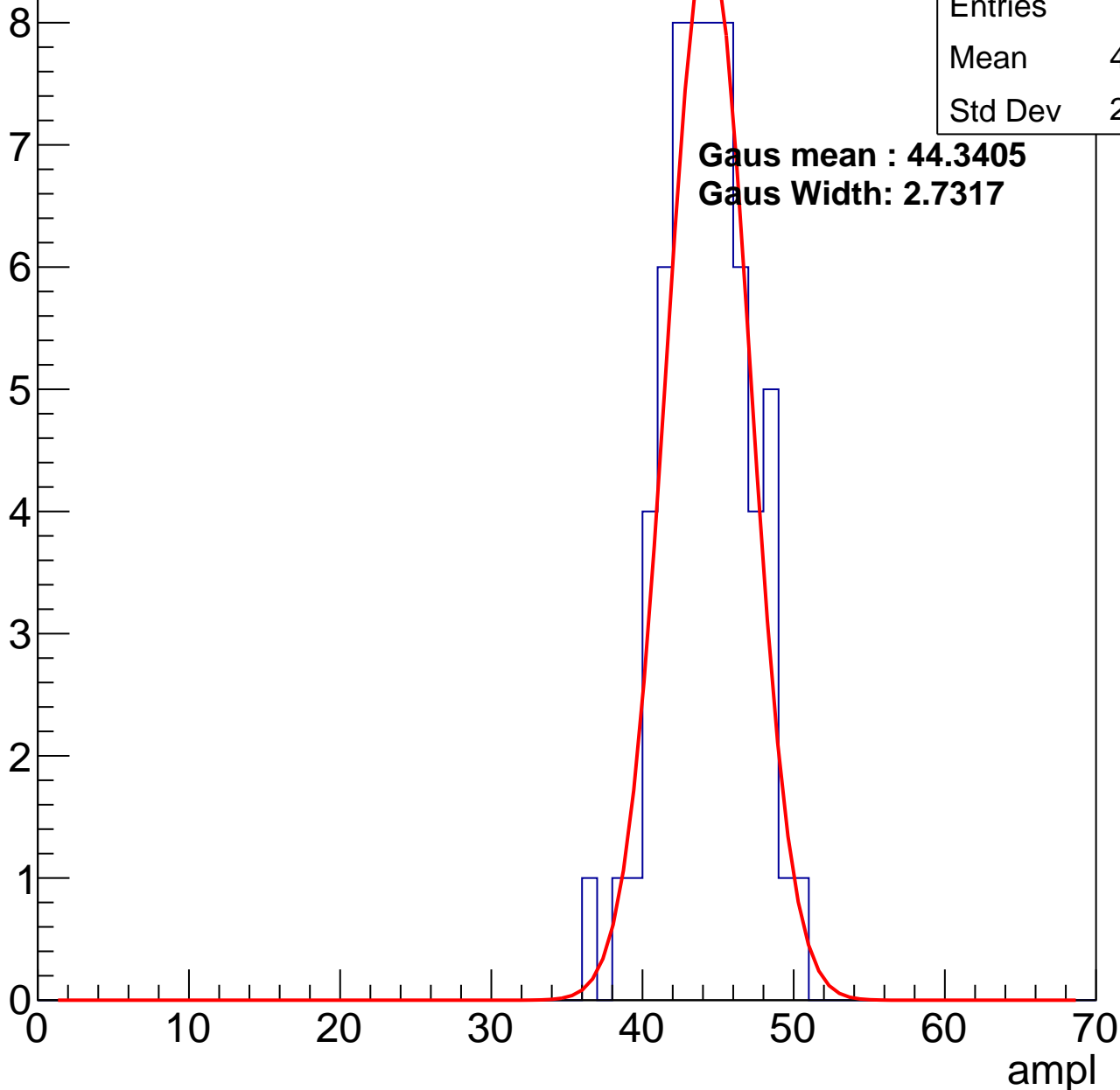
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.77
Std Dev	2.808

**Gaus mean : 44.3405**

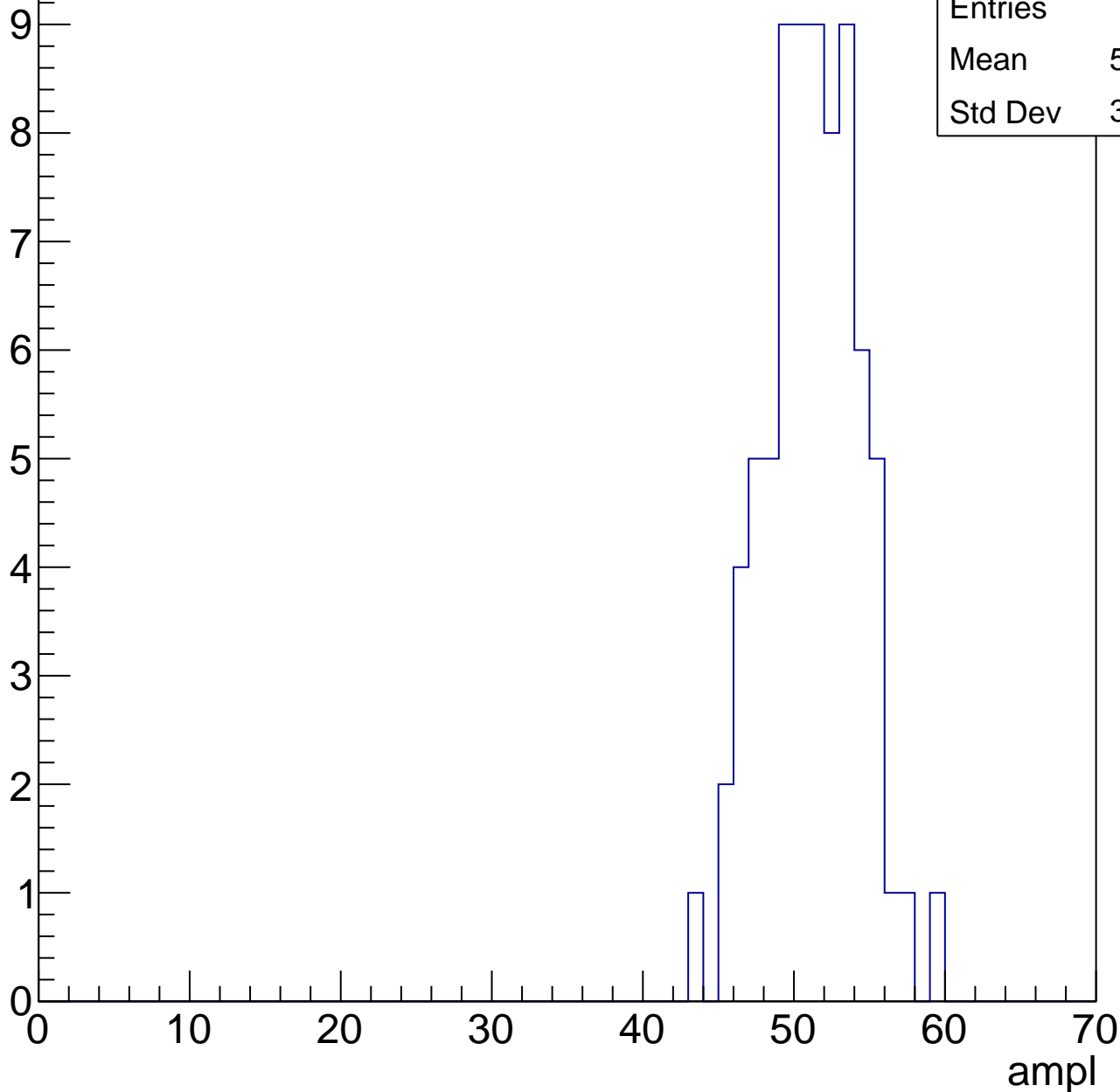
**Gaus Width: 2.7317**



# B0L002S, U8-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



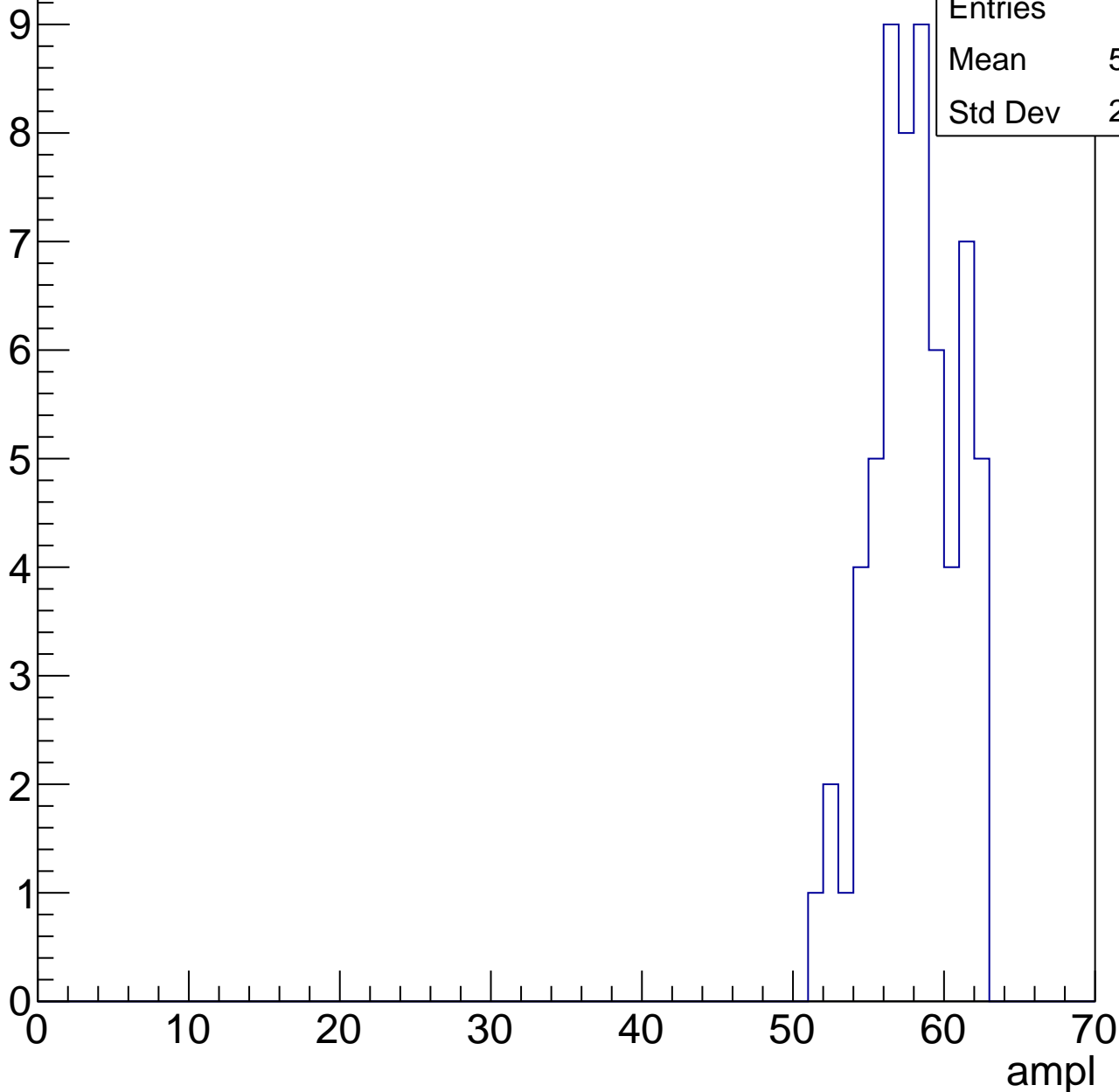
Entries	75
Mean	50.75
Std Dev	3.064

# B0L002S, U8-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	57.57
Std Dev	2.725

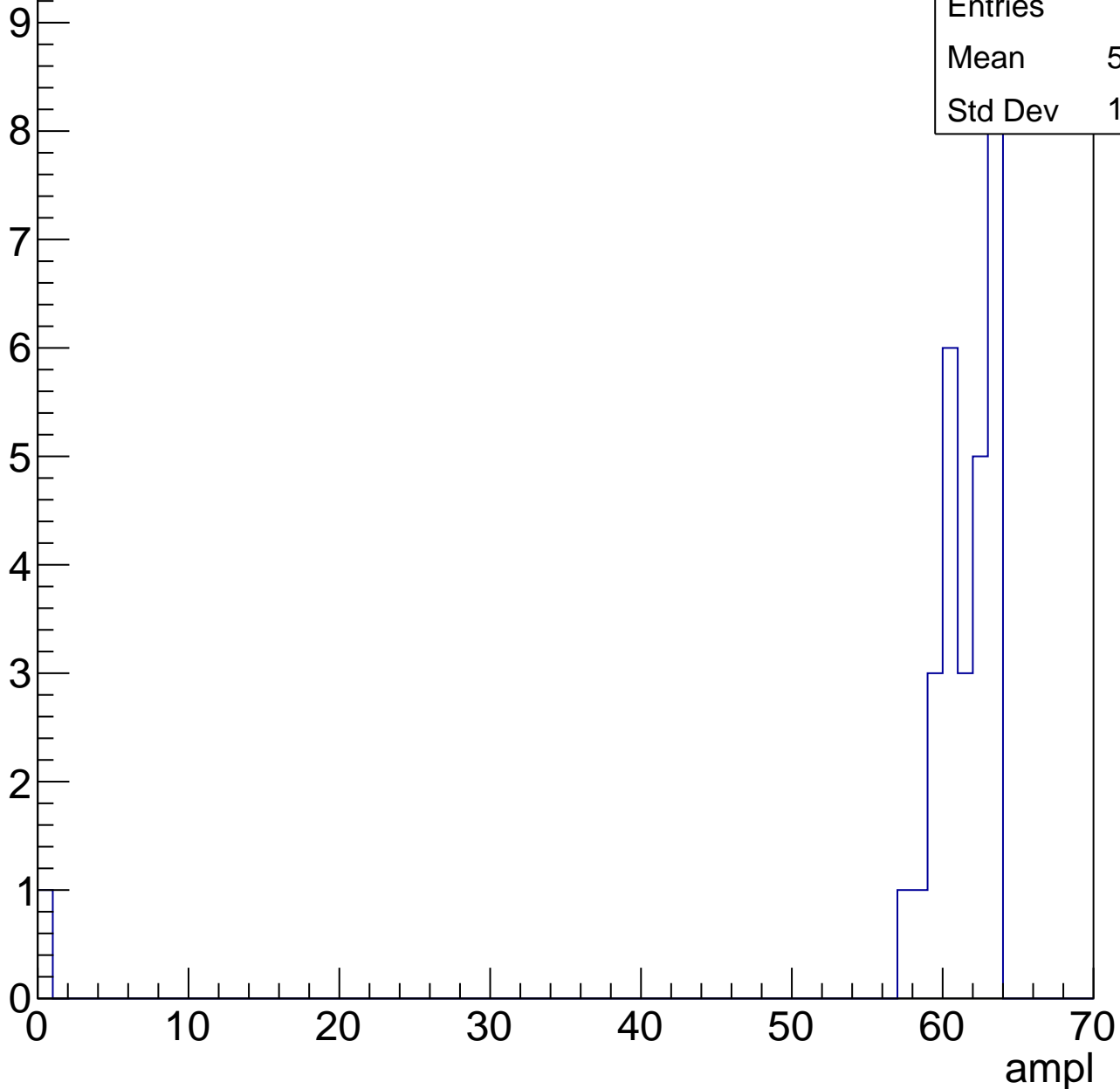


# B0L002S, U8-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.03
Std Dev	11.28



# B0L002S, U8-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L002S, U8-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch56, adc0

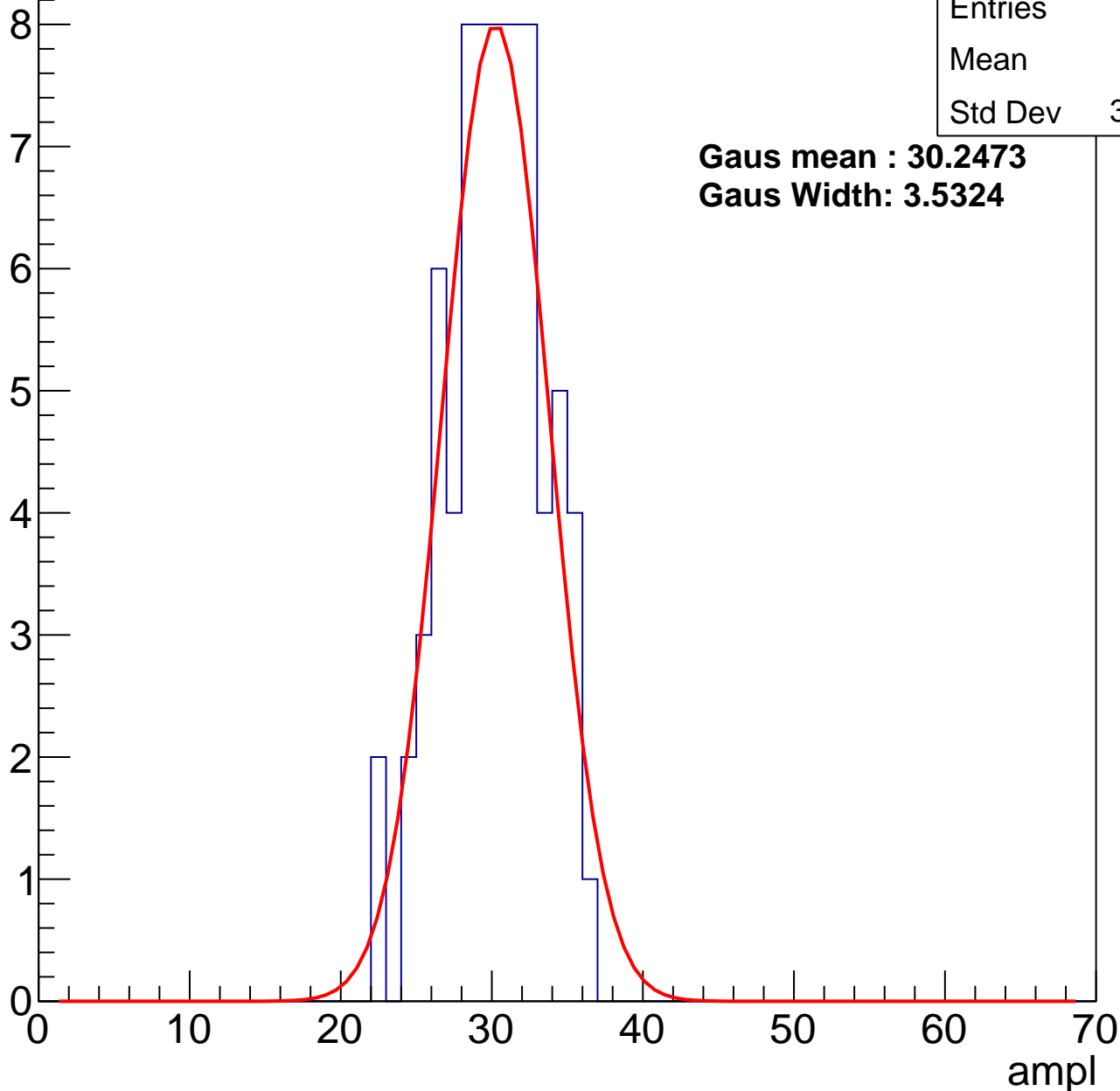
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.7
Std Dev	3.213

**Gaus mean : 30.2473**

**Gaus Width: 3.5324**



# B0L002S, U8-ch56, adc1

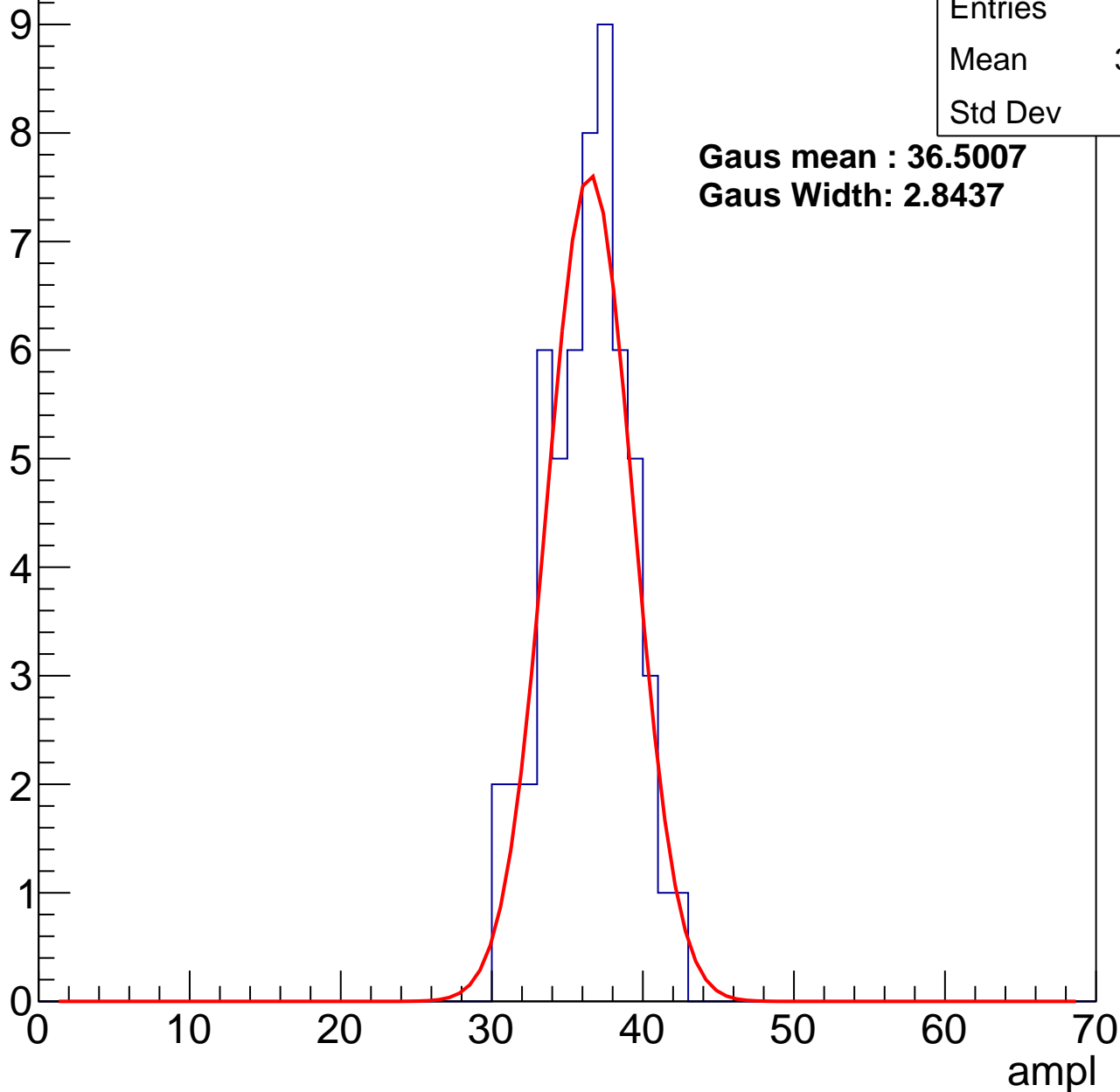
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	35.91
Std Dev	2.74

**Gaus mean : 36.5007**

**Gaus Width: 2.8437**



# B0L002S, U8-ch56, adc2

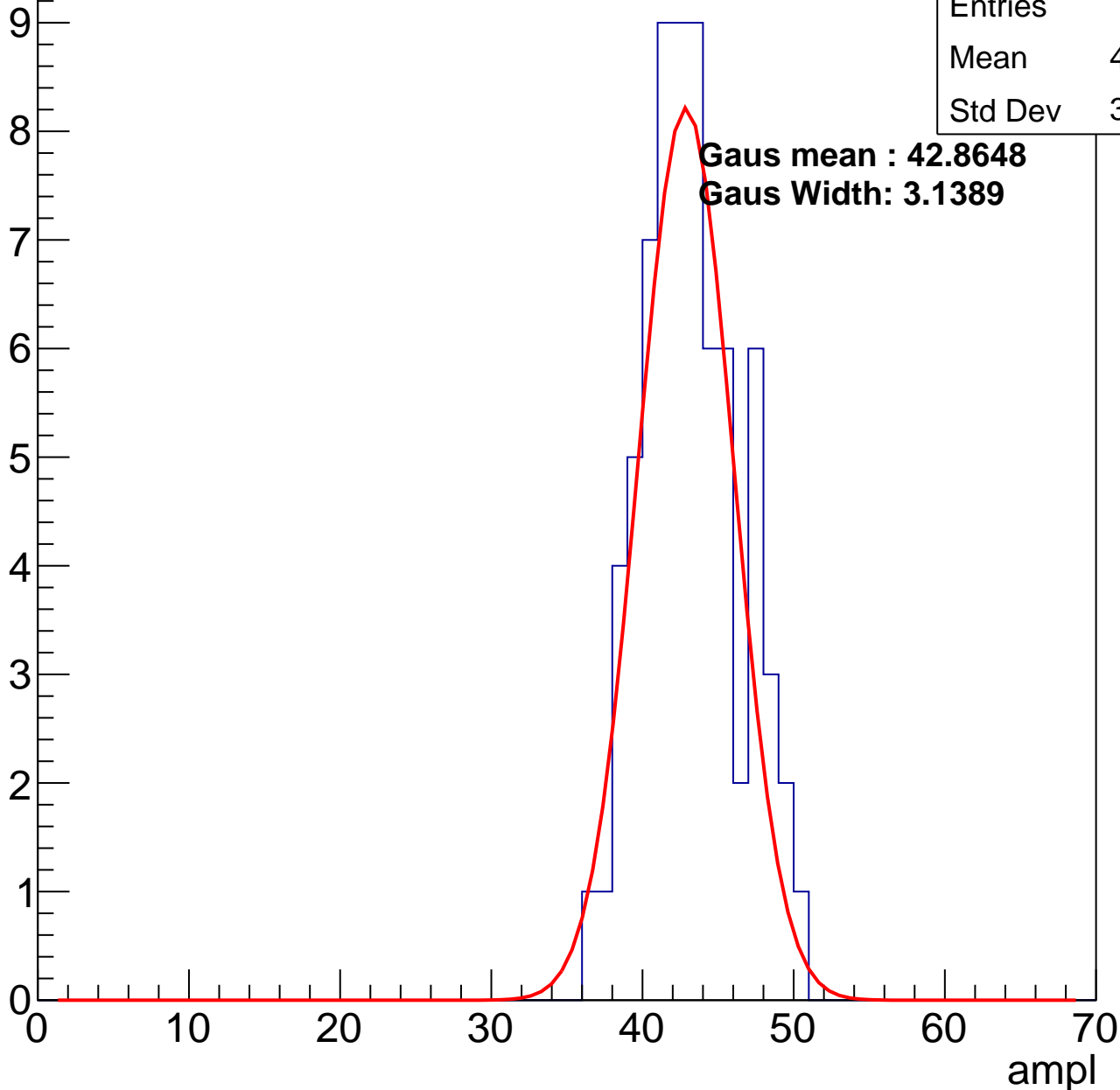
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.73
Std Dev	3.158

**Gaus mean : 42.8648**

**Gaus Width: 3.1389**

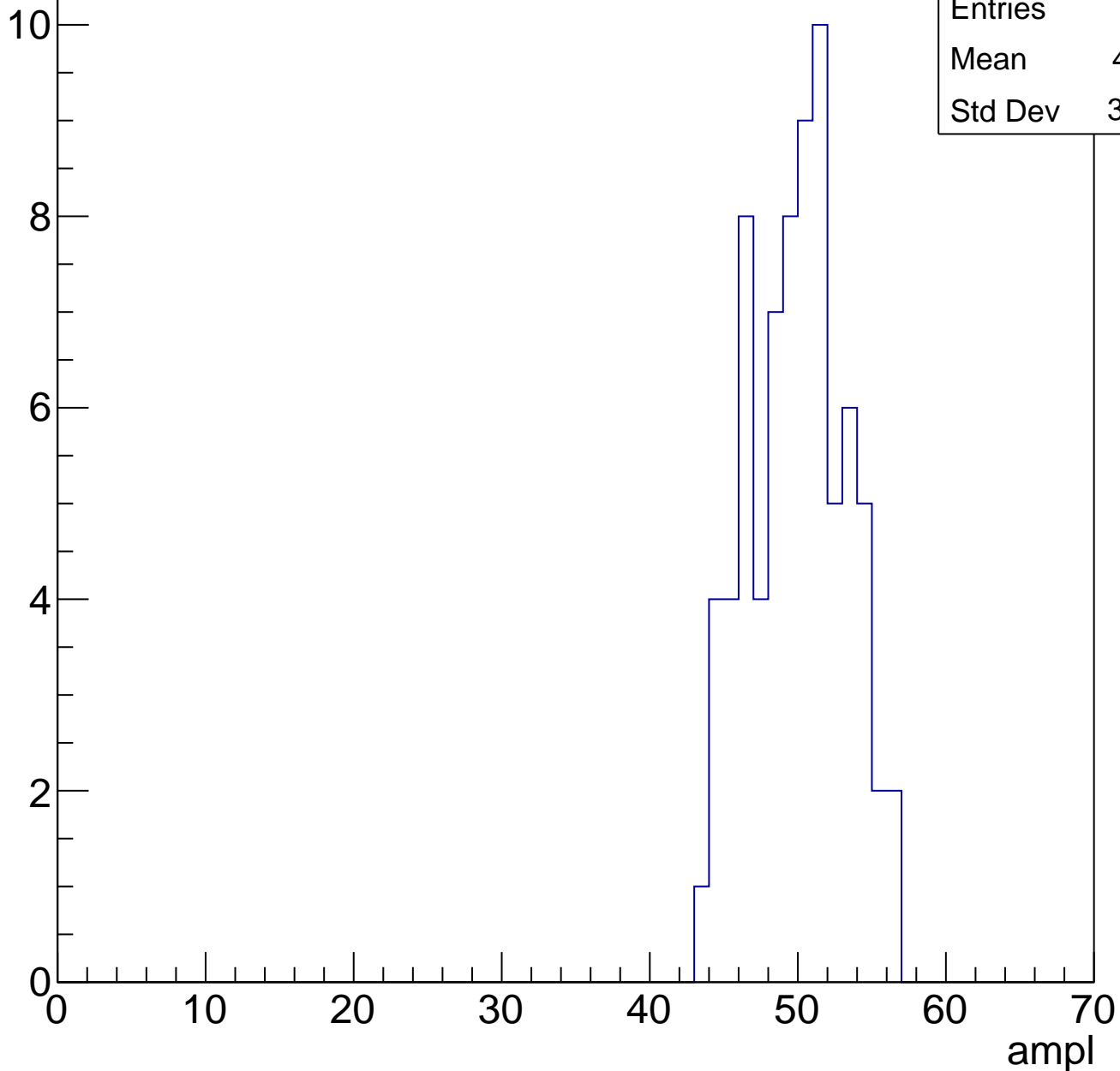


# B0L002S, U8-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	49.51
Std Dev	3.185

Entry

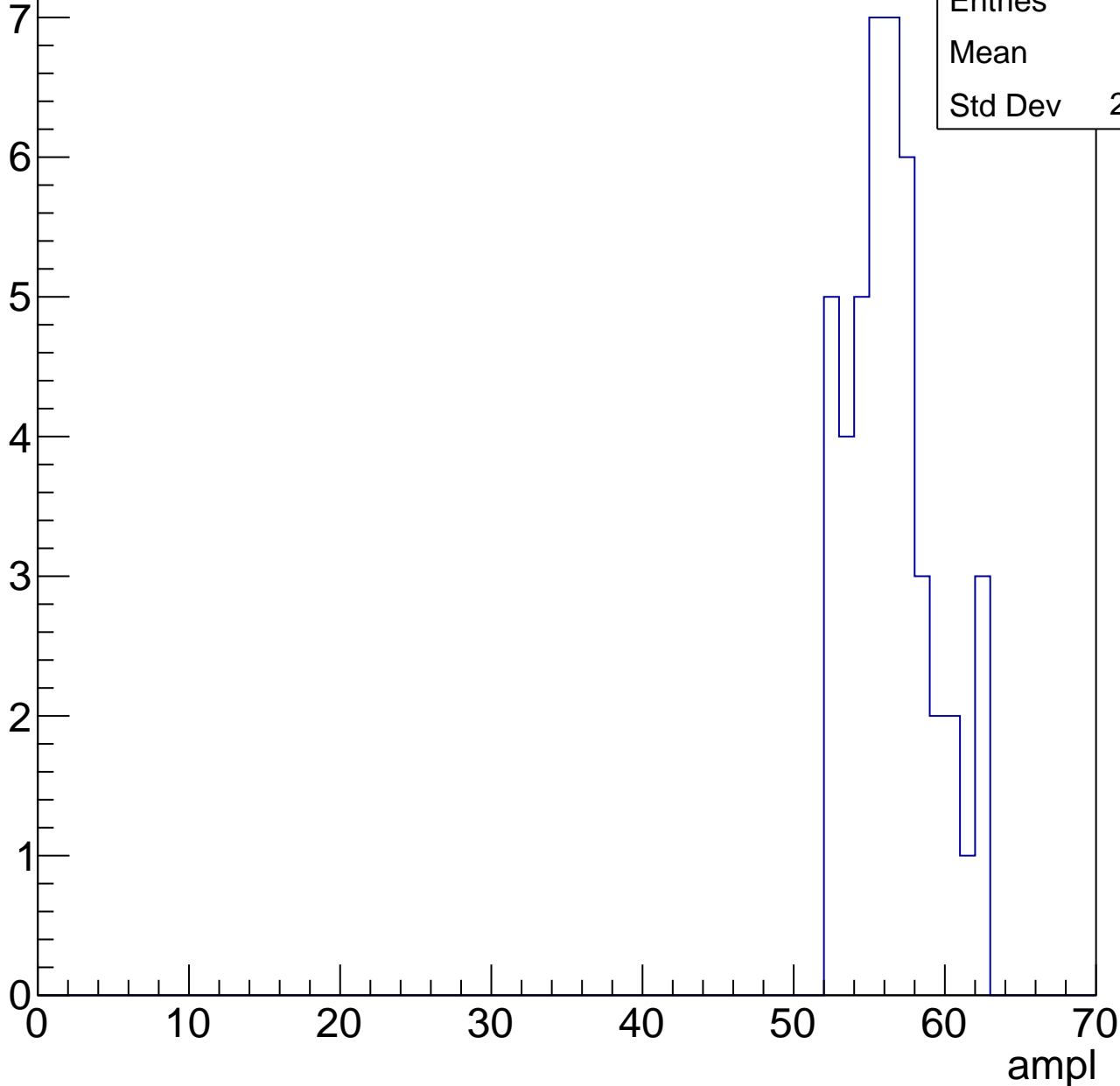


# B0L002S, U8-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	56
Std Dev	2.765



# B0L002S, U8-ch56, adc5

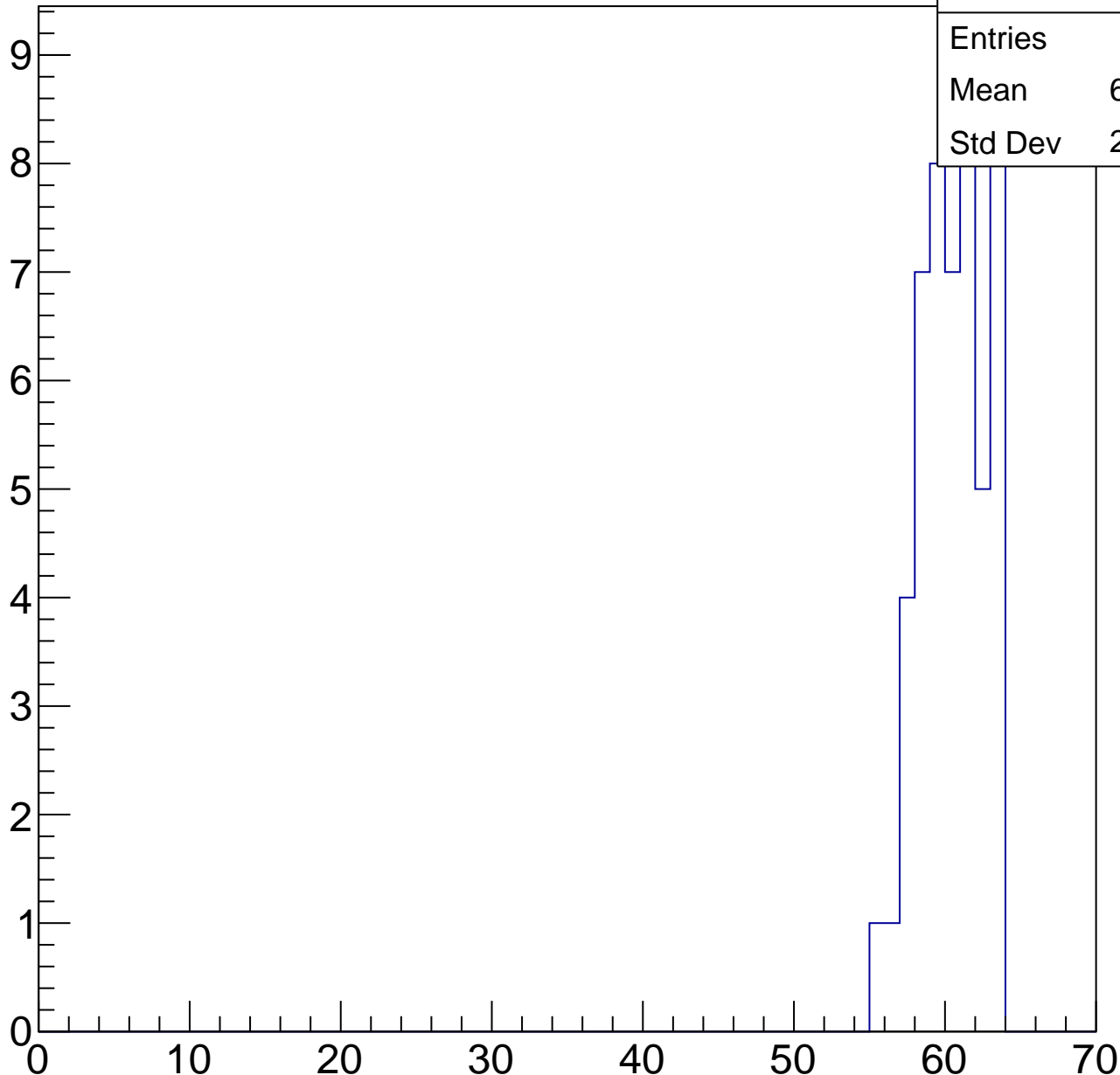
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	60.04
Std Dev	2.107

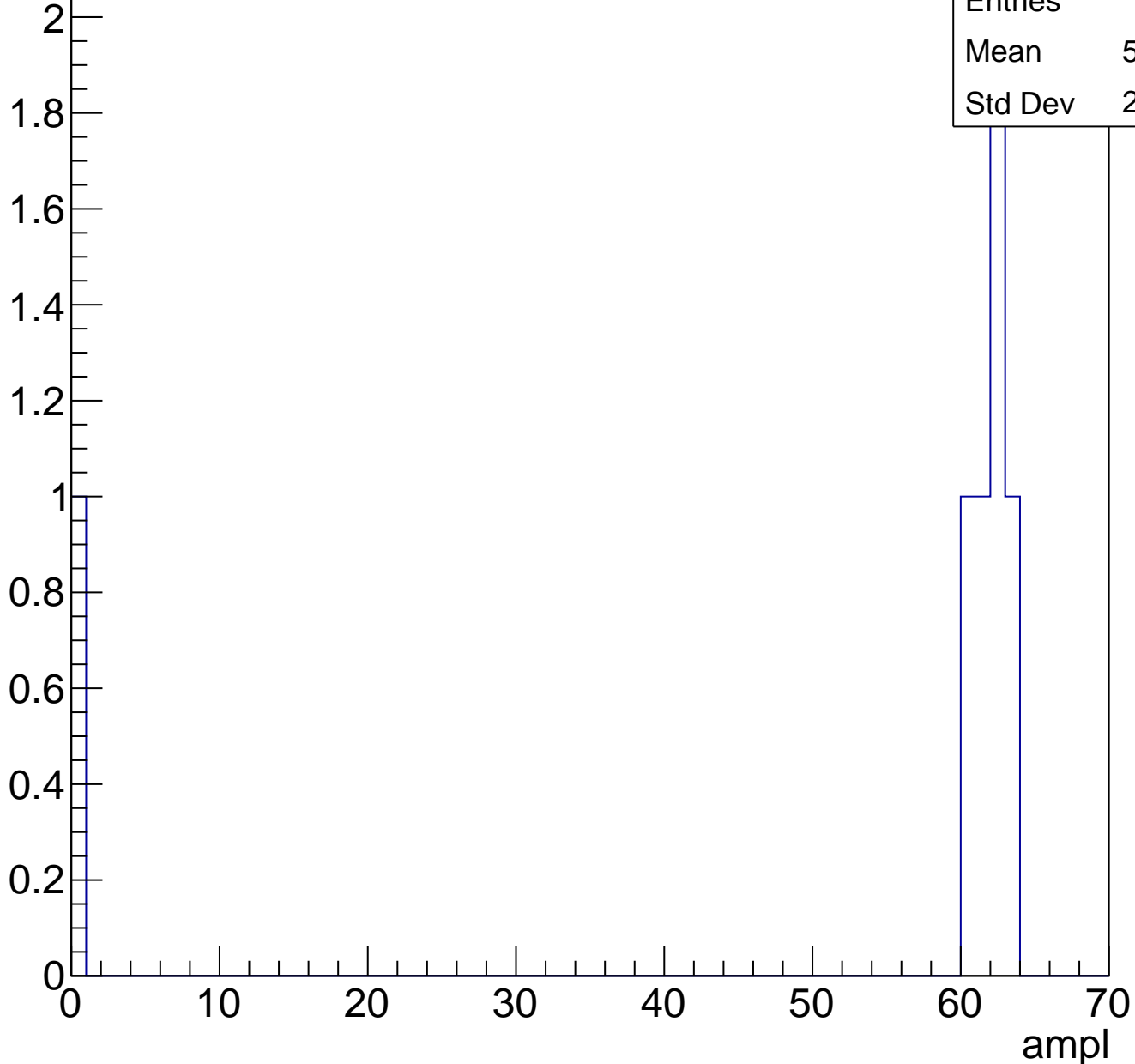
ampl



# B0L002S, U8-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch57, adc0

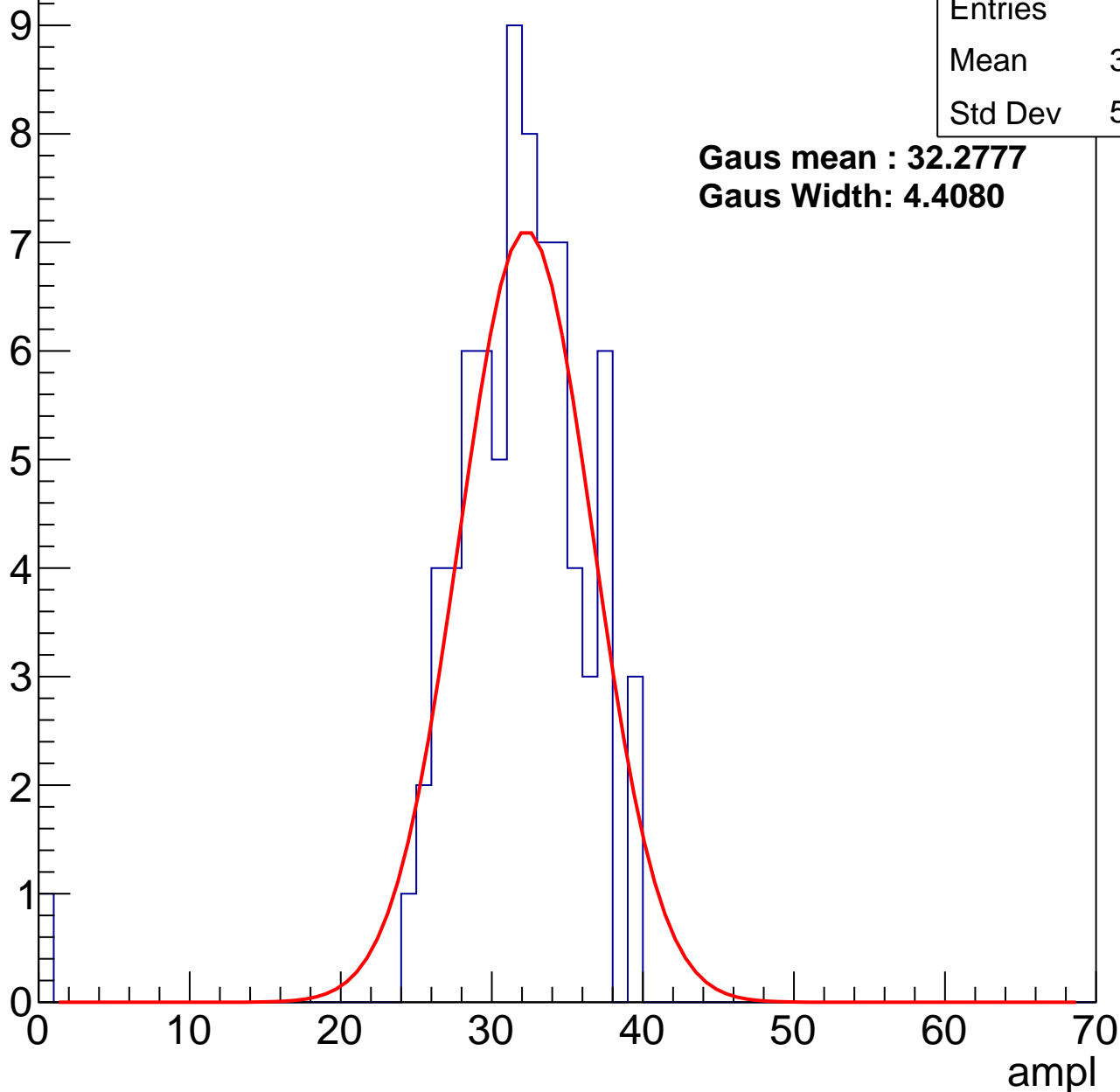
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.17
Std Dev	5.095

**Gaus mean : 32.2777**

**Gaus Width: 4.4080**



# B0L002S, U8-ch57, adc1

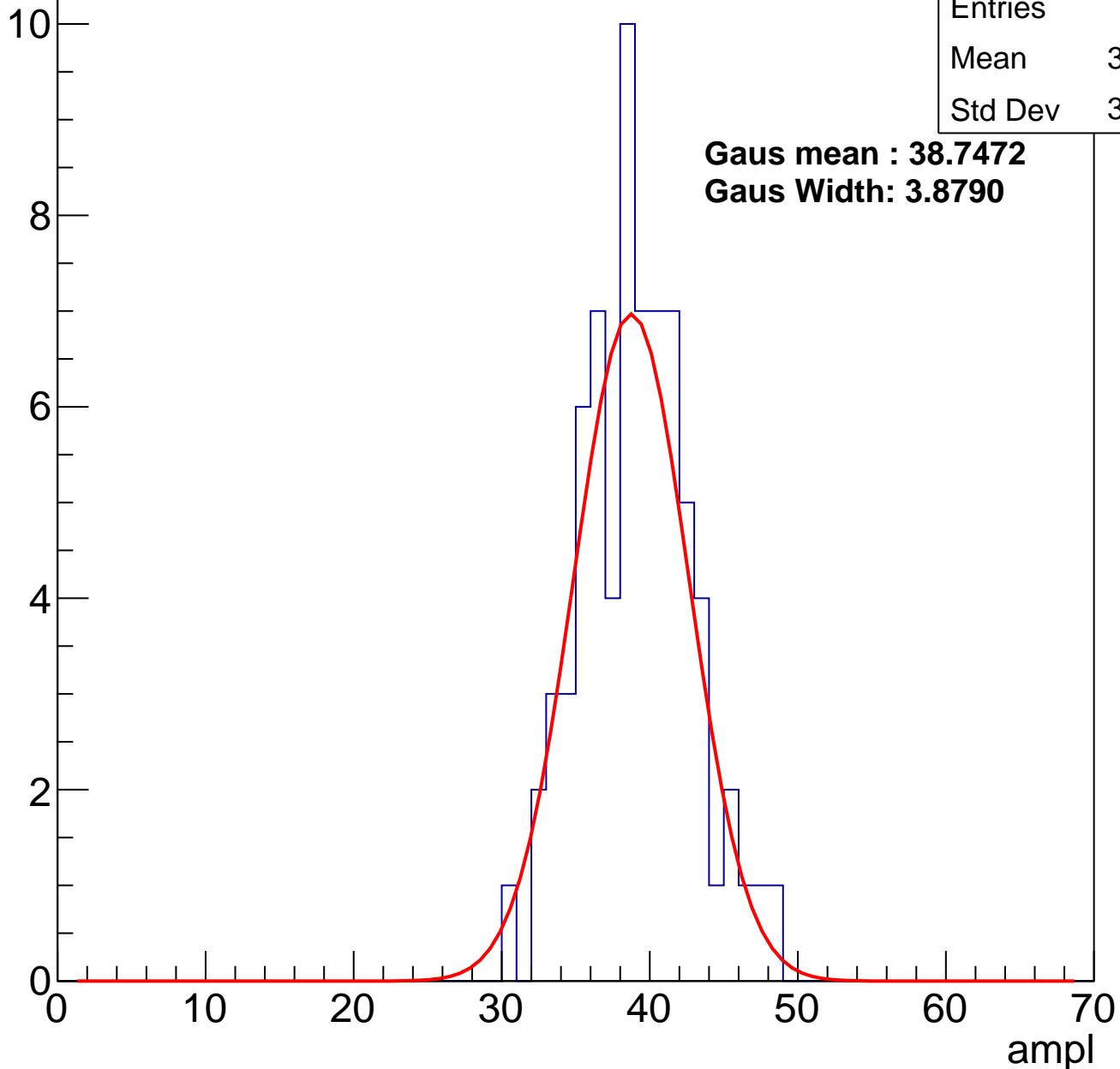
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	38.64
Std Dev	3.675

**Gaus mean : 38.7472**

**Gaus Width: 3.8790**

Entry



# B0L002S, U8-ch57, adc2

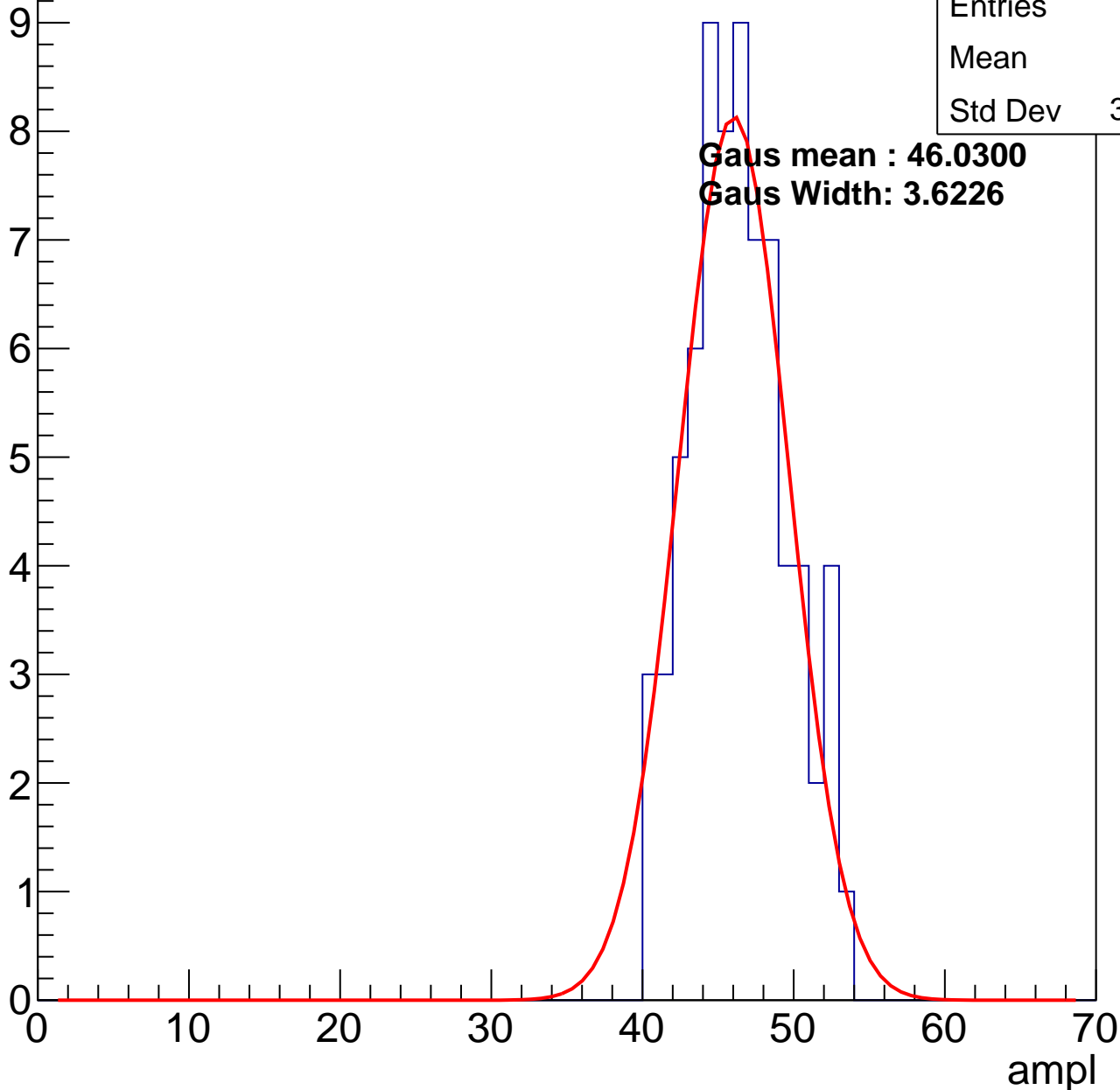
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.9
Std Dev	3.202

**Gaus mean : 46.0300**

**Gaus Width: 3.6226**

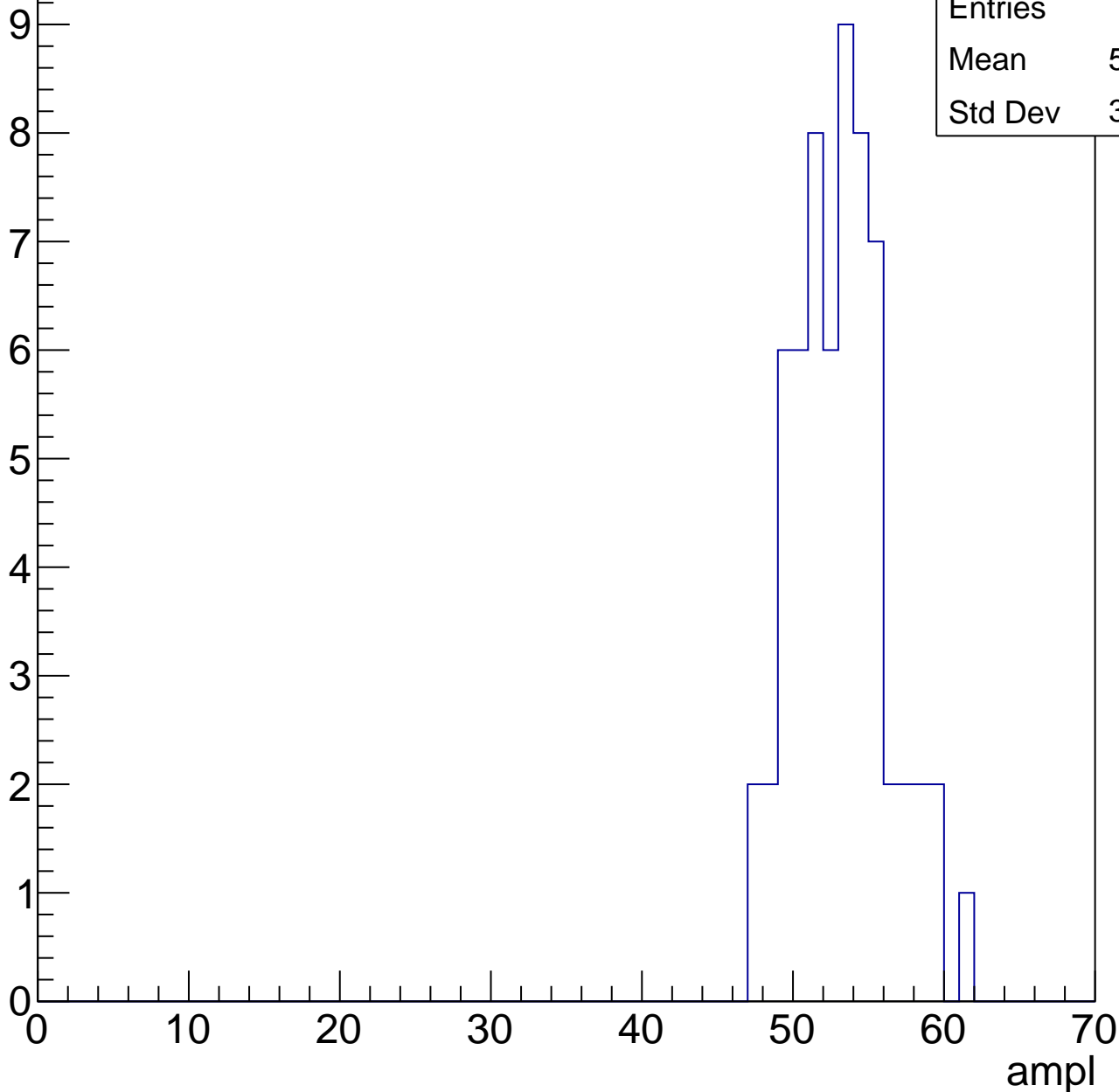


# B0L002S, U8-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	52.68
Std Dev	3.023



# B0L002S, U8-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

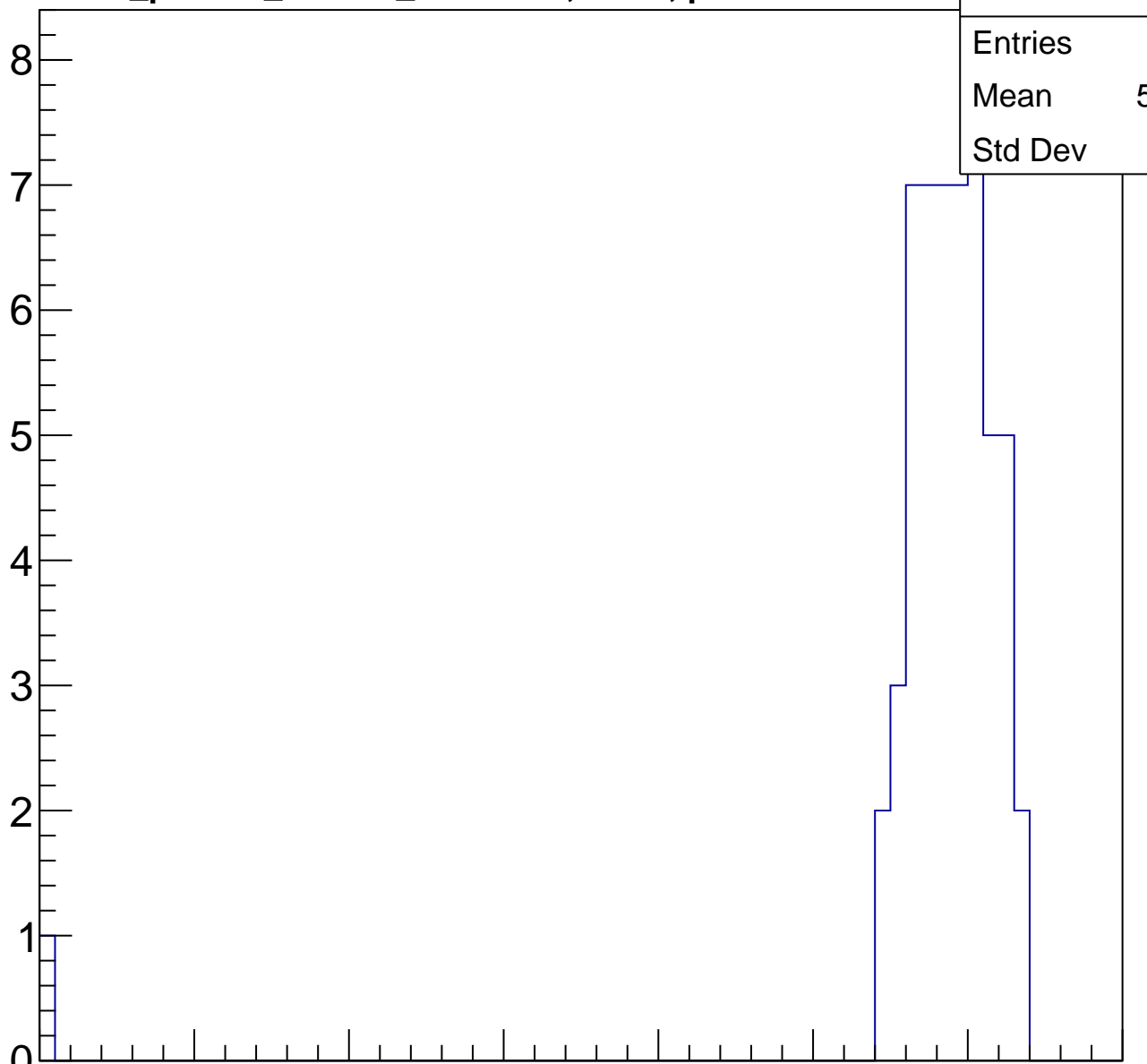
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	57.48
Std Dev	8.23

ampl

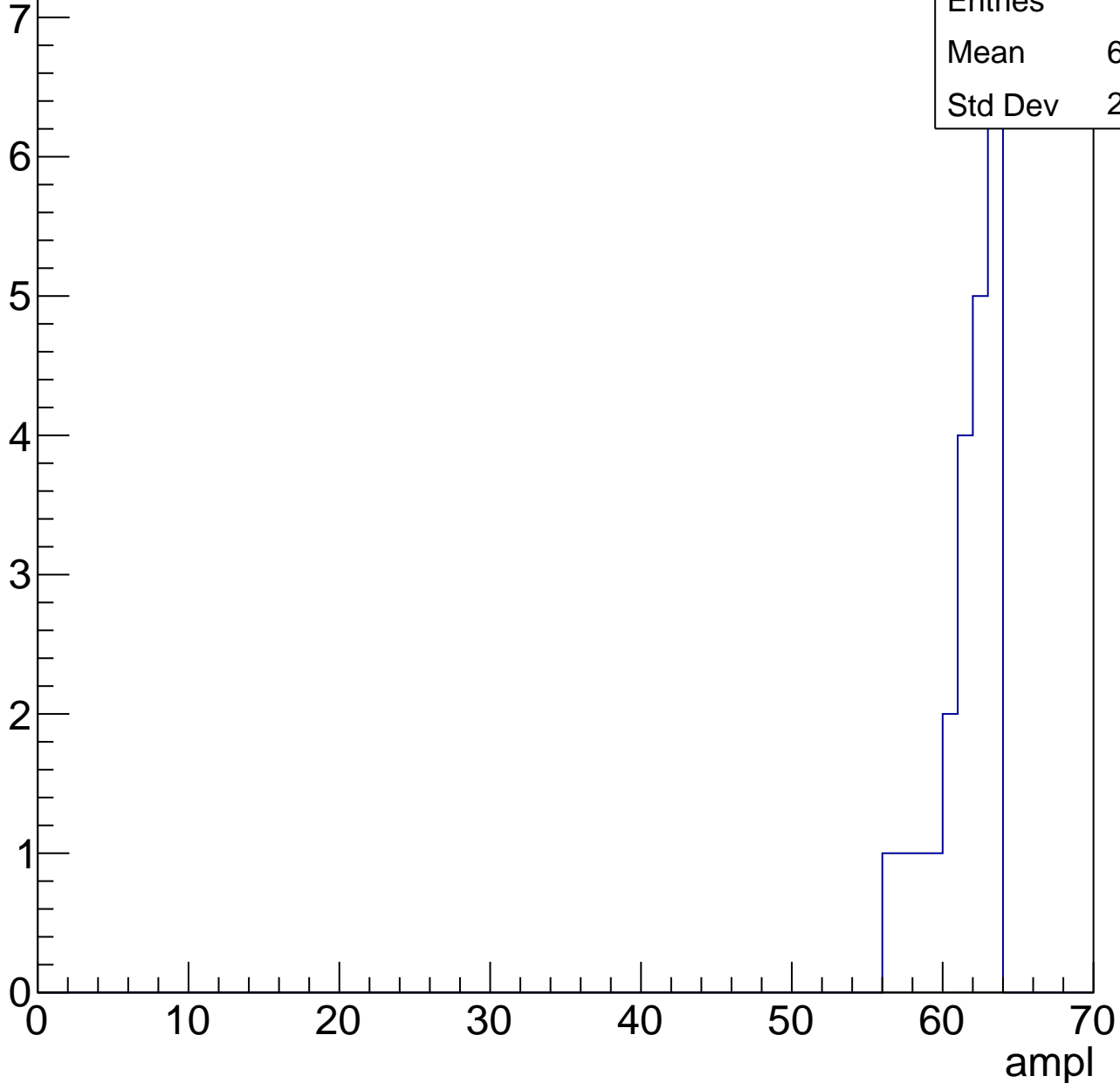
0 10 20 30 40 50 60 70



# B0L002S, U8-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch58, adc0

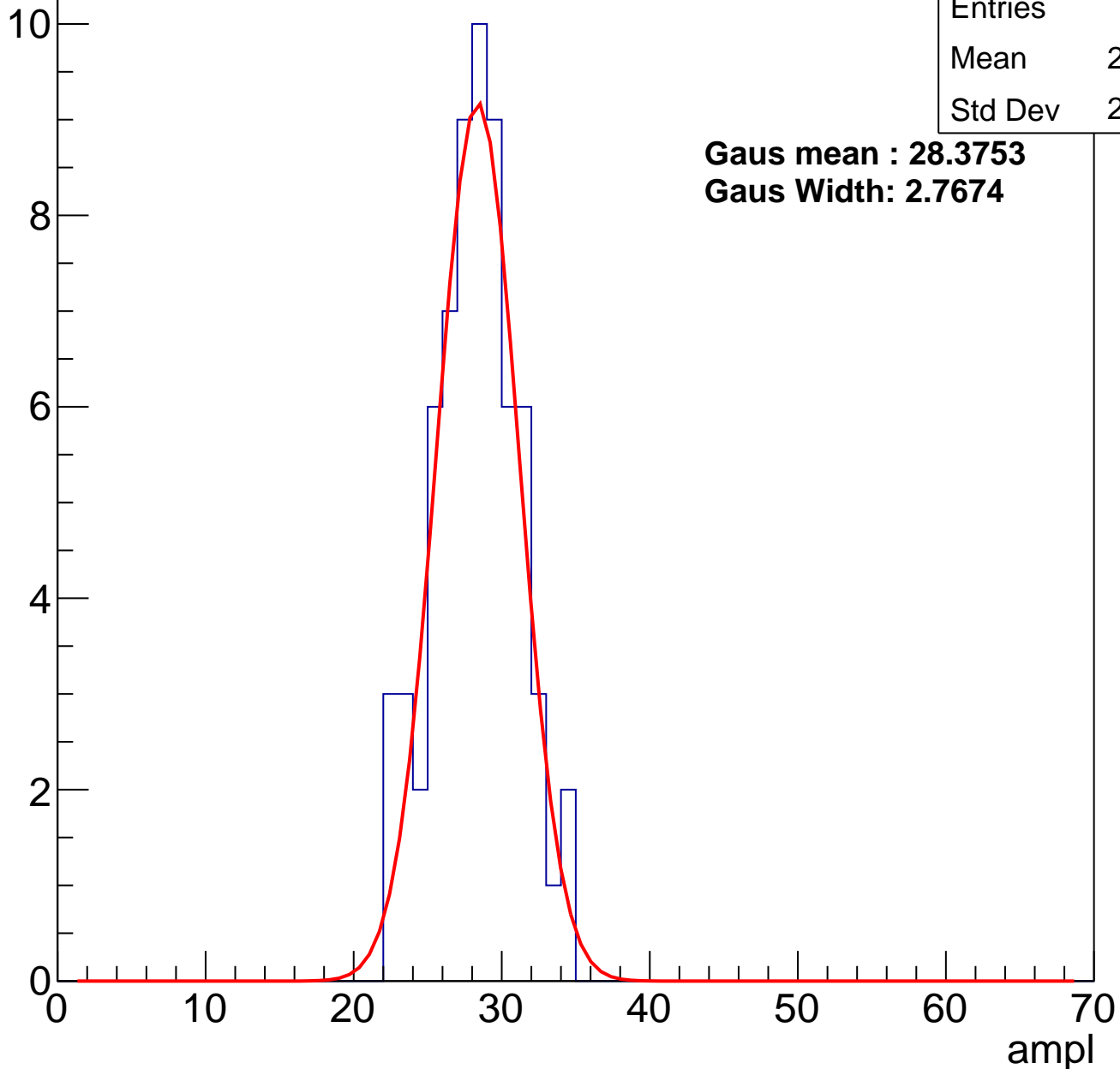
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	27.79
Std Dev	2.826

**Gaus mean : 28.3753**

**Gaus Width: 2.7674**

Entry



# B0L002S, U8-ch58, adc1

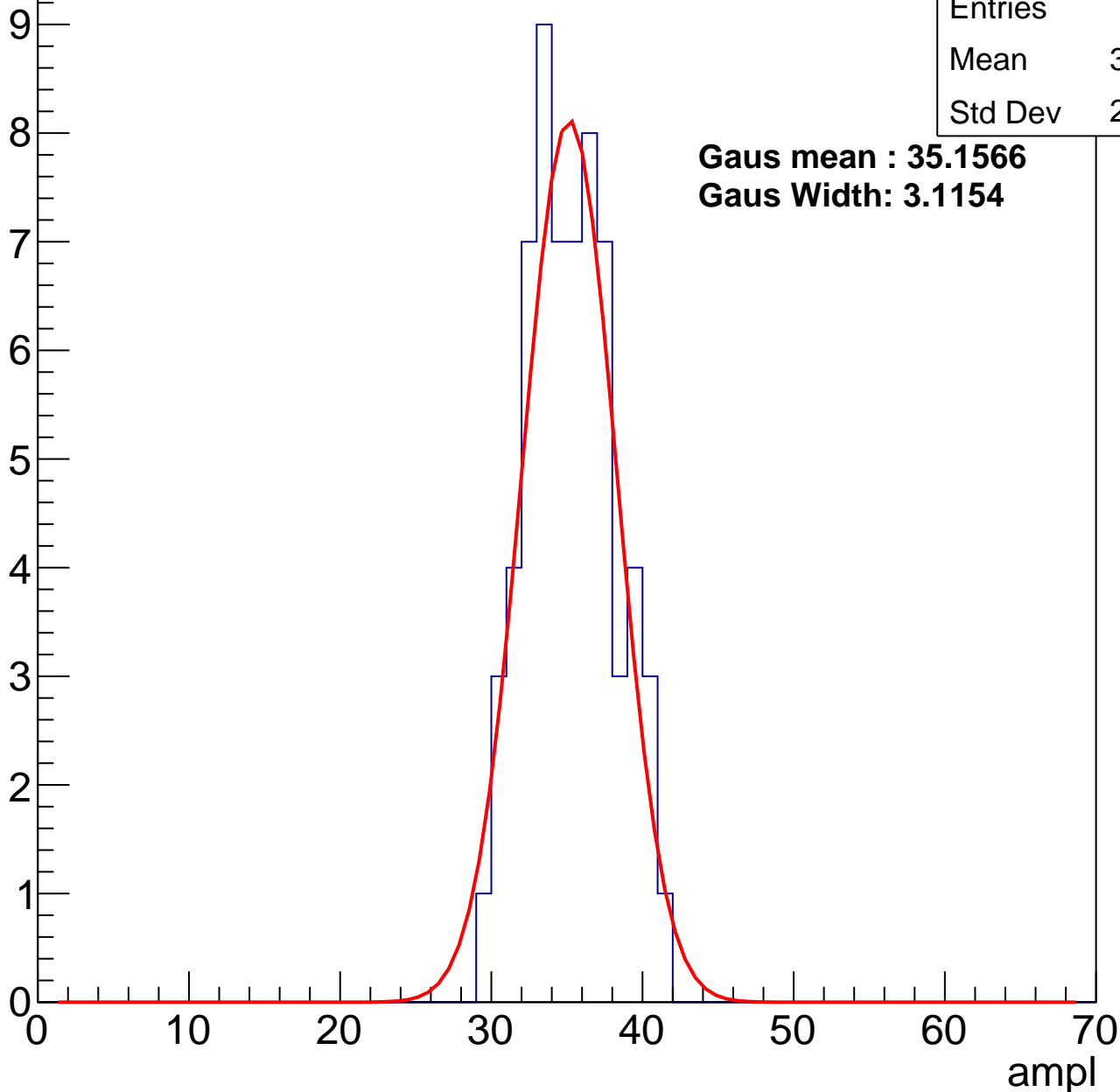
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	34.77
Std Dev	2.838

**Gaus mean : 35.1566**

**Gaus Width: 3.1154**



# B0L002S, U8-ch58, adc2

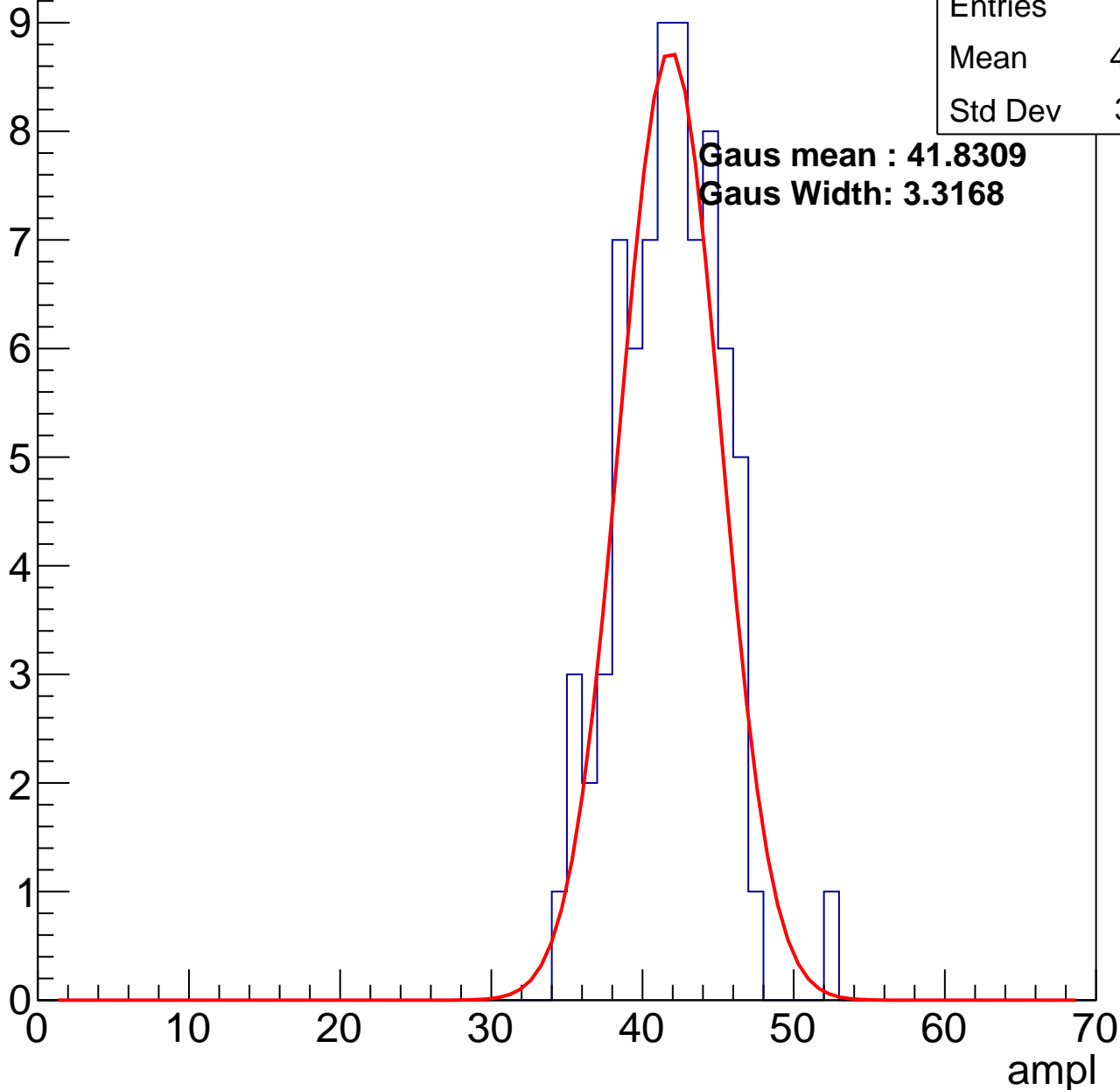
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	41.35
Std Dev	3.321

**Gaus mean : 41.8309**

**Gaus Width: 3.3168**

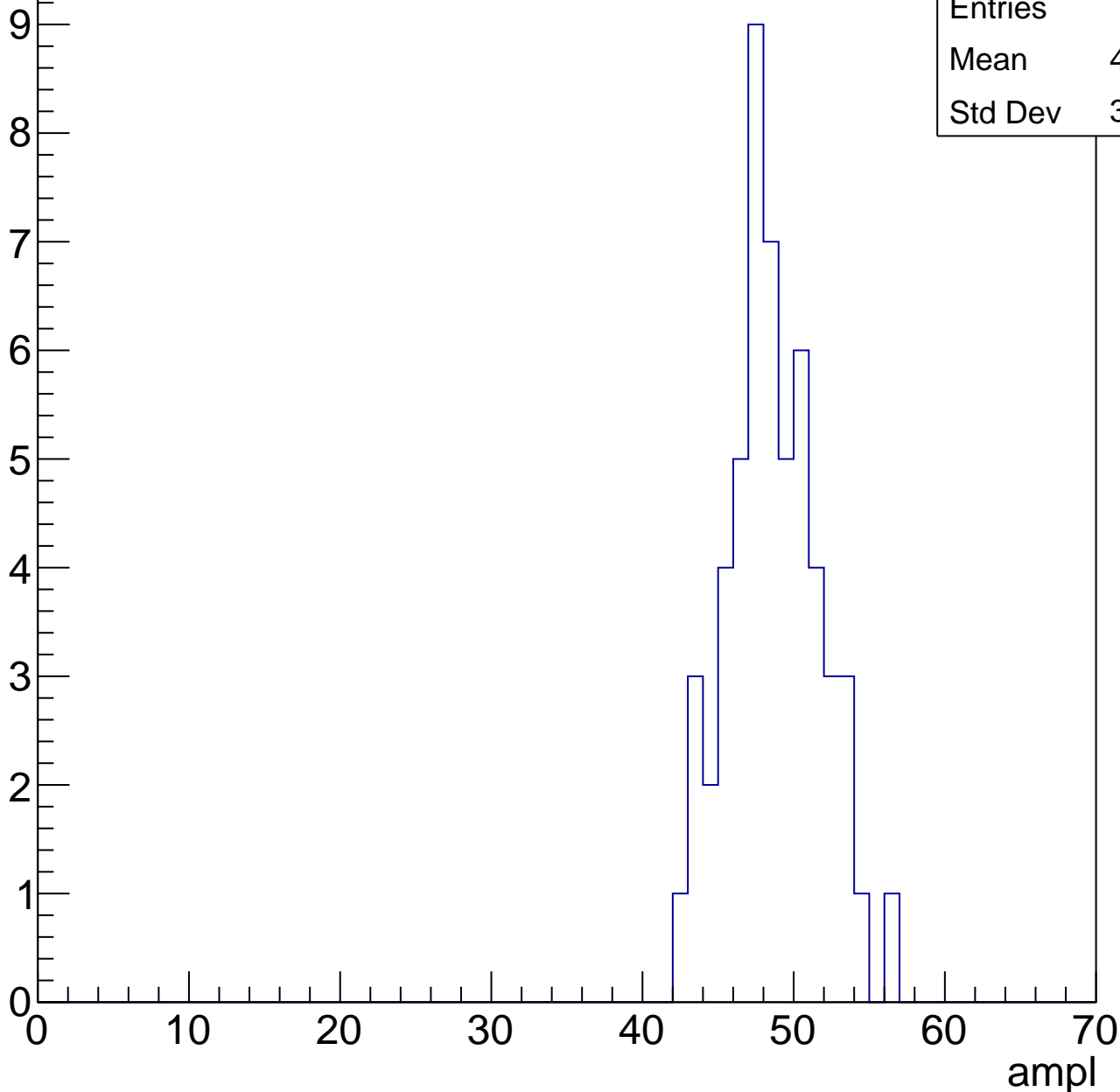


# B0L002S, U8-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	48.19
Std Dev	3.025



# B0L002S, U8-ch58, adc4

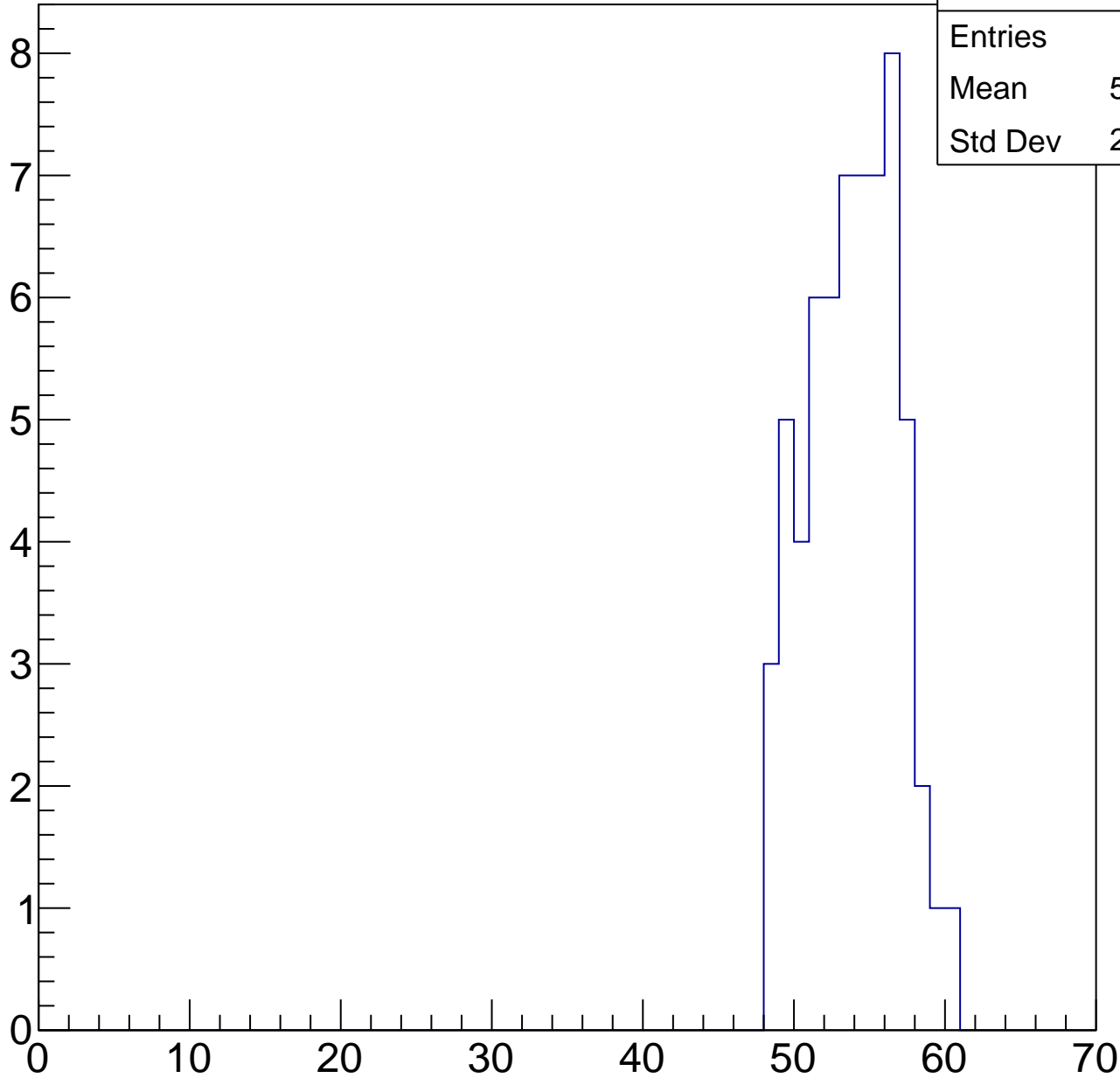
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	53.37
Std Dev	2.936

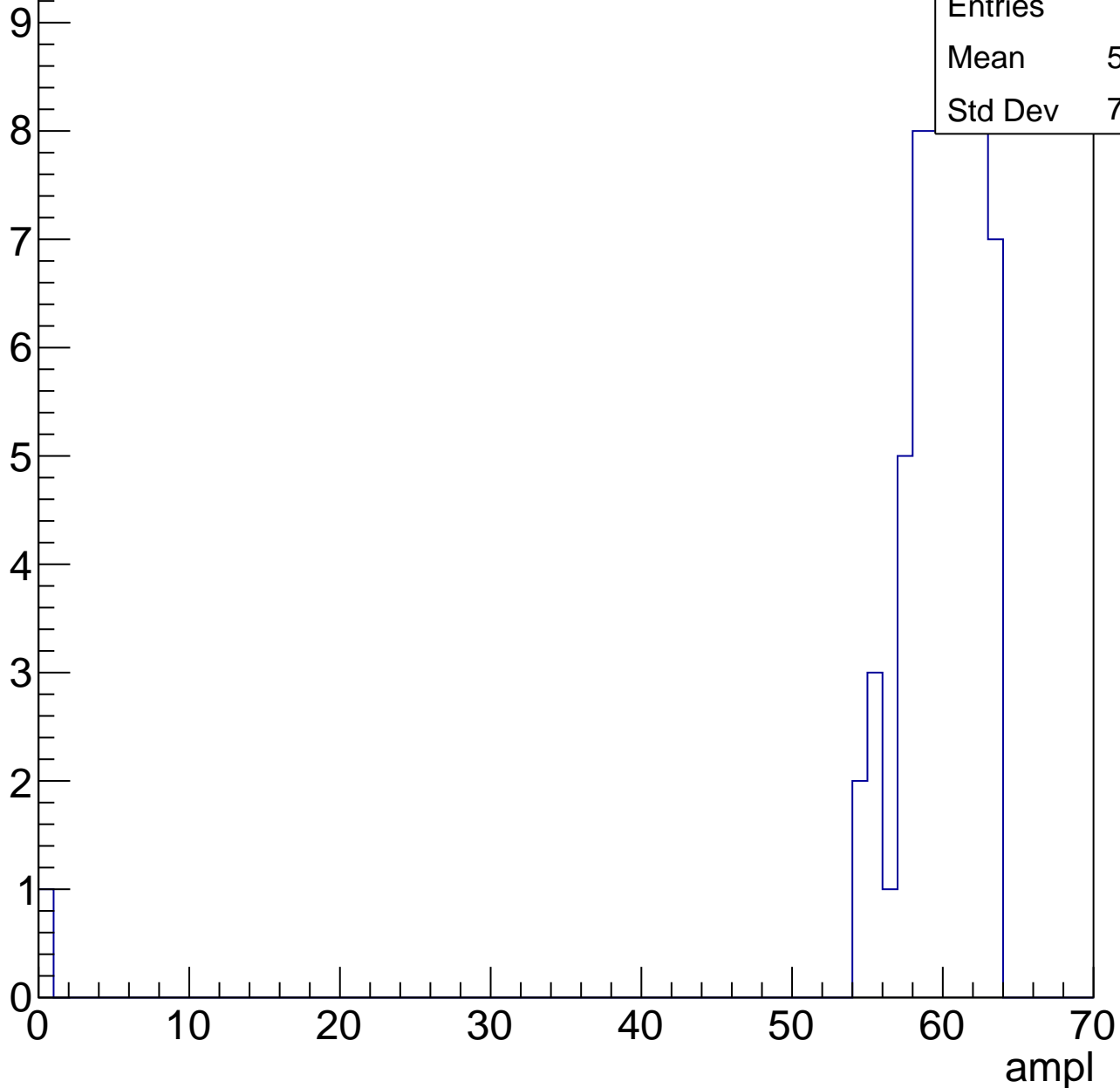
ampl



# B0L002S, U8-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

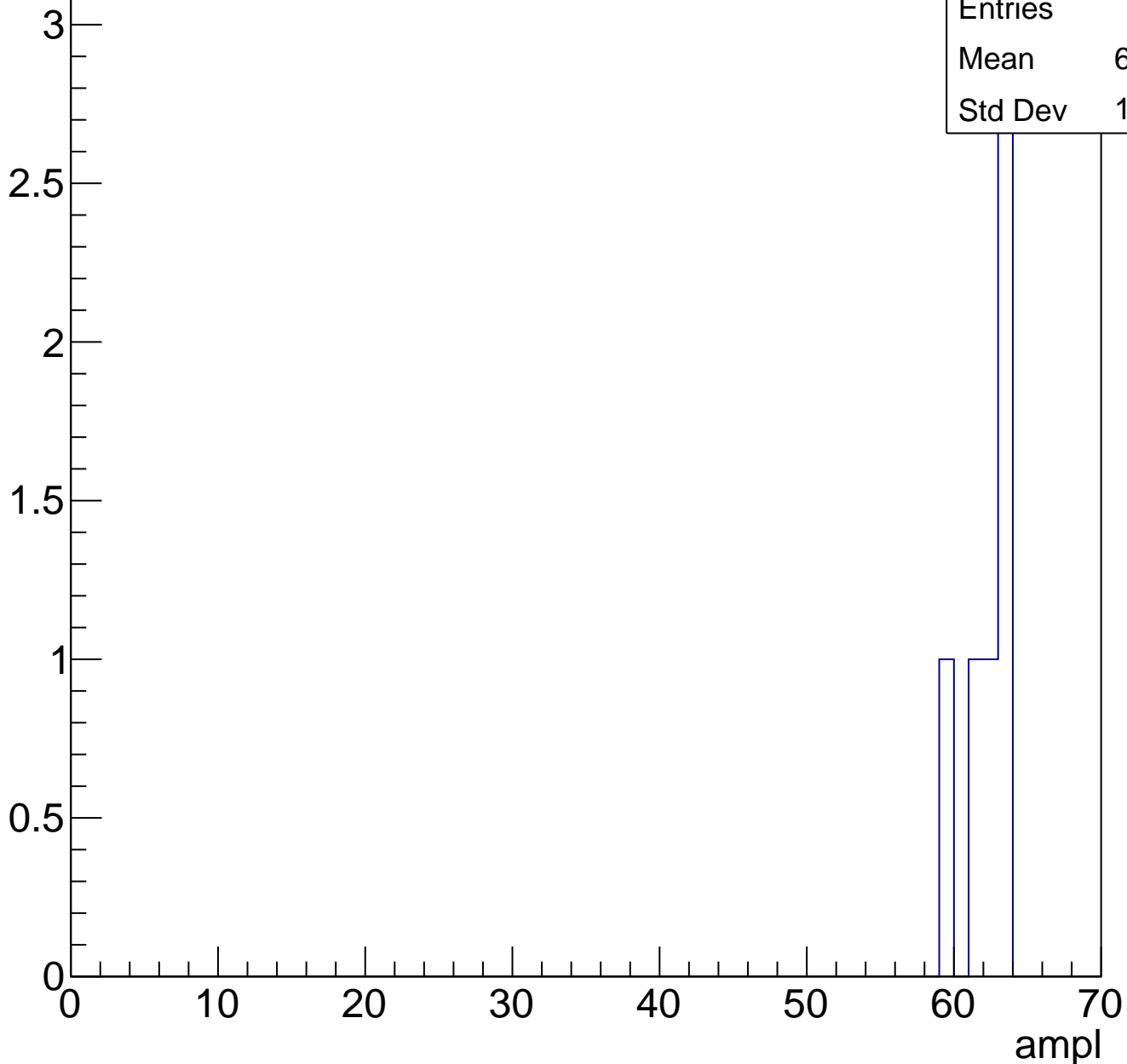
Entry



# B0L002S, U8-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	6
Mean	61.83
Std Dev	1.462



# B0L002S, U8-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

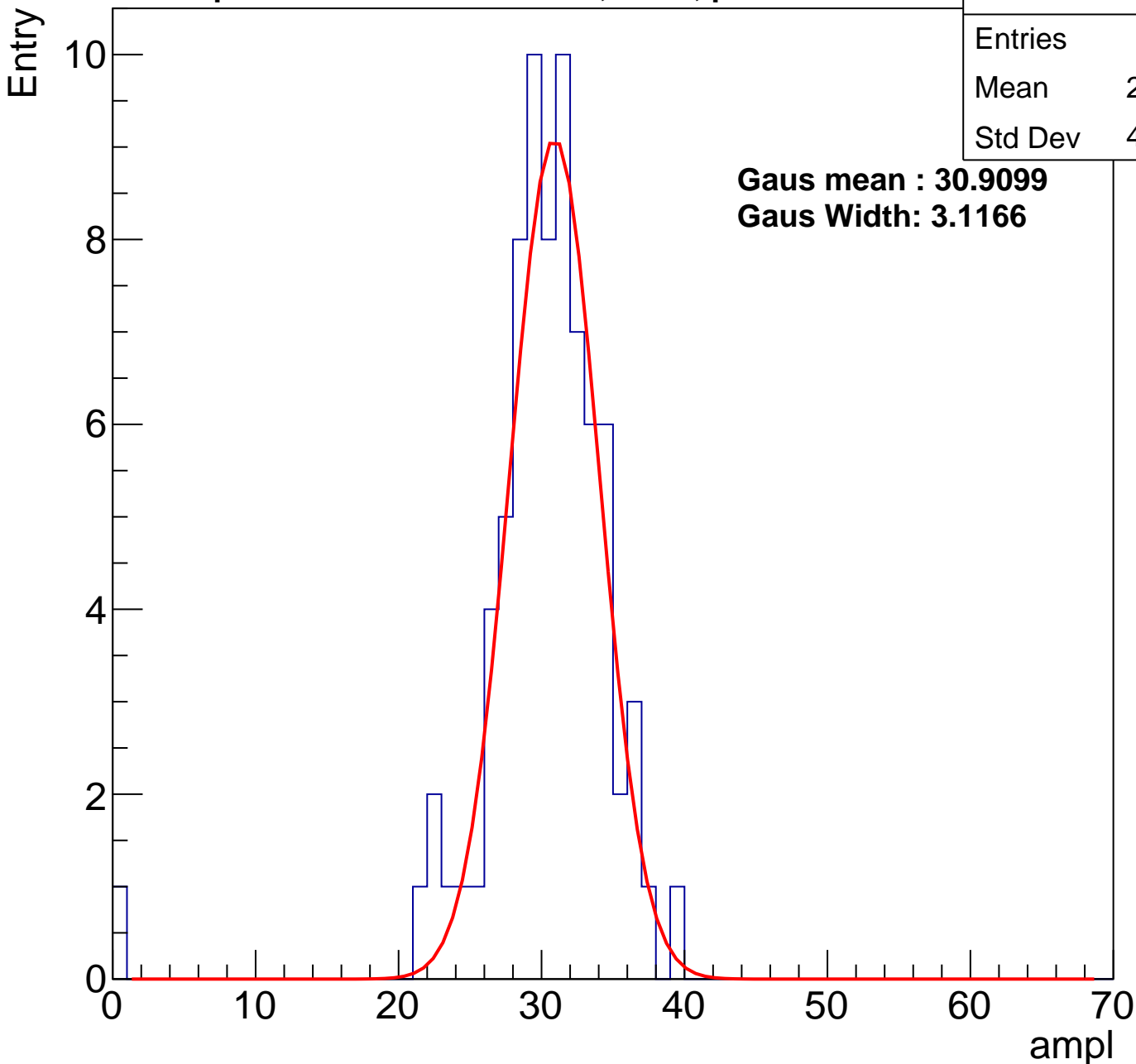
# B0L002S, U8-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	29.74
Std Dev	4.858

**Gaus mean : 30.9099**

**Gaus Width: 3.1166**



# B0L002S, U8-ch59, adc1

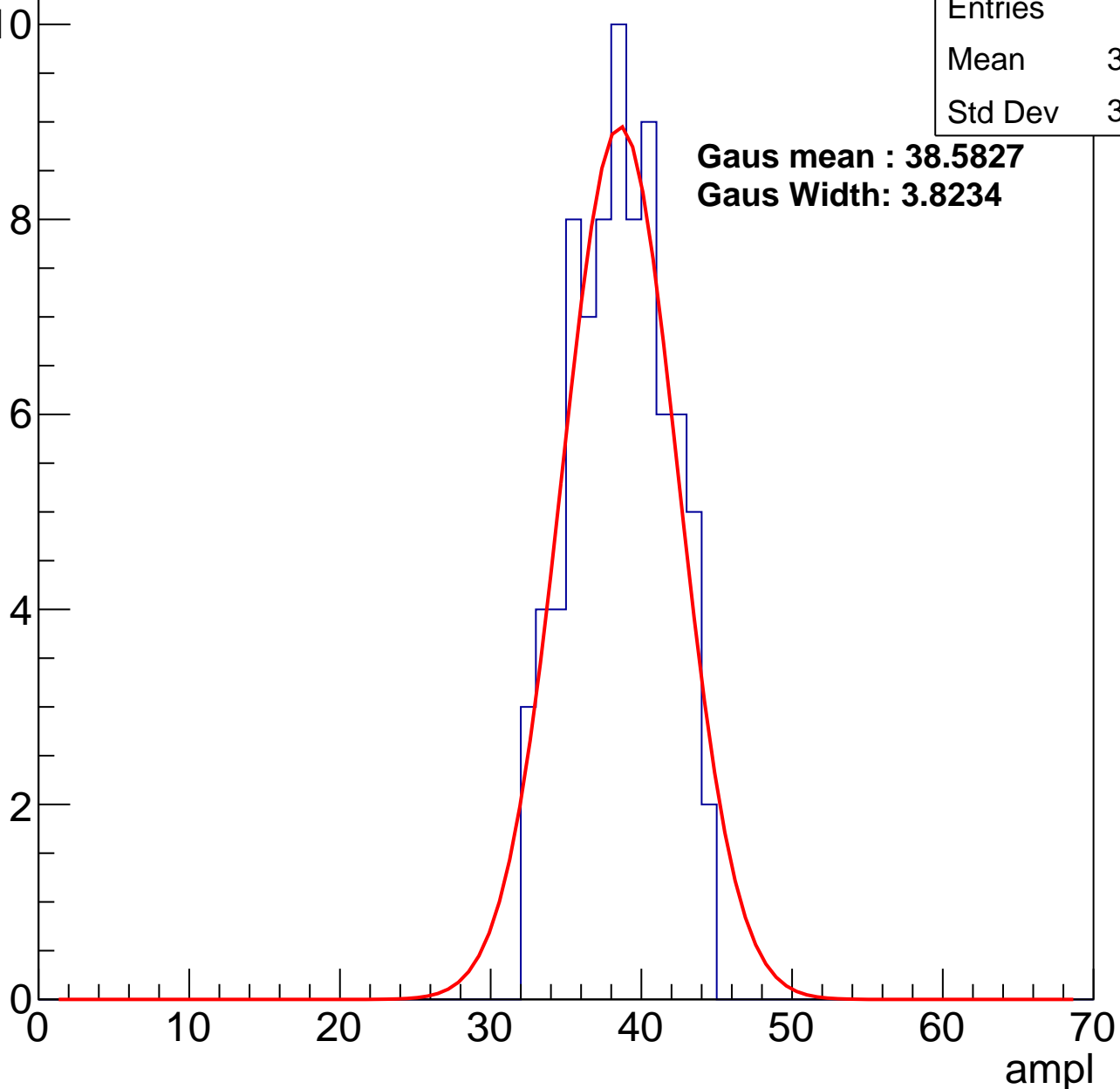
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	80
Mean	38.06
Std Dev	3.104

**Gaus mean : 38.5827**

**Gaus Width: 3.8234**



# B0L002S, U8-ch59, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	65
Mean	45.4
Std Dev	3.007

**Gaus mean : 45.9973**

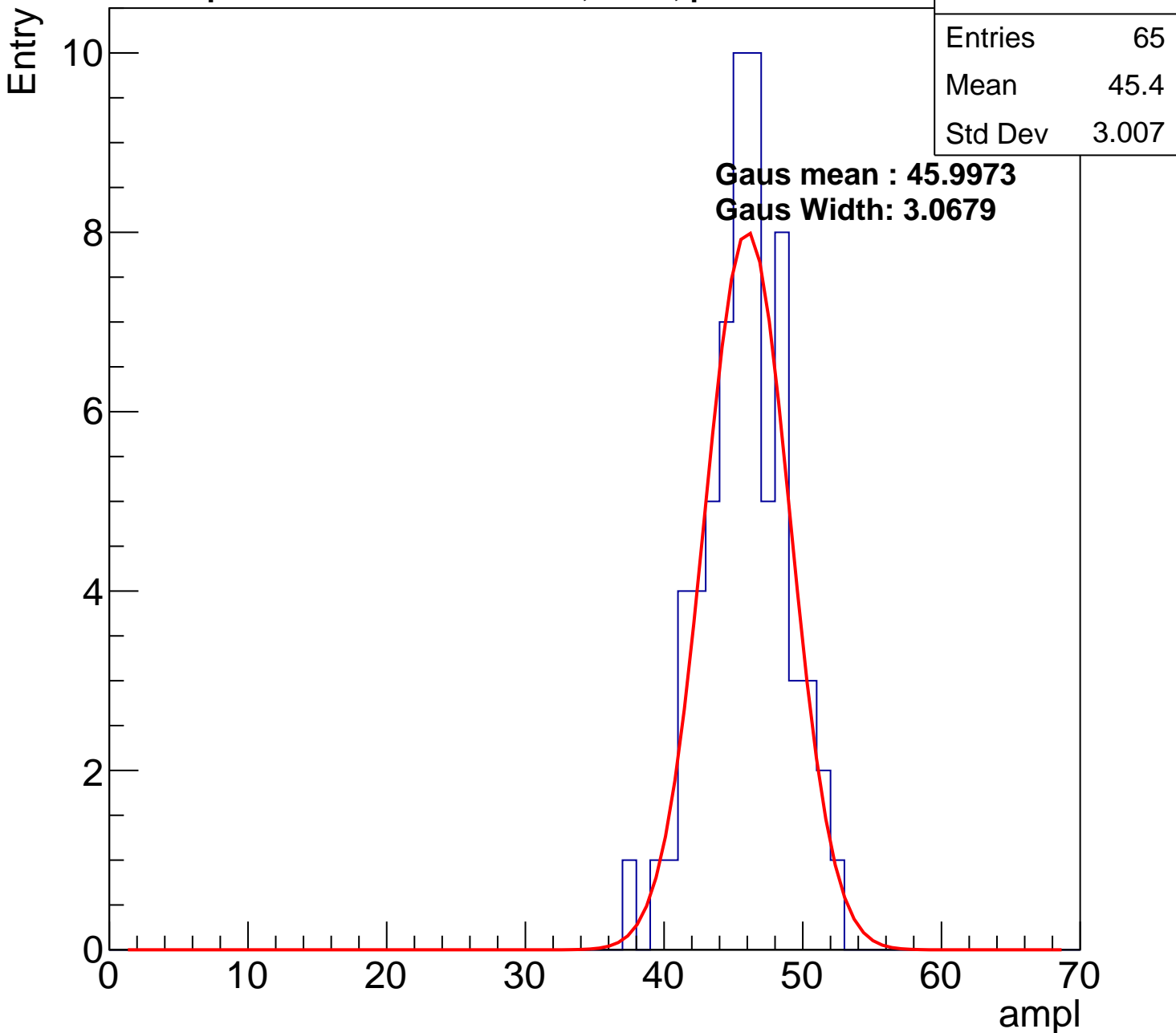
**Gaus Width: 3.0679**

Entry

10  
8  
6  
4  
2  
0

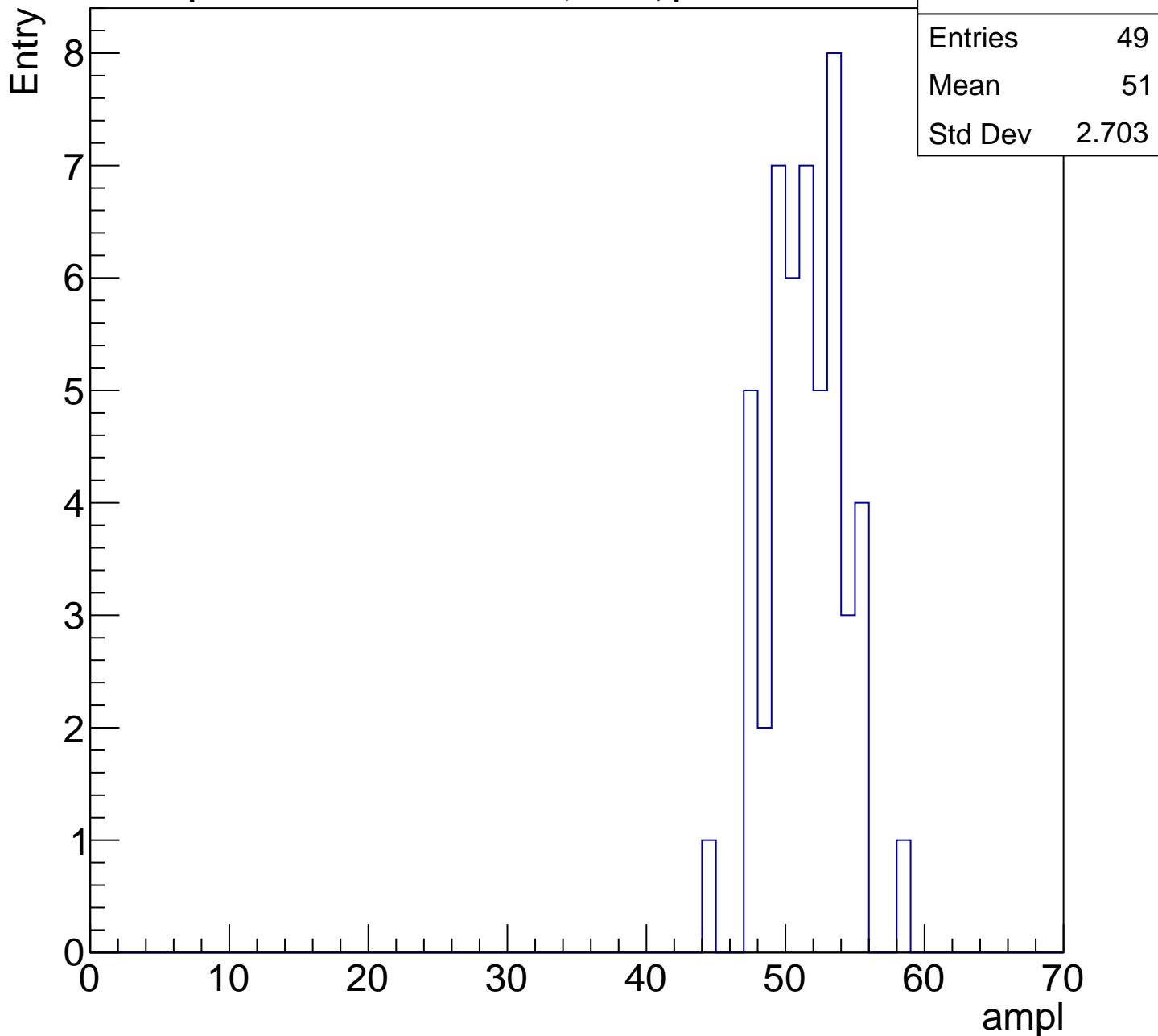
ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

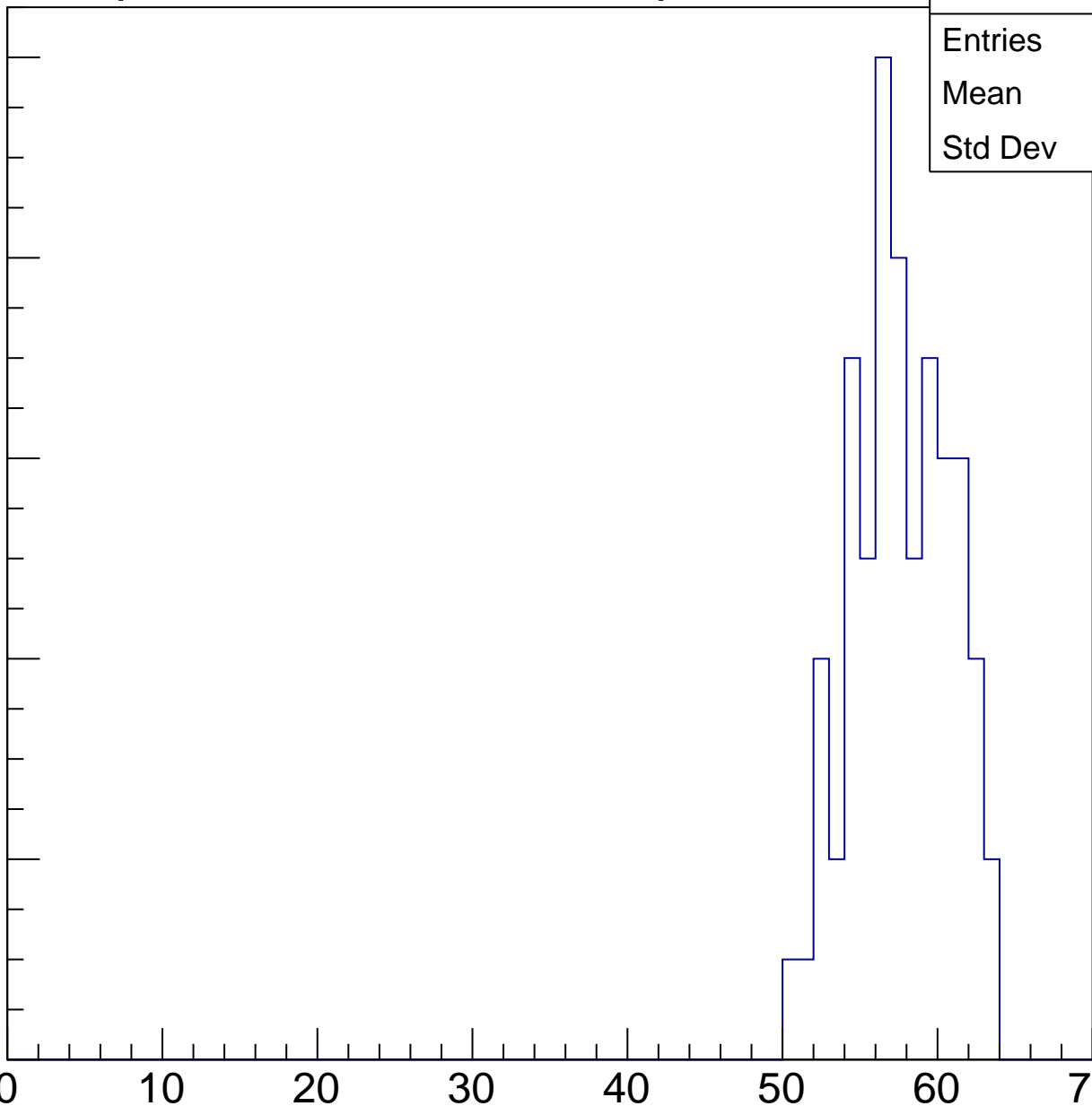
Entries	68
Mean	57.16
Std Dev	3.123

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

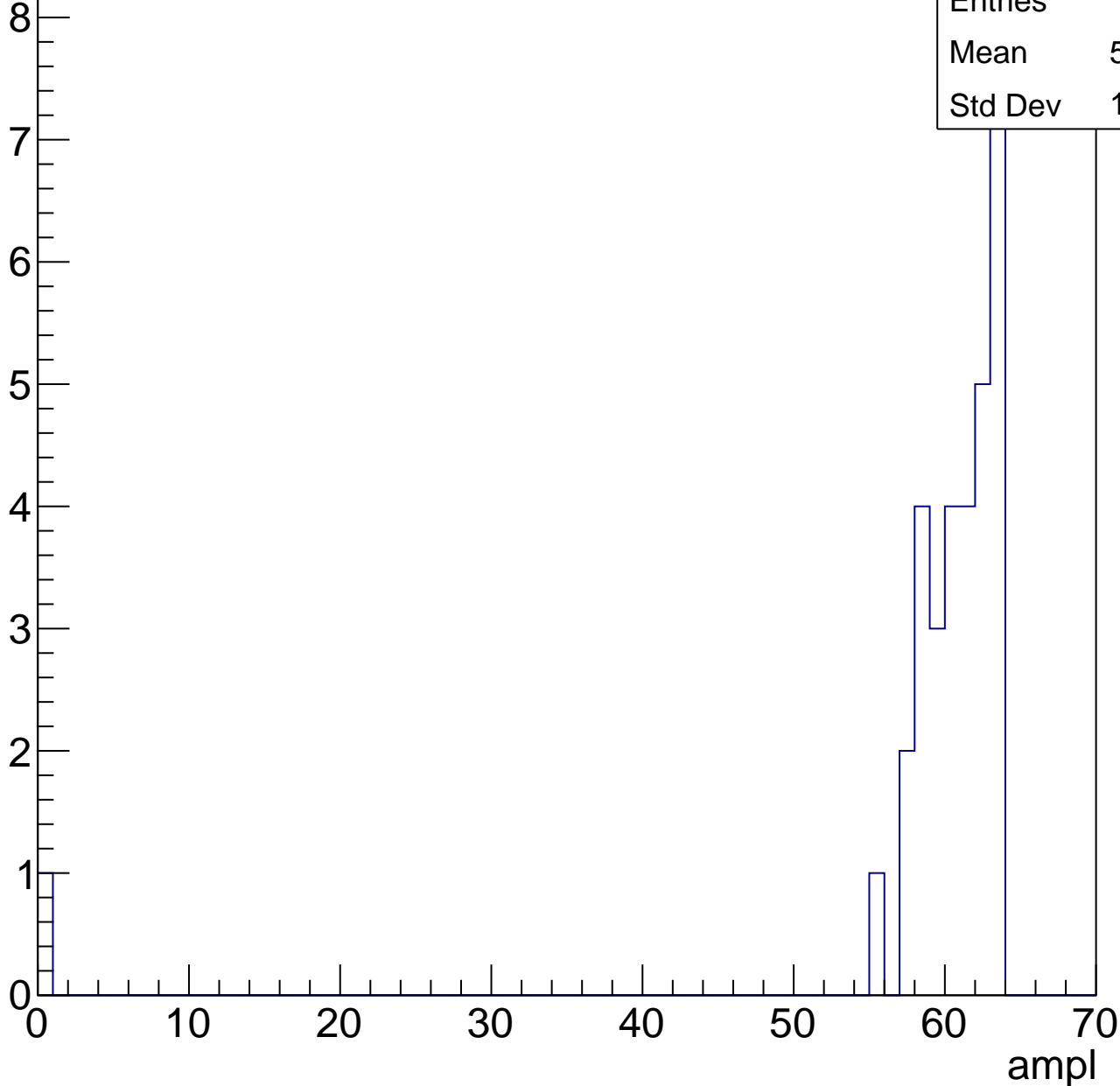


# B0L002S, U8-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.62
Std Dev	10.75



# B0L002S, U8-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	29.65
Std Dev	3.395

**Gaus mean : 30.4183**

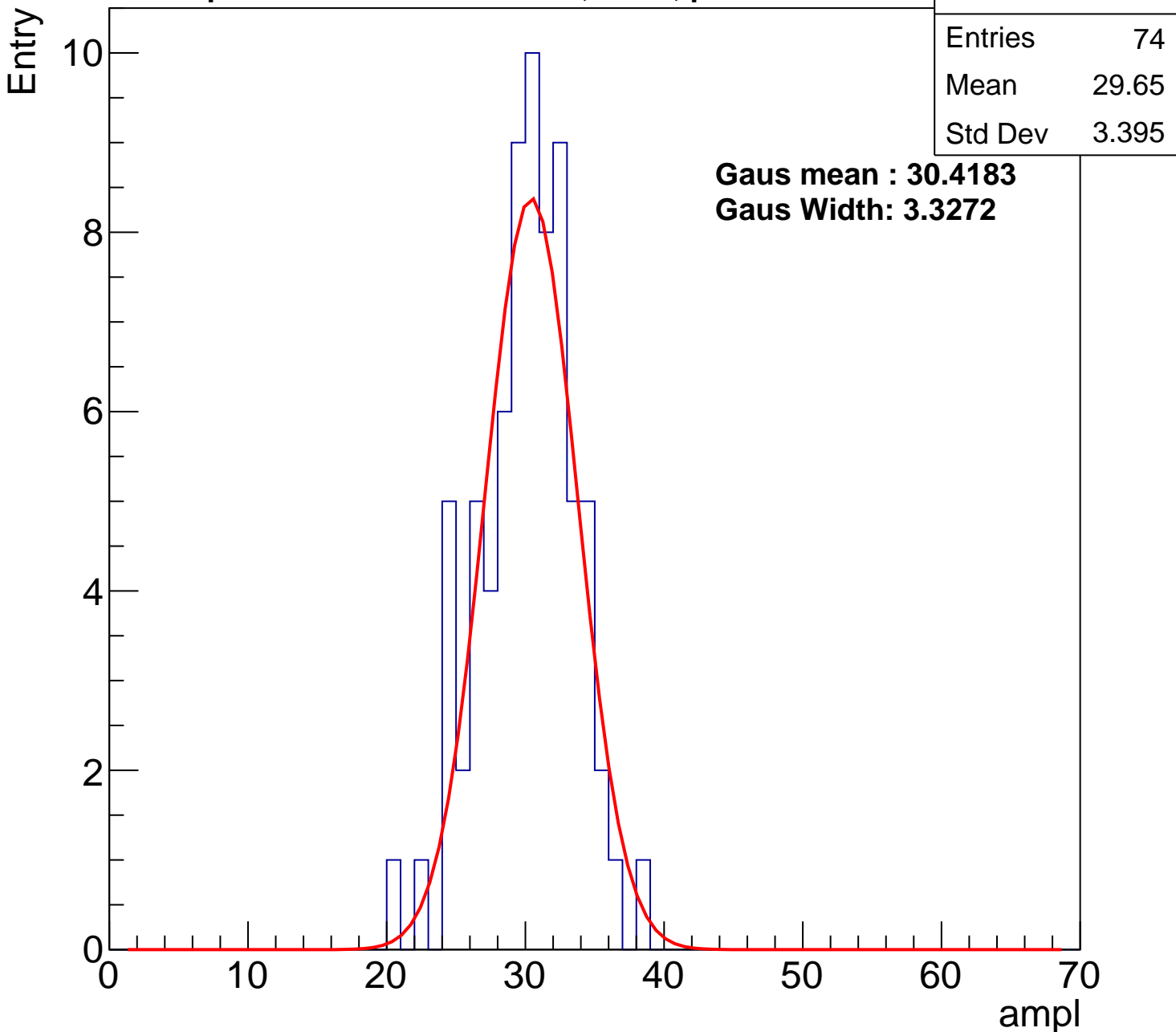
**Gaus Width: 3.3272**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch60, adc1

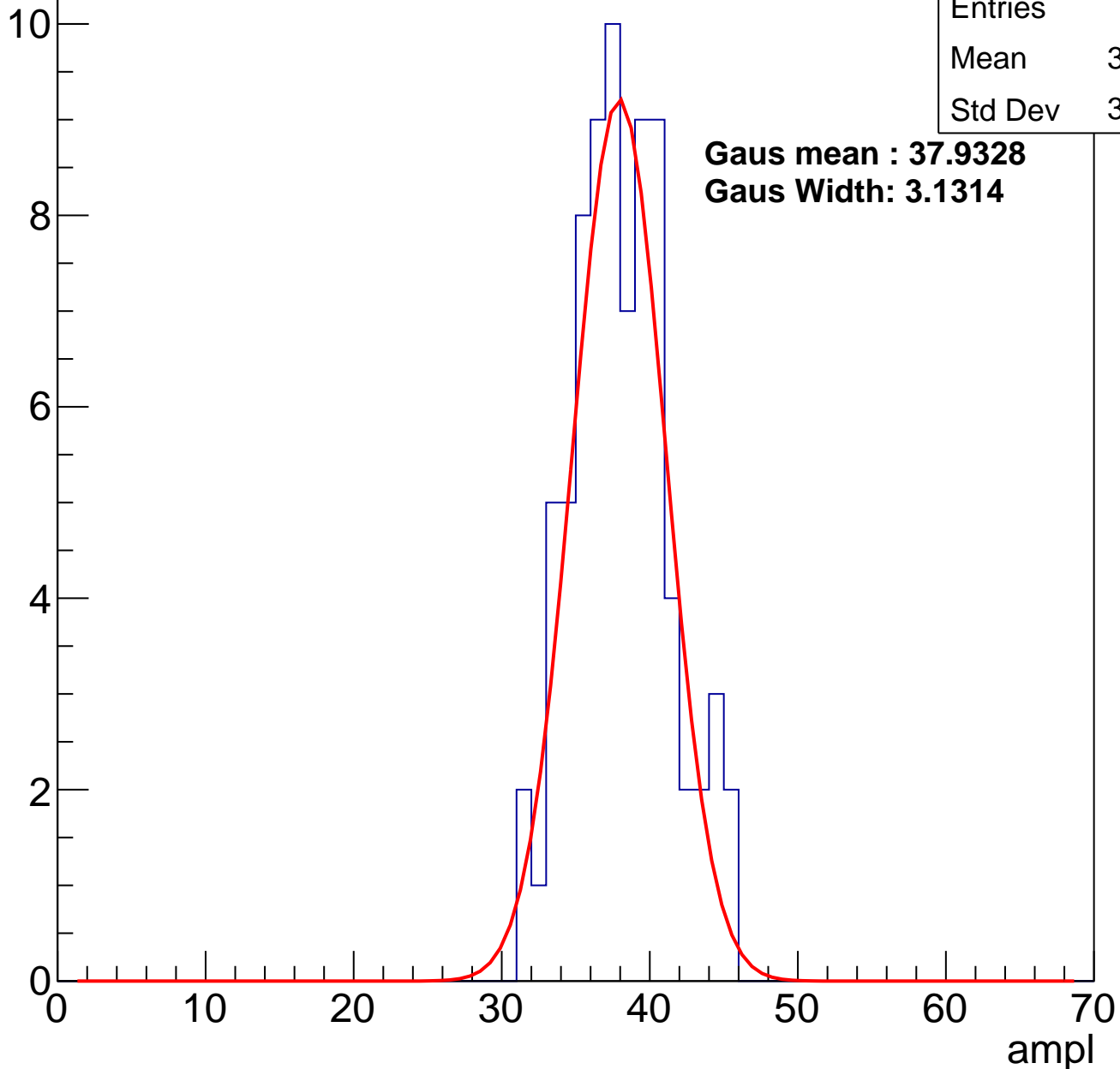
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	37.64
Std Dev	3.234

**Gaus mean : 37.9328**

**Gaus Width: 3.1314**

Entry



# B0L002S, U8-ch60, adc2

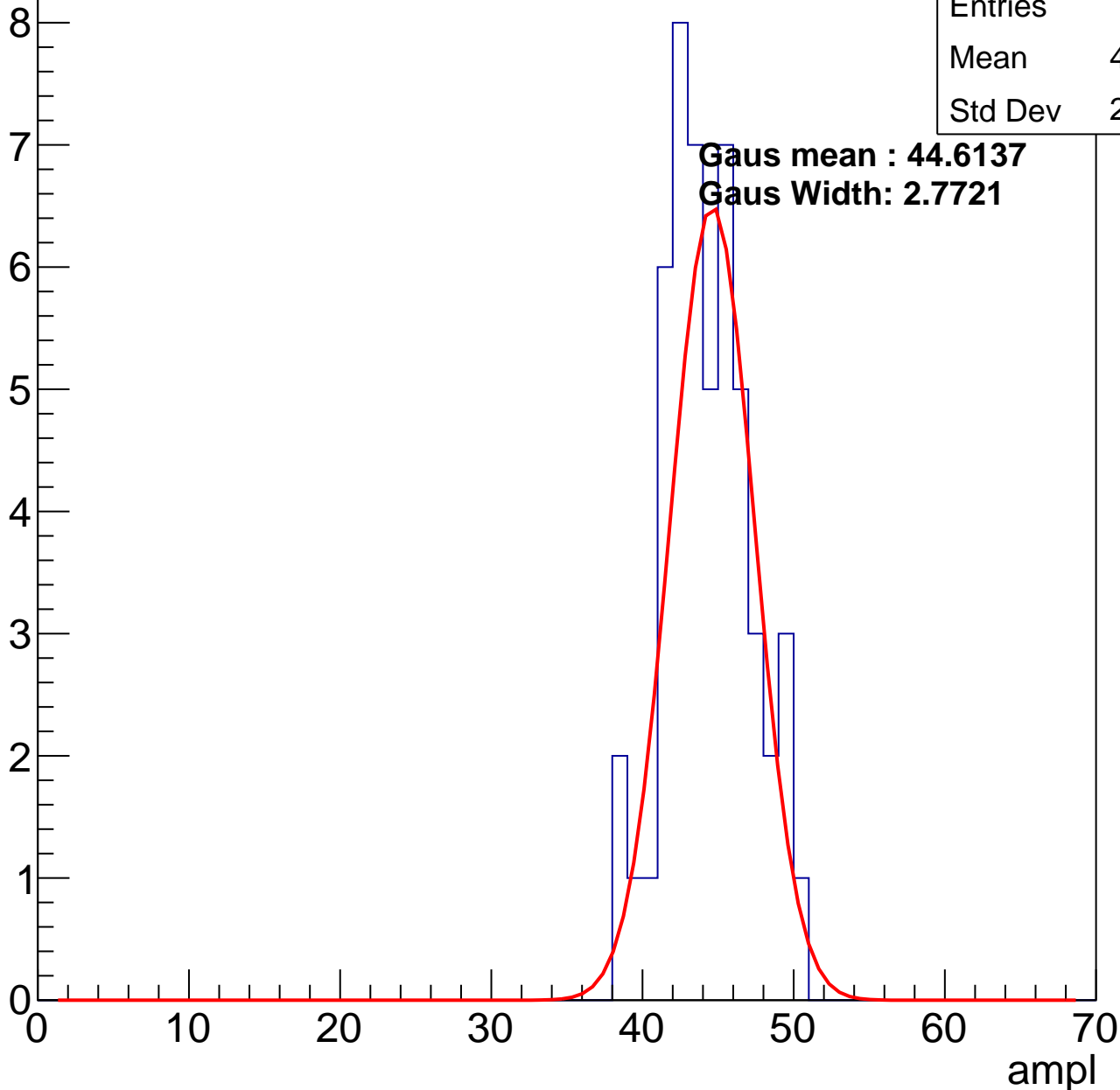
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.86
Std Dev	2.808

**Gaus mean : 44.6137**

**Gaus Width: 2.7721**

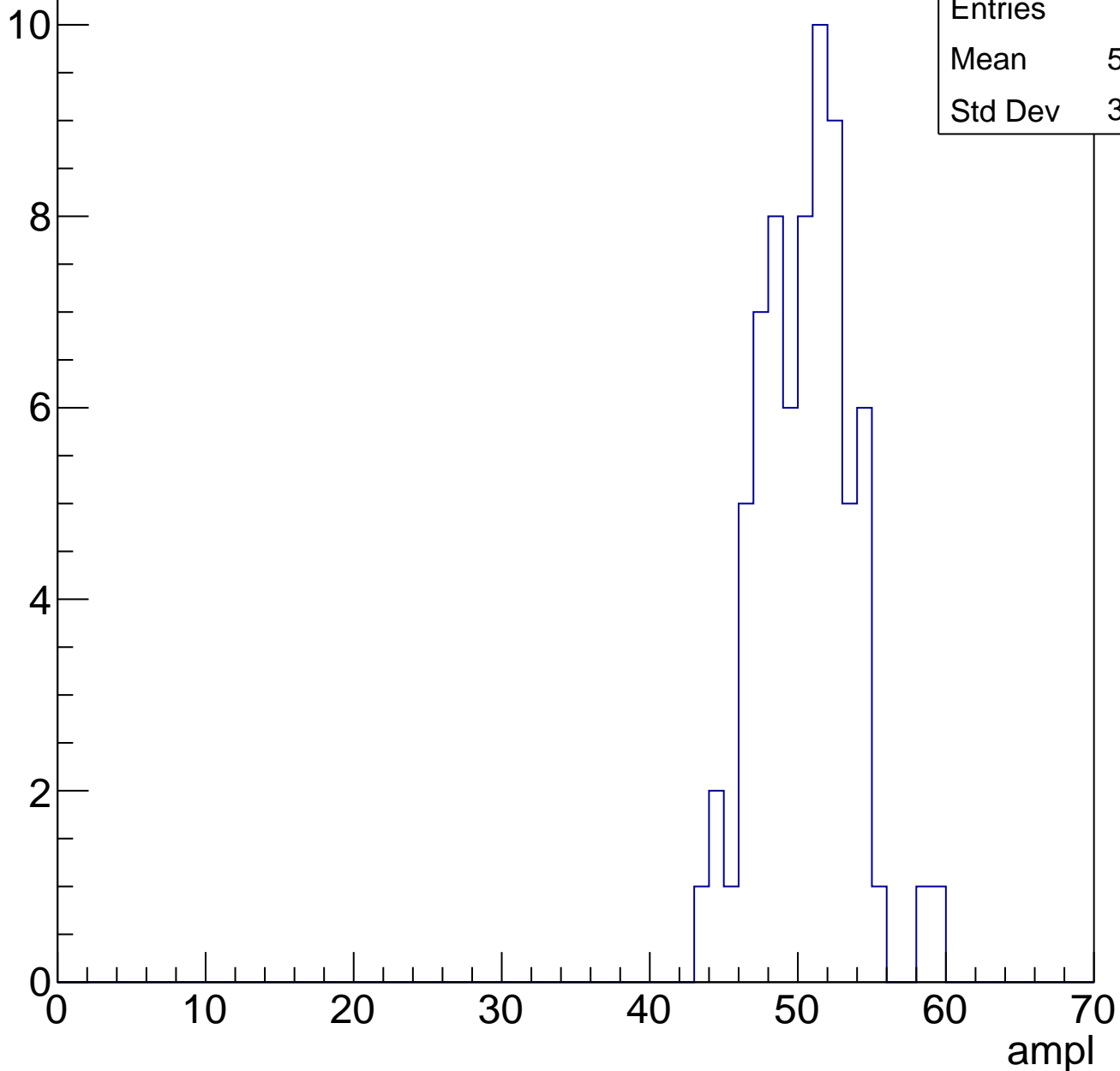


# B0L002S, U8-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	50.03
Std Dev	3.104

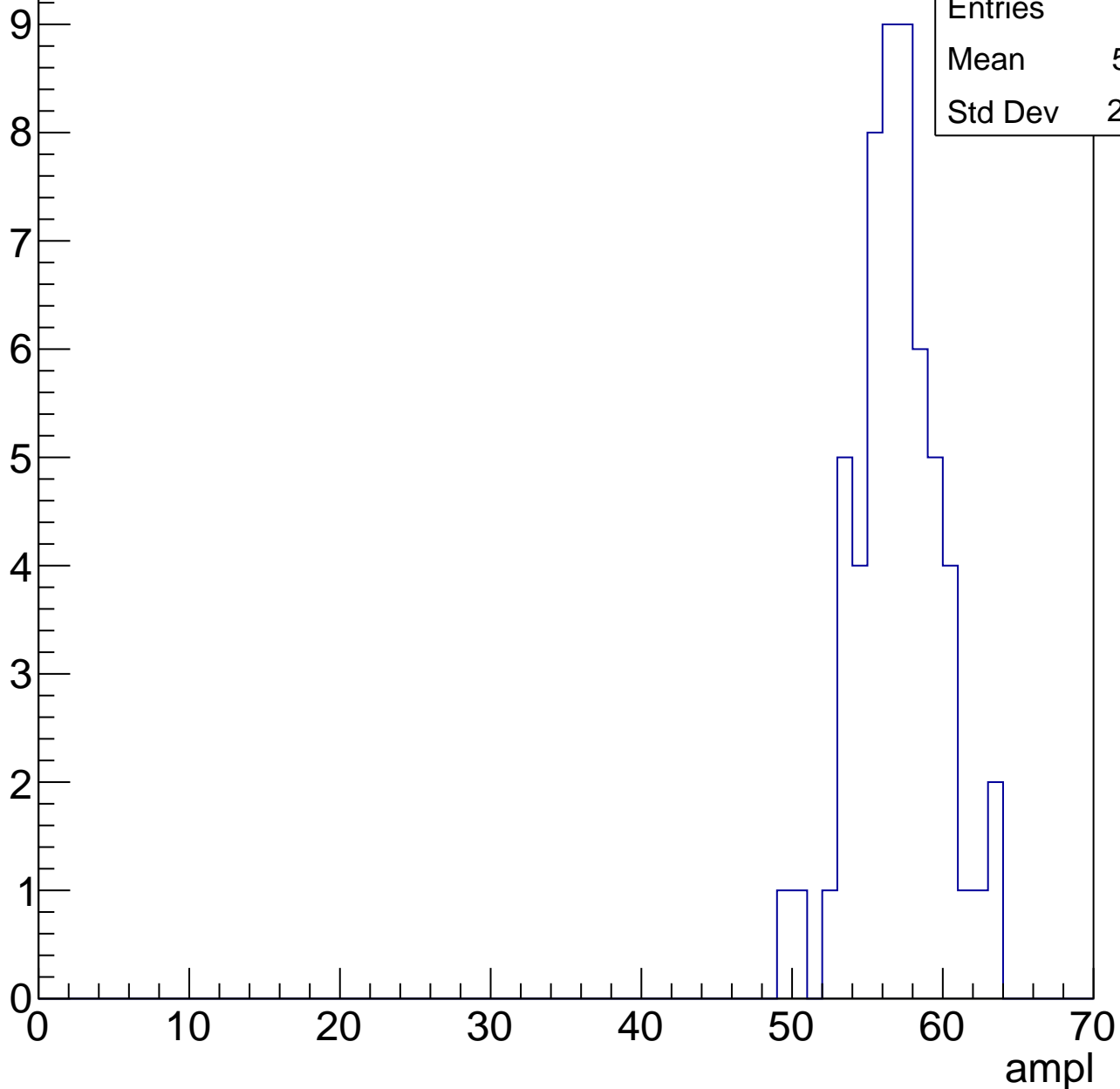
Entry



# B0L002S, U8-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

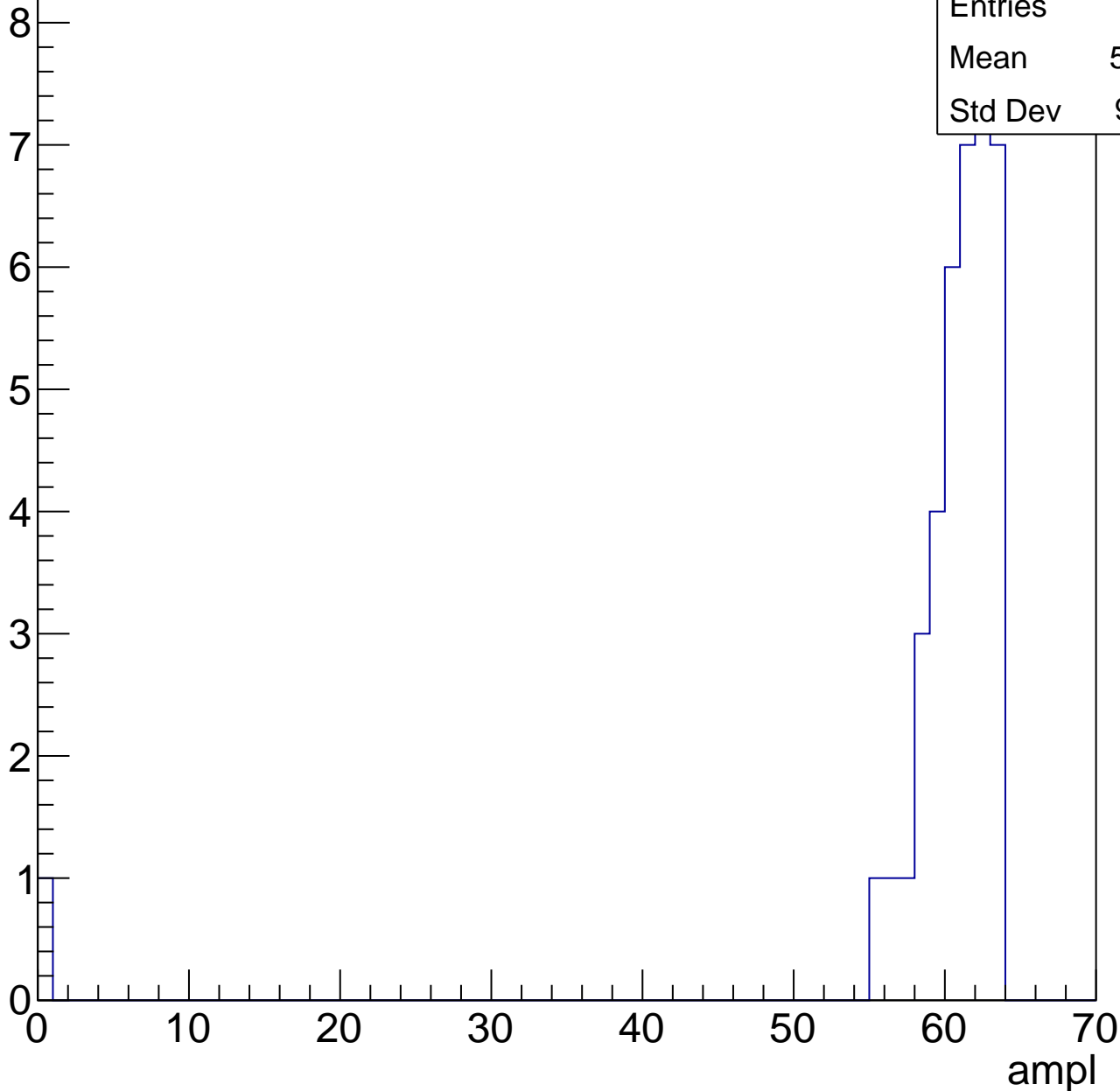


# B0L002S, U8-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.03
Std Dev	9.781



# B0L002S, U8-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch61, adc0

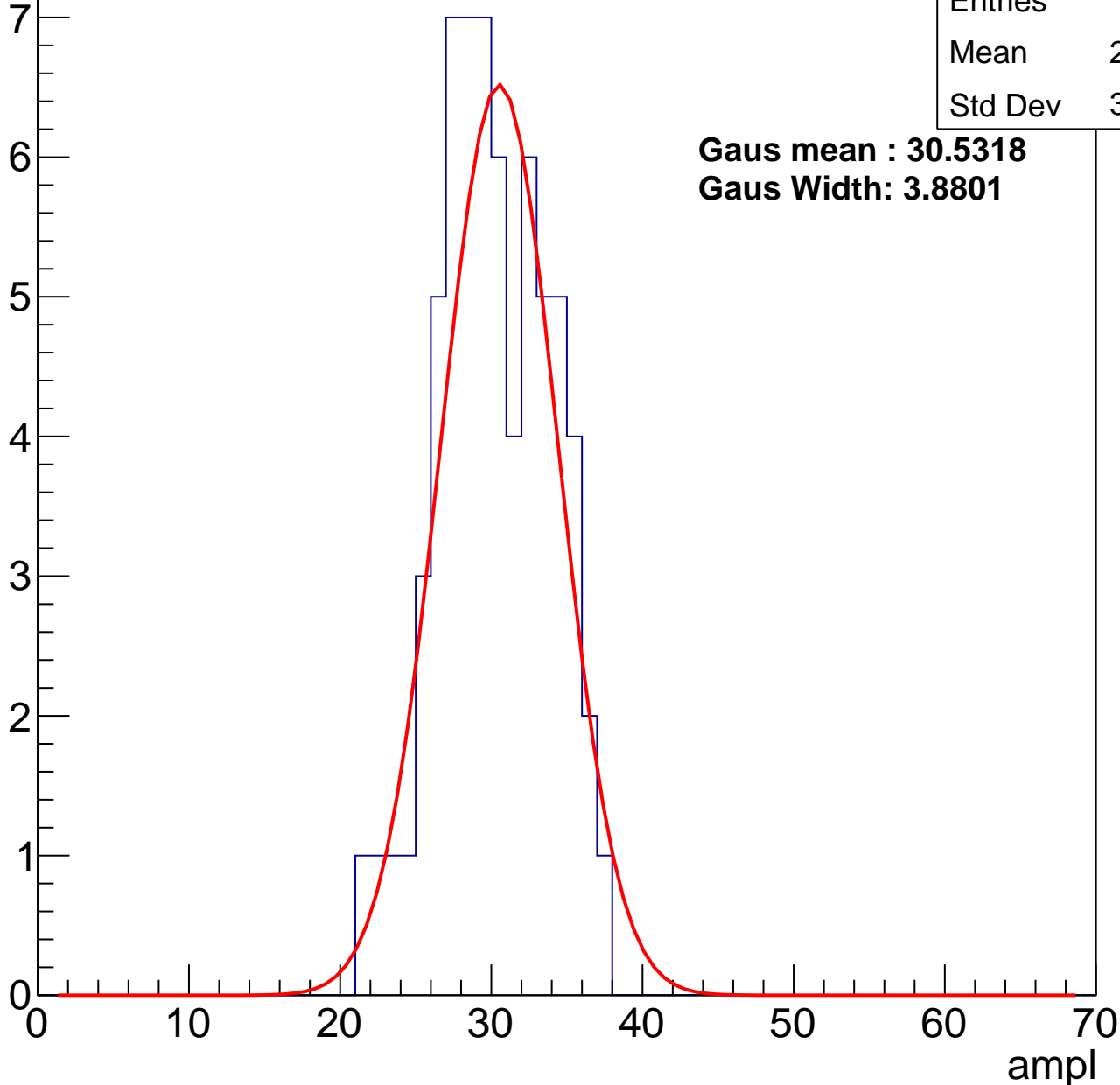
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.74
Std Dev	3.594

**Gaus mean : 30.5318**

**Gaus Width: 3.8801**



# B0L002S, U8-ch61, adc1

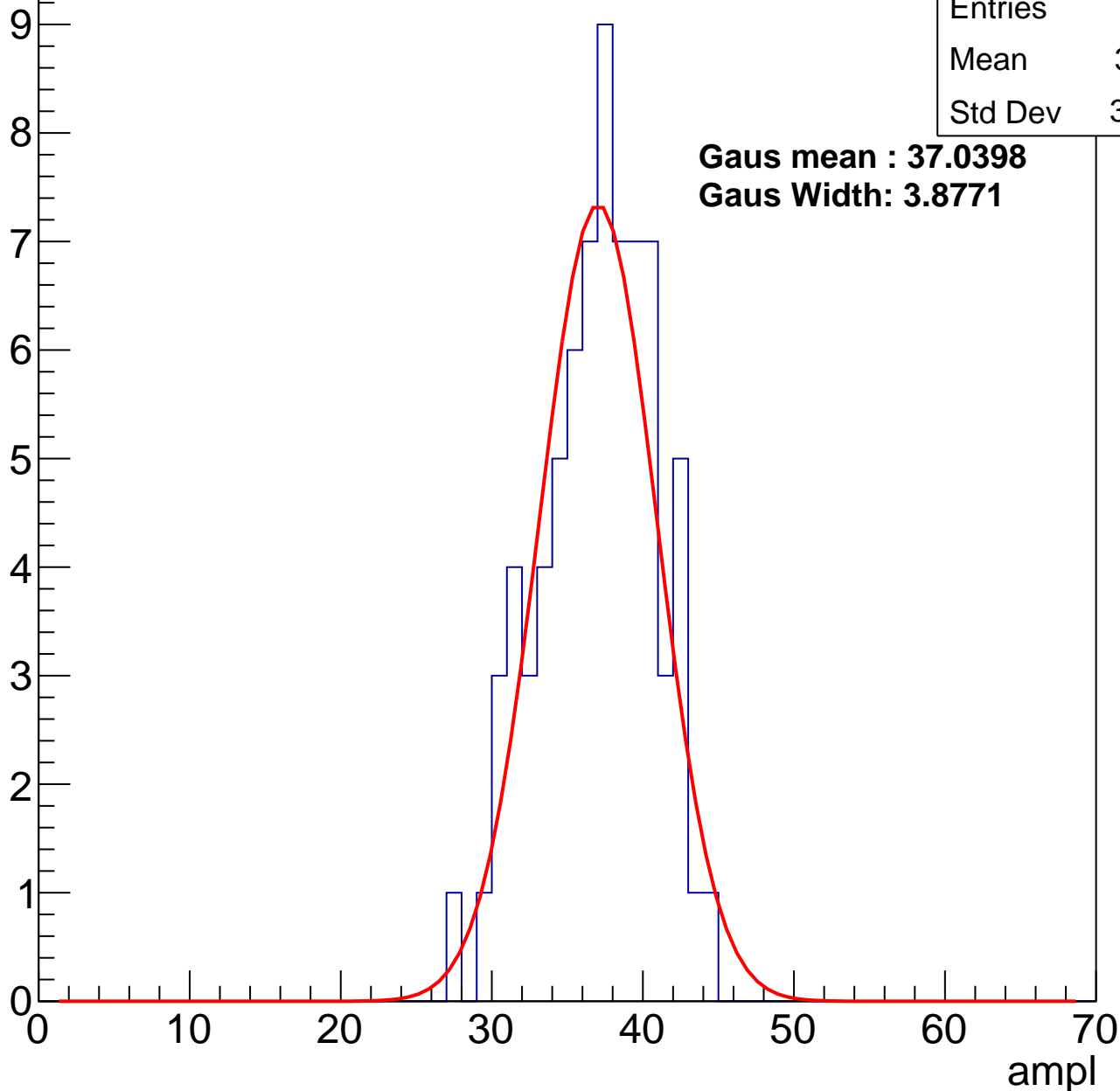
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.51
Std Dev	3.692

**Gaus mean : 37.0398**

**Gaus Width: 3.8771**



# B0L002S, U8-ch61, adc2

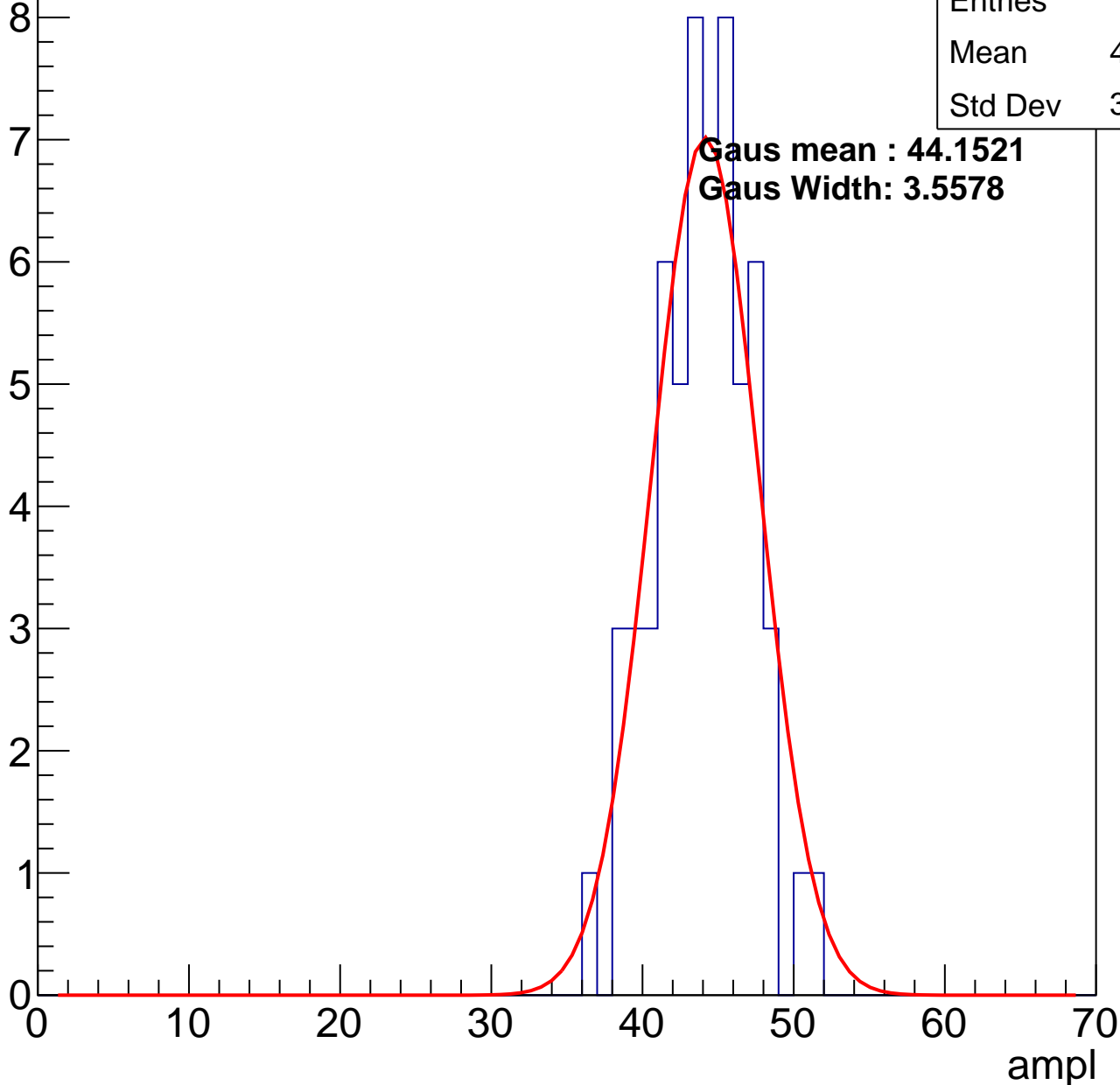
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.53
Std Dev	3.106

**Gaus mean : 44.1521**

**Gaus Width: 3.5578**

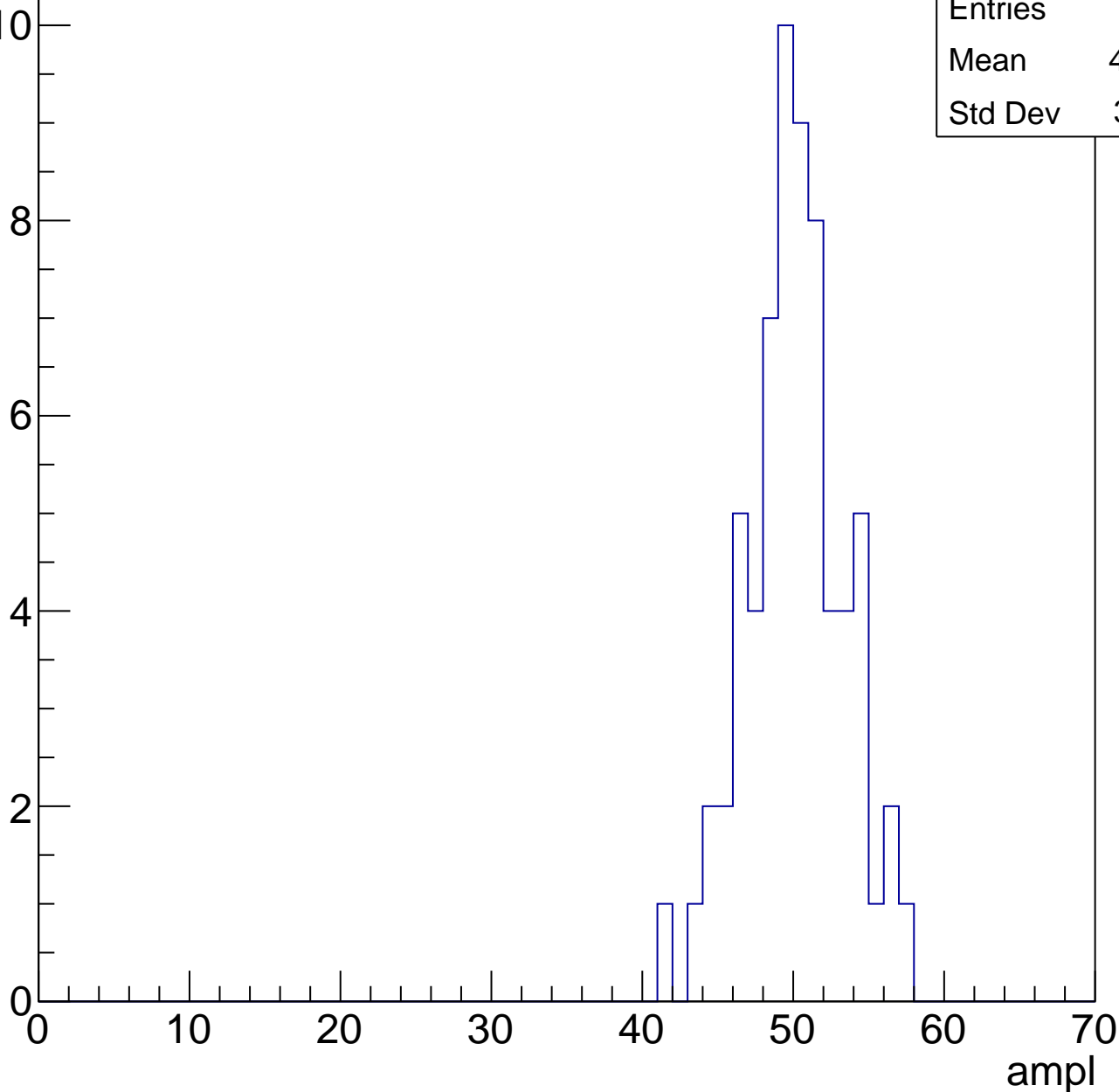


# B0L002S, U8-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	49.67
Std Dev	3.221

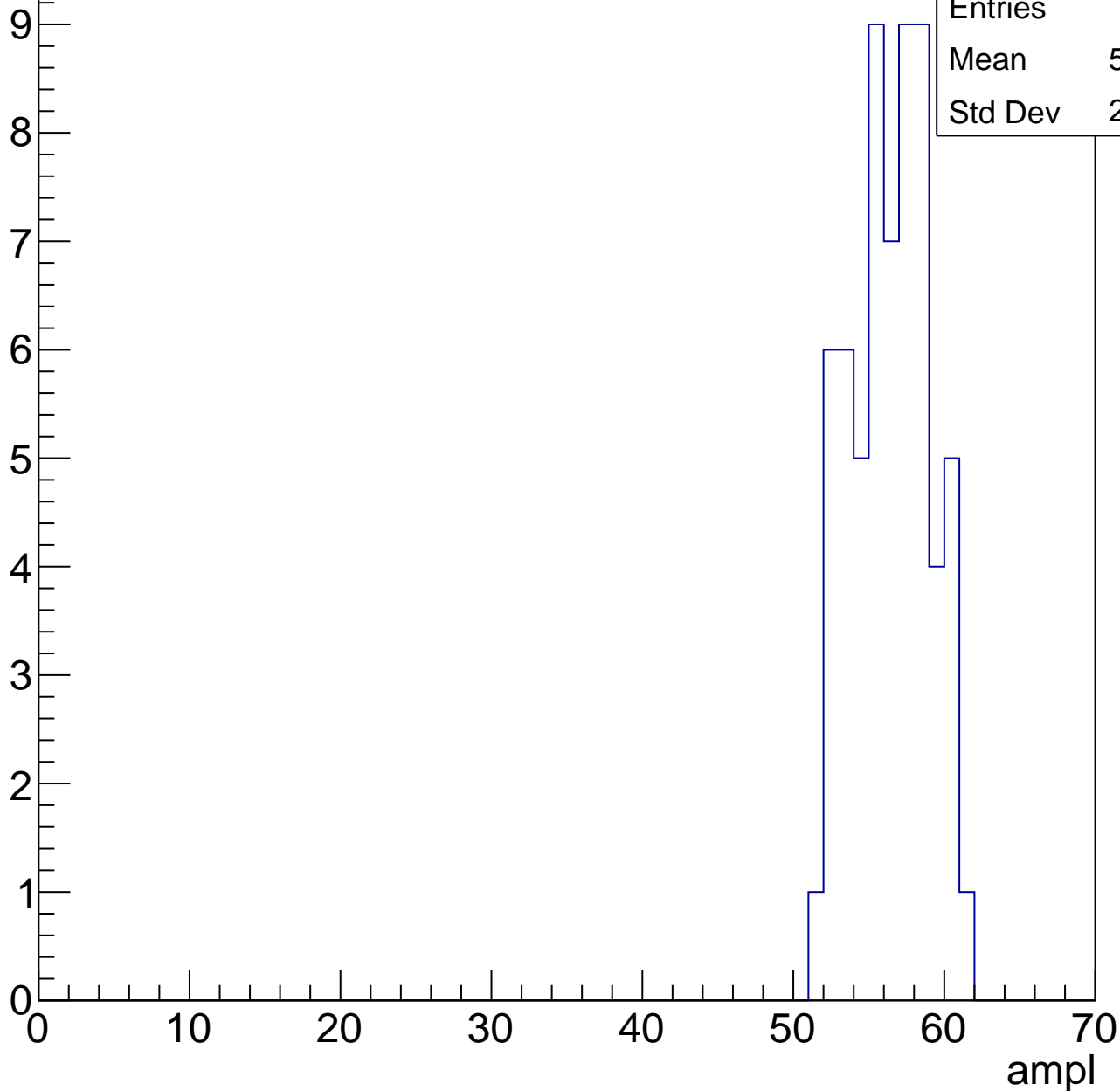


# B0L002S, U8-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.97
Std Dev	2.508

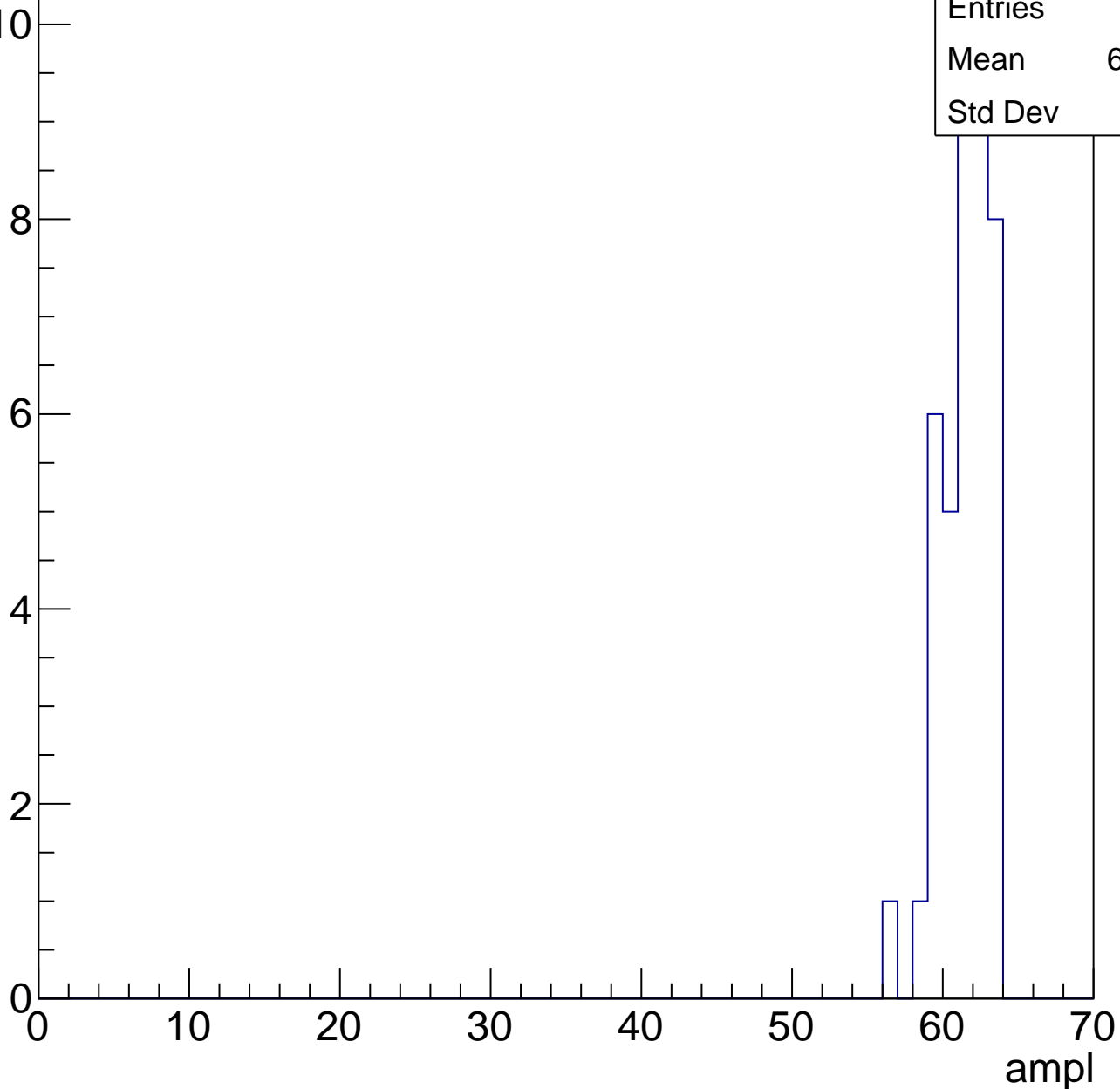


# B0L002S, U8-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	61.02
Std Dev	1.62



# B0L002S, U8-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch62, adc0

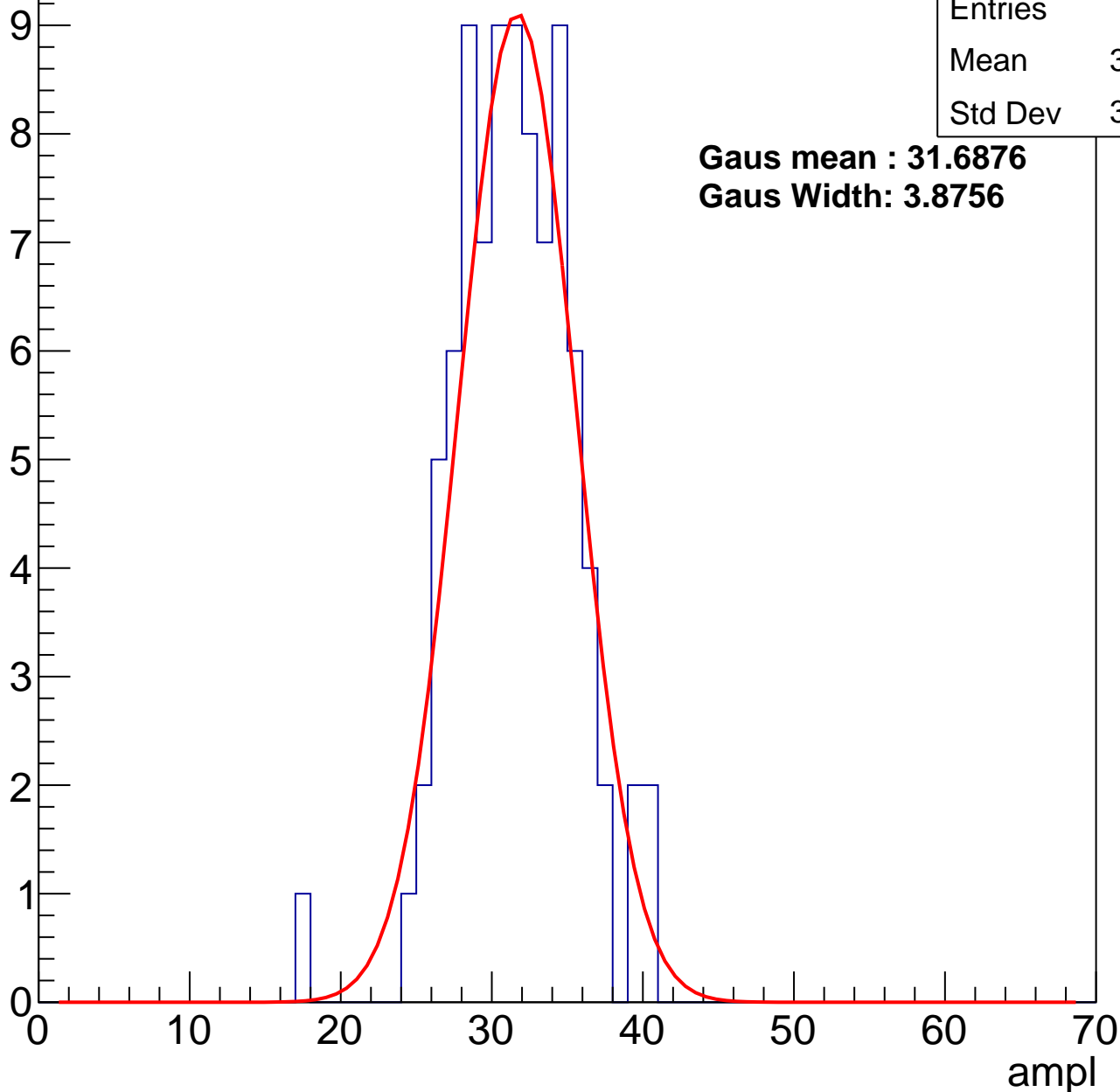
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	89
Mean	31.08
Std Dev	3.866

**Gaus mean : 31.6876**

**Gaus Width: 3.8756**



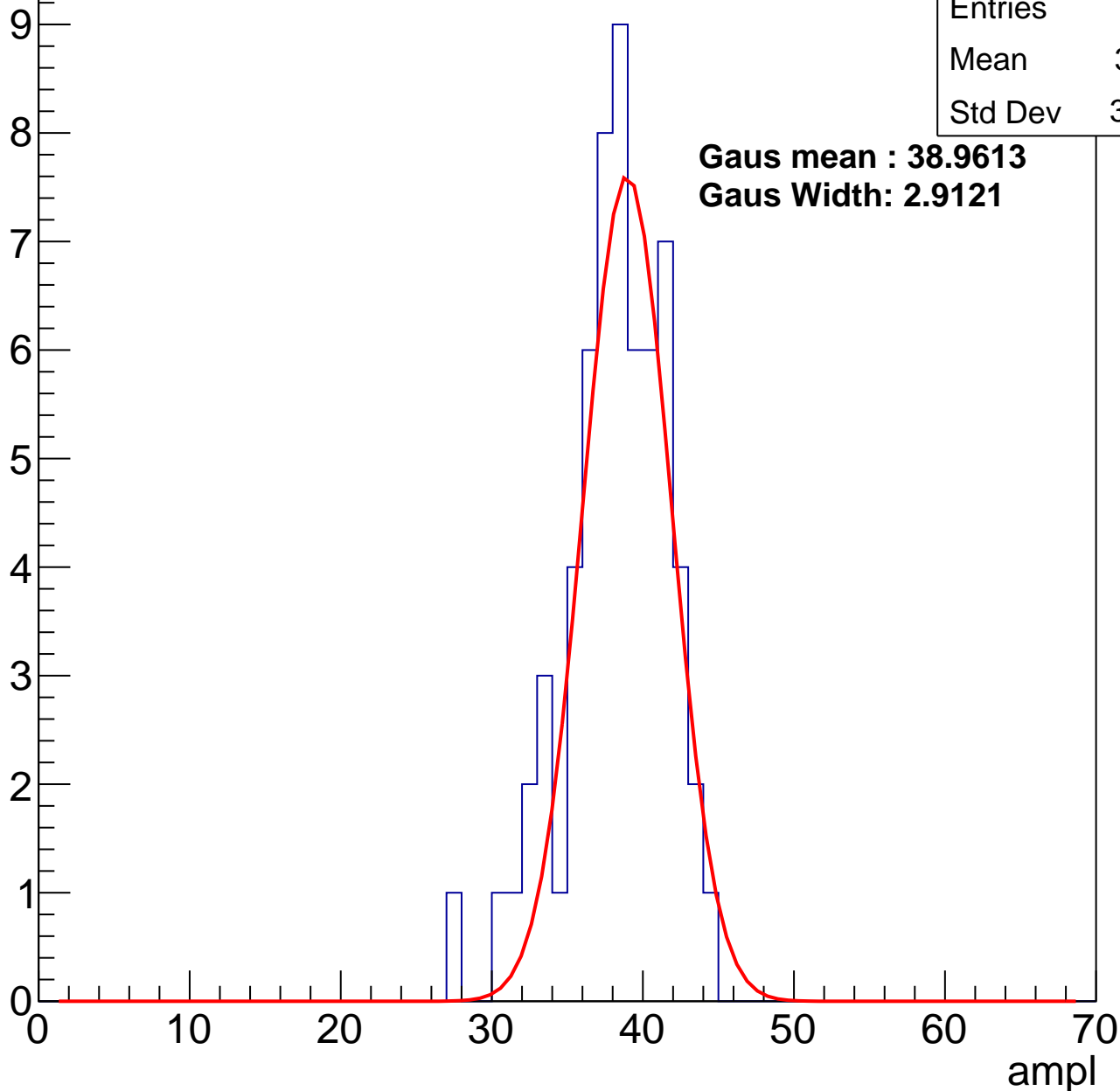
# B0L002S, U8-ch62, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.71
Std Dev	3.367

**Gaus mean : 38.9613**  
**Gaus Width: 2.9121**



# B0L002S, U8-ch62, adc2

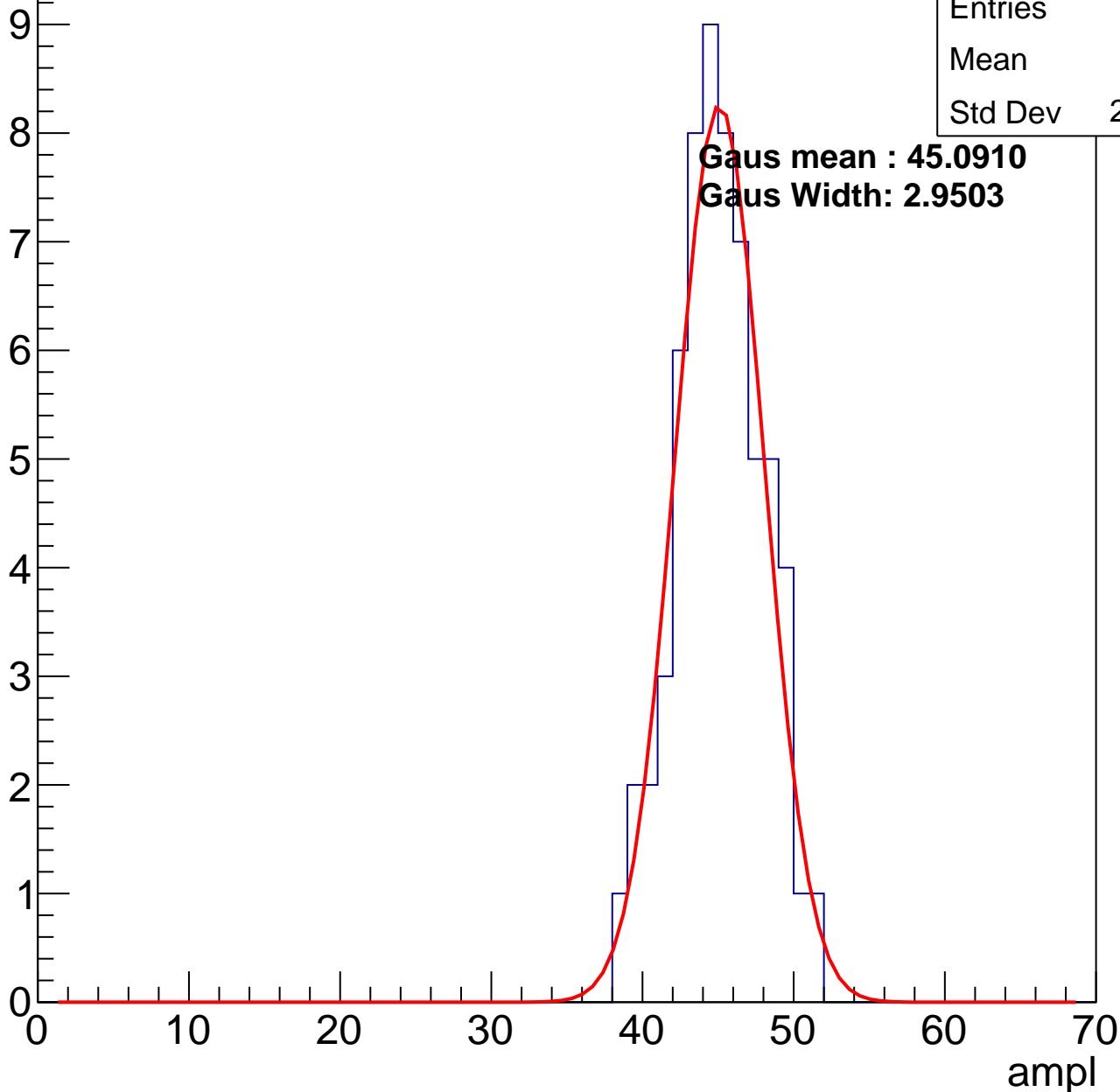
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.6
Std Dev	2.842

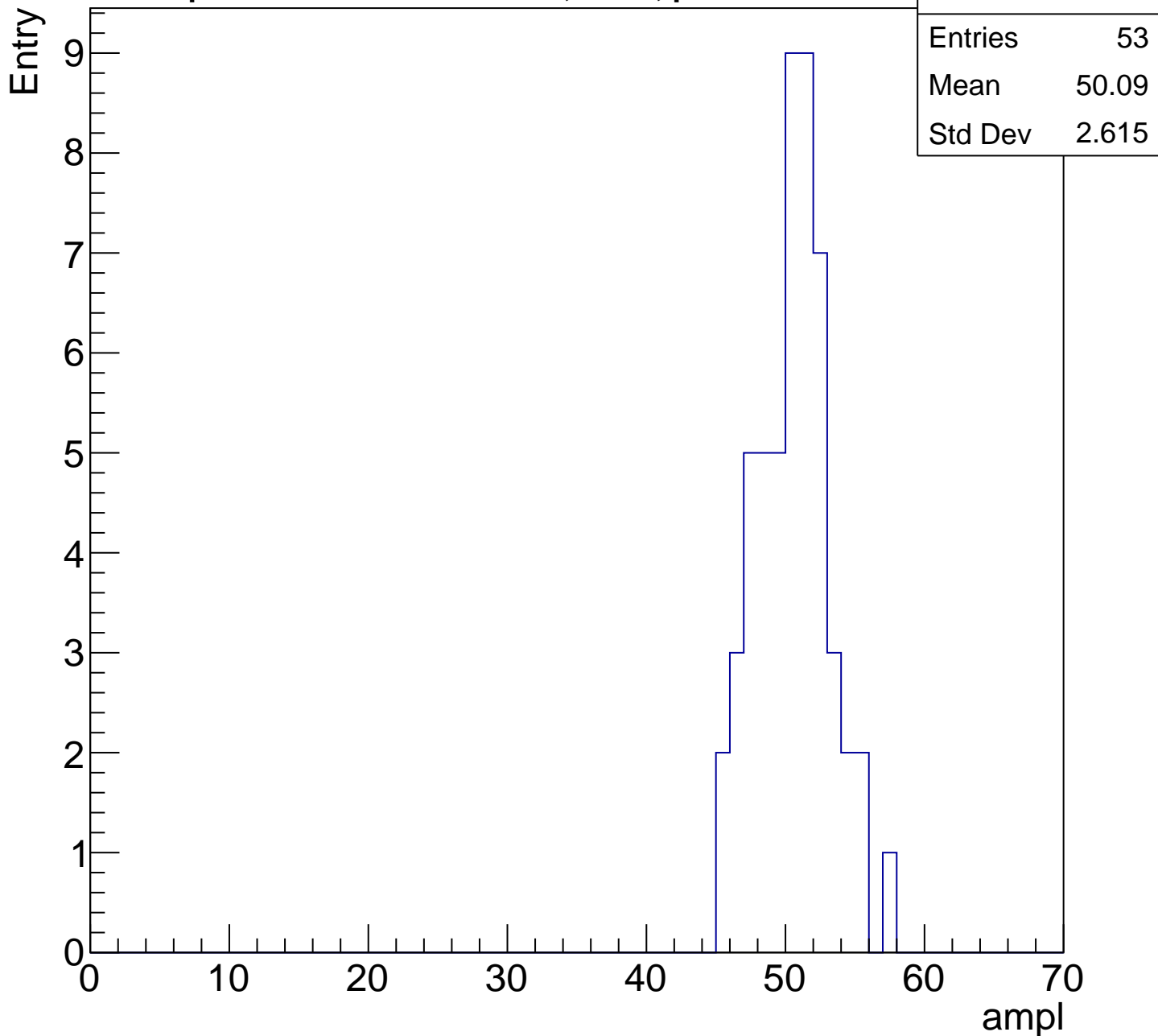
**Gaus mean : 45.0910**

**Gaus Width: 2.9503**



# B0L002S, U8-ch62, adc3

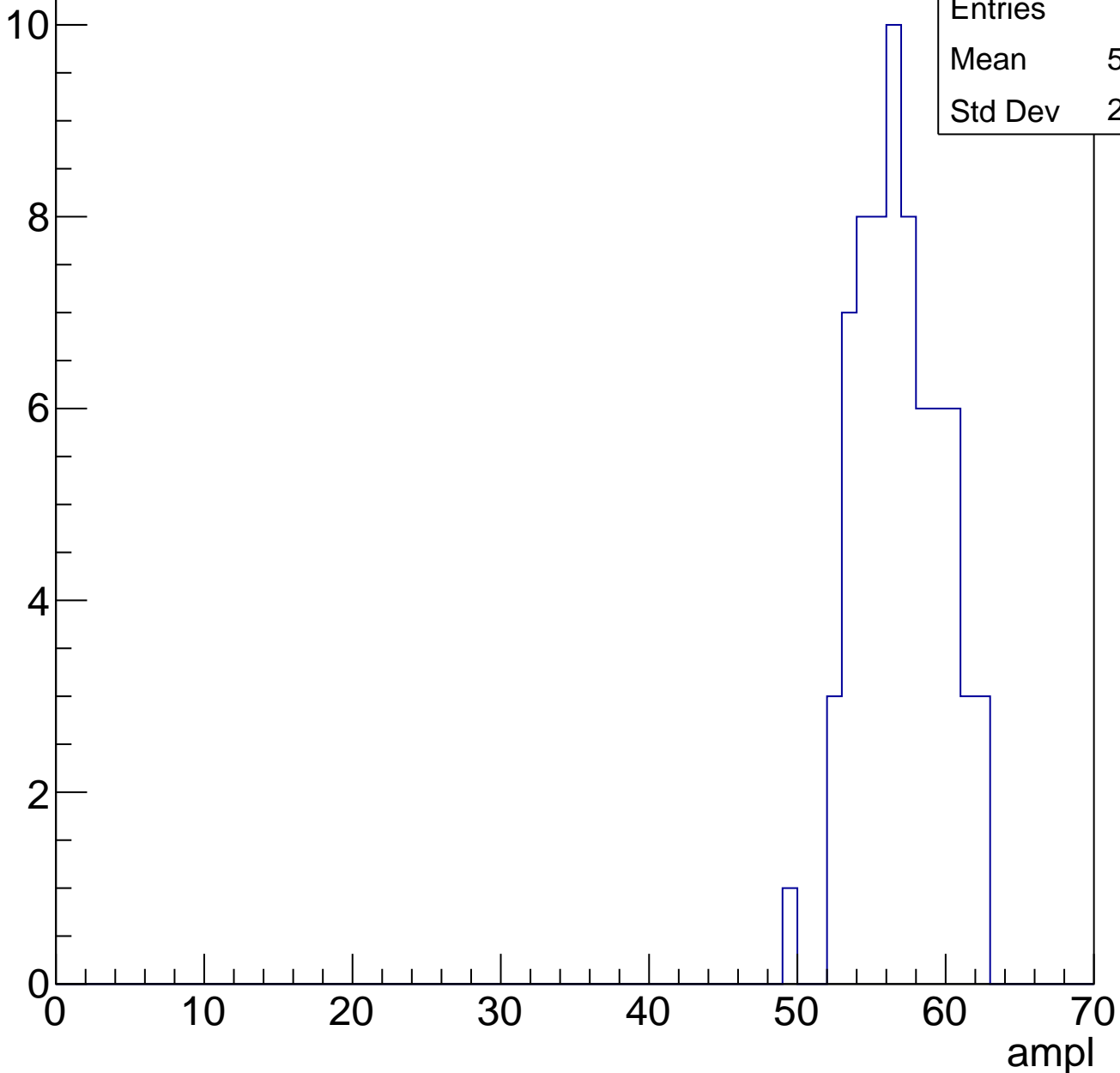
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

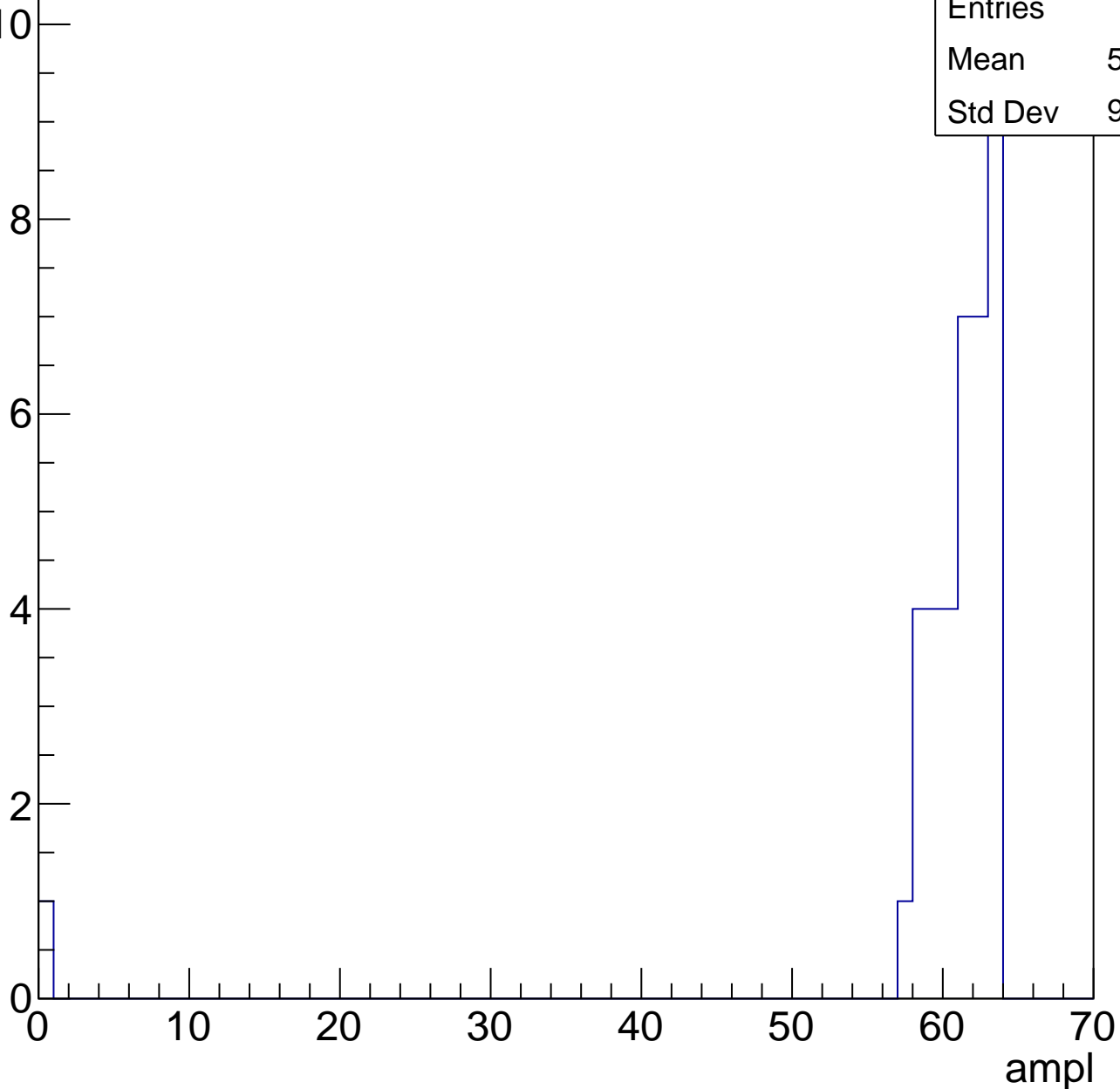


# B0L002S, U8-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.37
Std Dev	9.919



# B0L002S, U8-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch63, adc0

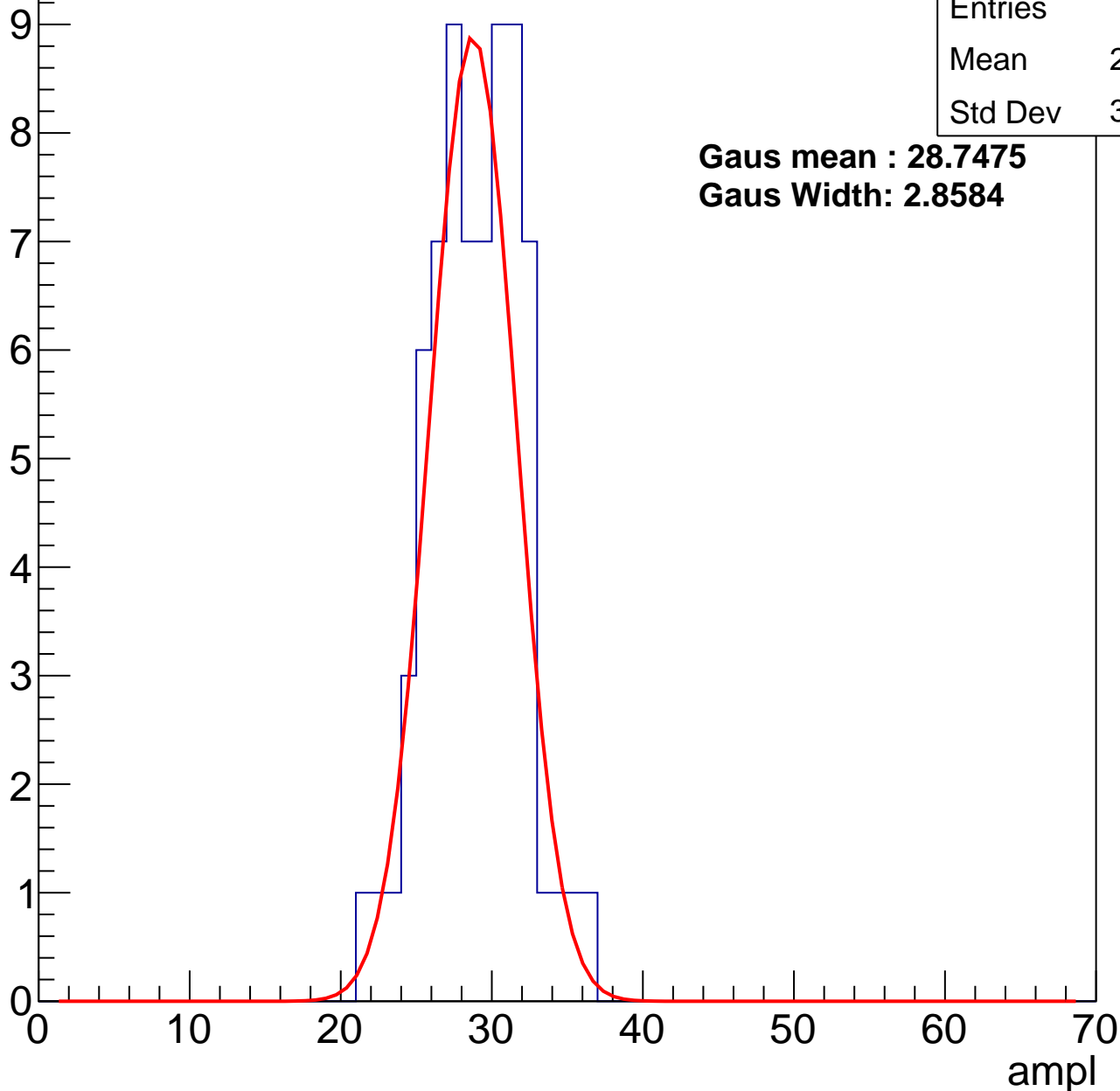
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	28.49
Std Dev	3.006

**Gaus mean : 28.7475**

**Gaus Width: 2.8584**



# B0L002S, U8-ch63, adc1

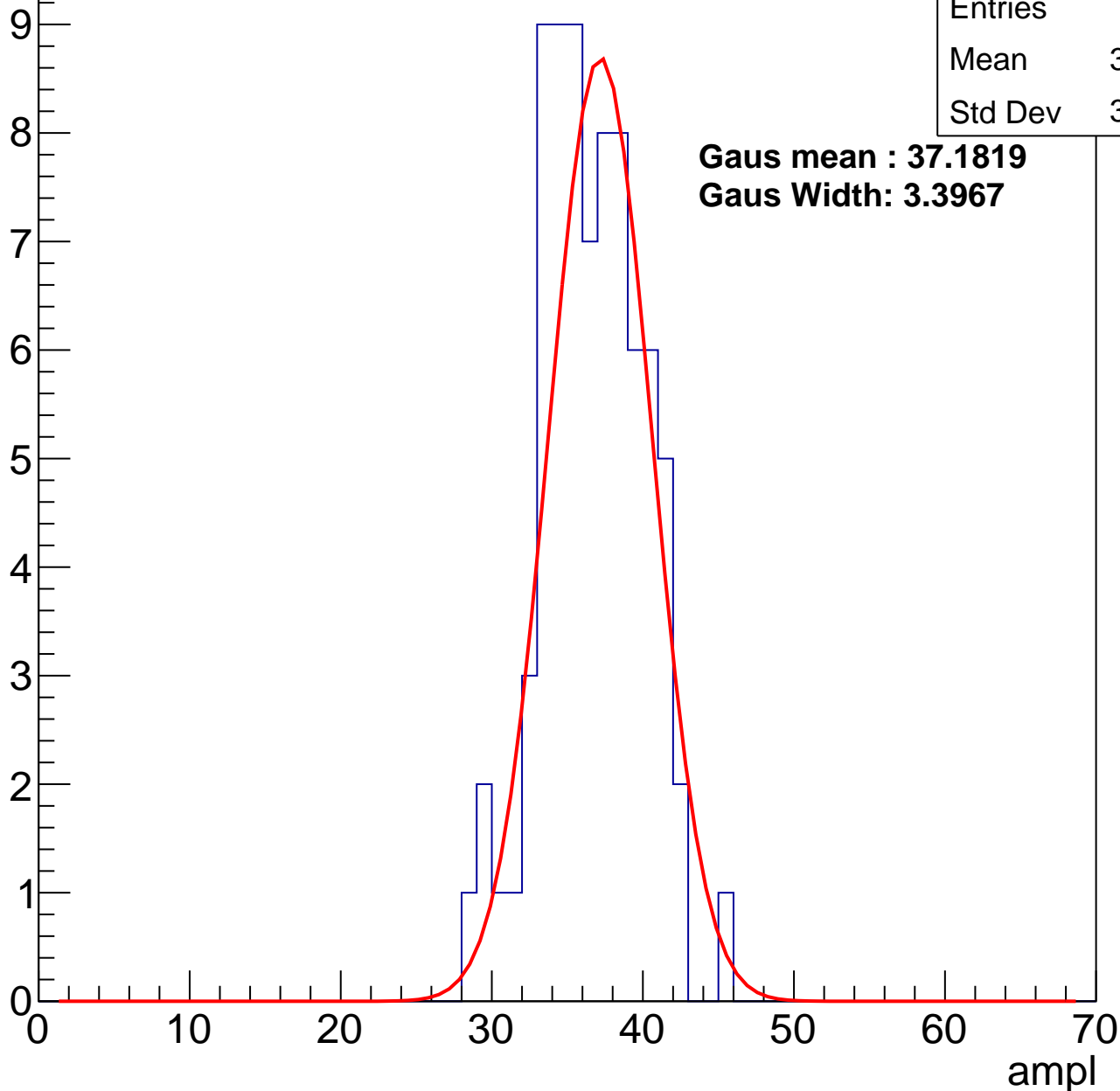
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	36.17
Std Dev	3.326

**Gaus mean : 37.1819**

**Gaus Width: 3.3967**



# B0L002S, U8-ch63, adc2

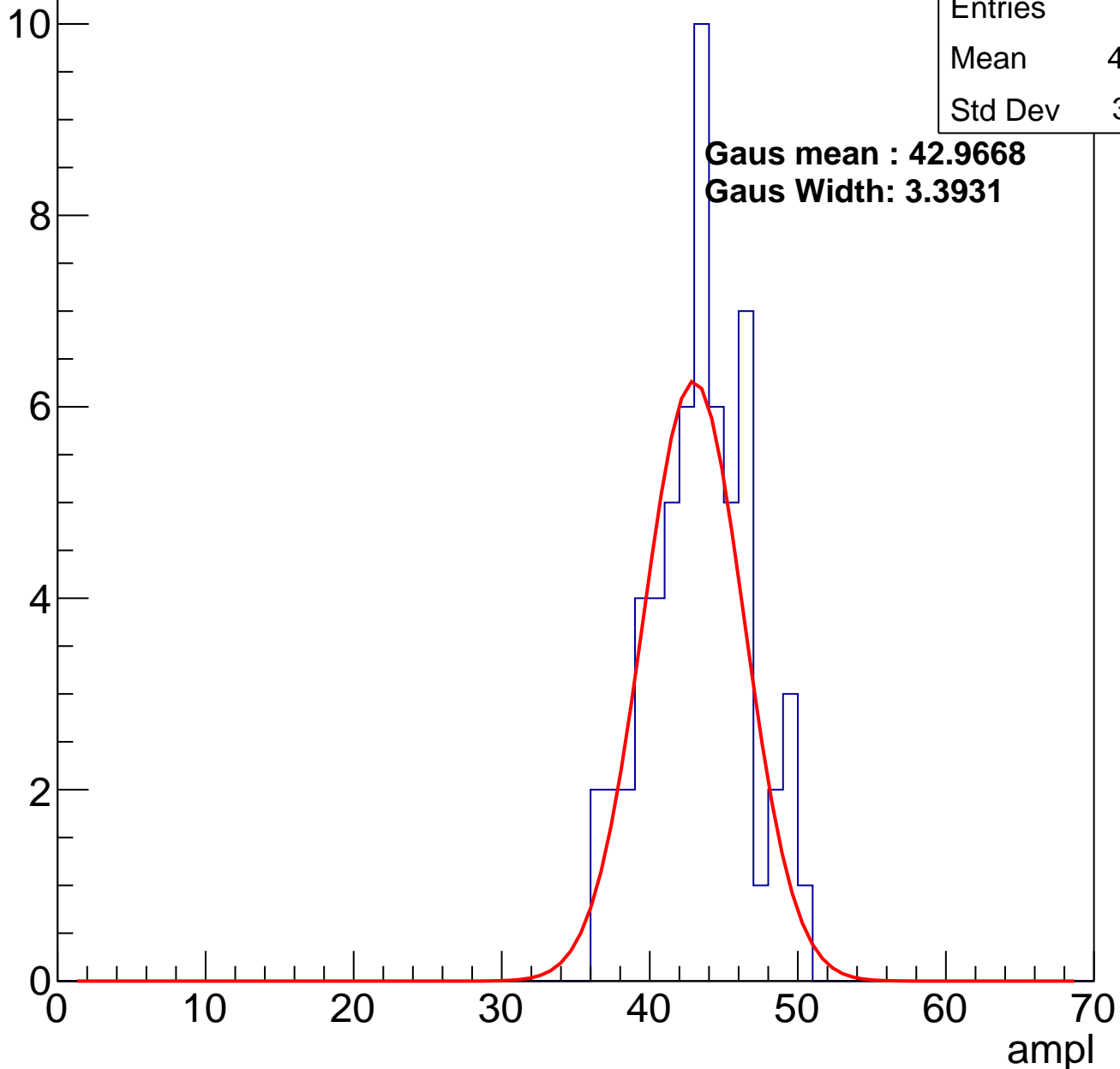
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	60
Mean	42.93
Std Dev	3.311

**Gaus mean : 42.9668**

**Gaus Width: 3.3931**

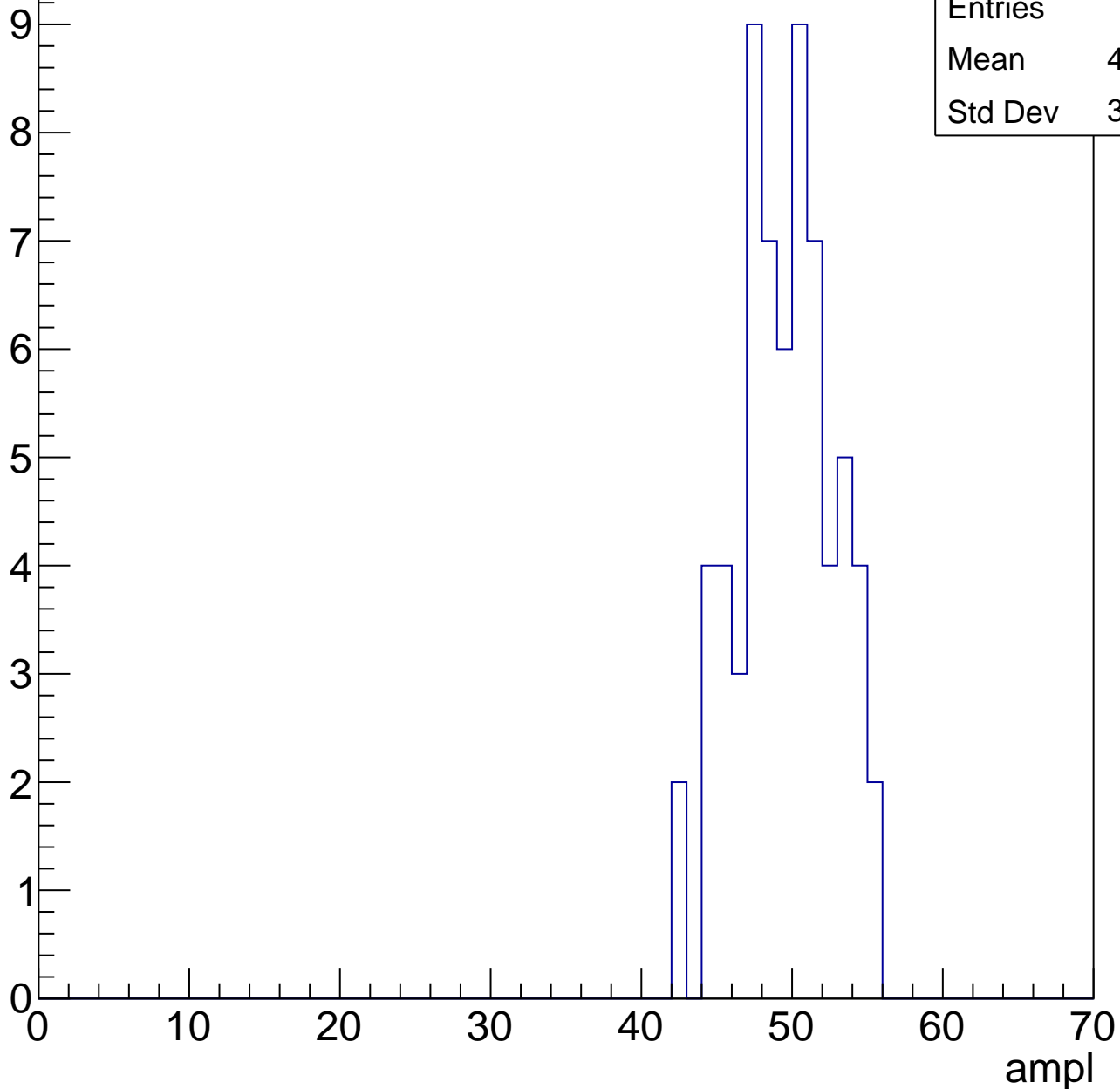
Entry



# B0L002S, U8-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

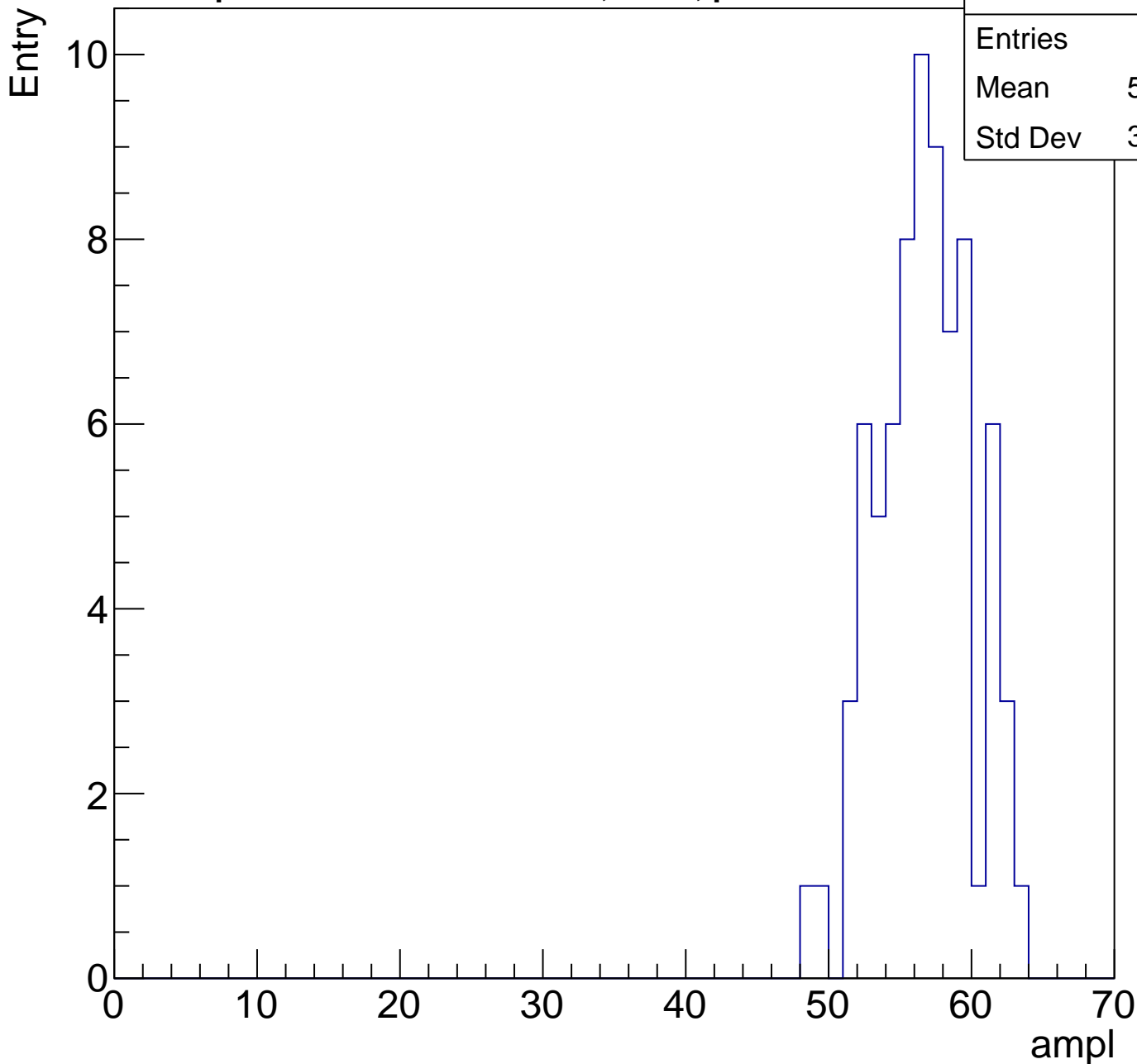


Entries	66
Mean	49.05
Std Dev	3.155

# B0L002S, U8-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	56.23
Std Dev	3.244



# B0L002S, U8-ch63, adc5

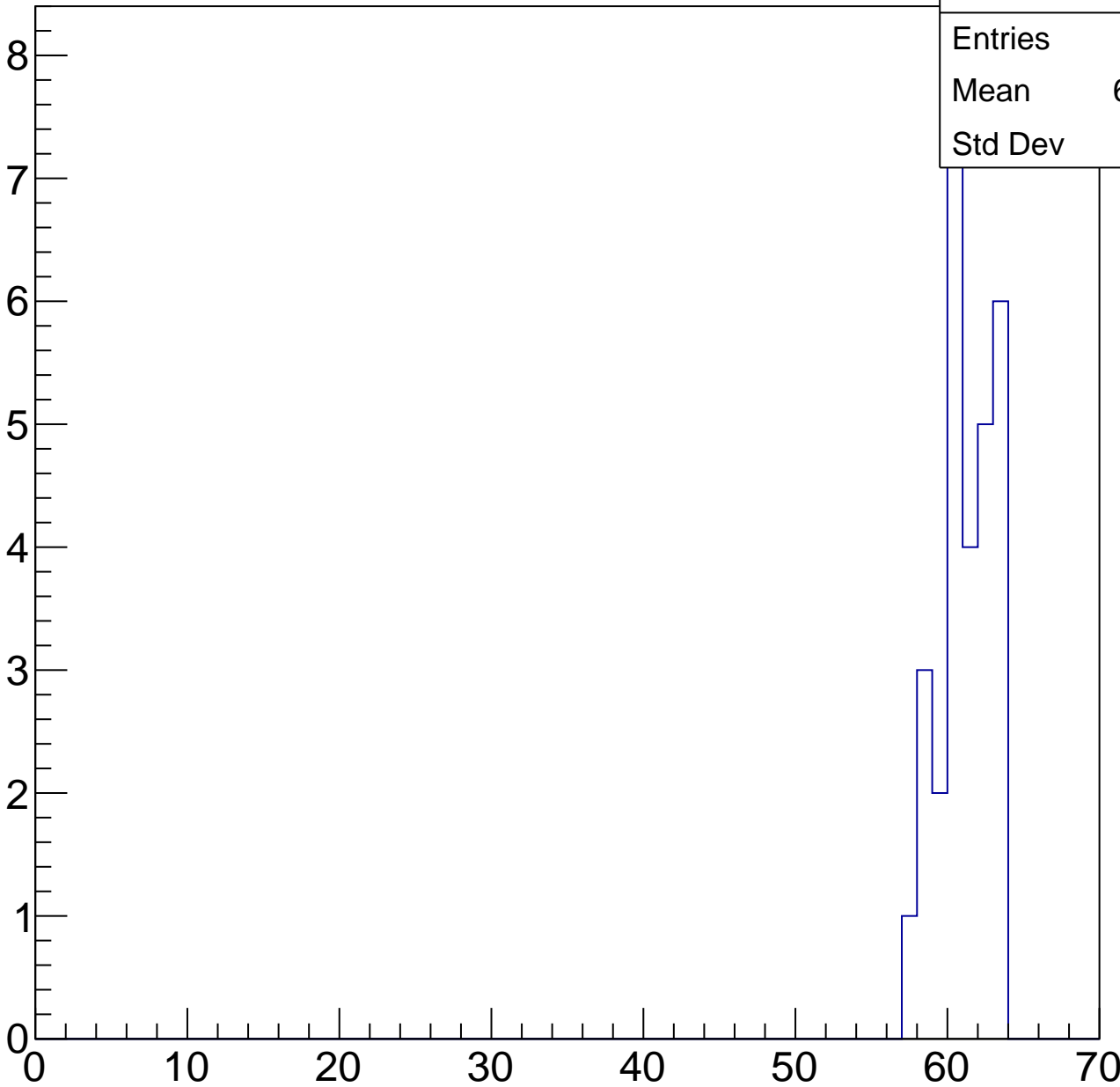
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	29
Mean	60.72
Std Dev	1.72

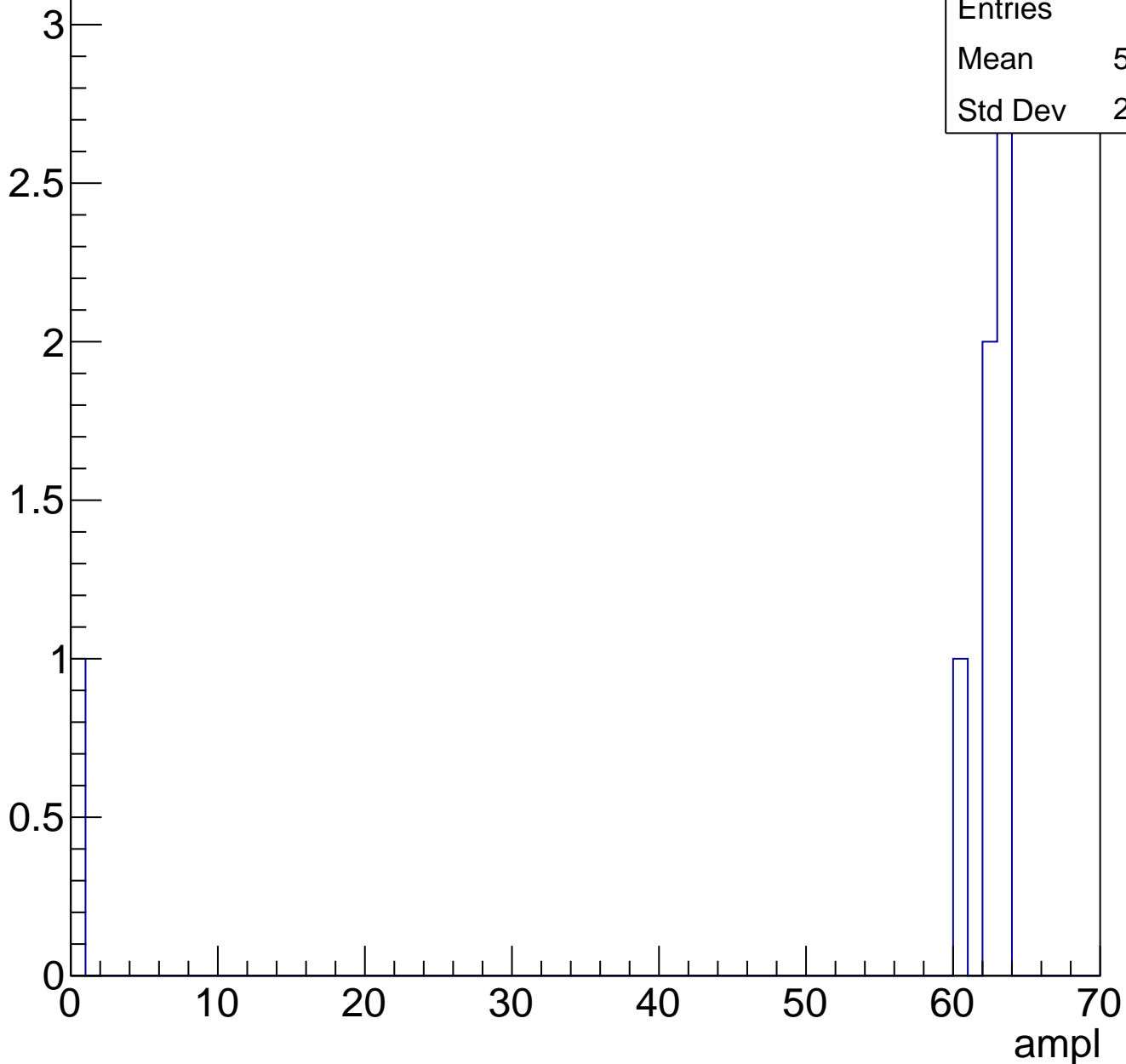
ampl



# B0L002S, U8-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	20
Std Dev	0

ampl

# B0L002S, U8-ch64, adc0

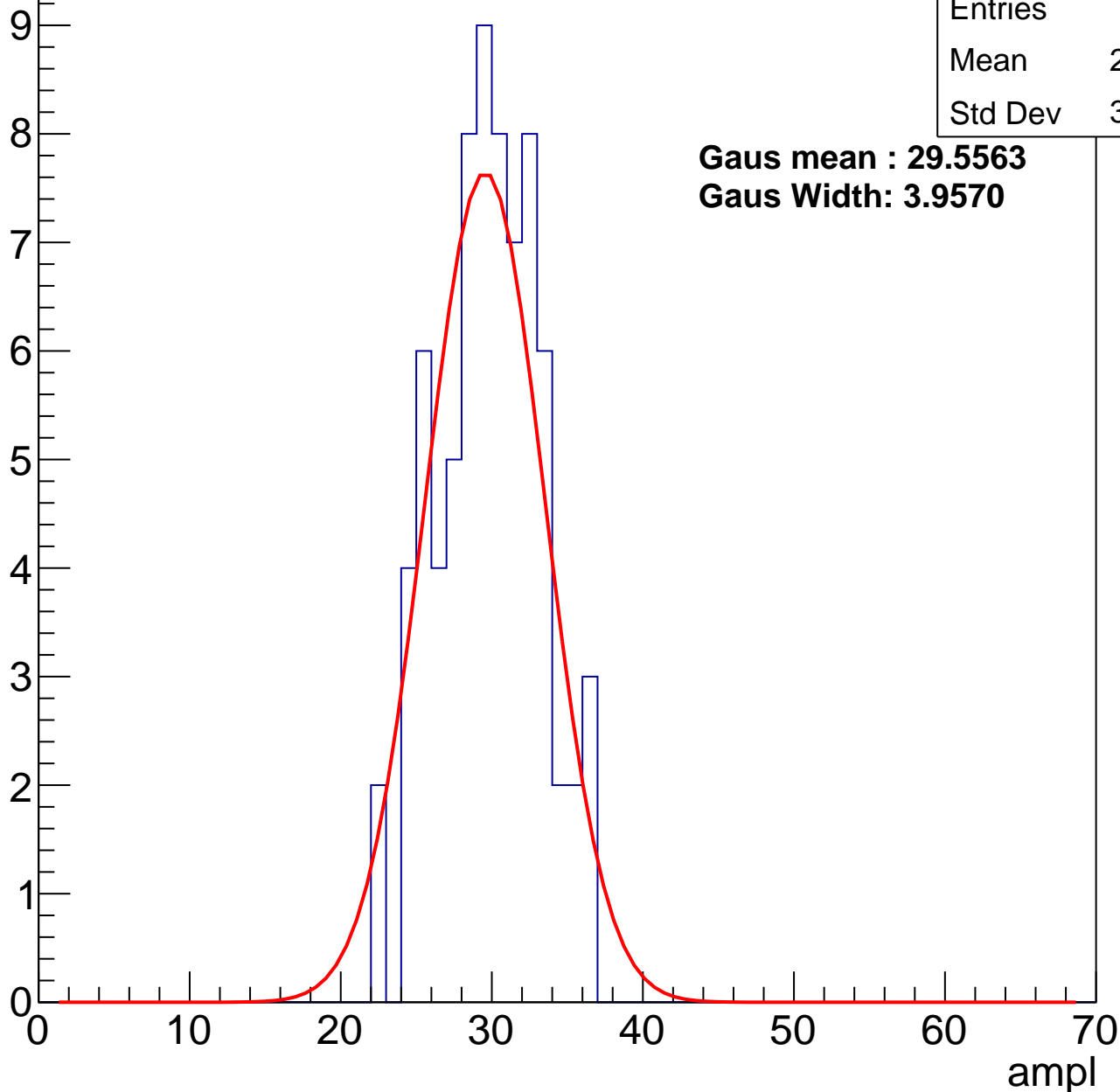
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.34
Std Dev	3.334

**Gaus mean : 29.5563**

**Gaus Width: 3.9570**



# B0L002S, U8-ch64, adc1

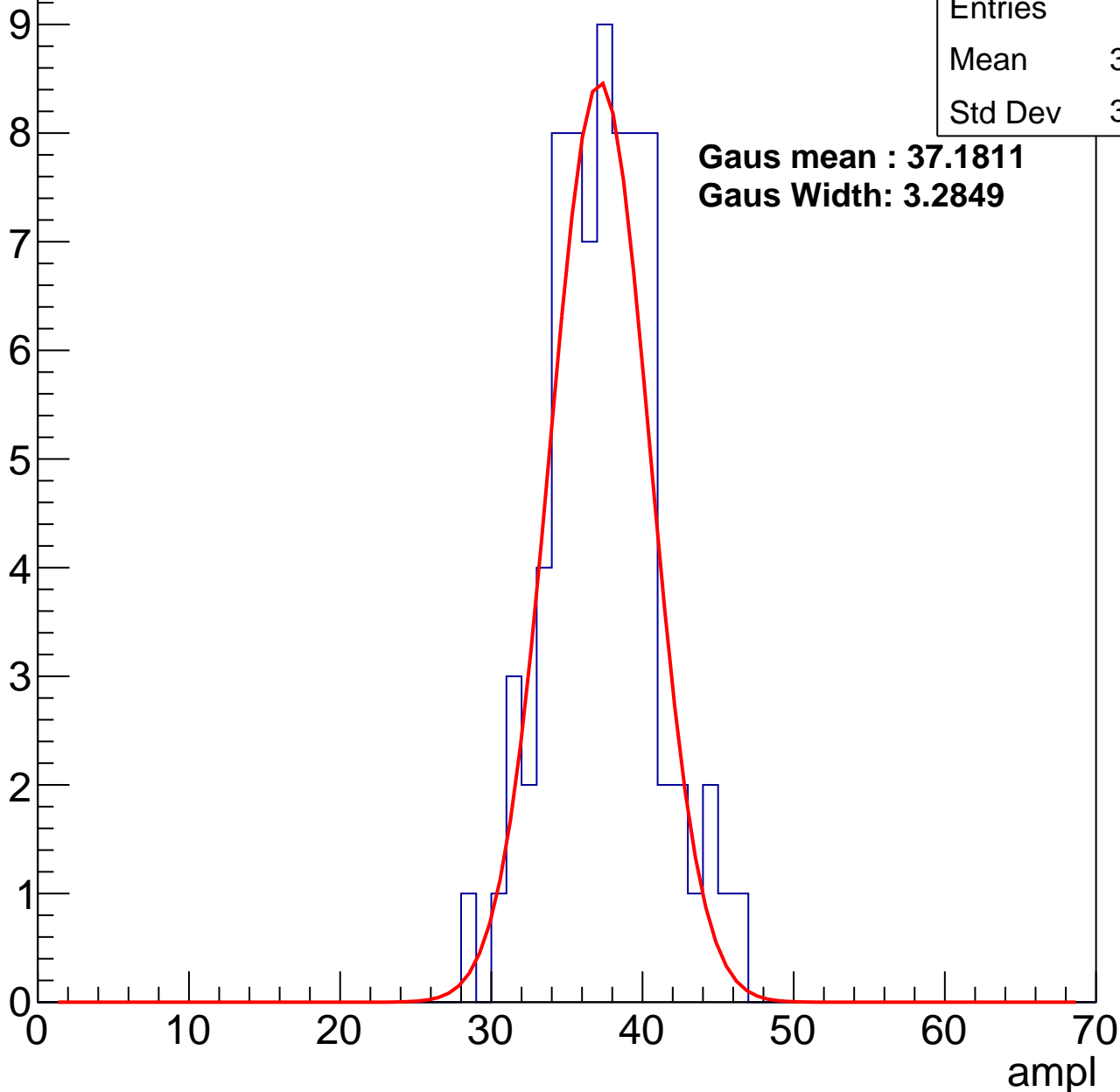
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.95
Std Dev	3.509

**Gaus mean : 37.1811**

**Gaus Width: 3.2849**



# B0L002S, U8-ch64, adc2

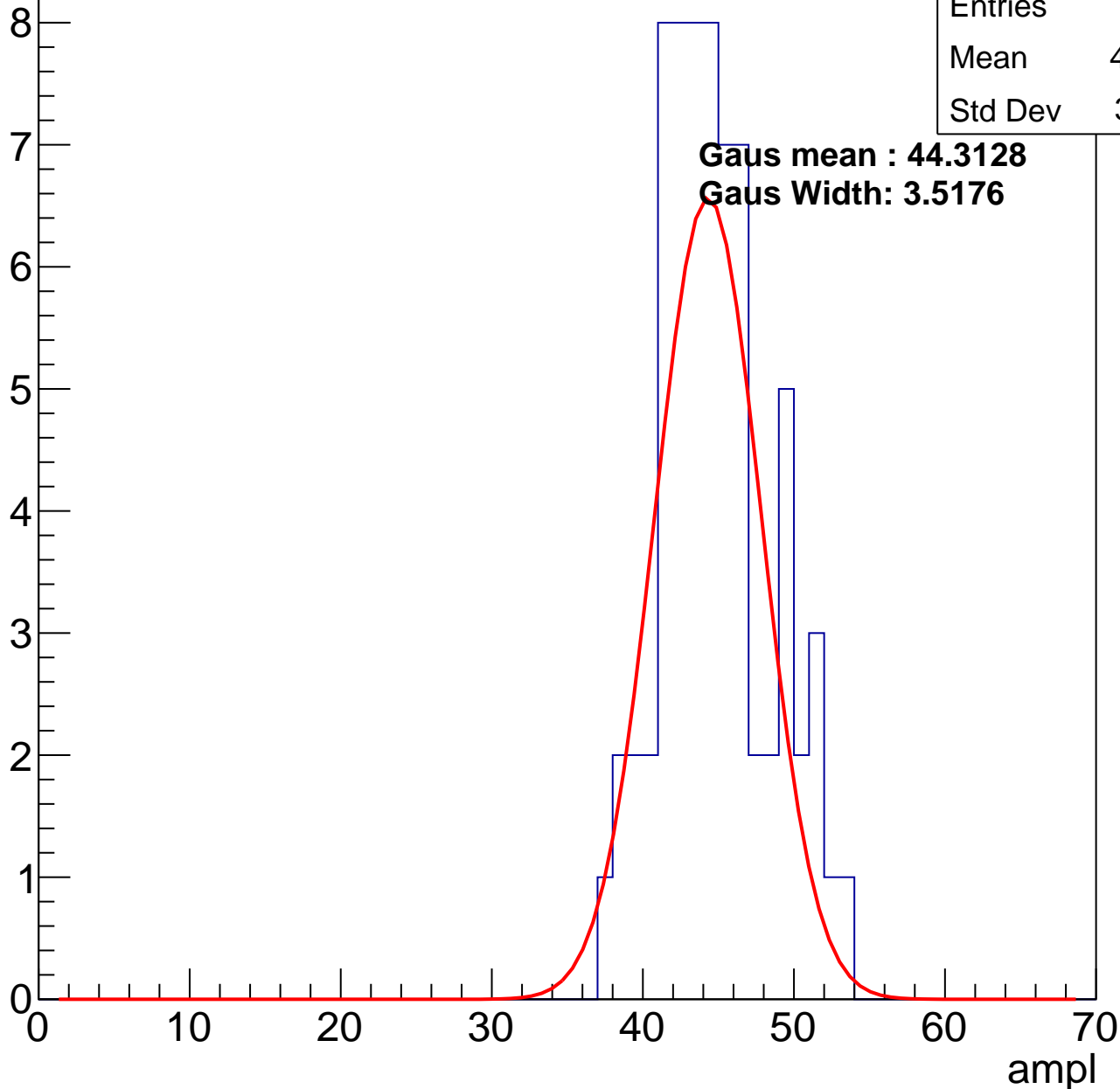
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.36
Std Dev	3.571

**Gaus mean : 44.3128**

**Gaus Width: 3.5176**

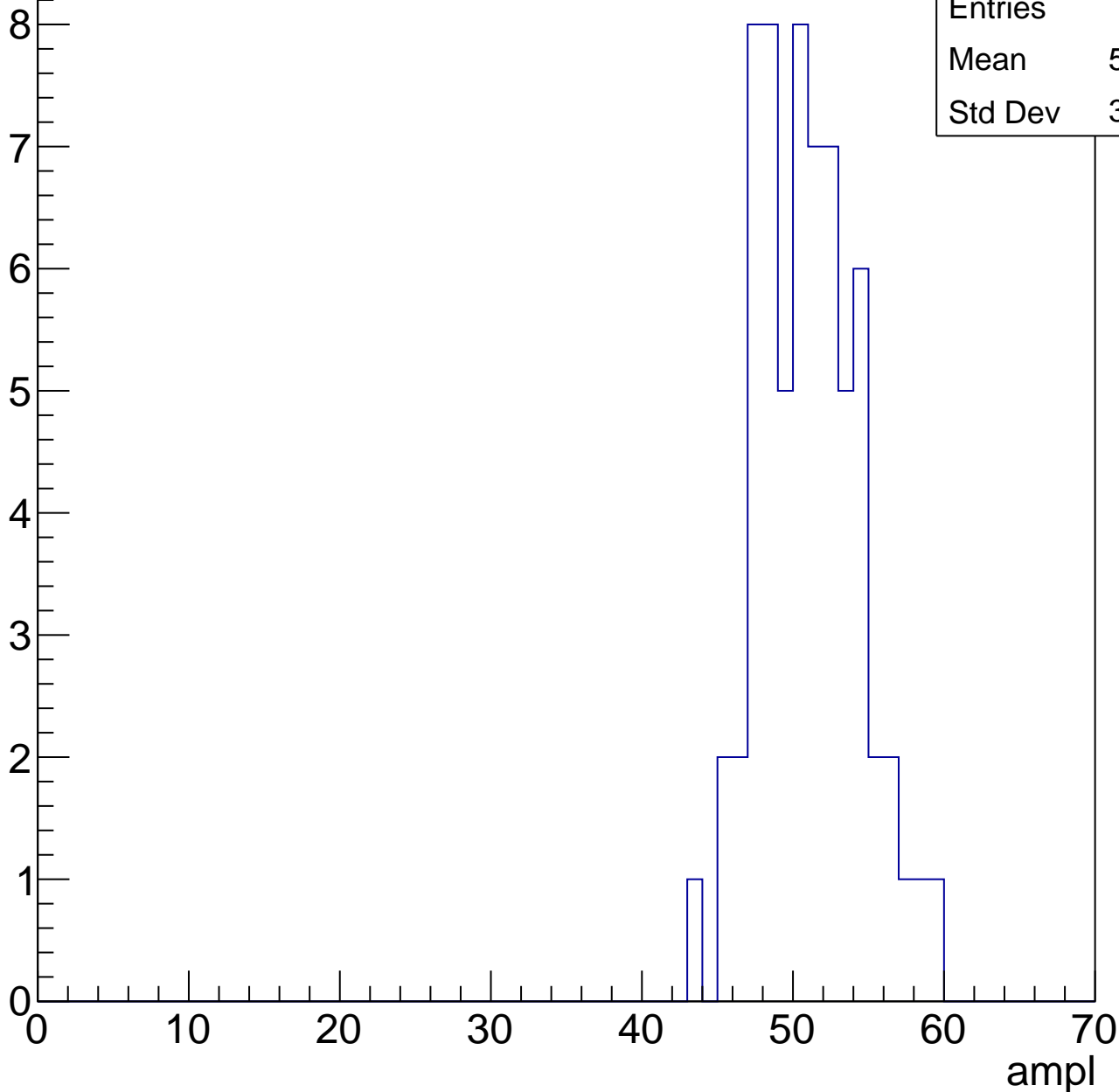


# B0L002S, U8-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

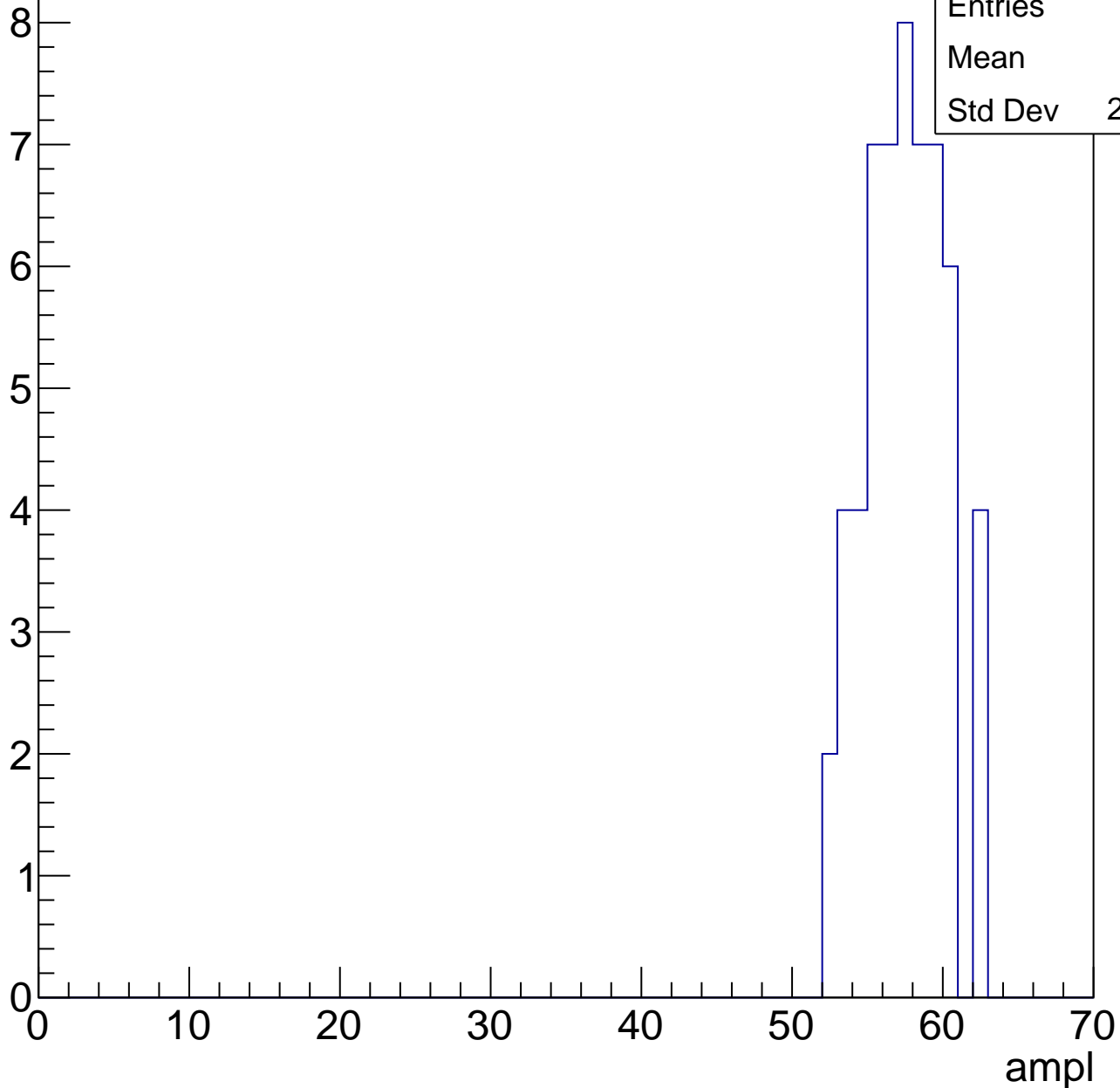
Entries	66
Mean	50.55
Std Dev	3.285



# B0L002S, U8-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

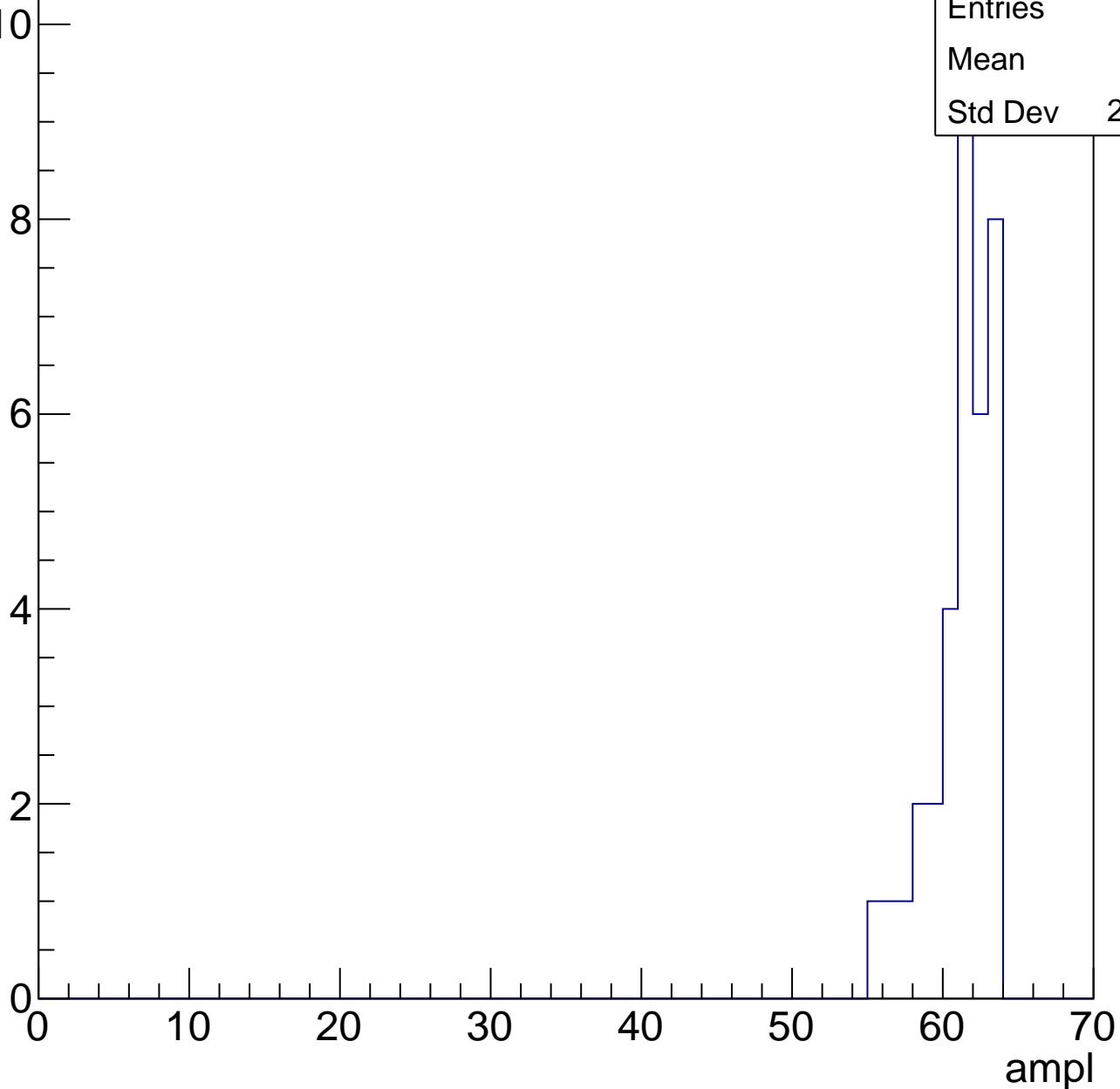


# B0L002S, U8-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

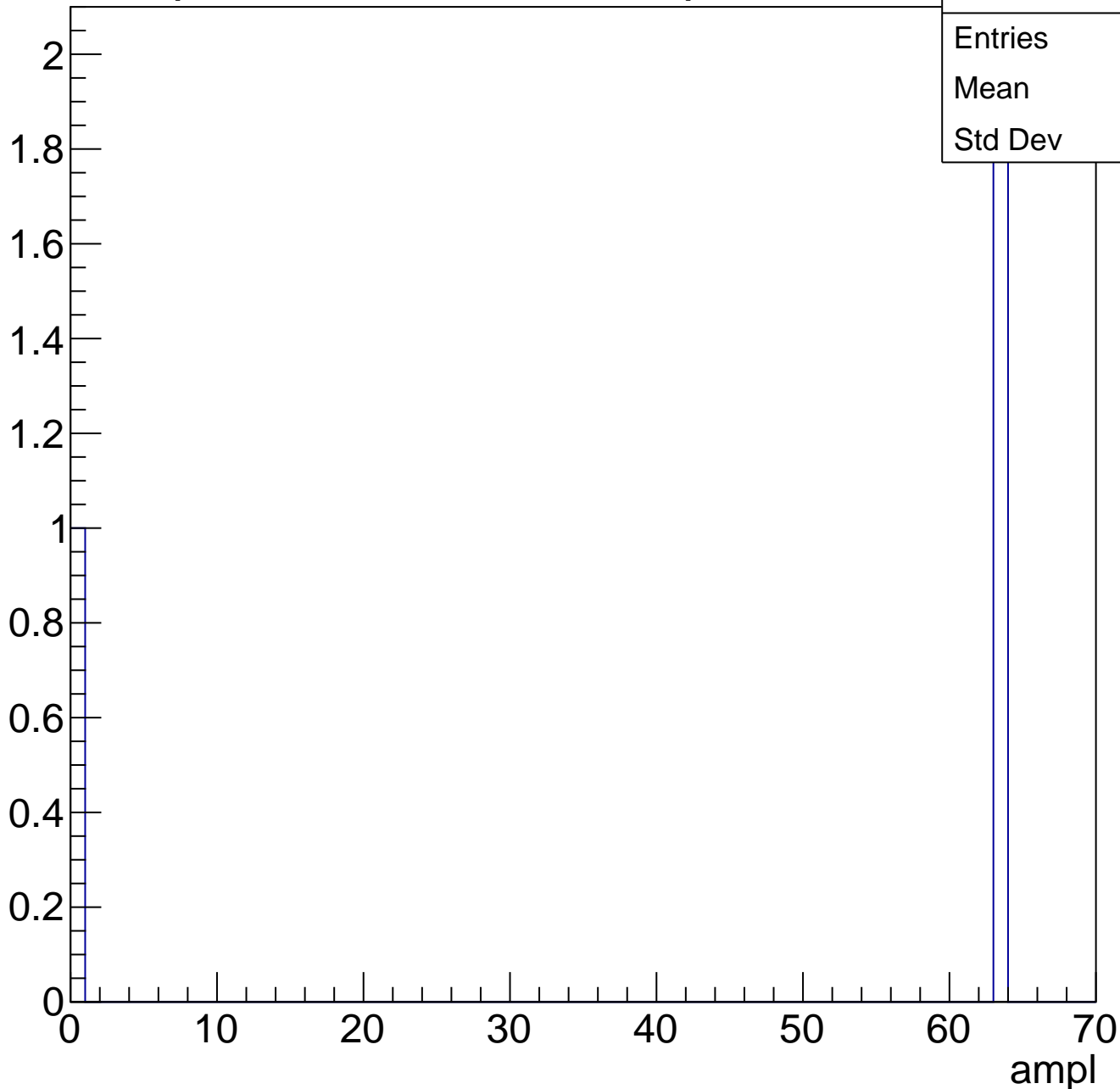
Entries	35
Mean	60.8
Std Dev	2.026



# B0L002S, U8-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch65, adc0

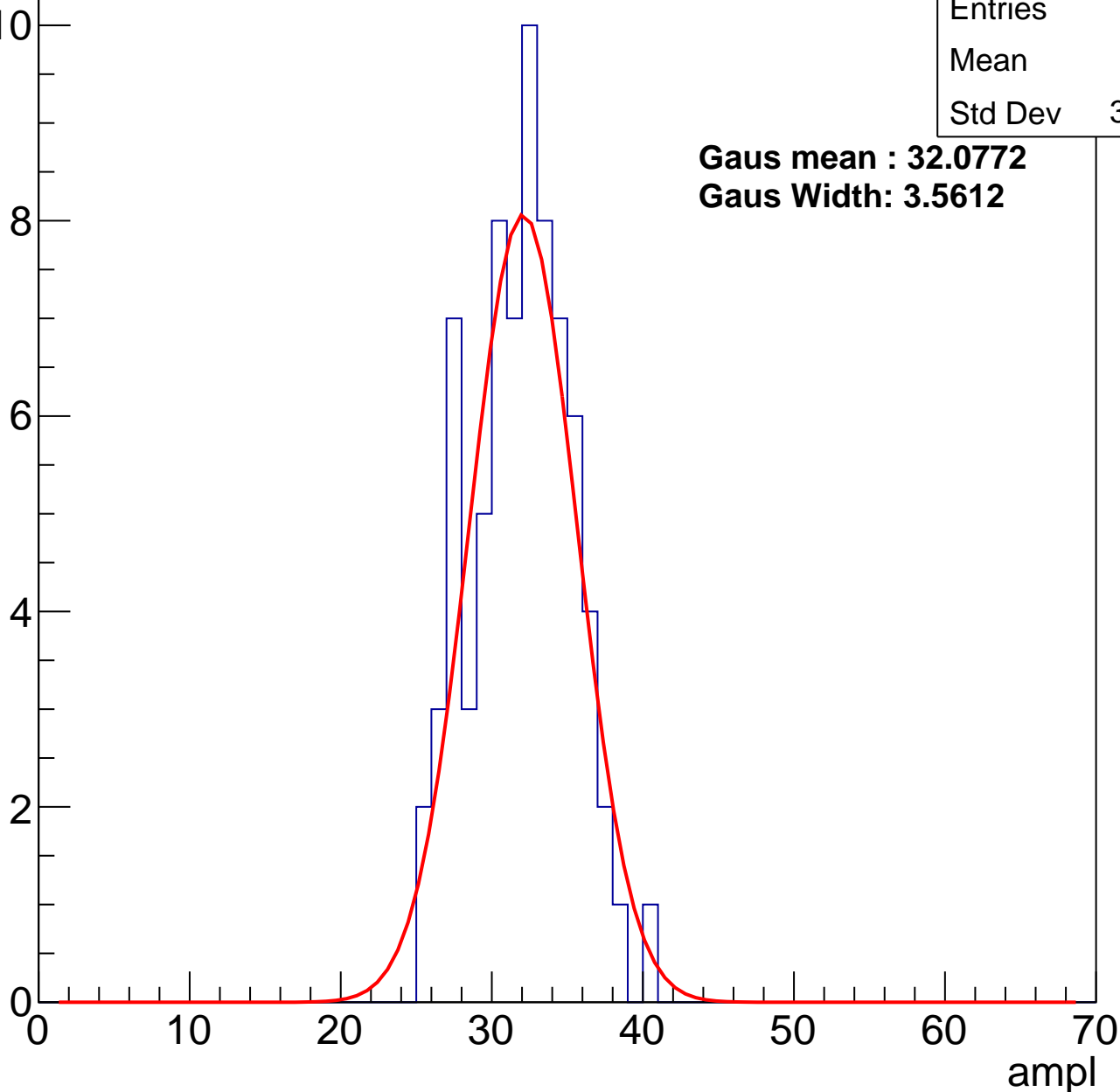
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	31.5
Std Dev	3.285

**Gaus mean : 32.0772**

**Gaus Width: 3.5612**



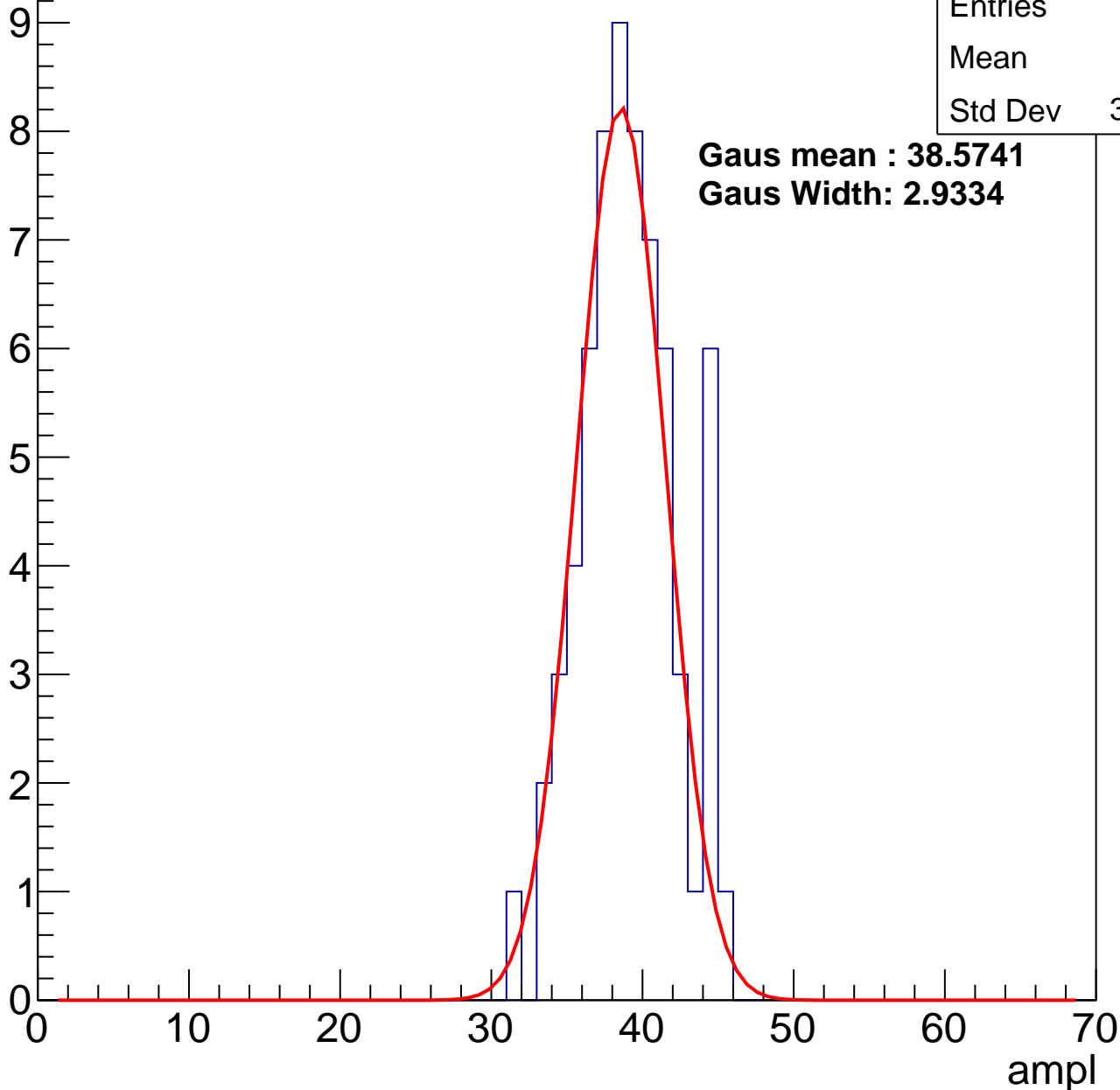
# B0L002S, U8-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.6
Std Dev	3.087

**Gaus mean : 38.5741**  
**Gaus Width: 2.9334**



# B0L002S, U8-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

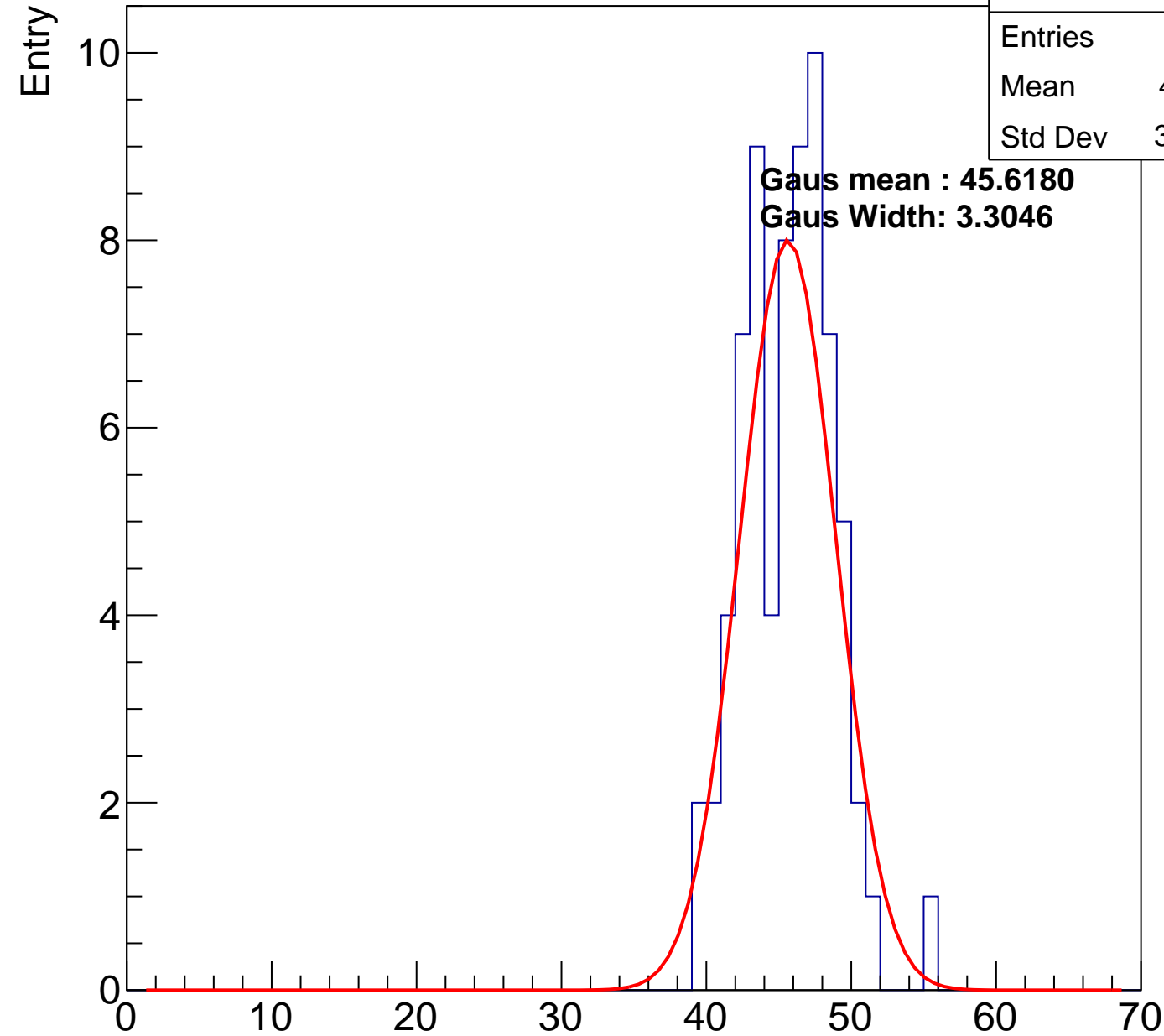
Entries	71
Mean	45.21
Std Dev	3.058

**Gaus mean : 45.6180**

**Gaus Width: 3.3046**

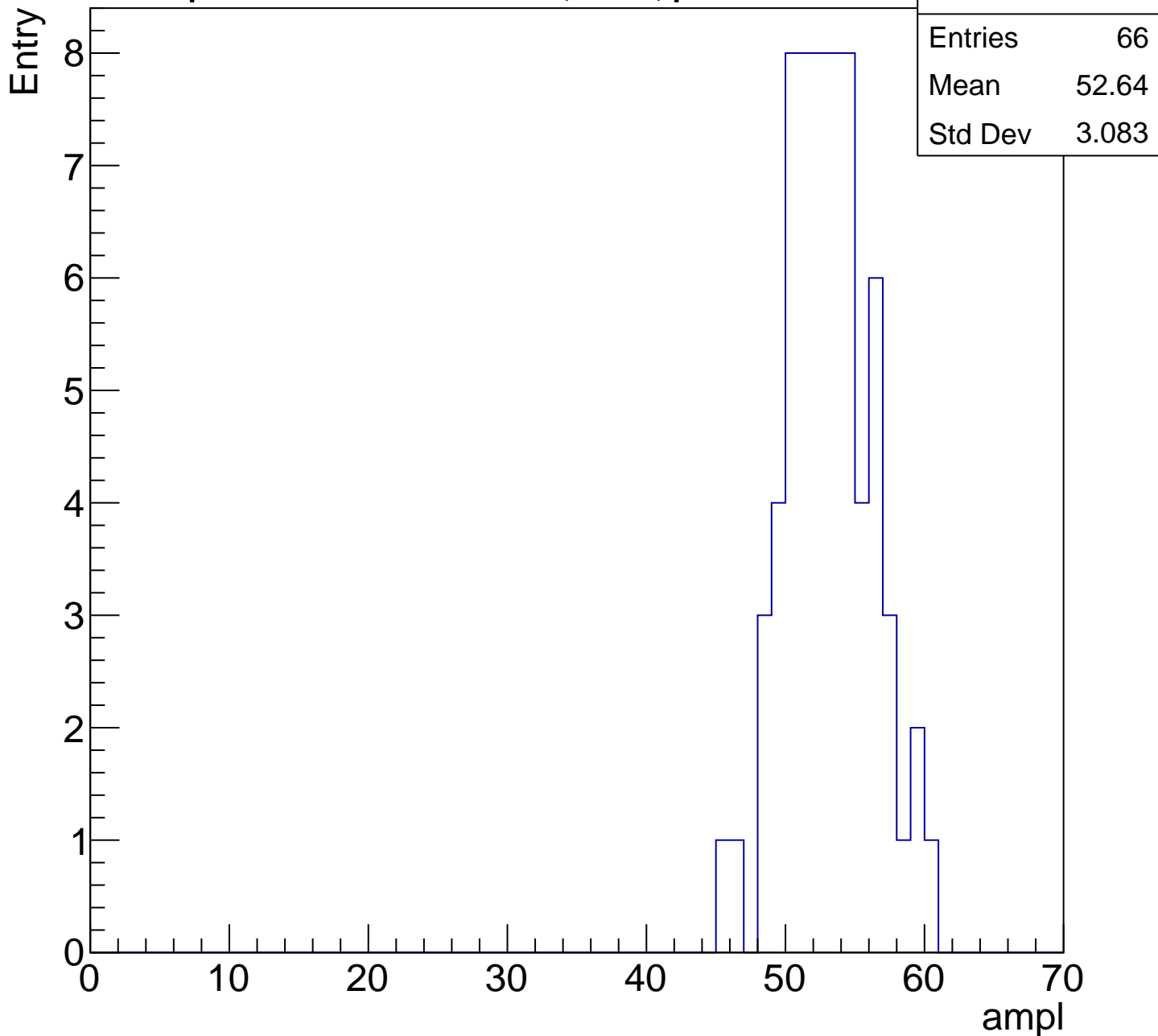
10  
8  
6  
4  
2  
0

ampl



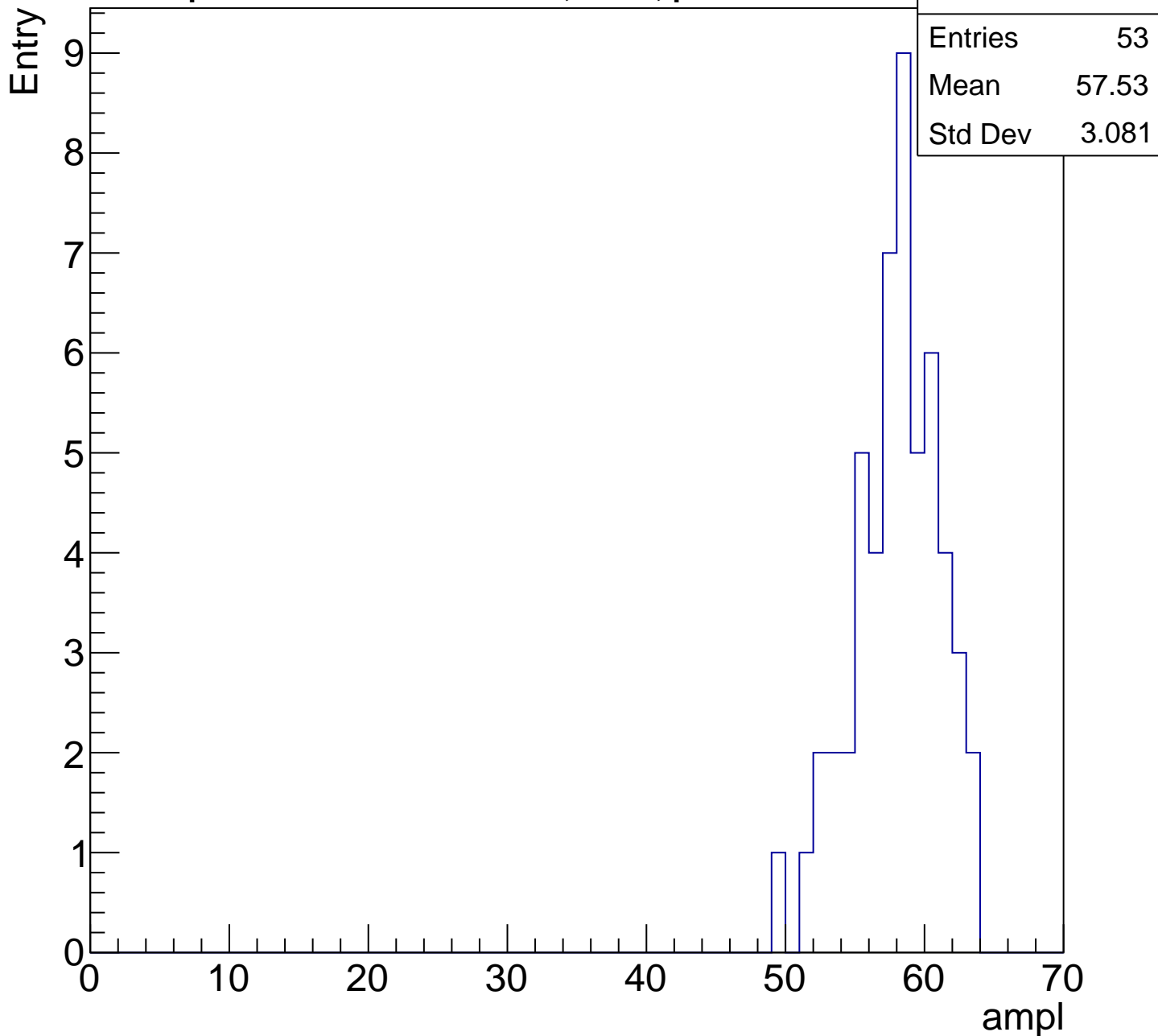
# B0L002S, U8-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

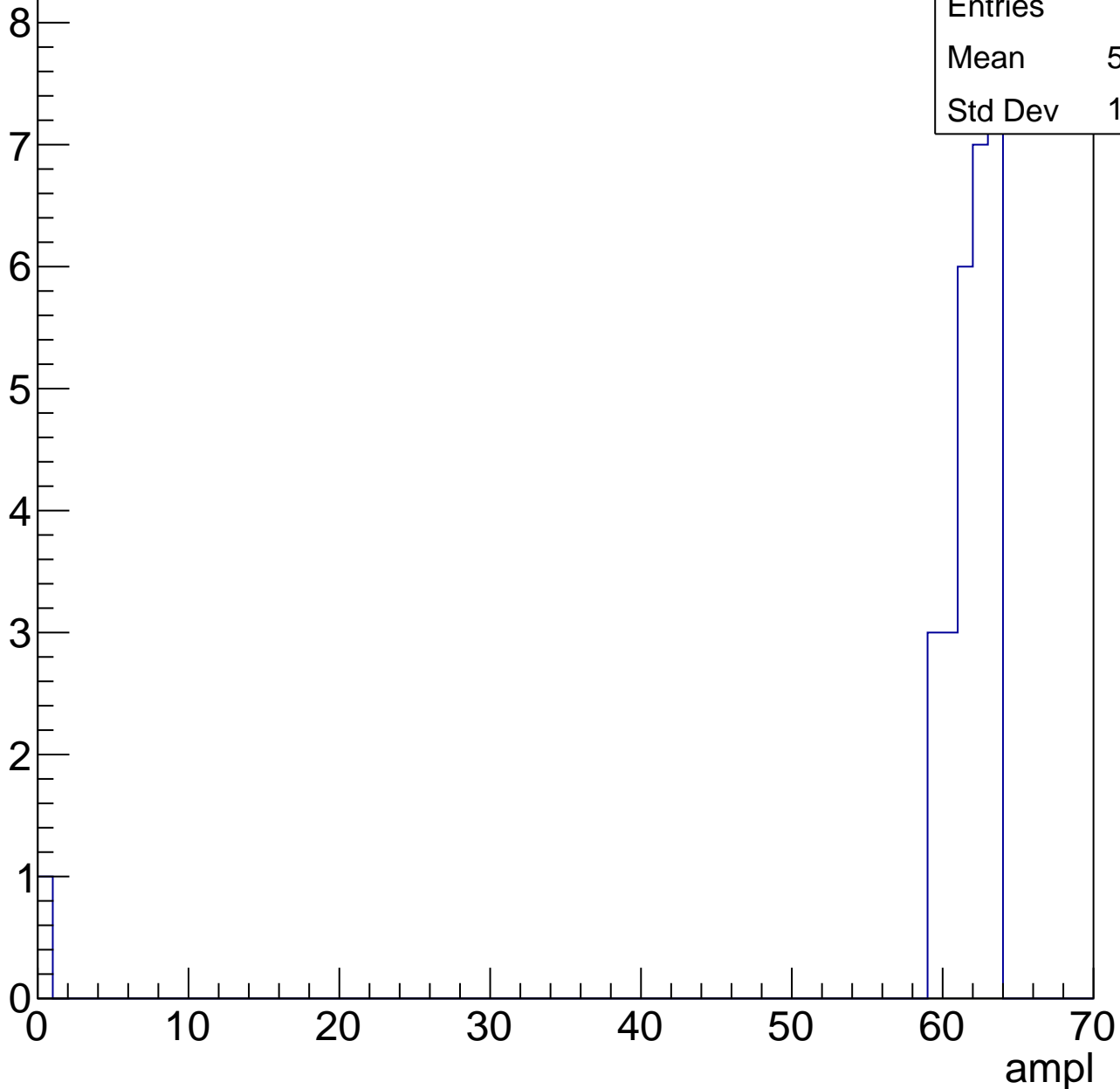


# B0L002S, U8-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59.32
Std Dev	11.49



# B0L002S, U8-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl

# B0L002S, U8-ch66, adc0

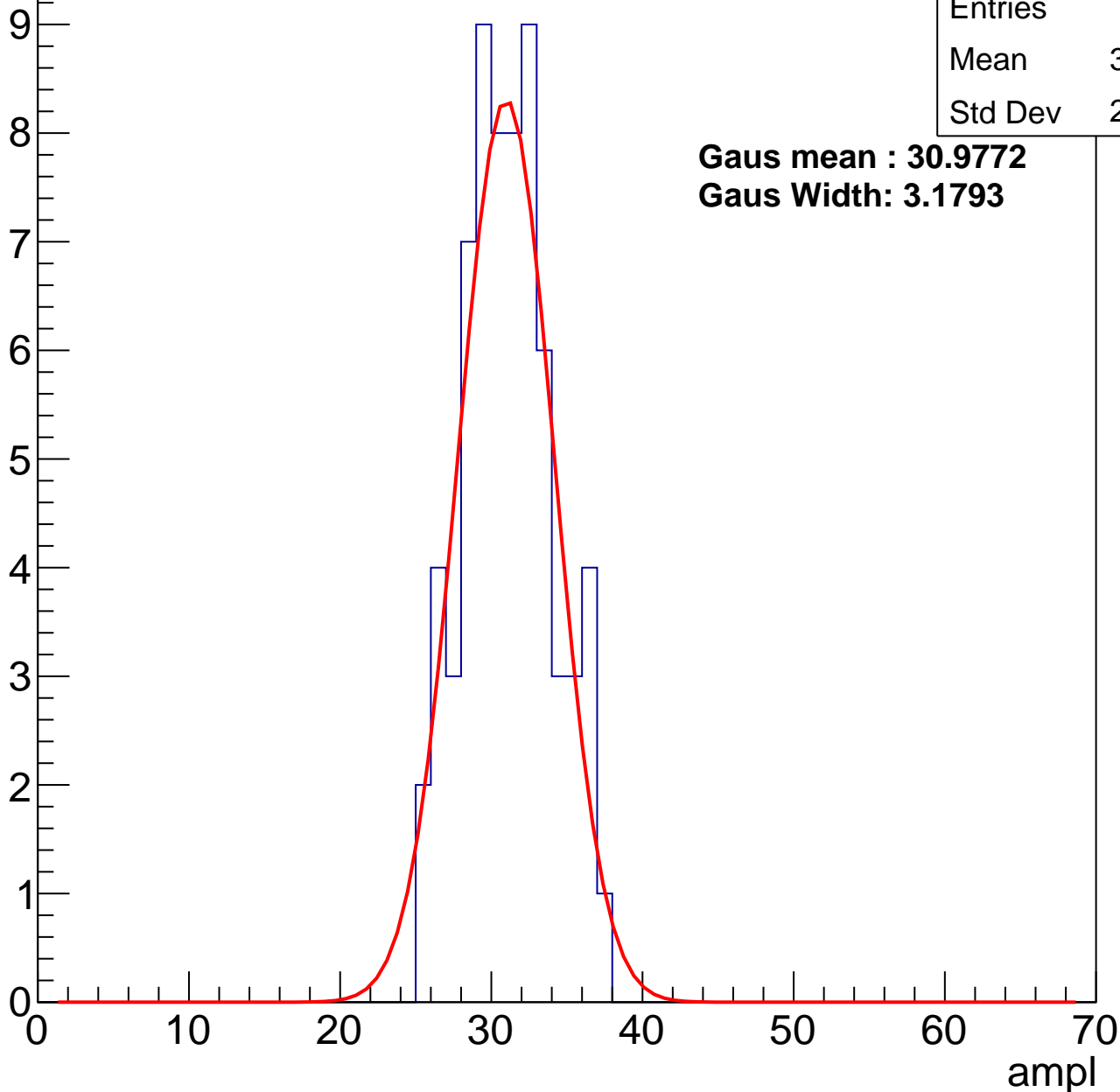
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.66
Std Dev	2.899

**Gaus mean : 30.9772**

**Gaus Width: 3.1793**



# B0L002S, U8-ch66, adc1

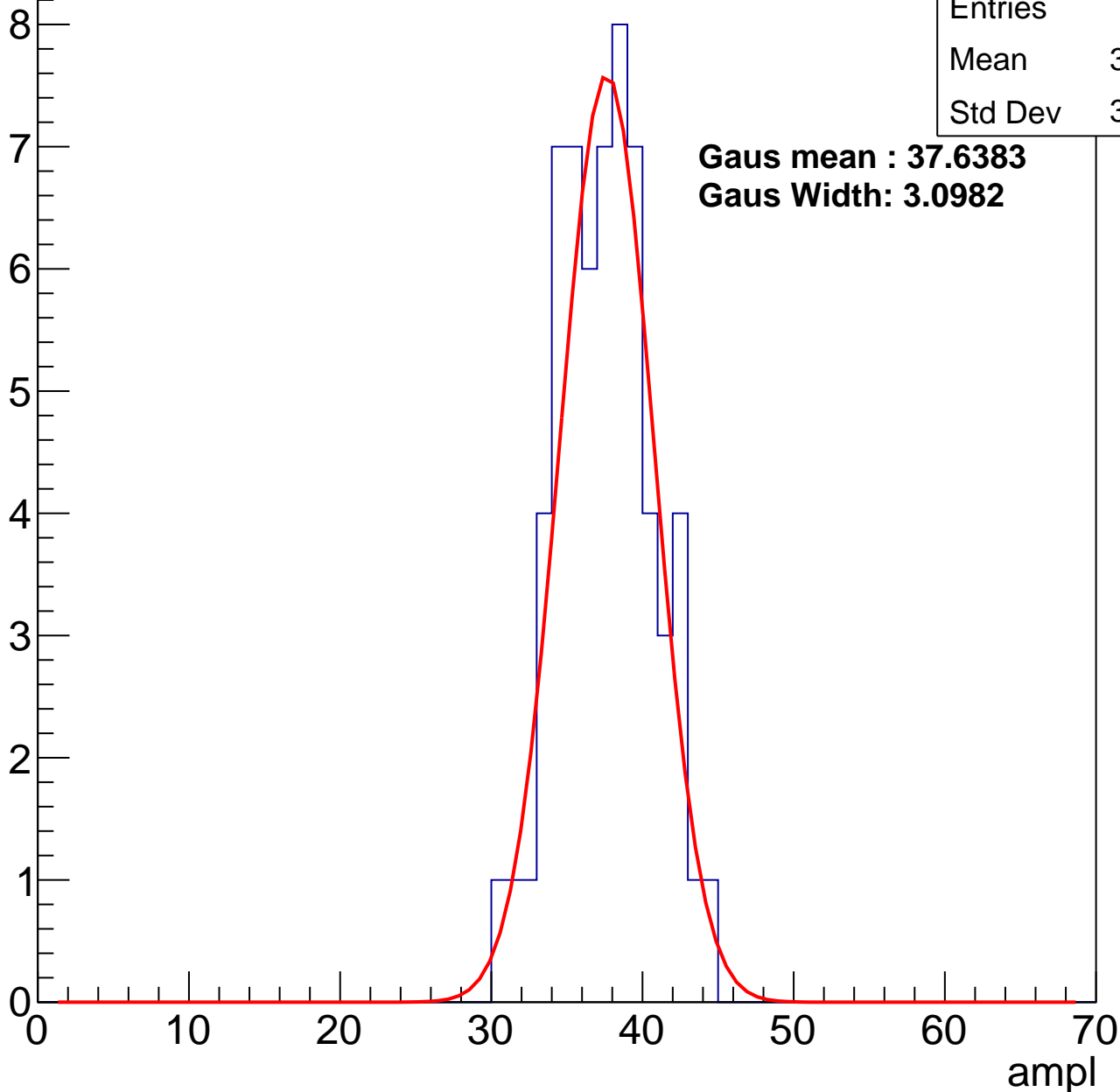
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.06
Std Dev	3.047

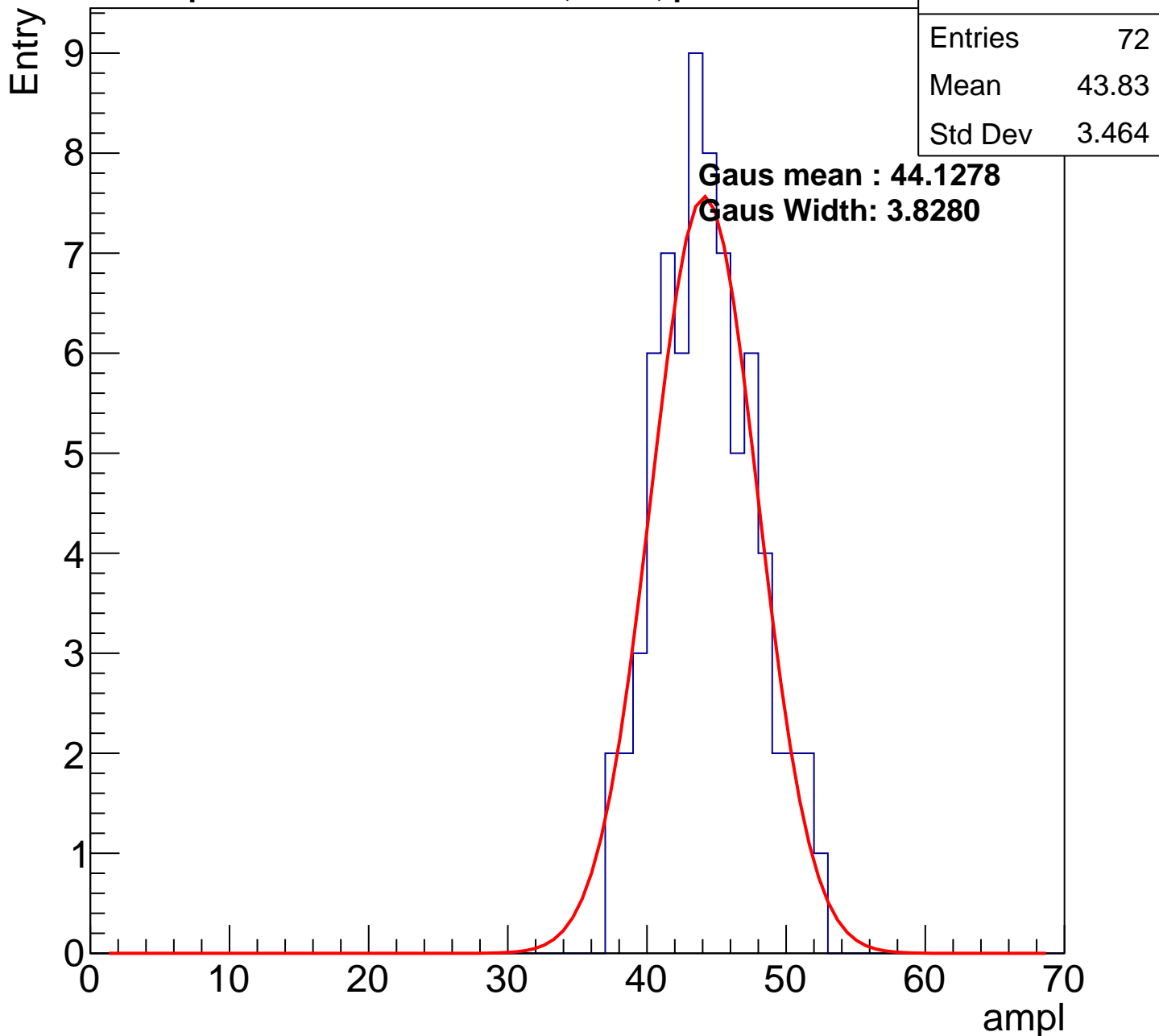
**Gaus mean : 37.6383**

**Gaus Width: 3.0982**



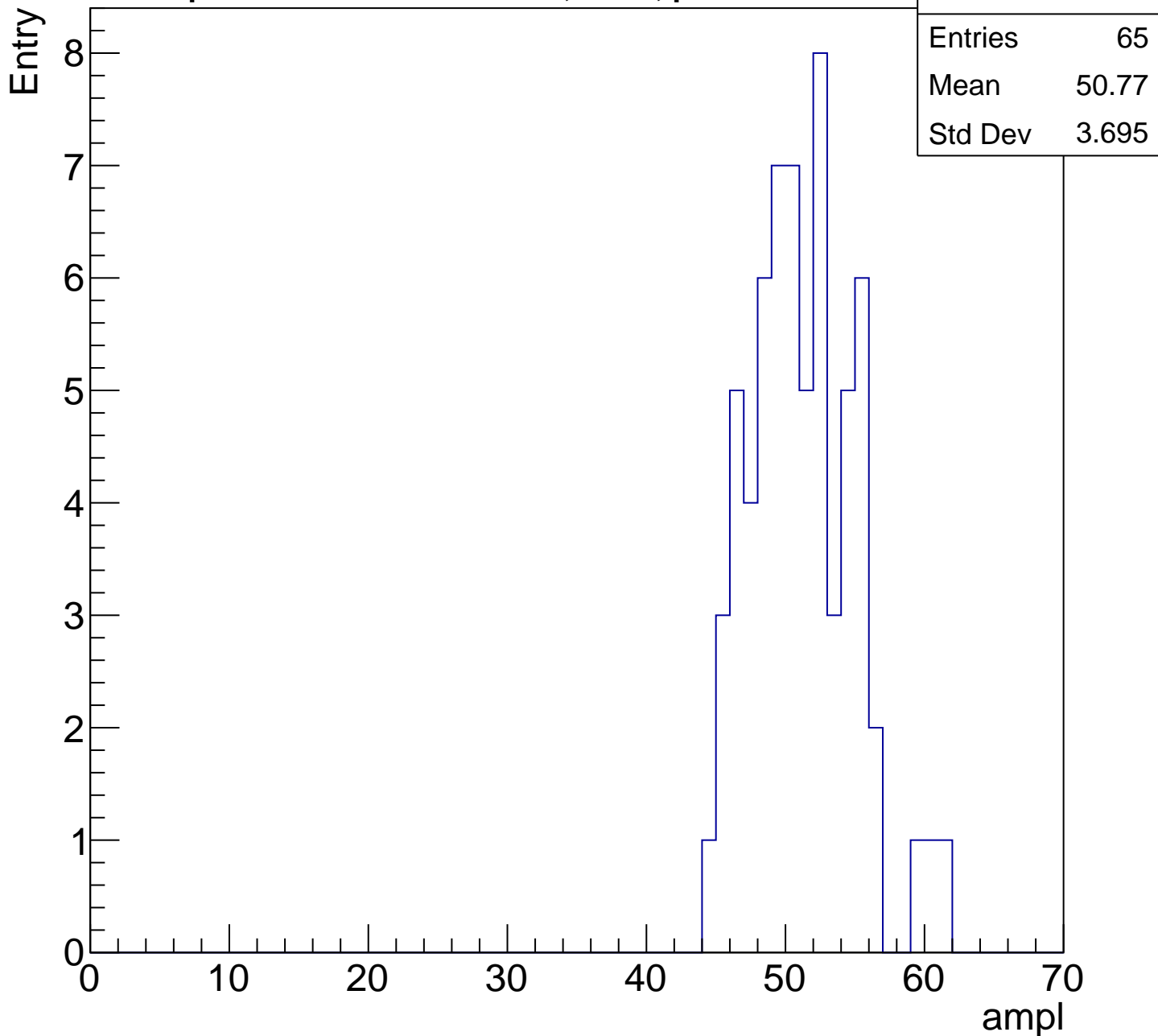
# B0L002S, U8-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

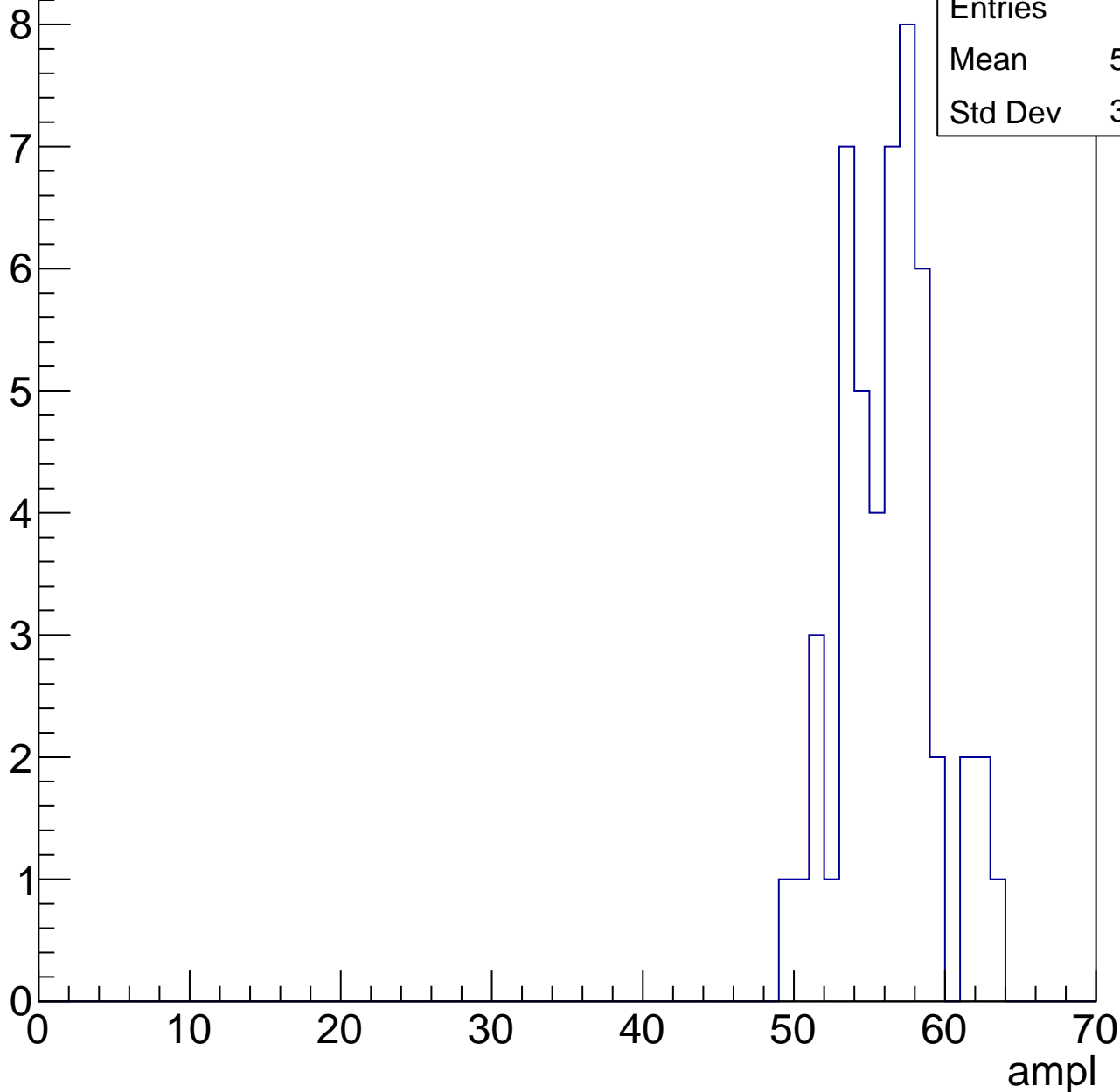


# B0L002S, U8-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

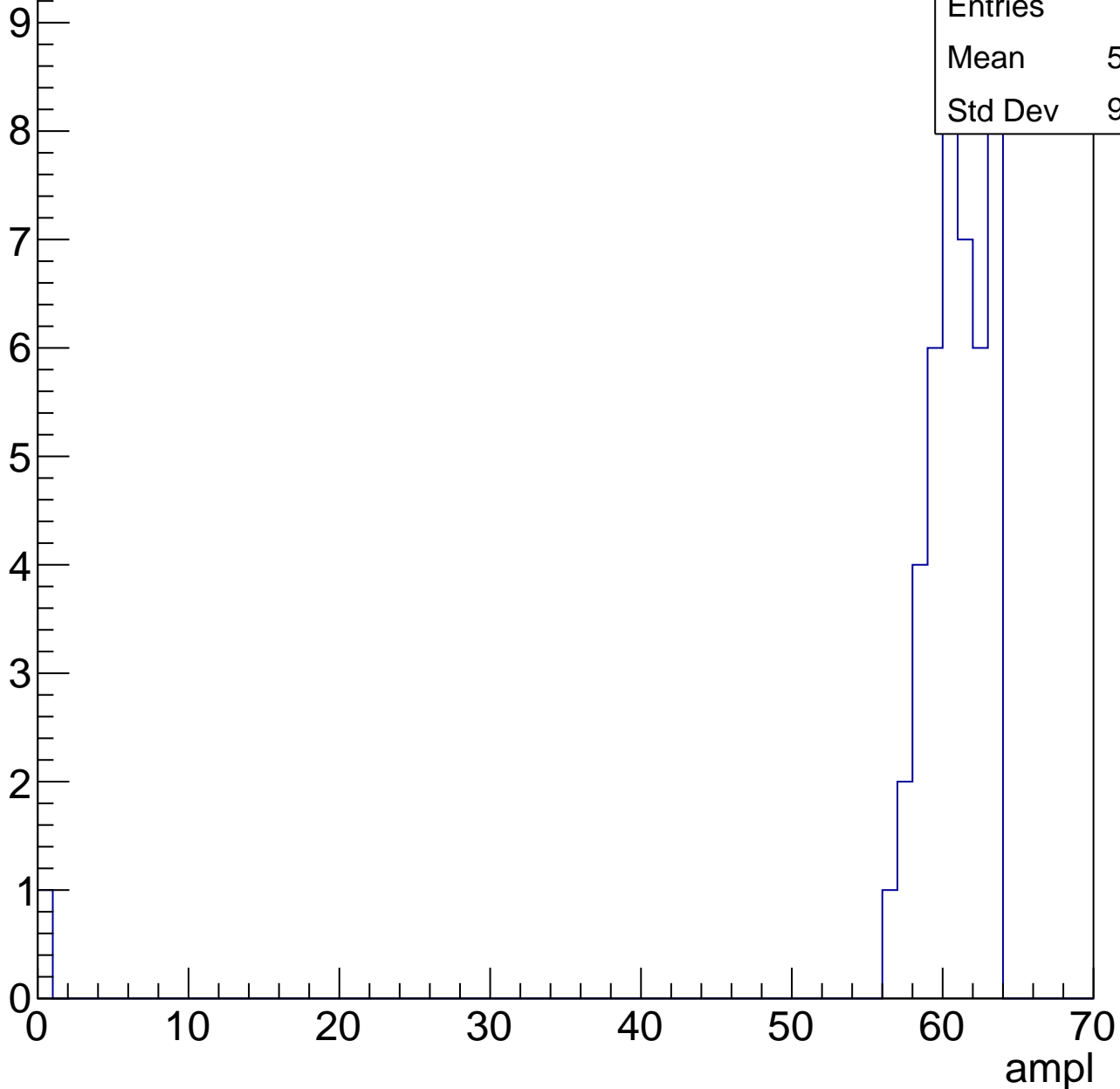
Entries	50
Mean	55.76
Std Dev	3.102



# B0L002S, U8-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L002S, U8-ch67, adc0

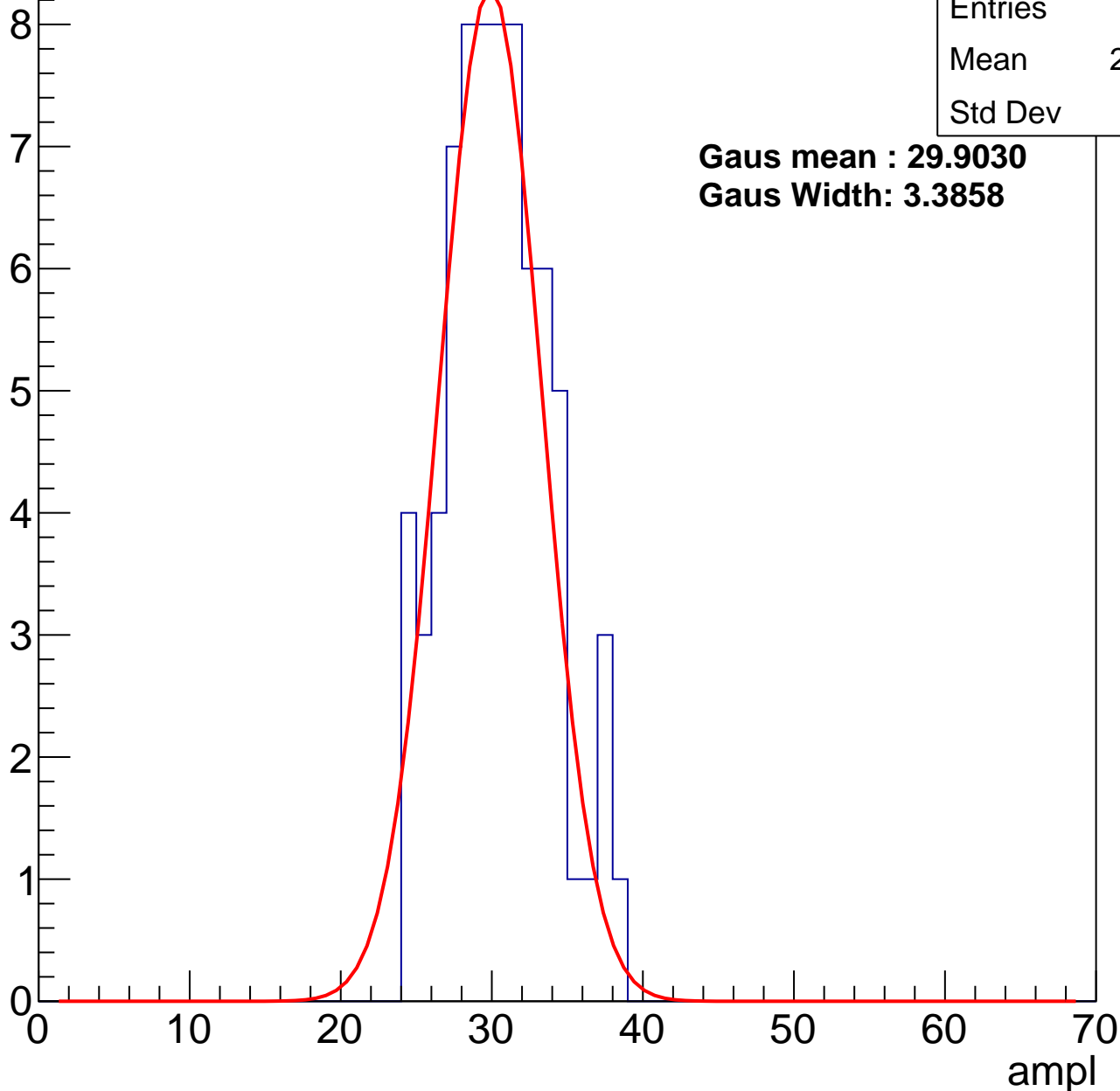
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.97
Std Dev	3.36

**Gaus mean : 29.9030**

**Gaus Width: 3.3858**



# B0L002S, U8-ch67, adc1

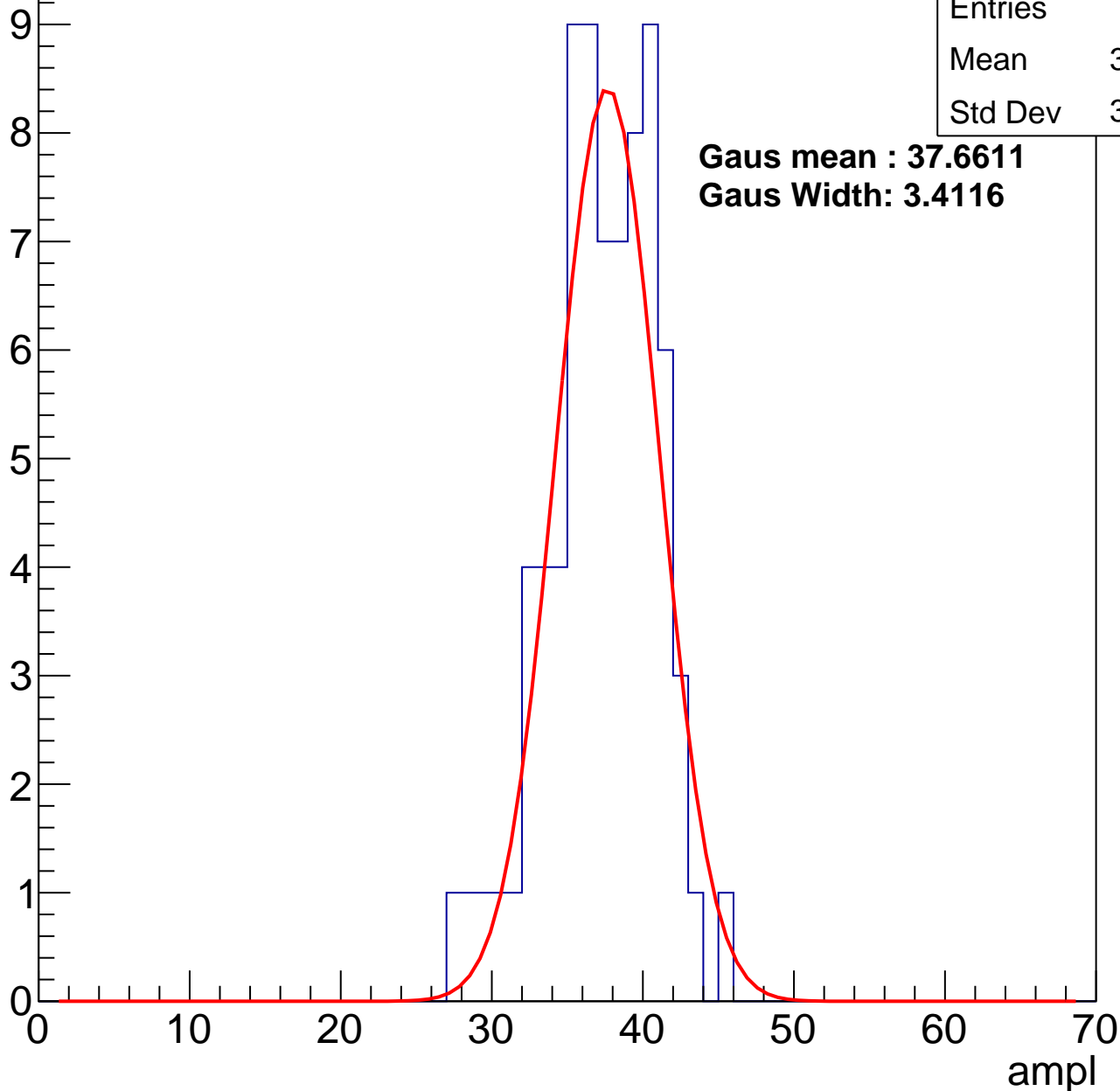
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.84
Std Dev	3.539

**Gaus mean : 37.6611**

**Gaus Width: 3.4116**



# B0L002S, U8-ch67, adc2

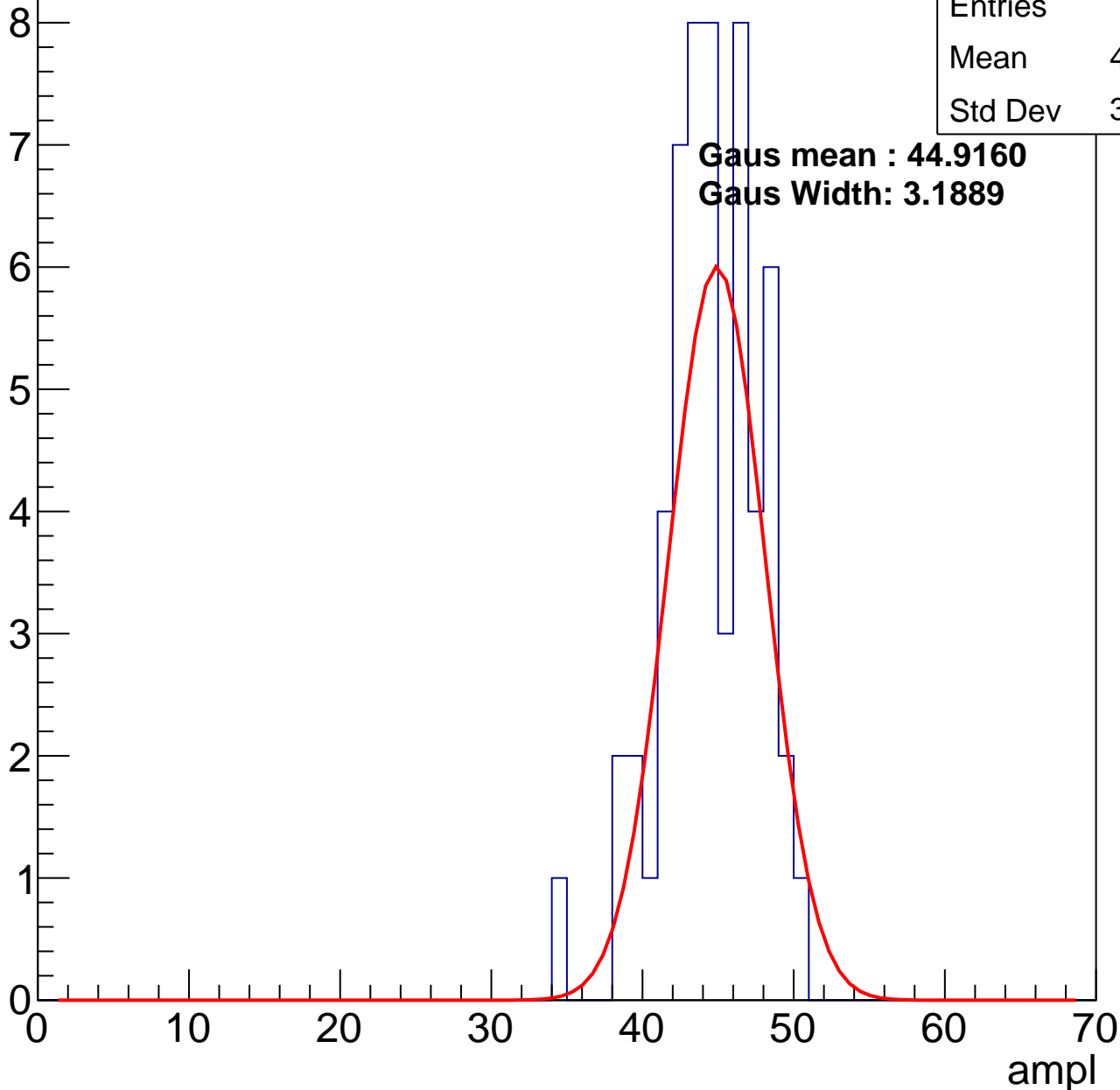
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.02
Std Dev	3.143

**Gaus mean : 44.9160**

**Gaus Width: 3.1889**

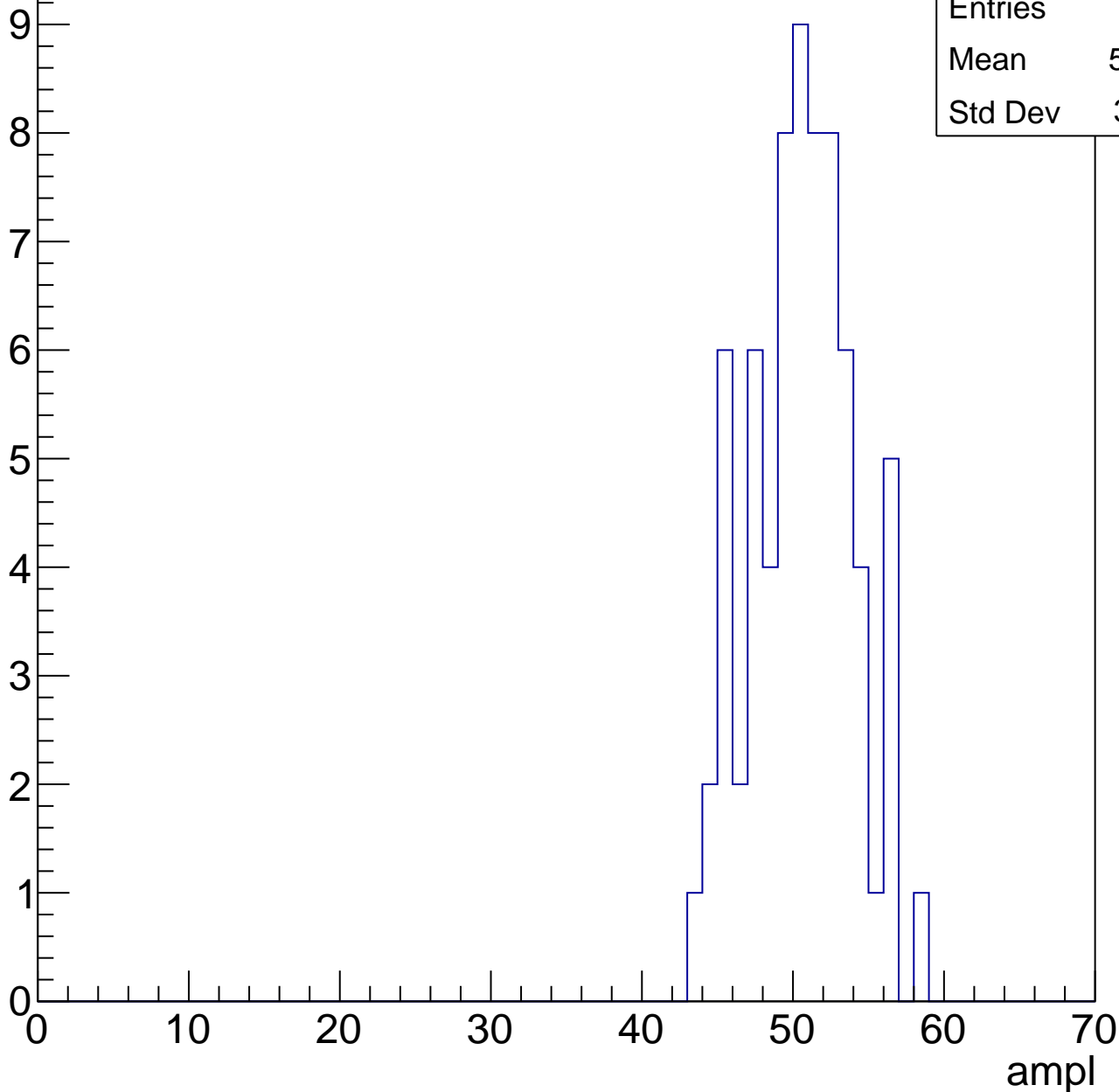


# B0L002S, U8-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.14
Std Dev	3.371

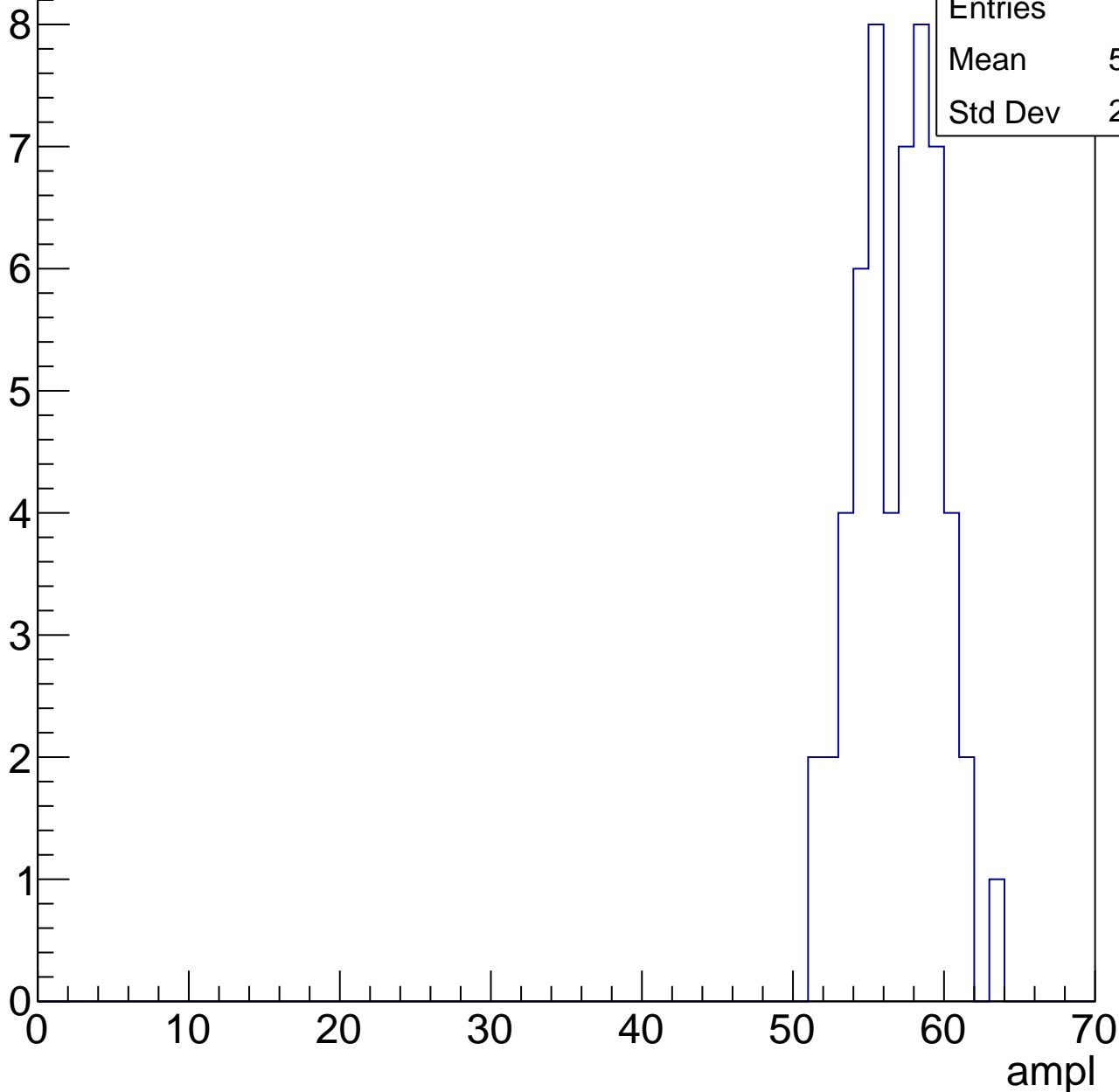


# B0L002S, U8-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.49
Std Dev	2.703

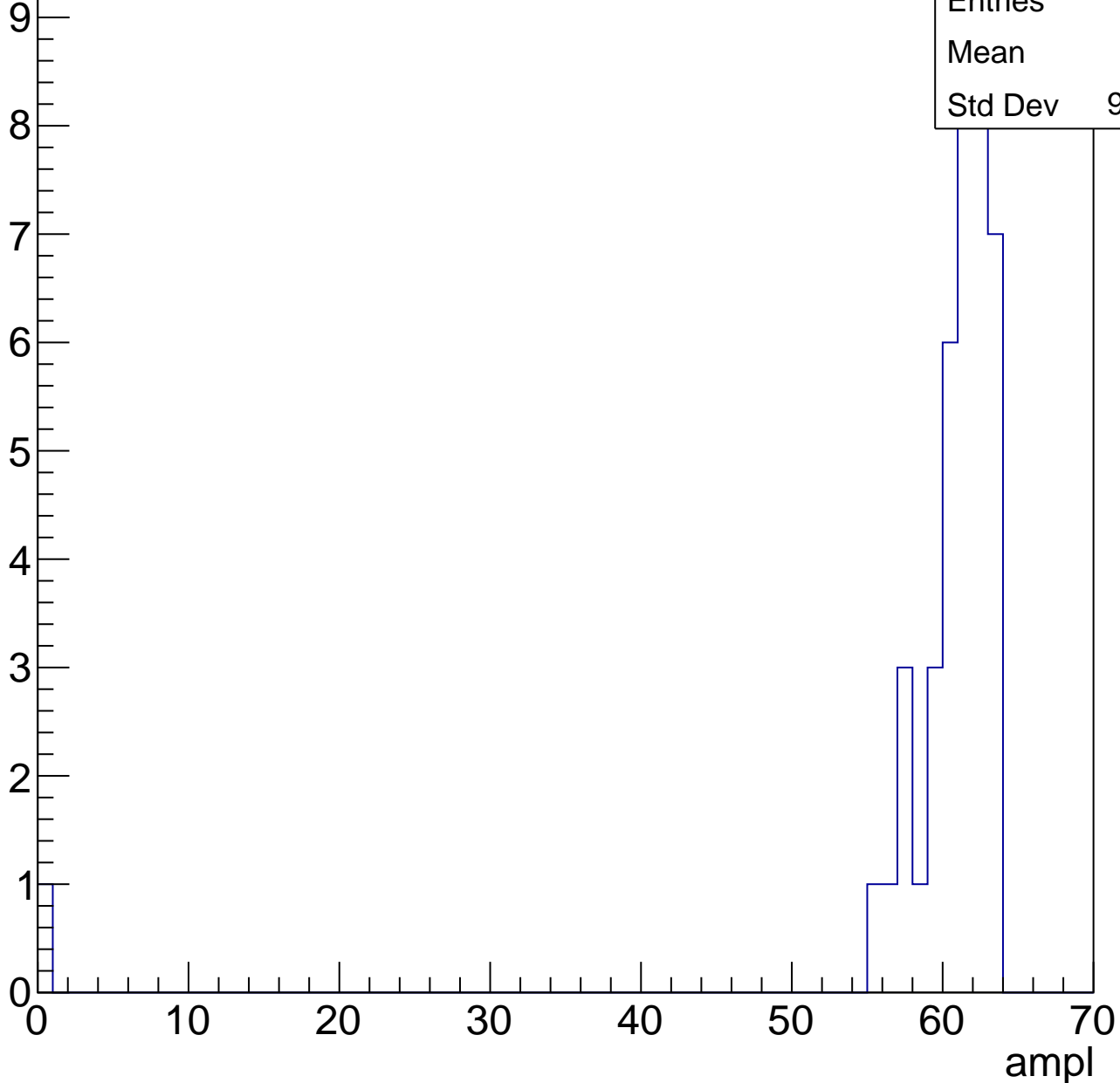


# B0L002S, U8-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.1
Std Dev	9.682



# B0L002S, U8-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U8-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	30.68
Std Dev	4.794

**Gaus mean : 31.3742**

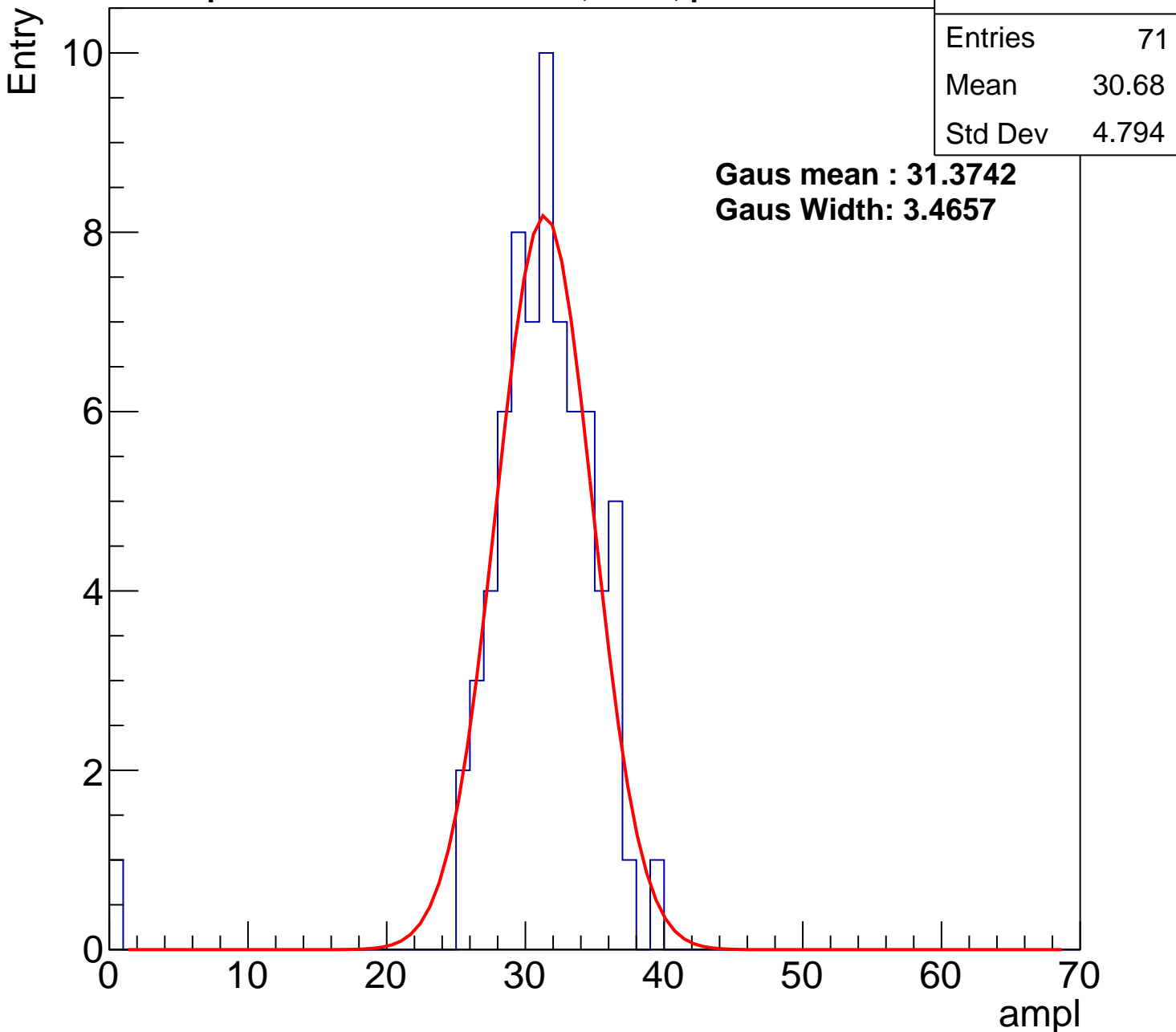
**Gaus Width: 3.4657**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch68, adc1

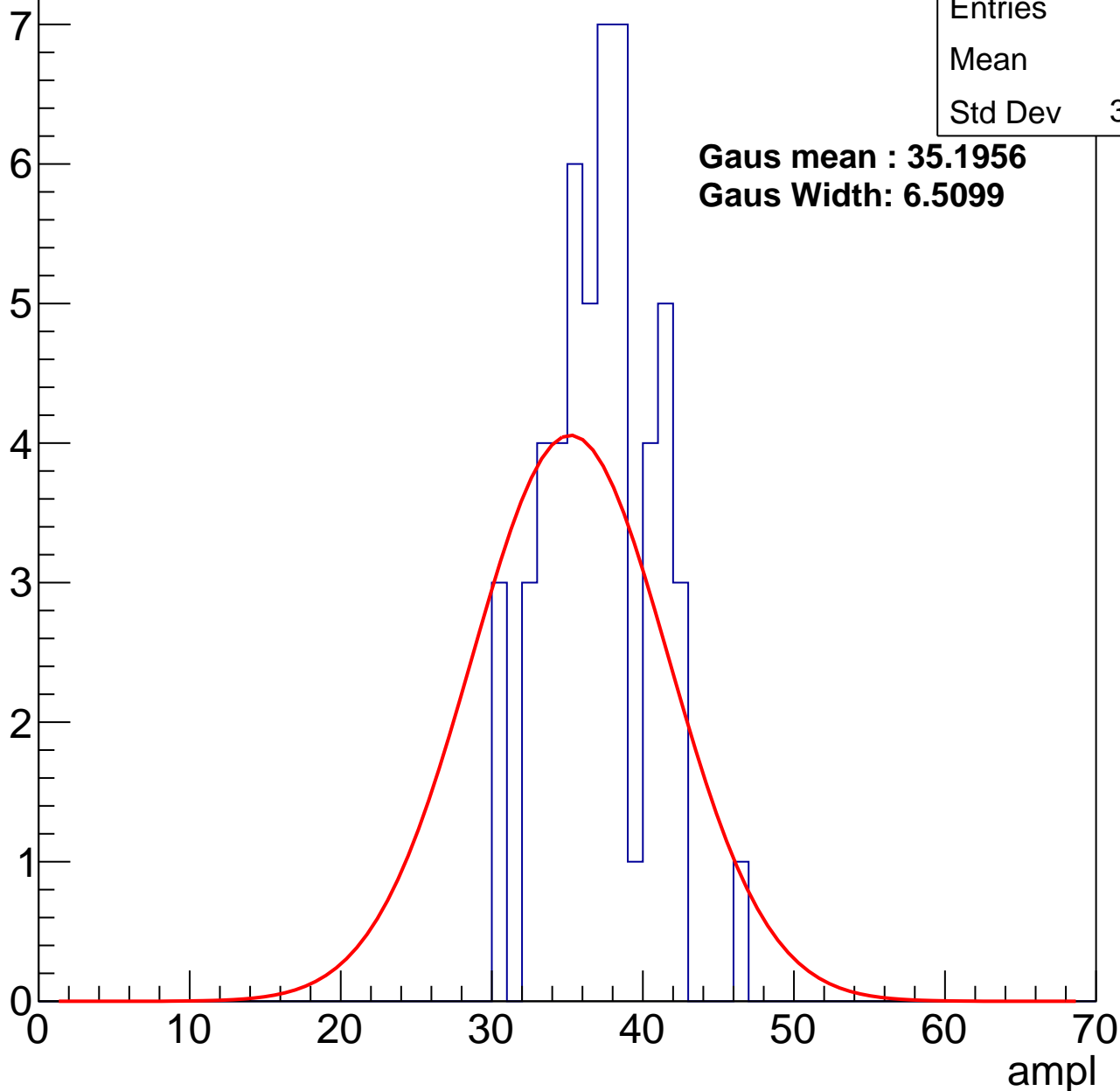
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	36.7
Std Dev	3.445

**Gaus mean : 35.1956**

**Gaus Width: 6.5099**



# B0L002S, U8-ch68, adc2

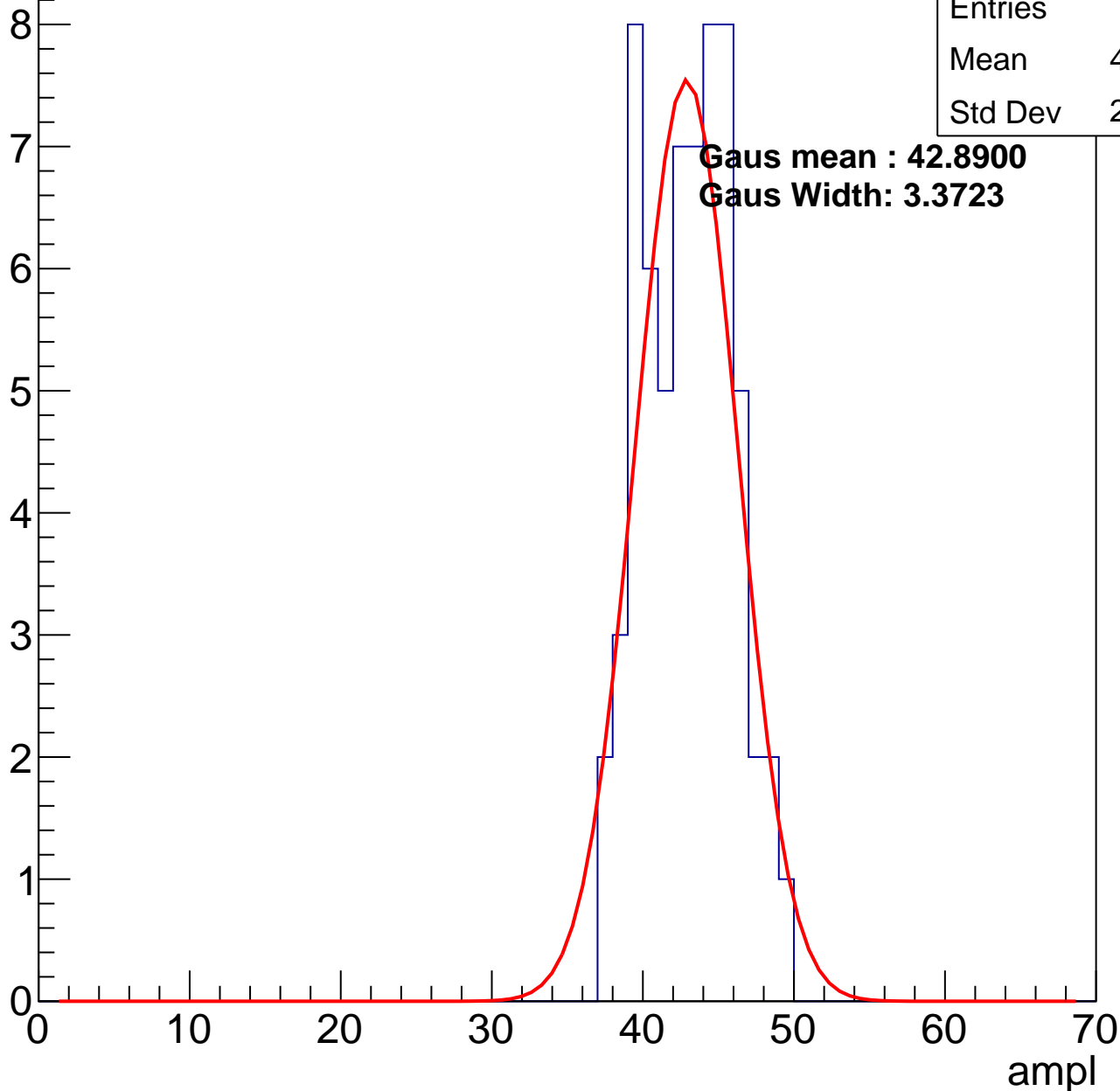
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.52
Std Dev	2.915

**Gaus mean : 42.8900**

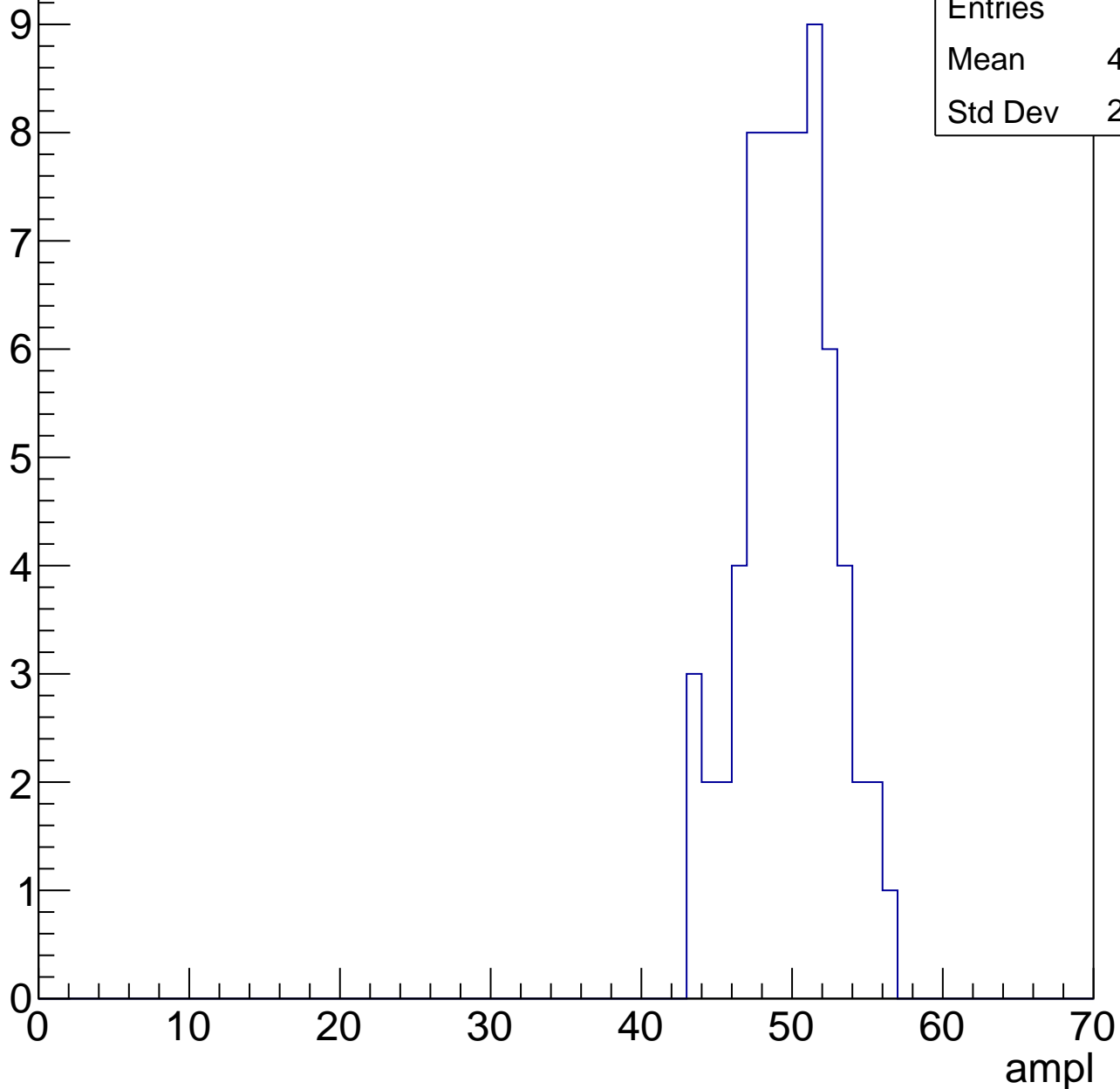
**Gaus Width: 3.3723**



# B0L002S, U8-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

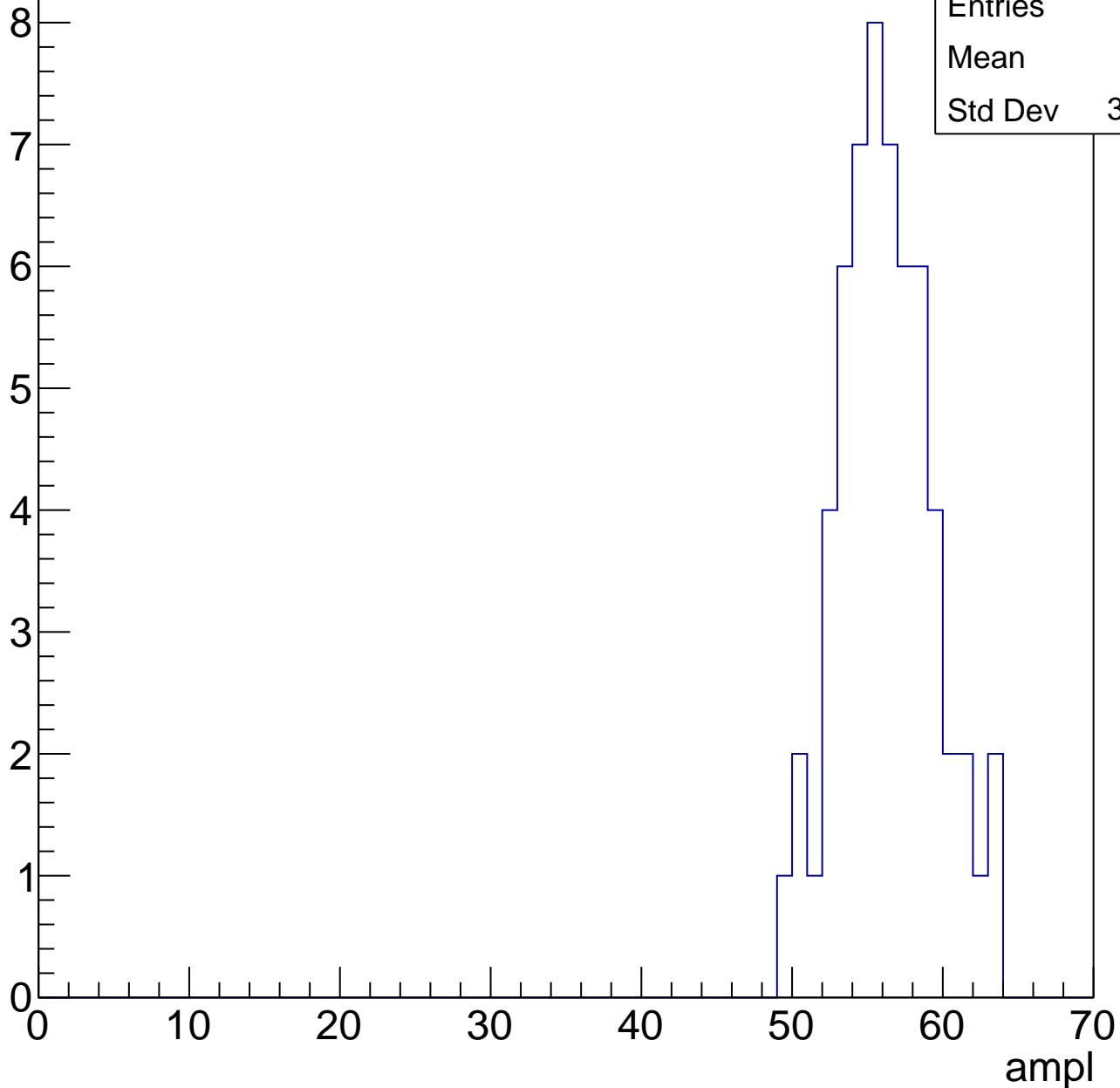


Entries	67
Mean	49.25
Std Dev	2.979

# B0L002S, U8-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

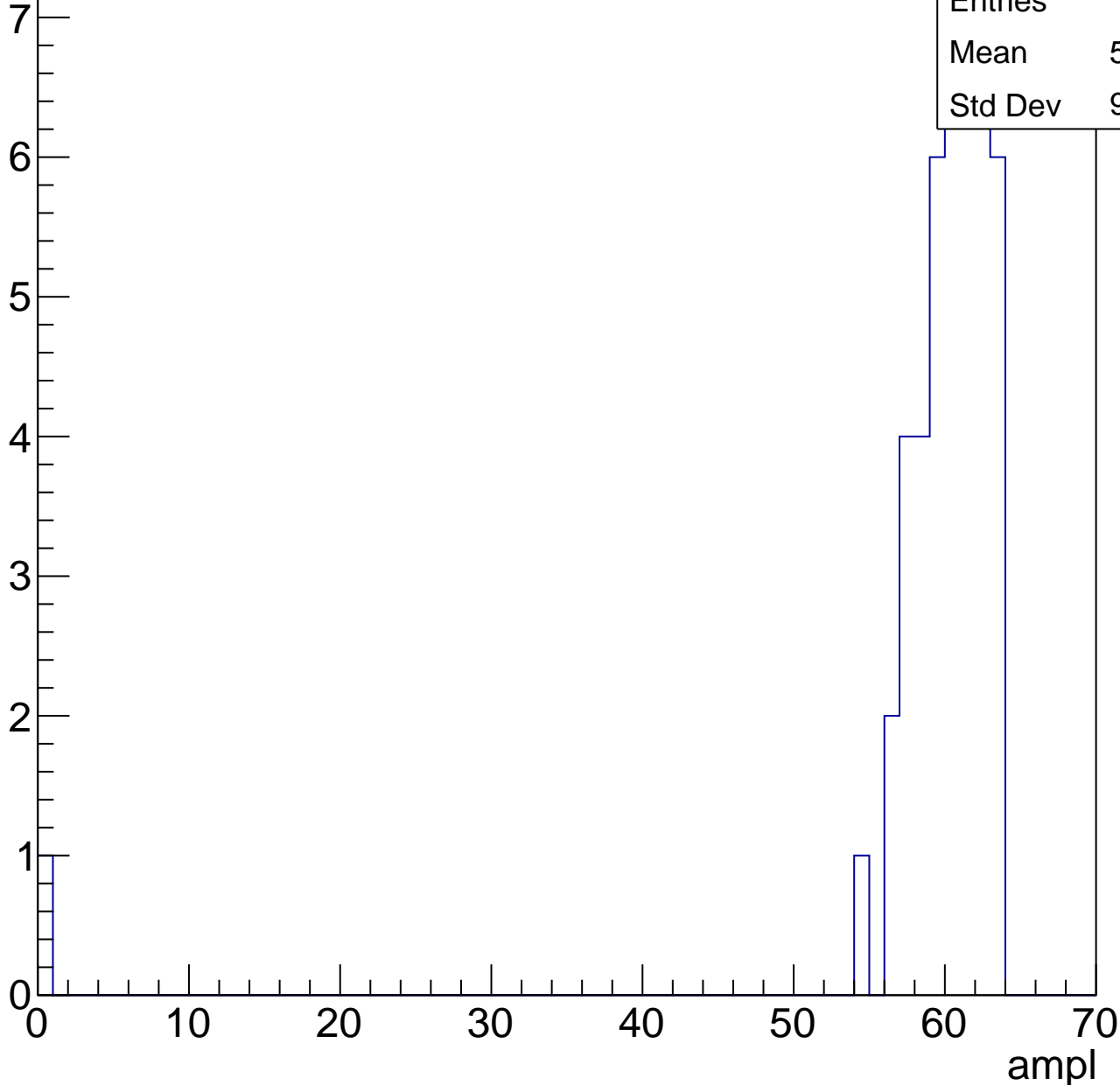


# B0L002S, U8-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

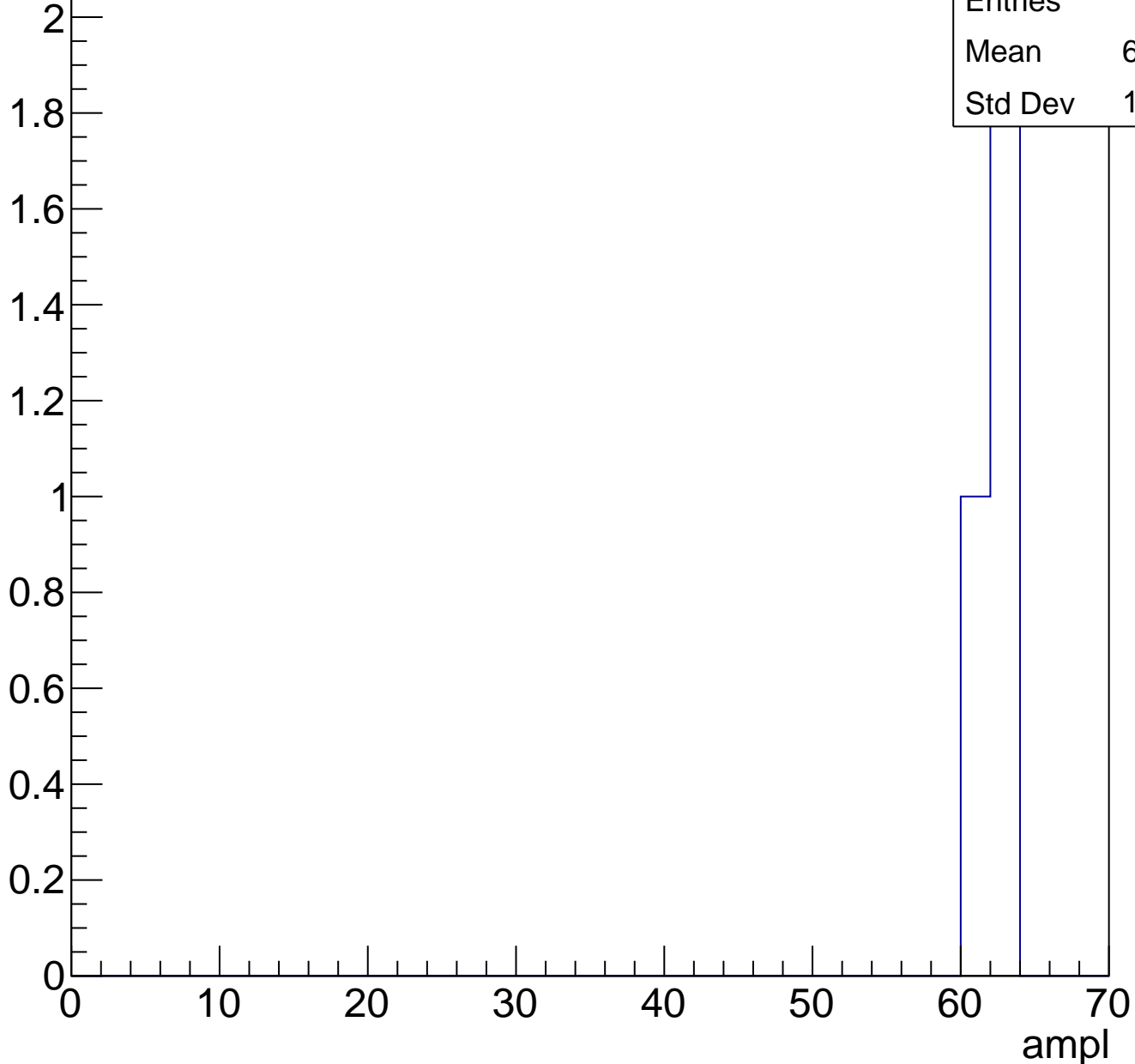
Entries	45
Mean	58.64
Std Dev	9.107



# B0L002S, U8-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch69, adc0

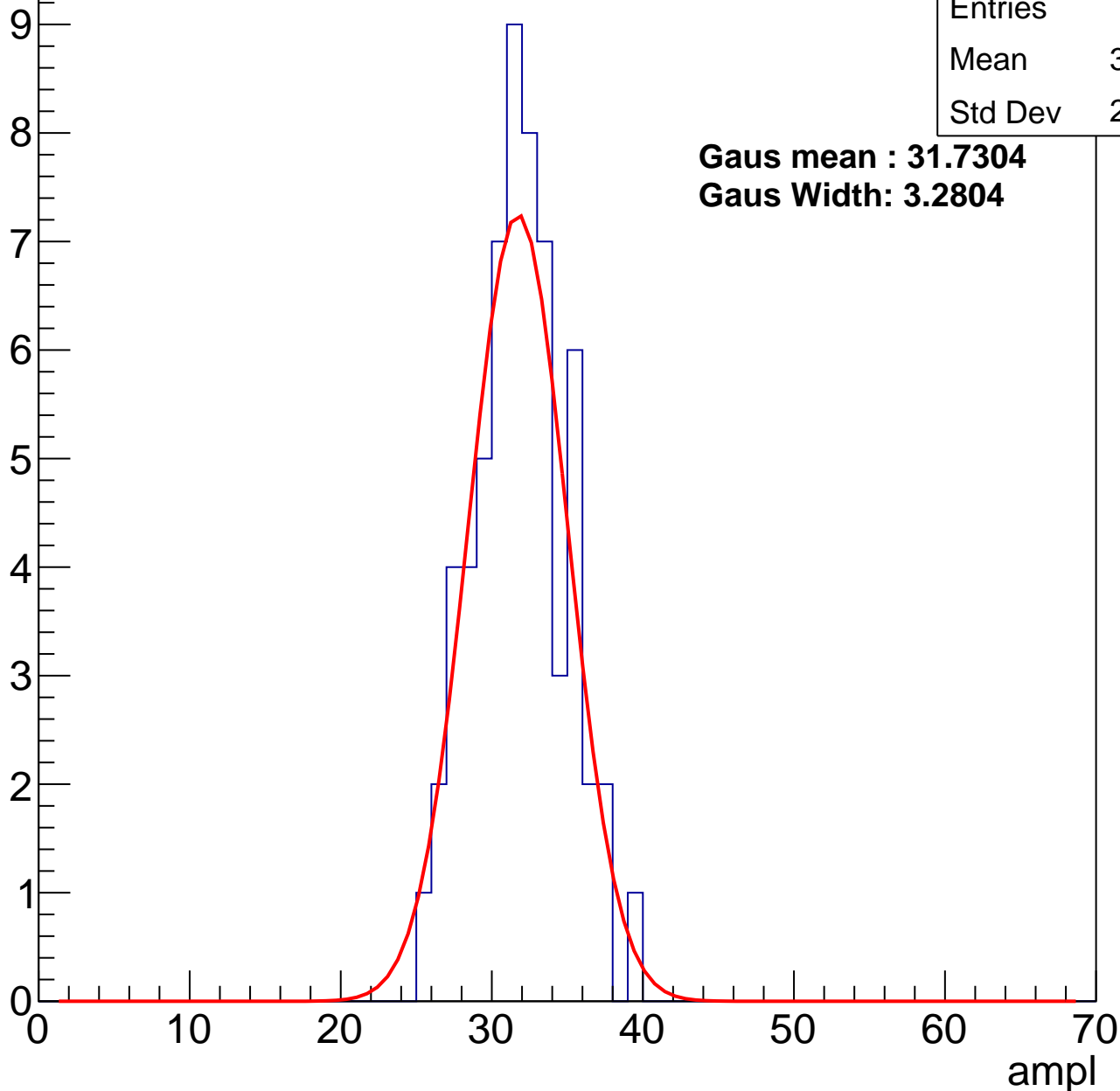
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.39
Std Dev	2.999

**Gaus mean : 31.7304**

**Gaus Width: 3.2804**



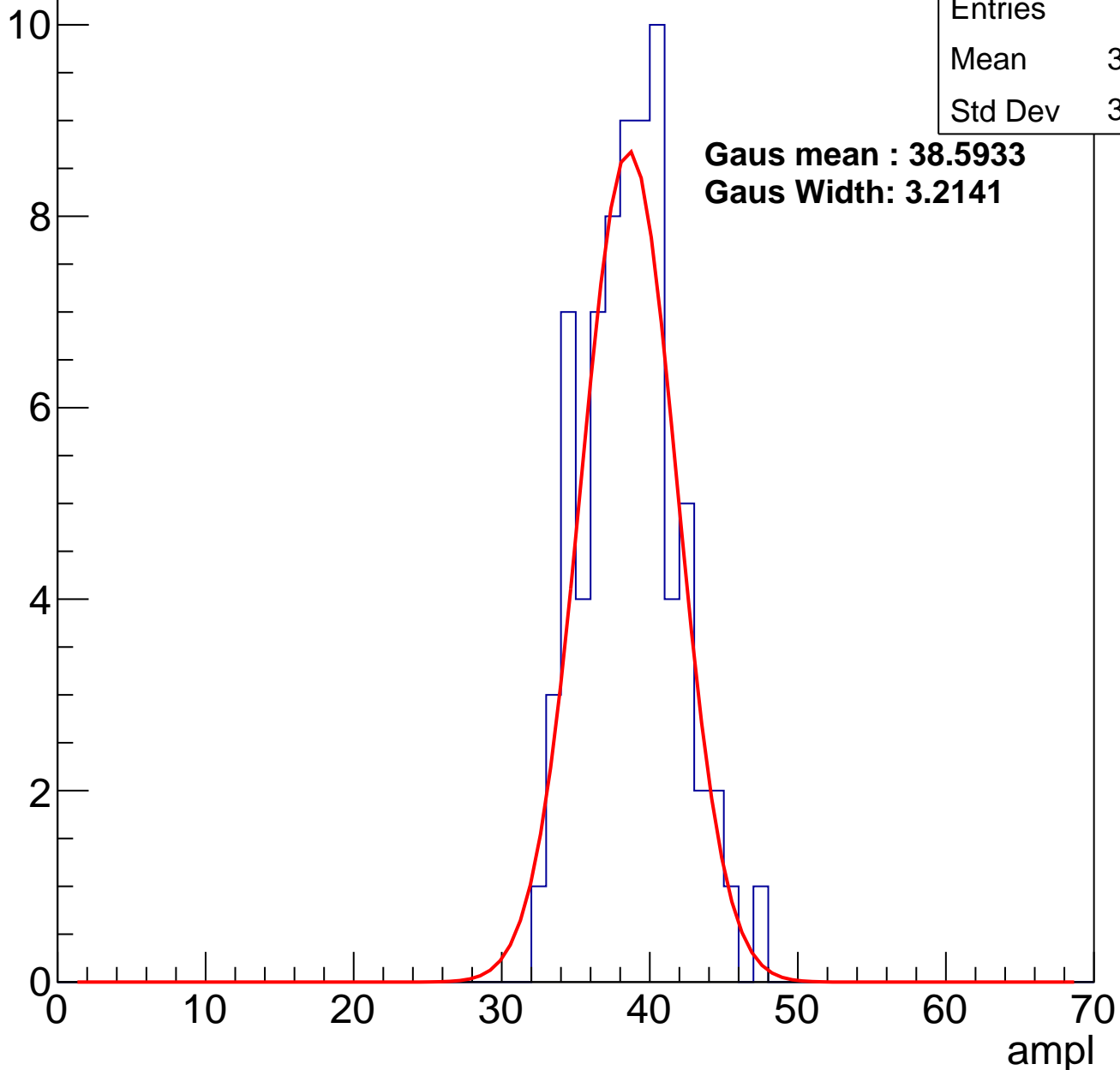
# B0L002S, U8-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	38.22
Std Dev	3.115

**Gaus mean : 38.5933**  
**Gaus Width: 3.2141**

Entry



# B0L002S, U8-ch69, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	45.5
Std Dev	3.235

**Gaus mean : 45.4483**

**Gaus Width: 3.7523**

10

8

6

4

2

0

0

10

20

30

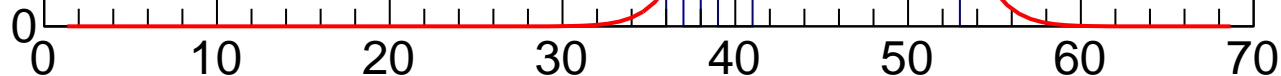
40

50

60

70

ampl

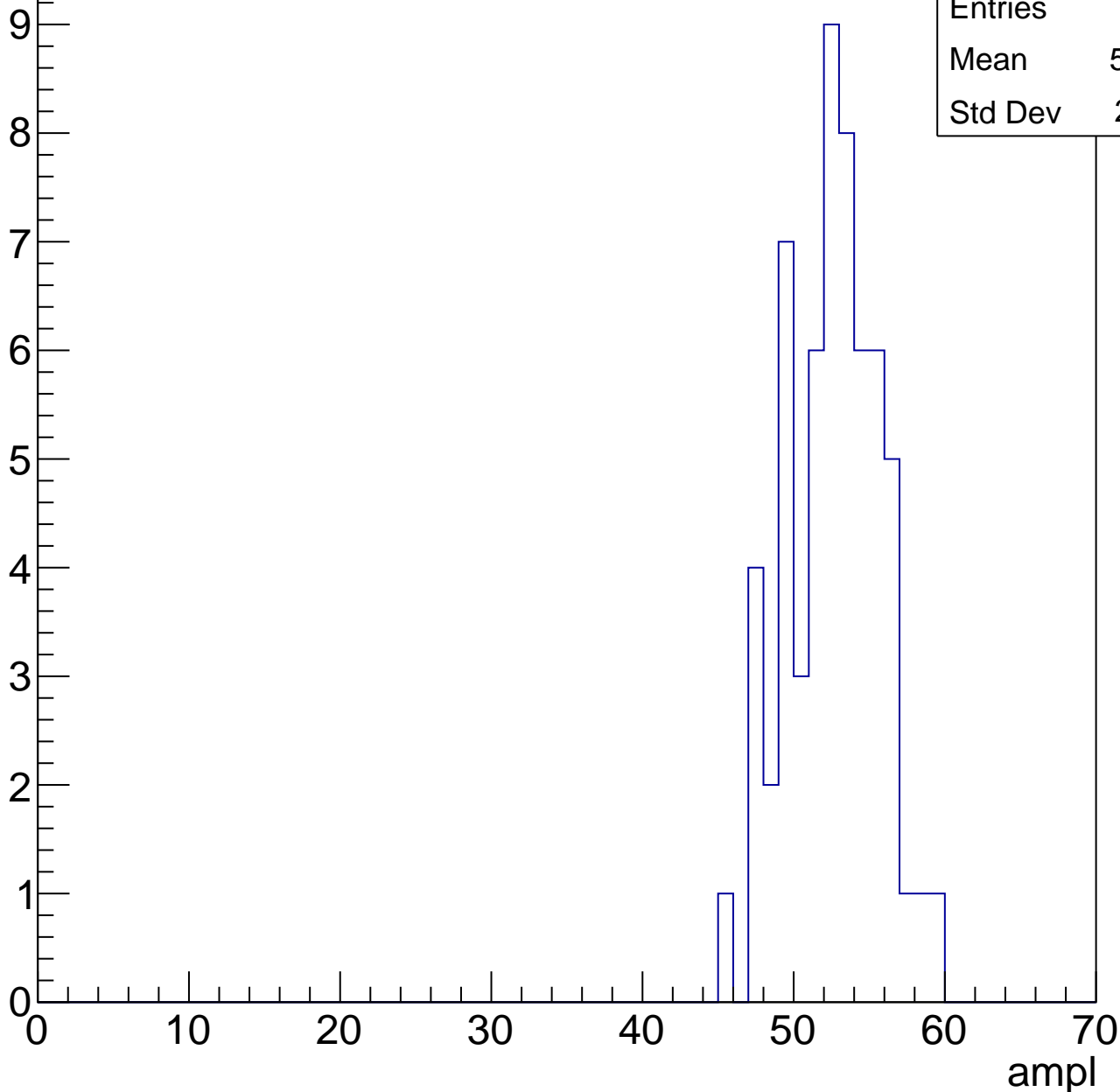


# B0L002S, U8-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

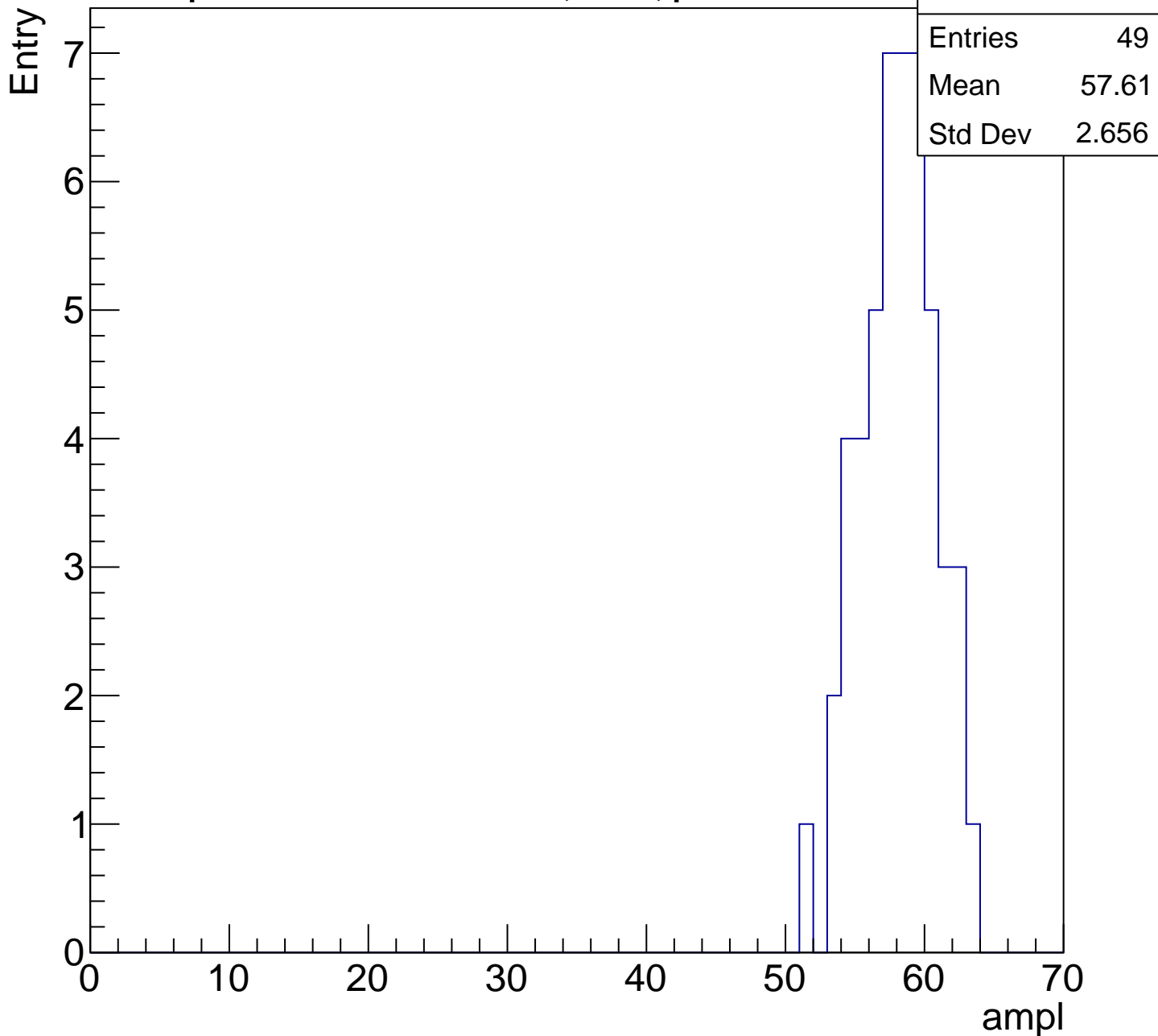
Entry

Entries	60
Mean	52.13
Std Dev	2.991



# B0L002S, U8-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

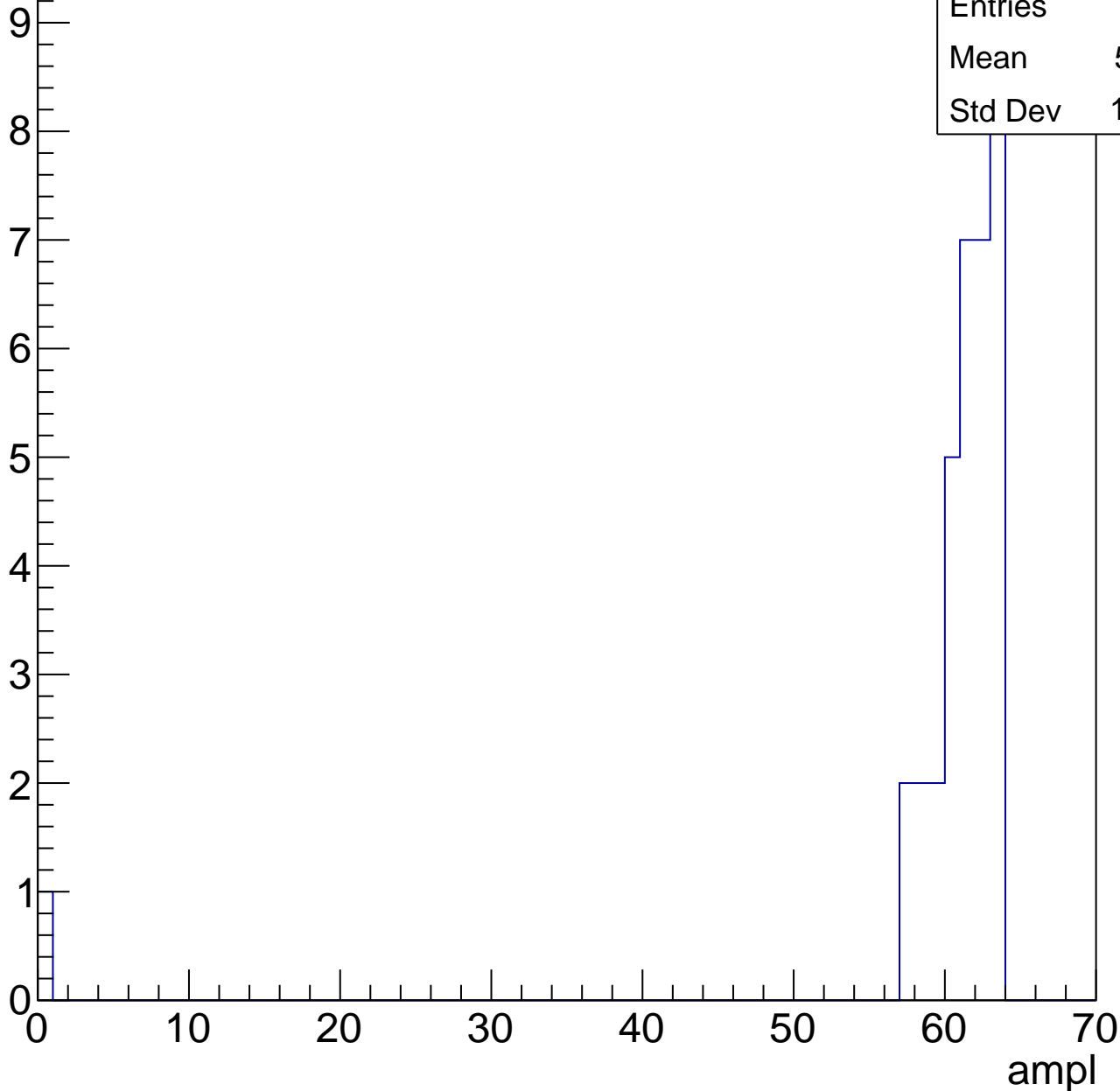


# B0L002S, U8-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.31
Std Dev	10.32



# B0L002S, U8-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch70, adc0

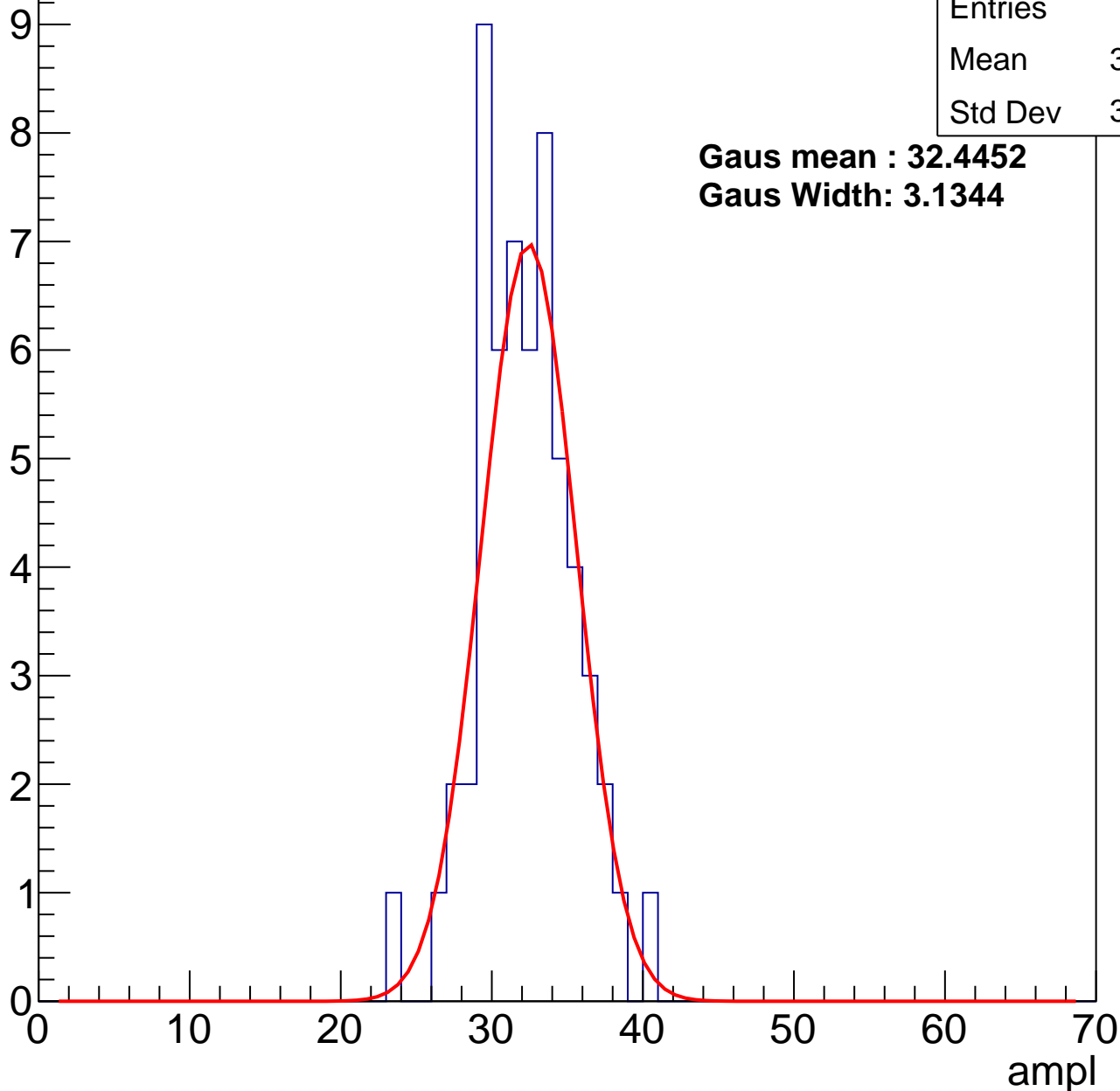
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.78
Std Dev	3.135

**Gaus mean : 32.4452**

**Gaus Width: 3.1344**



# B0L002S, U8-ch70, adc1

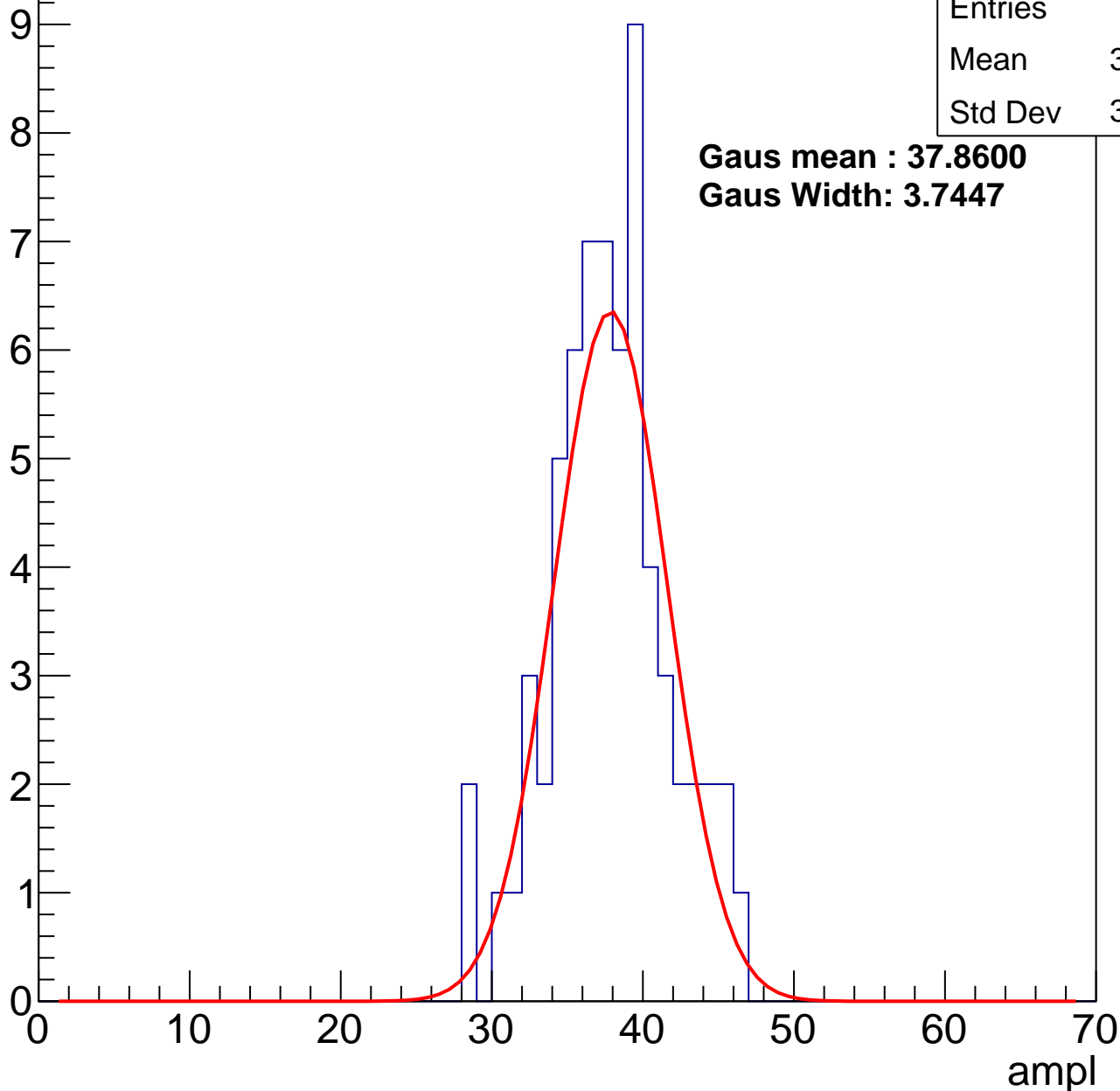
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.32
Std Dev	3.879

**Gaus mean : 37.8600**

**Gaus Width: 3.7447**



# B0L002S, U8-ch70, adc2

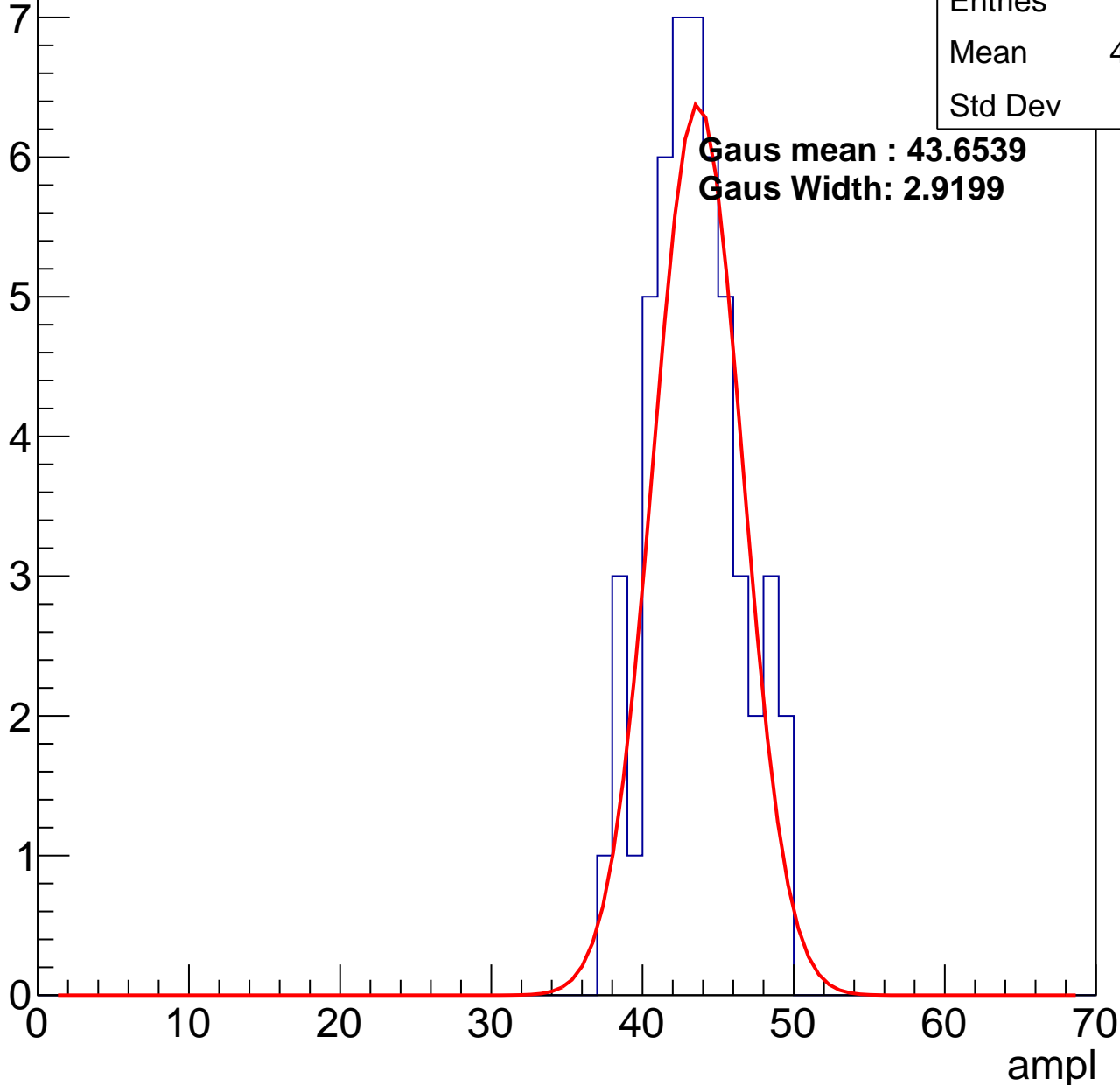
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.02
Std Dev	2.92

**Gaus mean : 43.6539**

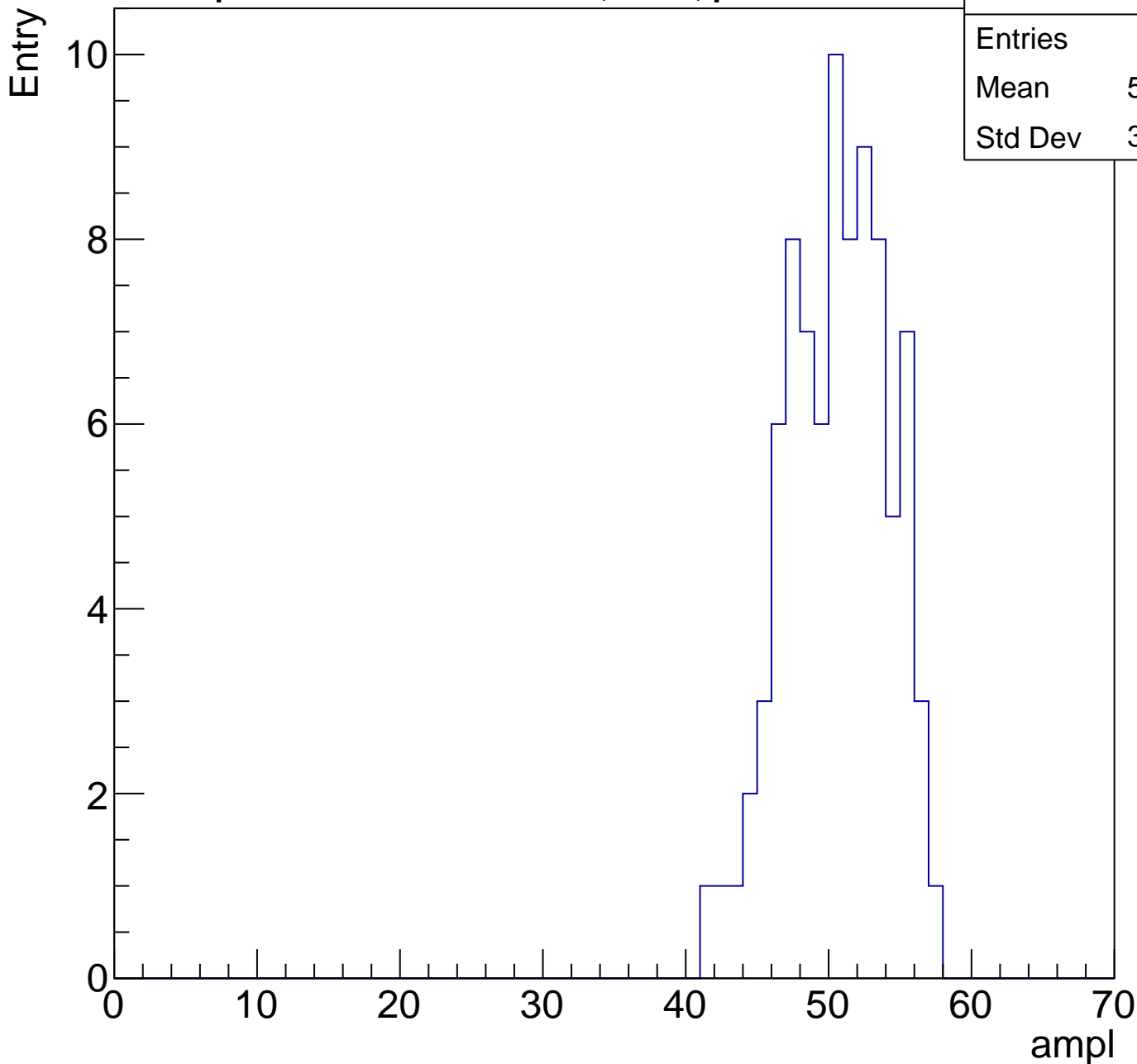
**Gaus Width: 2.9199**



# B0L002S, U8-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	86
Mean	50.13
Std Dev	3.527

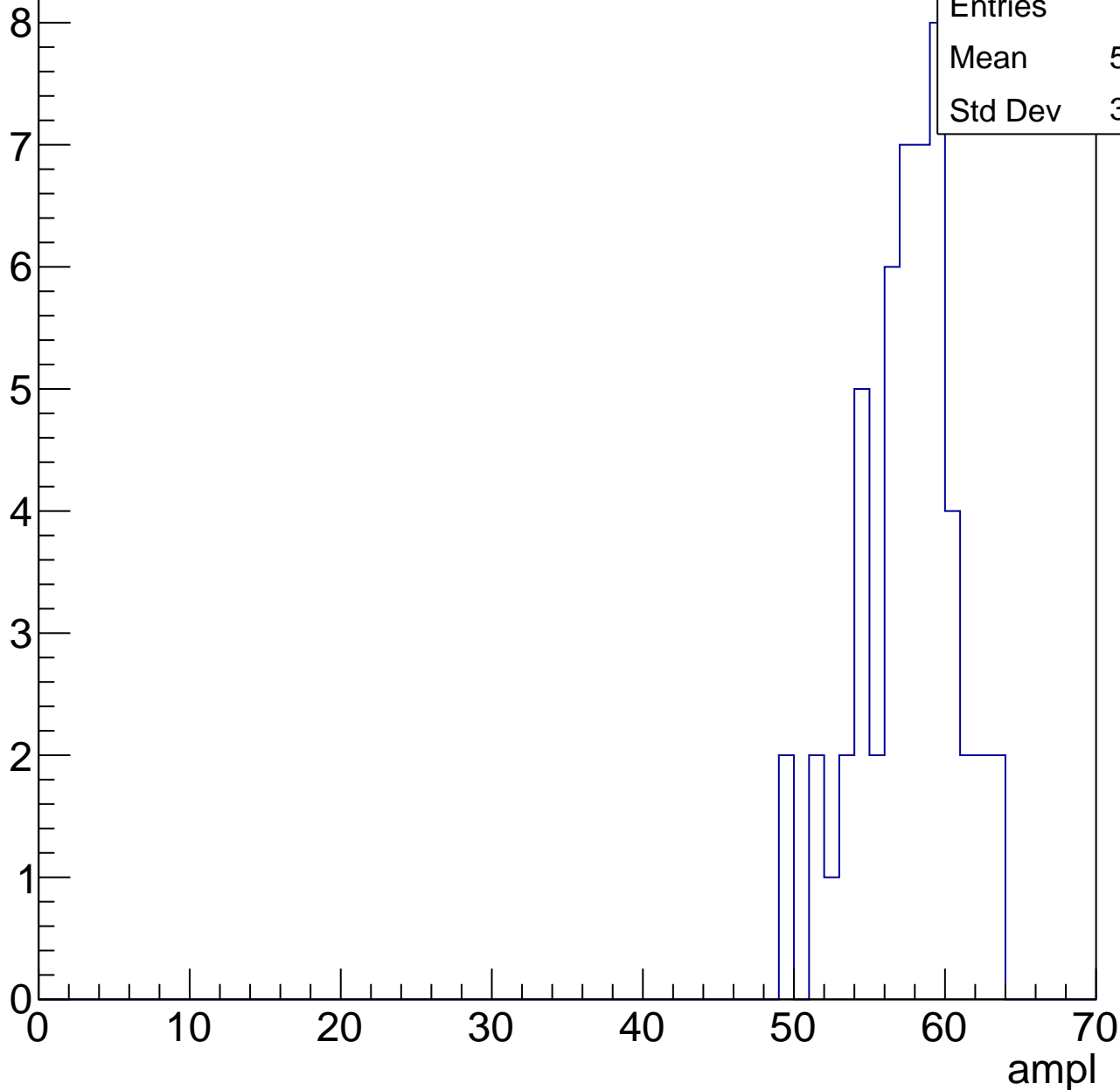


# B0L002S, U8-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.98
Std Dev	3.237

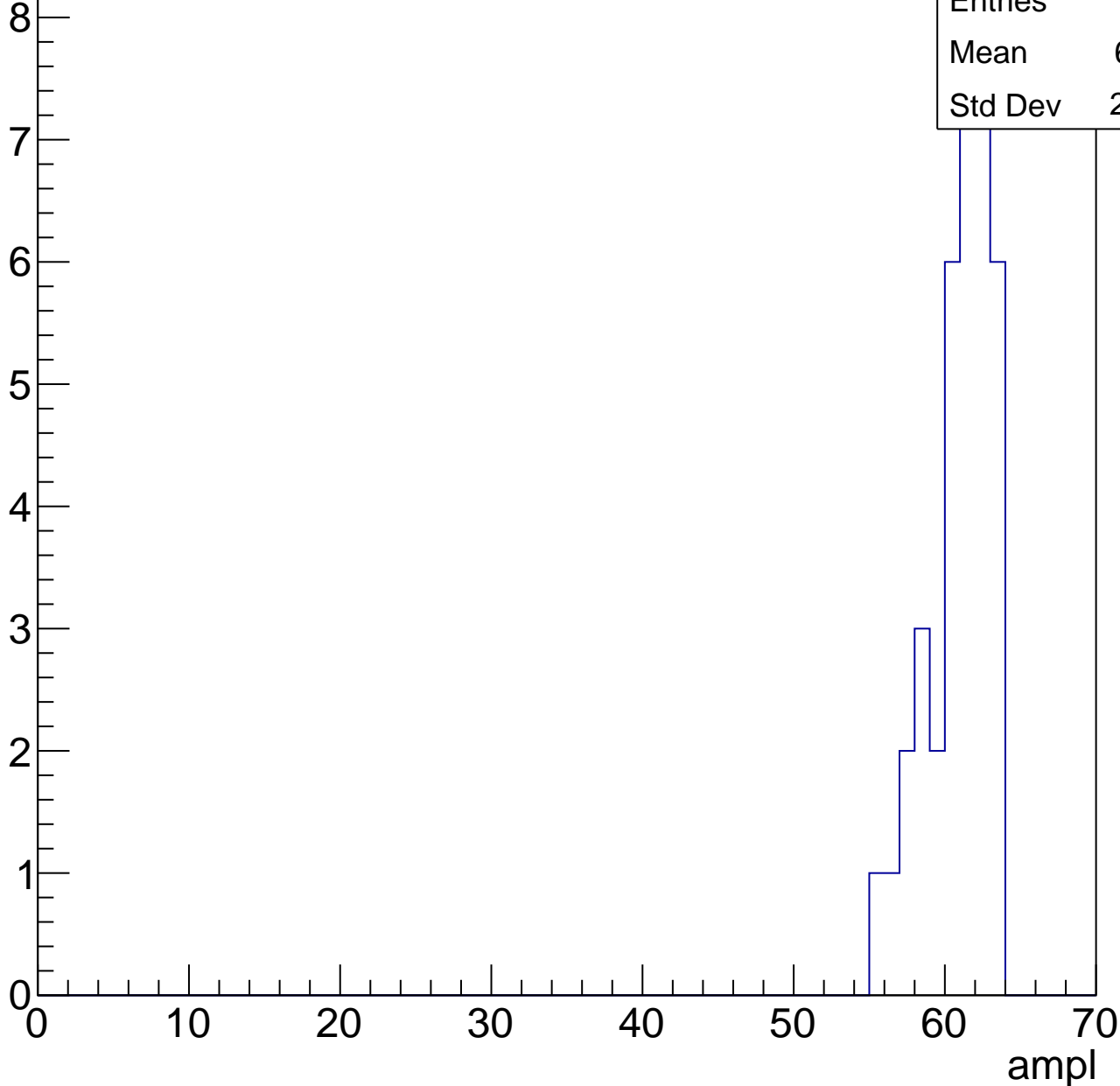


# B0L002S, U8-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.51
Std Dev	2.062



# B0L002S, U8-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch71, adc0

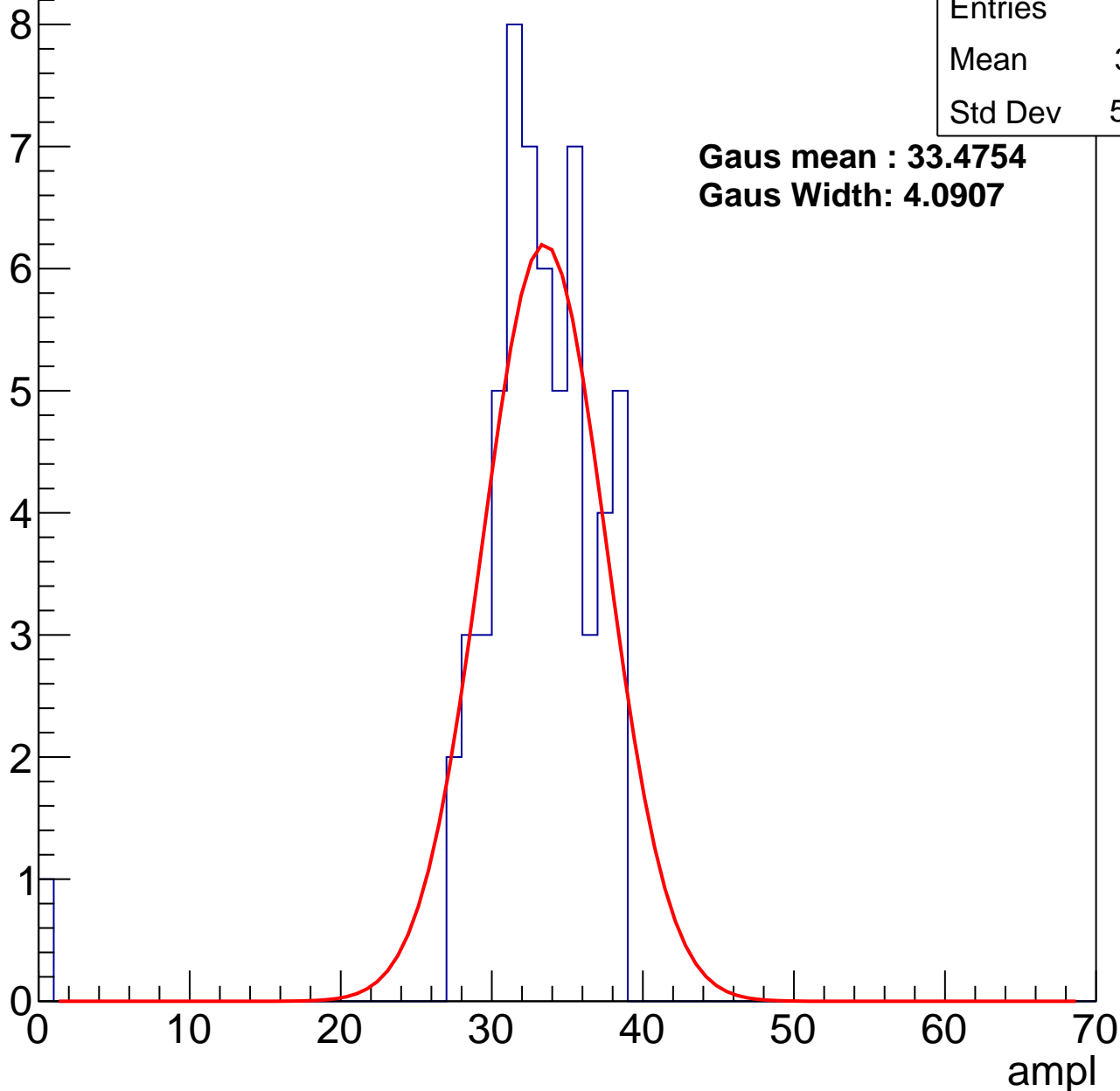
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	32.31
Std Dev	5.189

**Gaus mean : 33.4754**

**Gaus Width: 4.0907**



# B0L002S, U8-ch71, adc1

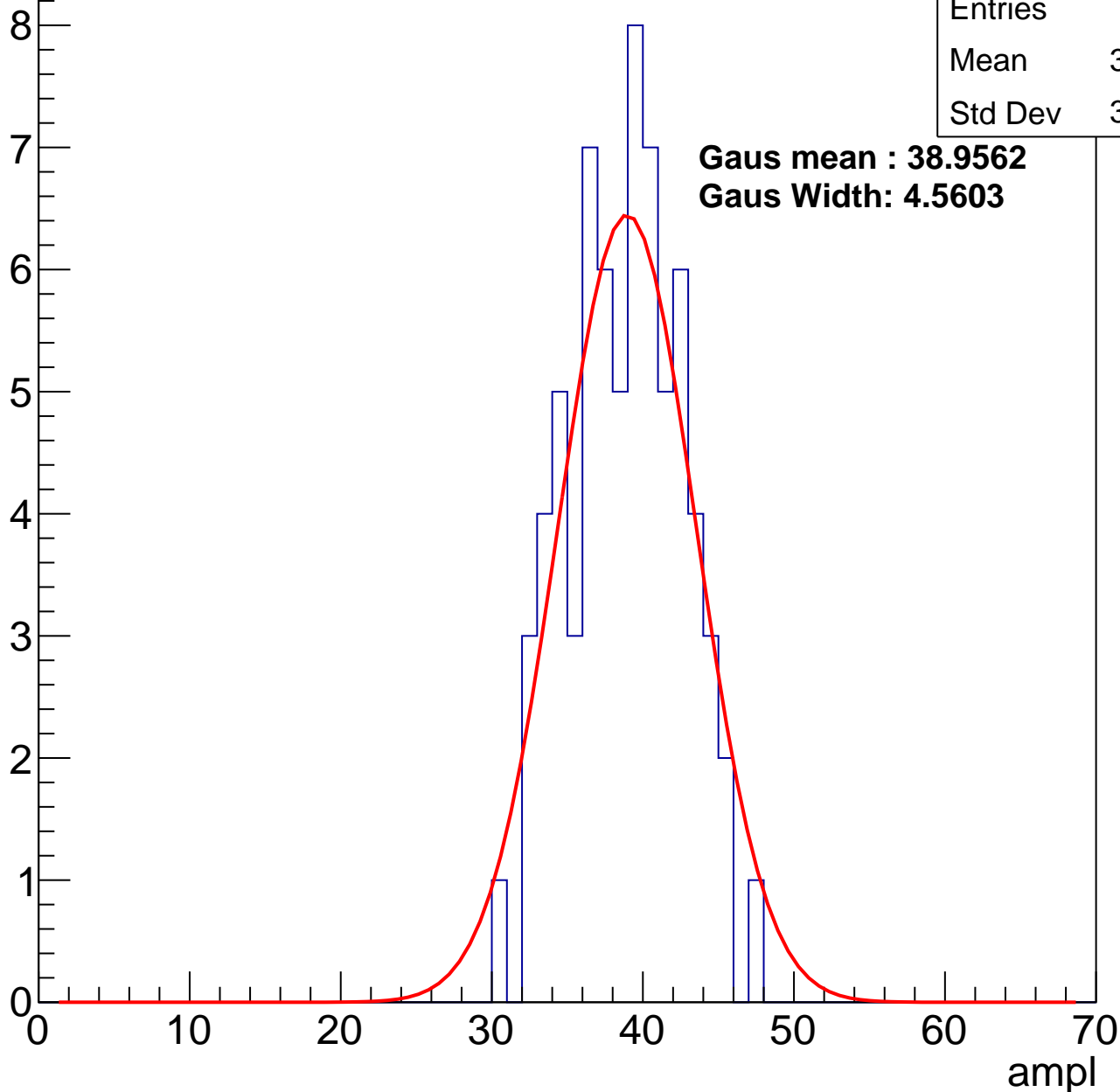
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.39
Std Dev	3.716

**Gaus mean : 38.9562**

**Gaus Width: 4.5603**



# B0L002S, U8-ch71, adc2

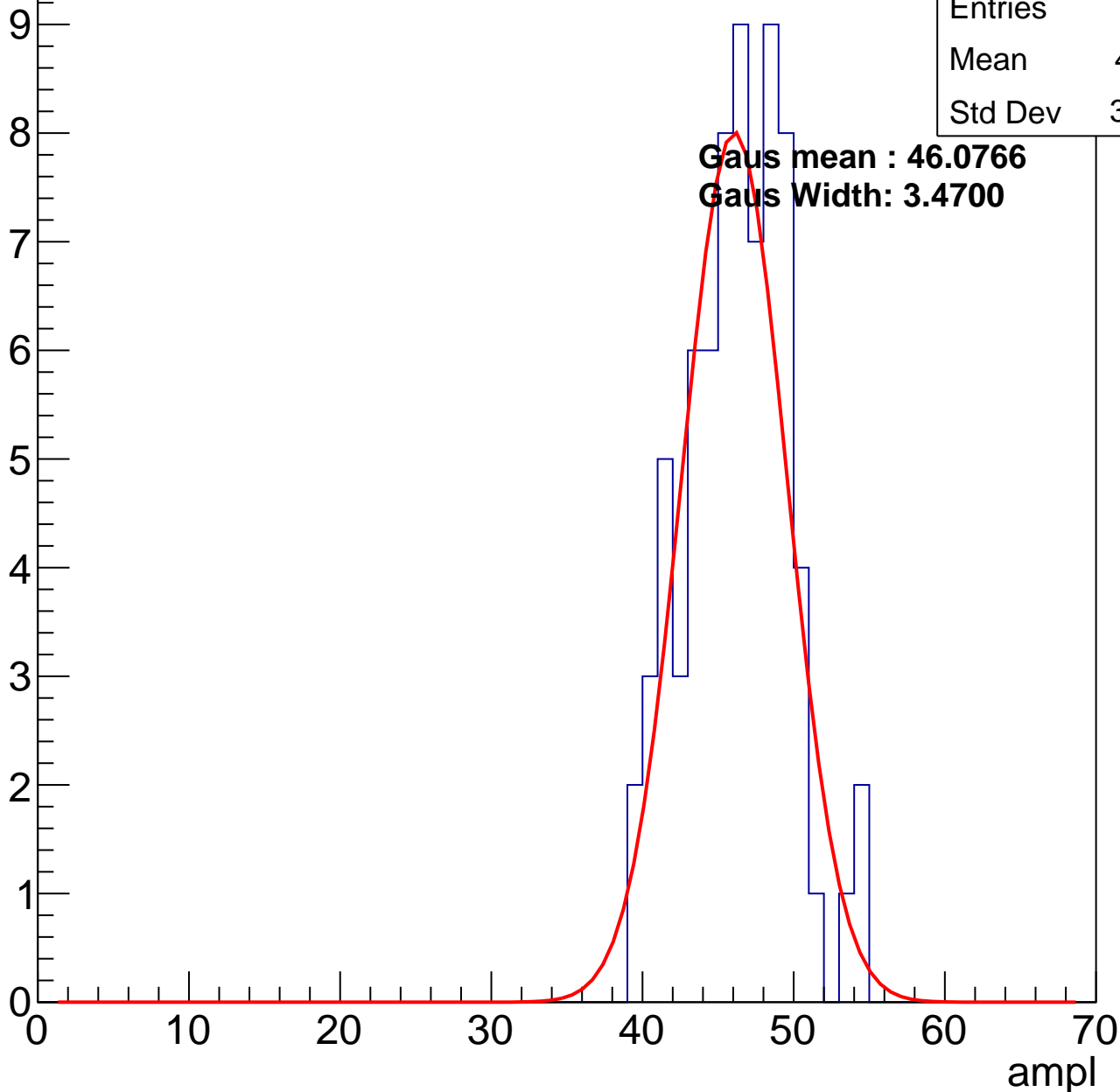
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	45.81
Std Dev	3.376

**Gaus mean : 46.0766**

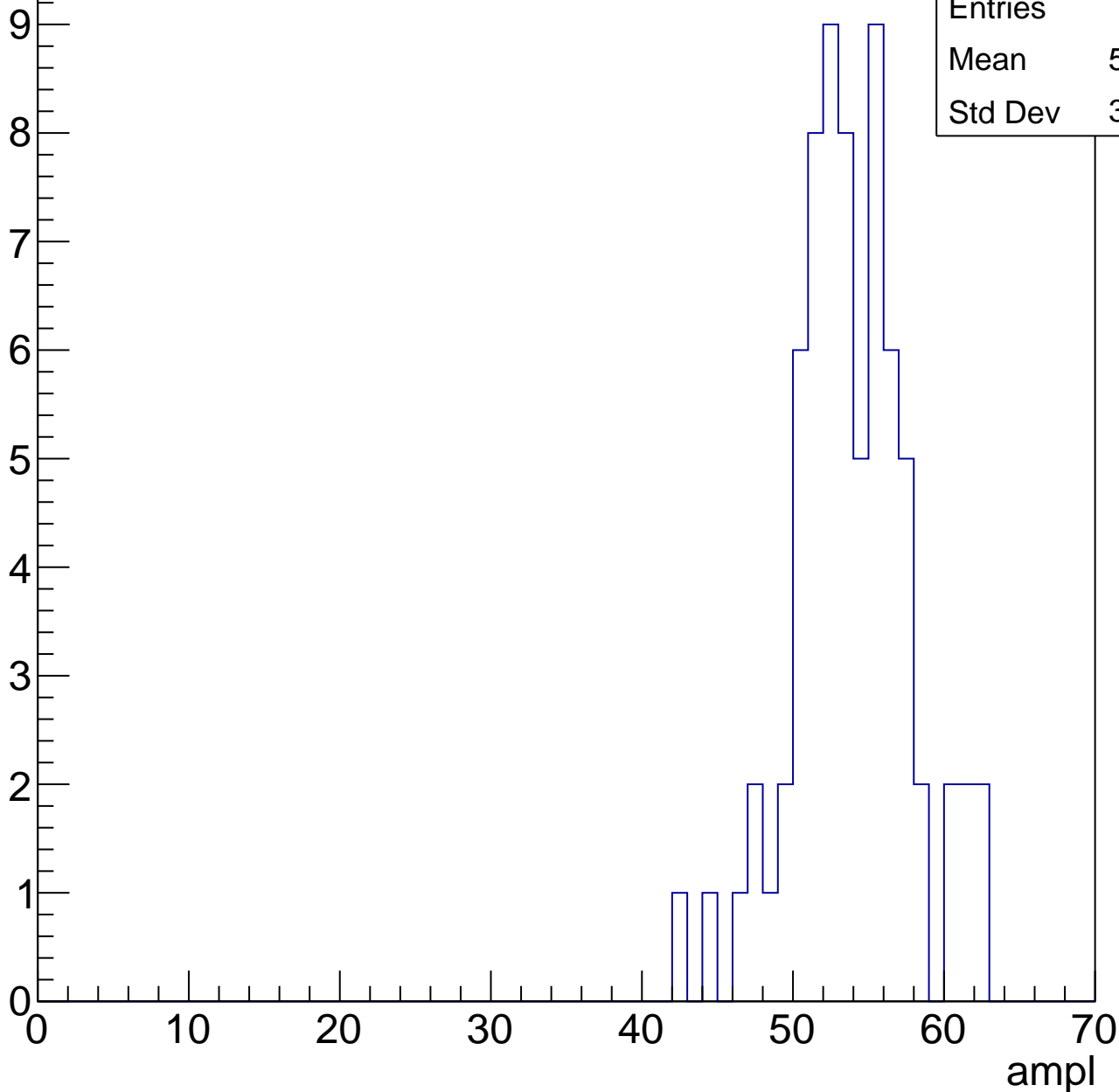
**Gaus Width: 3.4700**



# B0L002S, U8-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



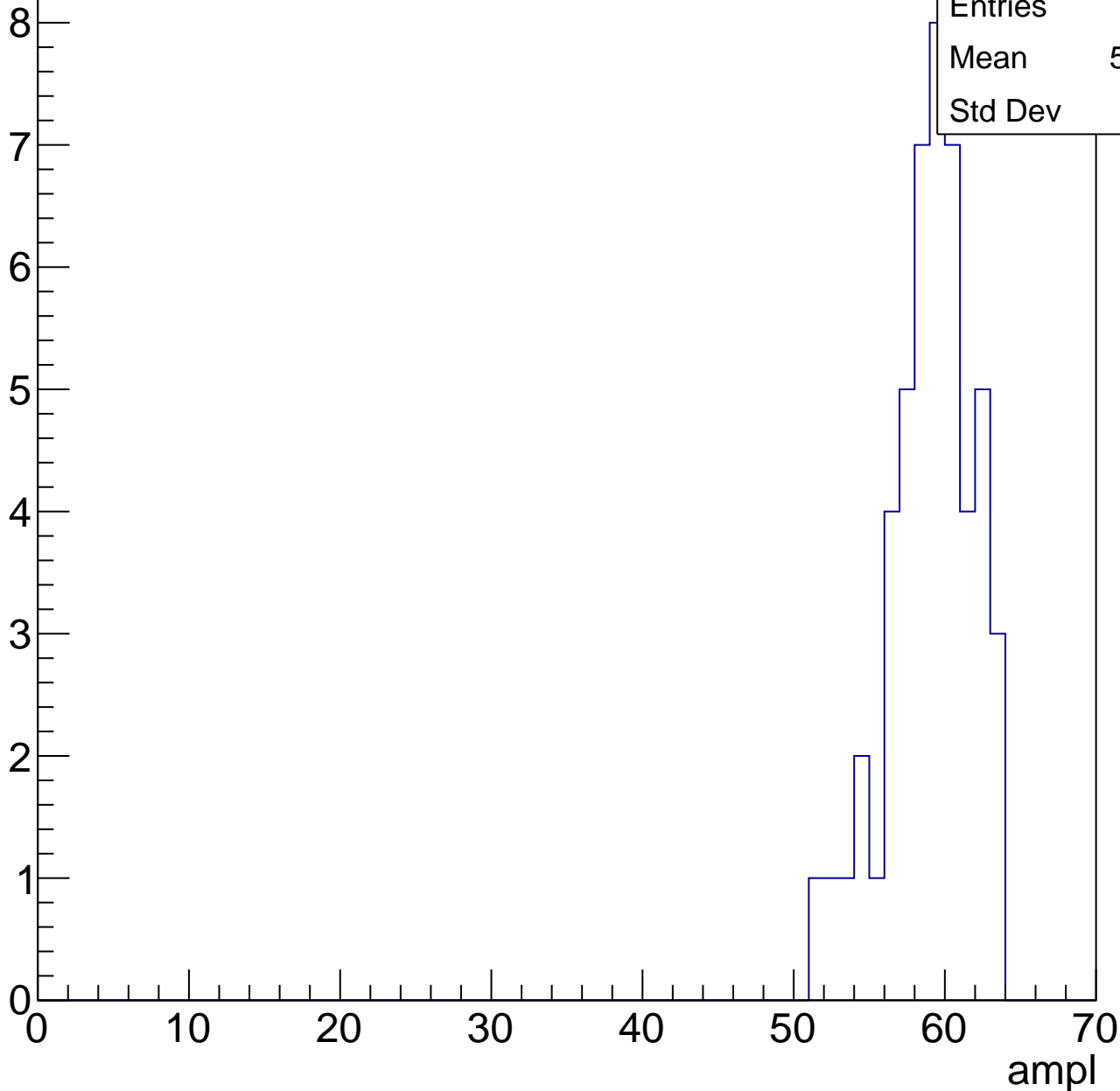
Entries	72
Mean	53.33
Std Dev	3.873

# B0L002S, U8-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.55
Std Dev	2.8

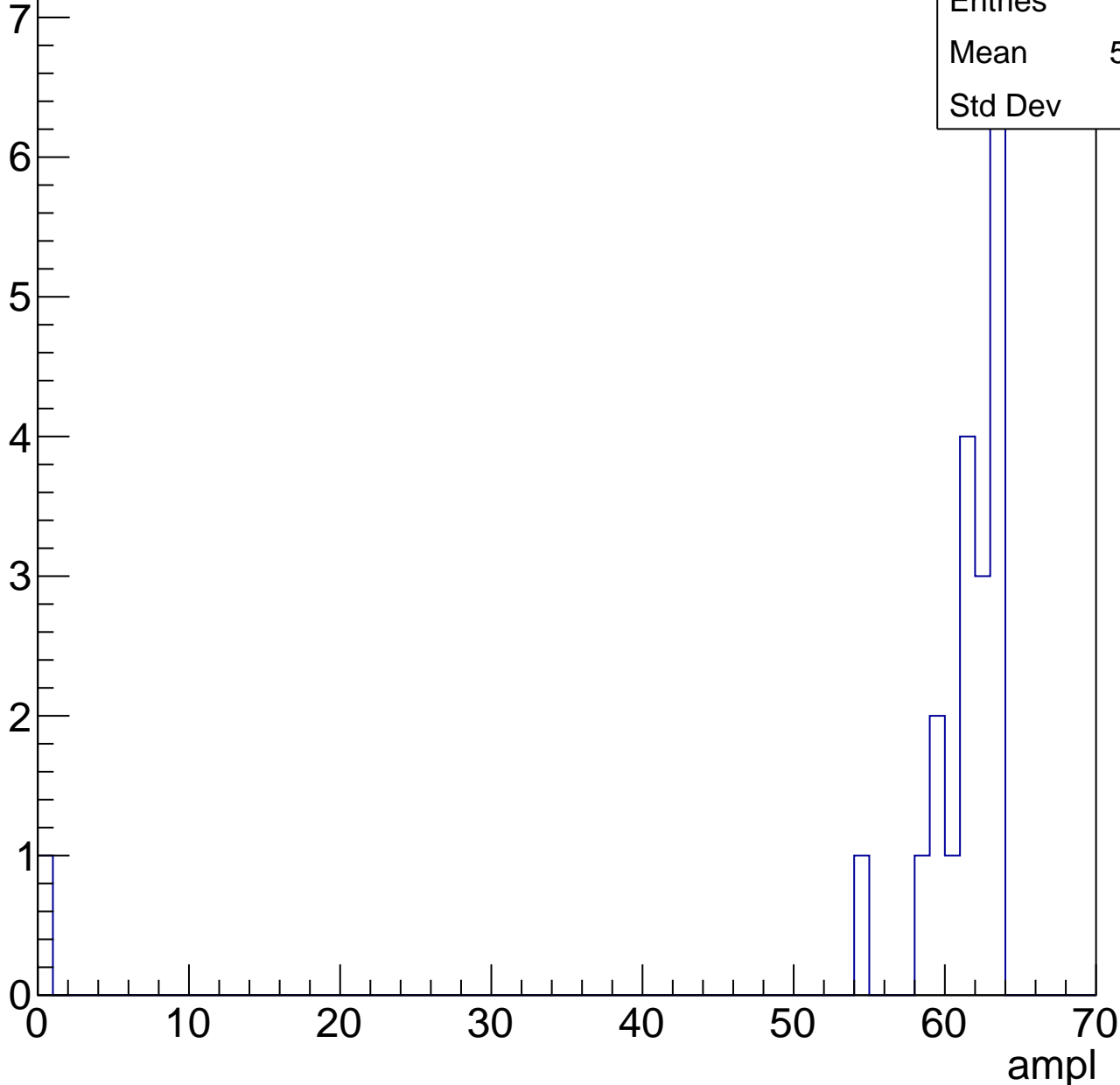


# B0L002S, U8-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.05
Std Dev	13.5



# B0L002S, U8-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch72, adc0

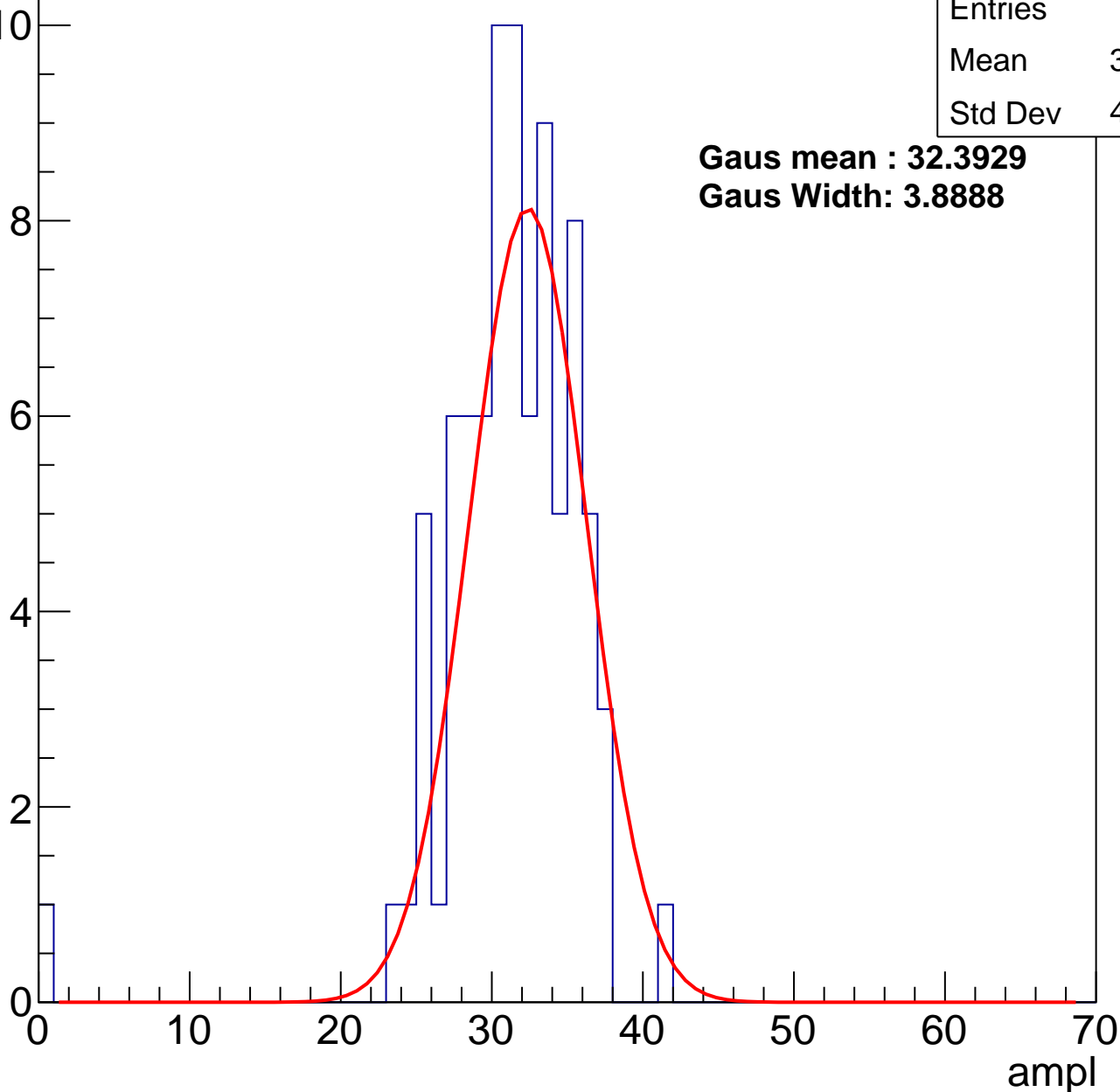
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	84
Mean	30.75
Std Dev	4.887

**Gaus mean : 32.3929**

**Gaus Width: 3.8888**



# B0L002S, U8-ch72, adc1

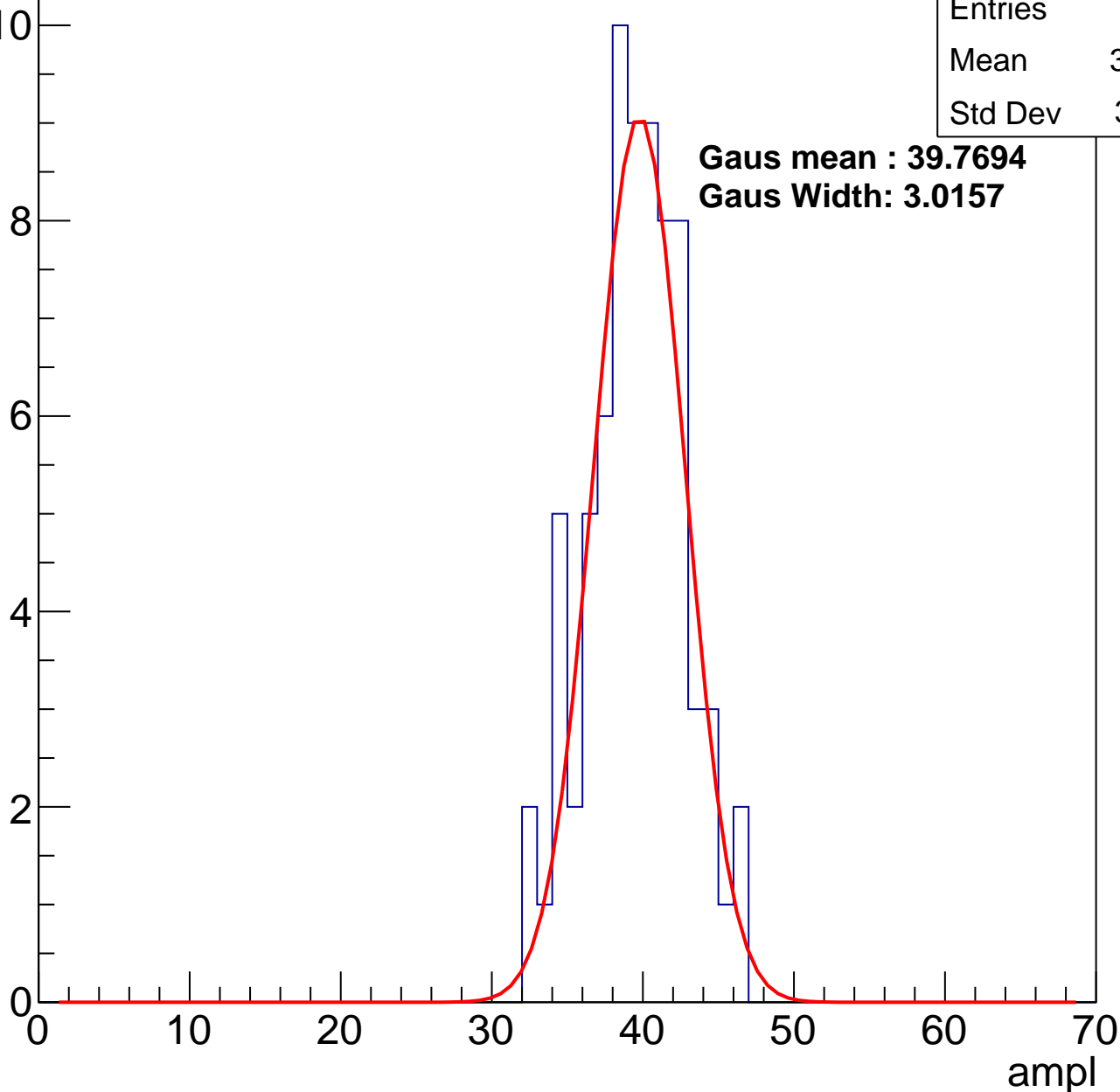
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	39.08
Std Dev	3.161

**Gaus mean : 39.7694**

**Gaus Width: 3.0157**



# B0L002S, U8-ch72, adc2

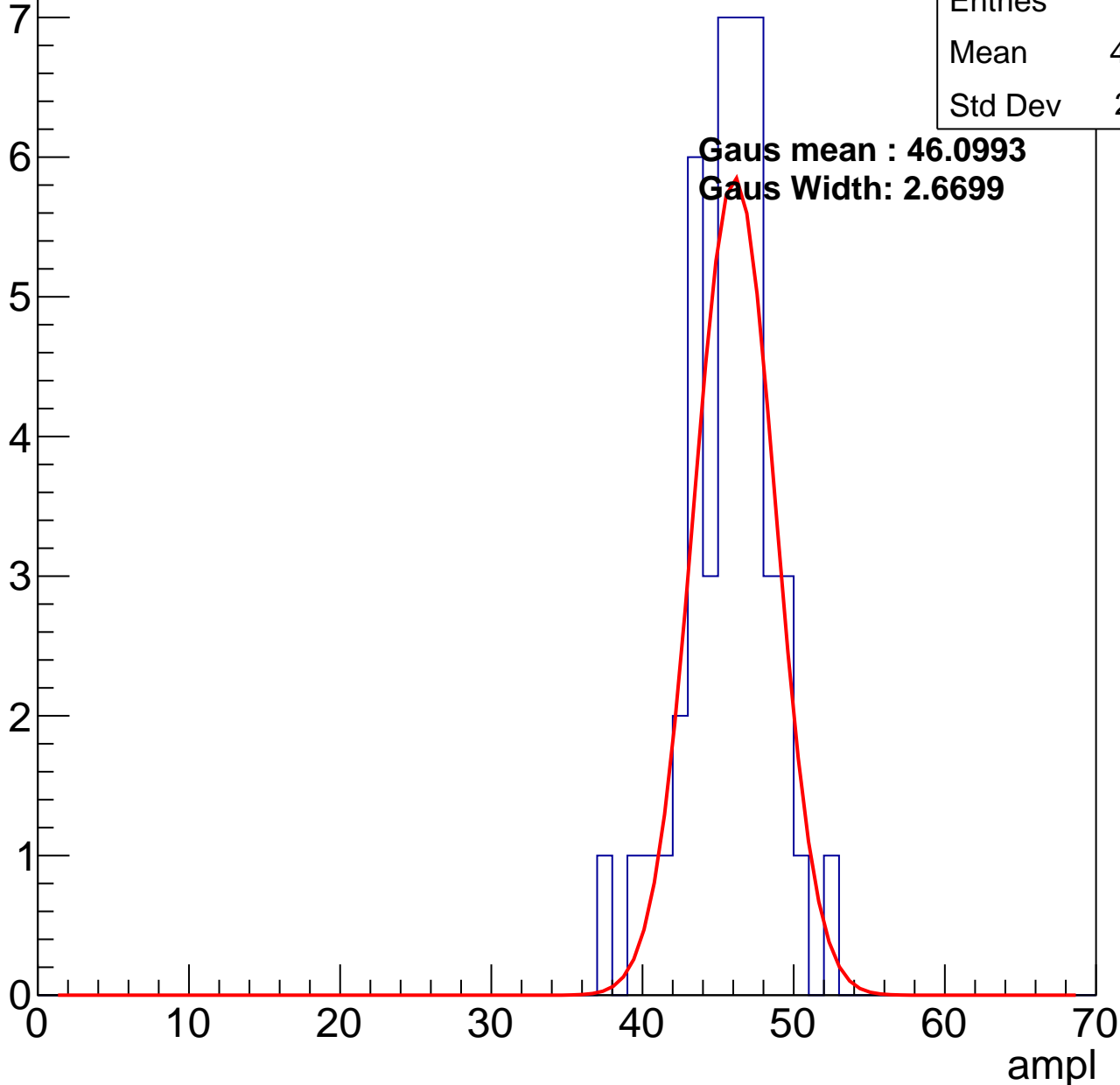
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	45.23
Std Dev	2.891

**Gaus mean : 46.0993**

**Gaus Width: 2.6699**

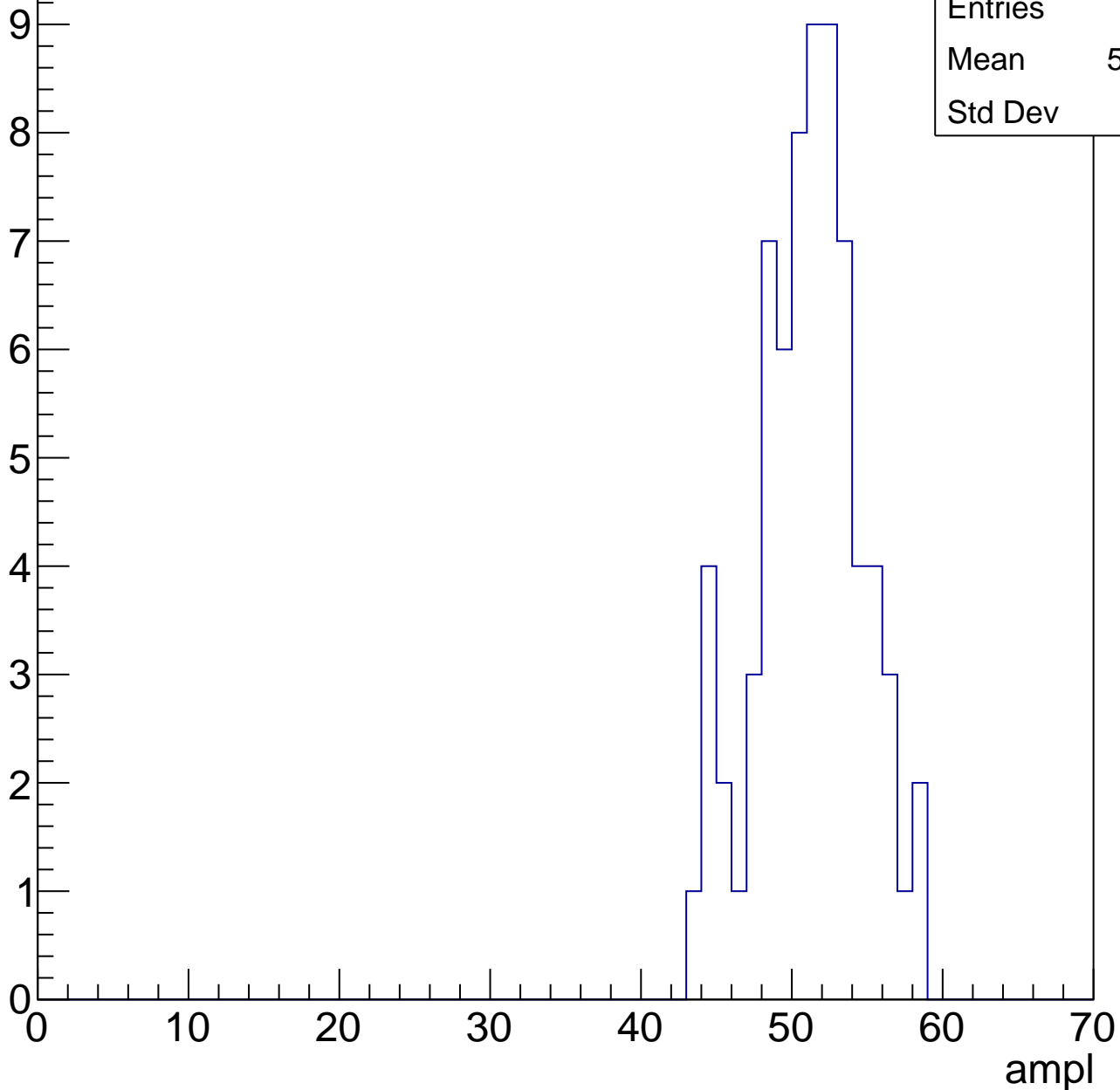


# B0L002S, U8-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

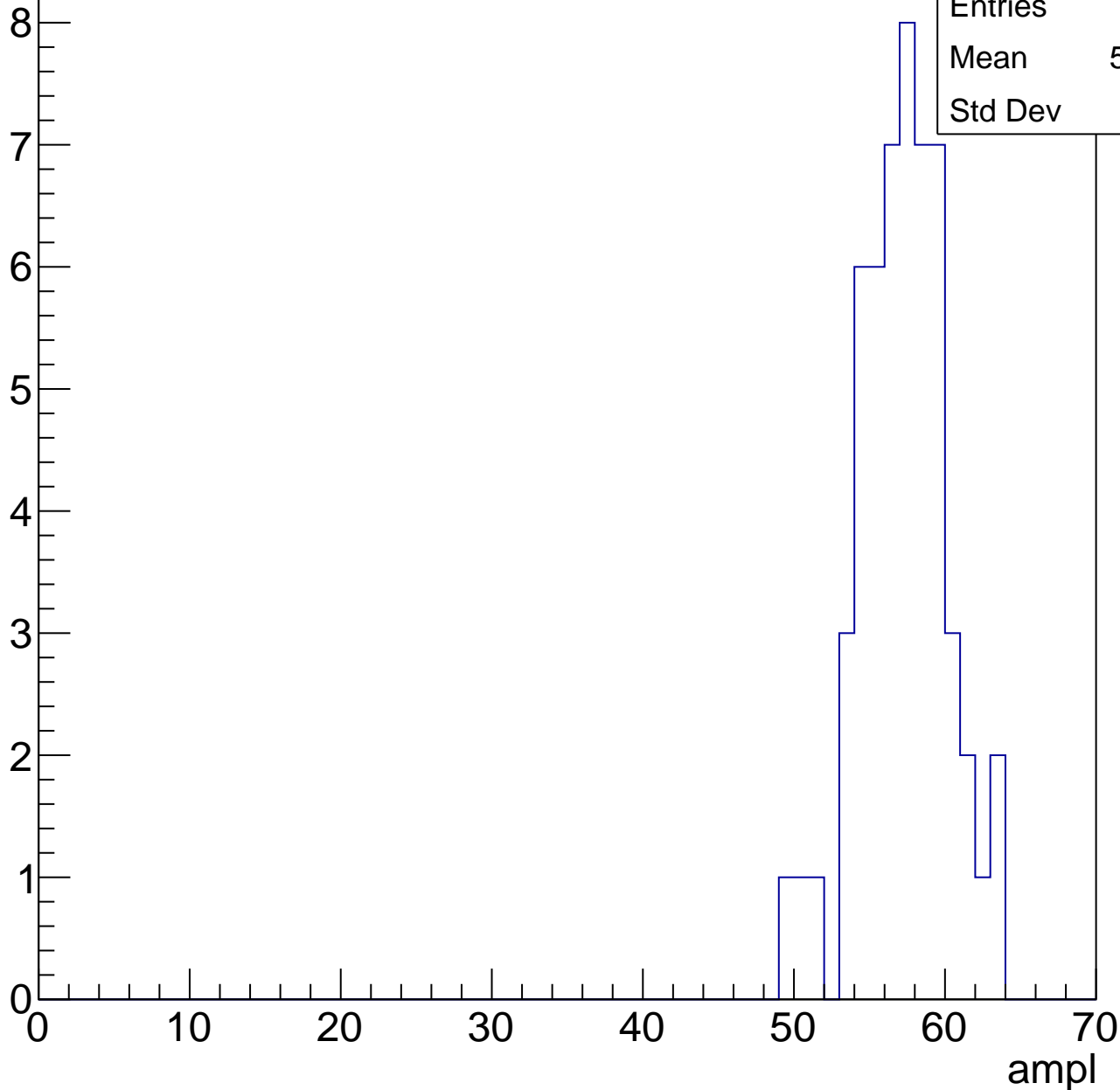
Entries	71
Mean	50.72
Std Dev	3.44



# B0L002S, U8-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



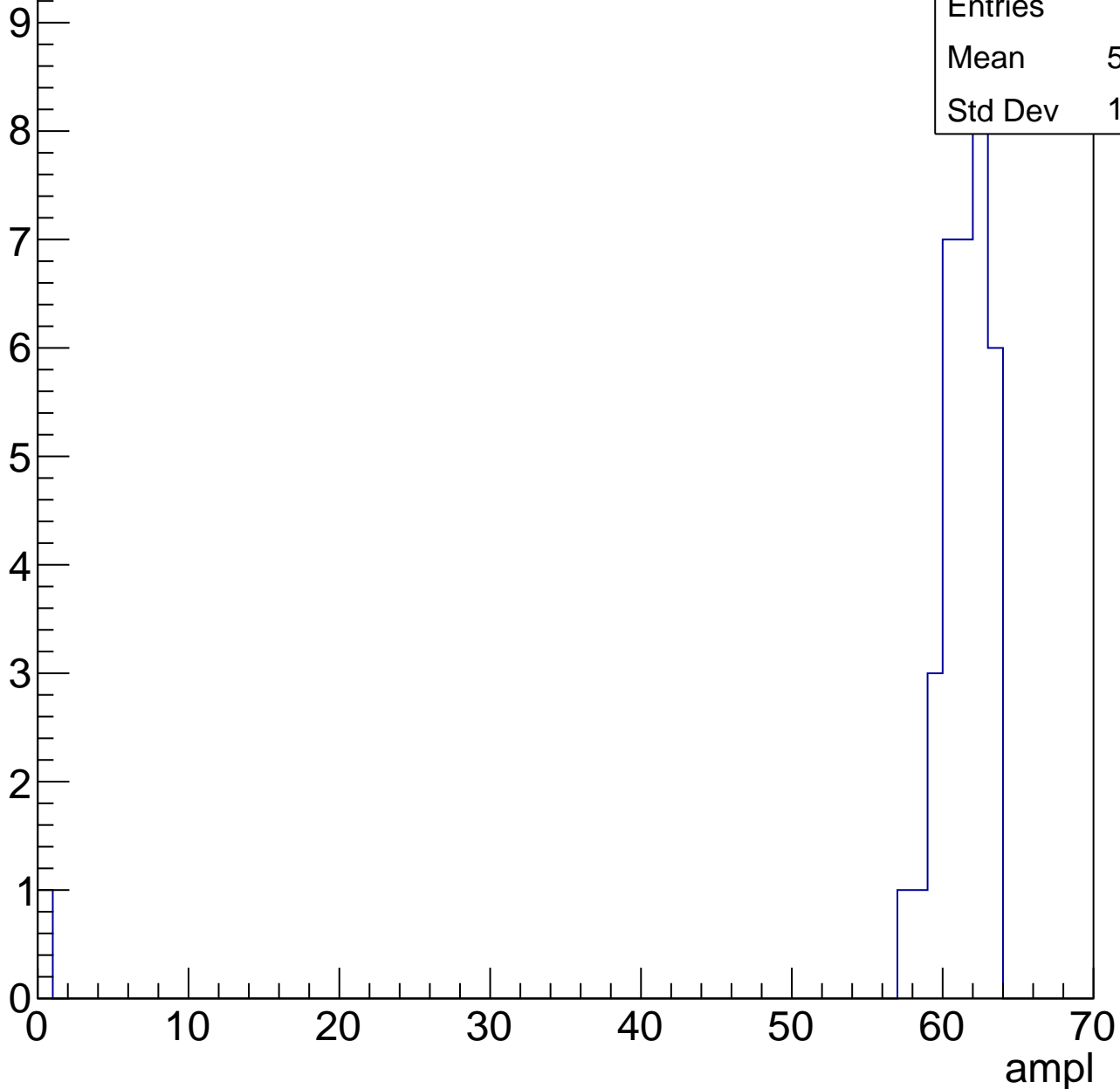
Entries	55
Mean	56.73
Std Dev	2.92

# B0L002S, U8-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.29
Std Dev	10.27



# B0L002S, U8-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L002S, U8-ch73, adc0

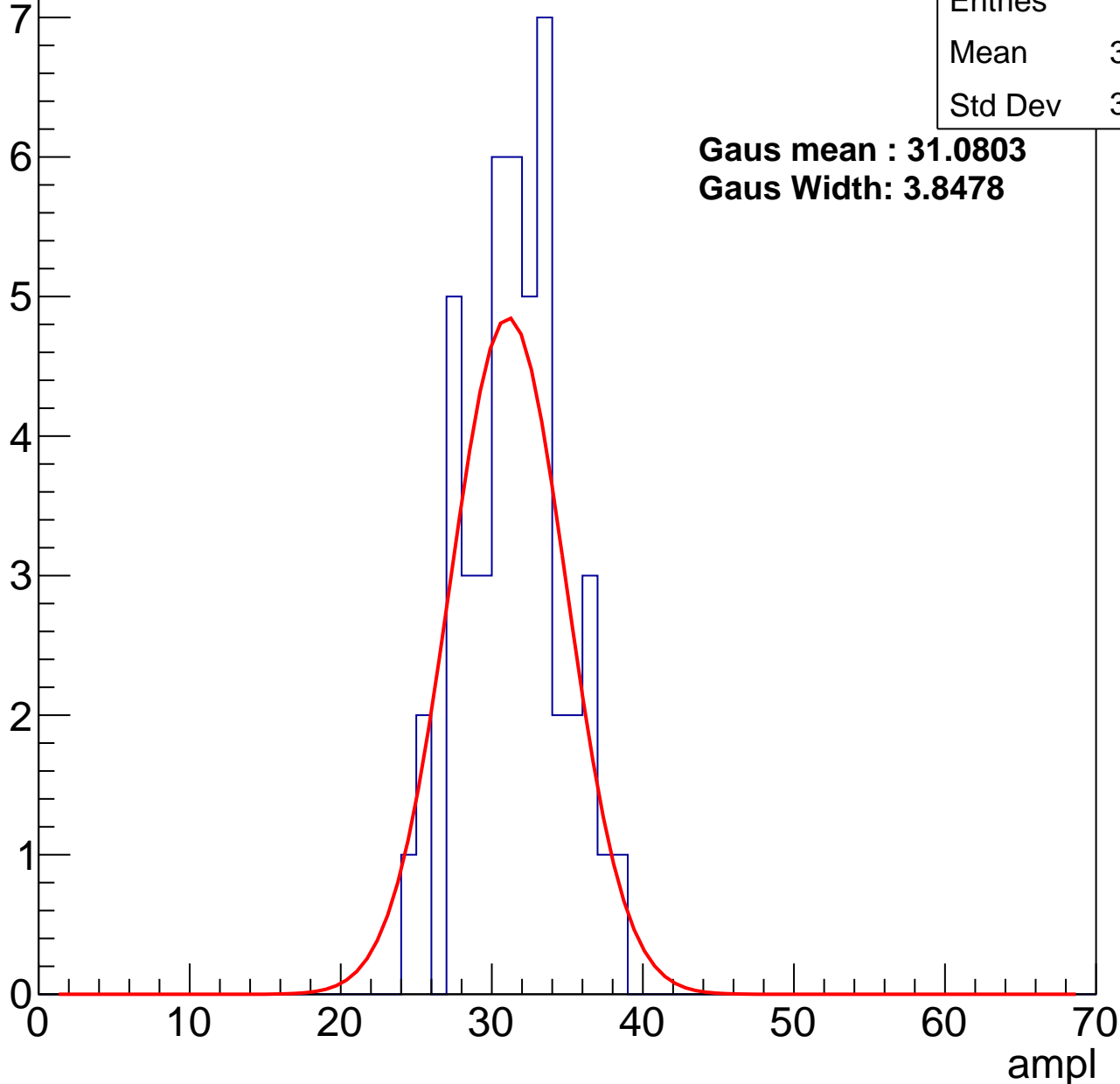
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	31.02
Std Dev	3.225

**Gaus mean : 31.0803**

**Gaus Width: 3.8478**



# B0L002S, U8-ch73, adc1

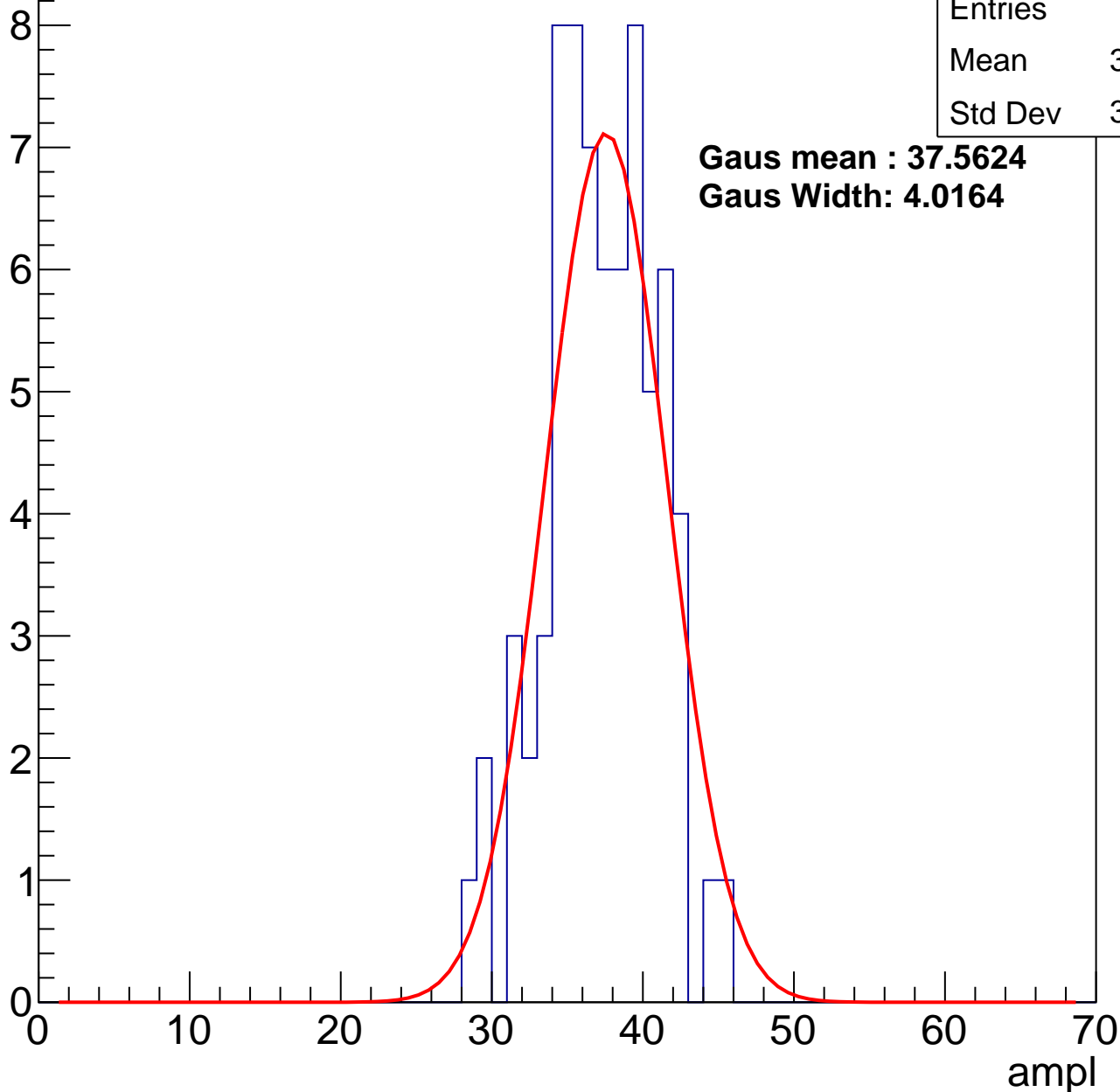
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.77
Std Dev	3.589

**Gaus mean : 37.5624**

**Gaus Width: 4.0164**



# B0L002S, U8-ch73, adc2

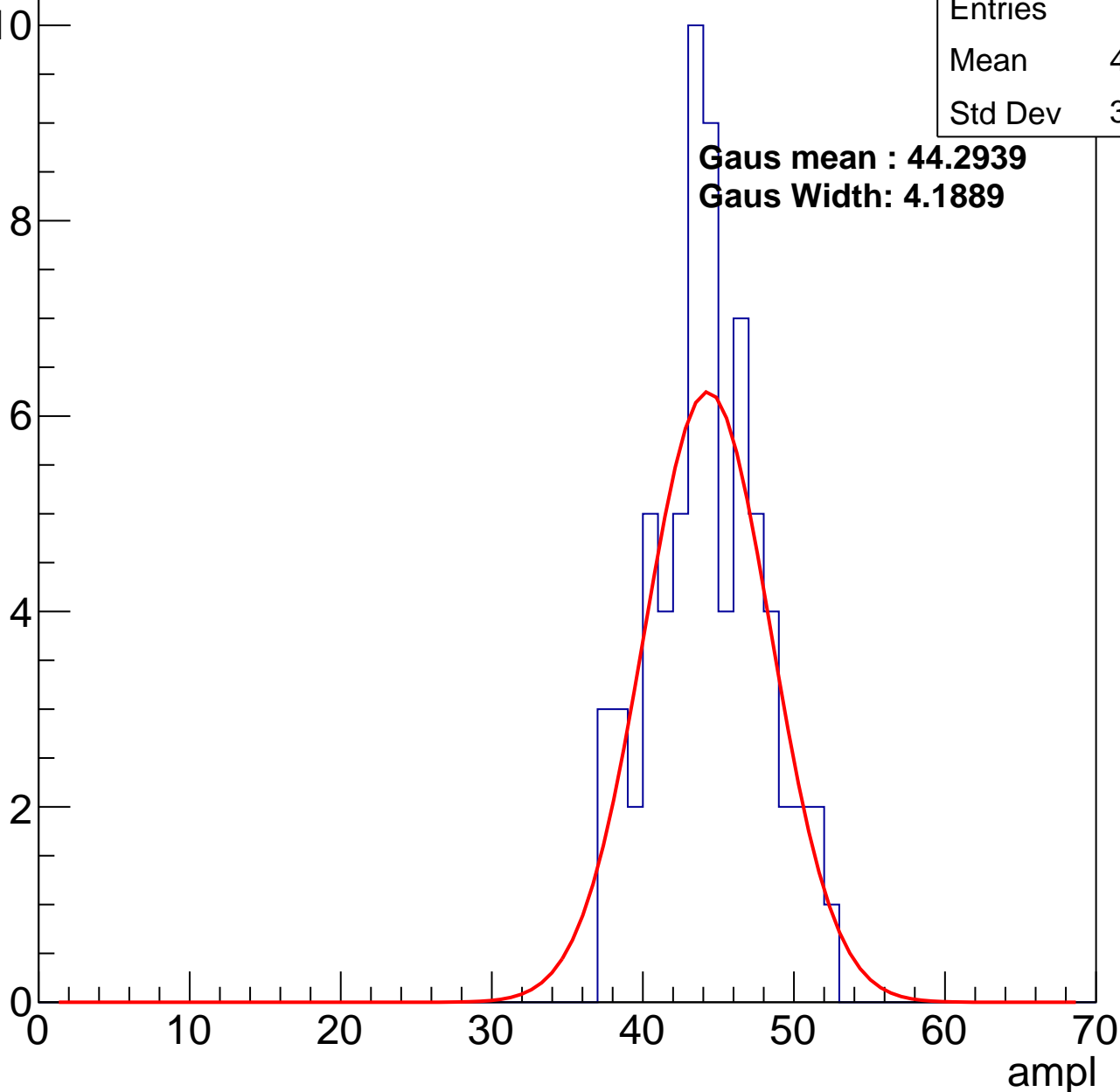
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.88
Std Dev	3.587

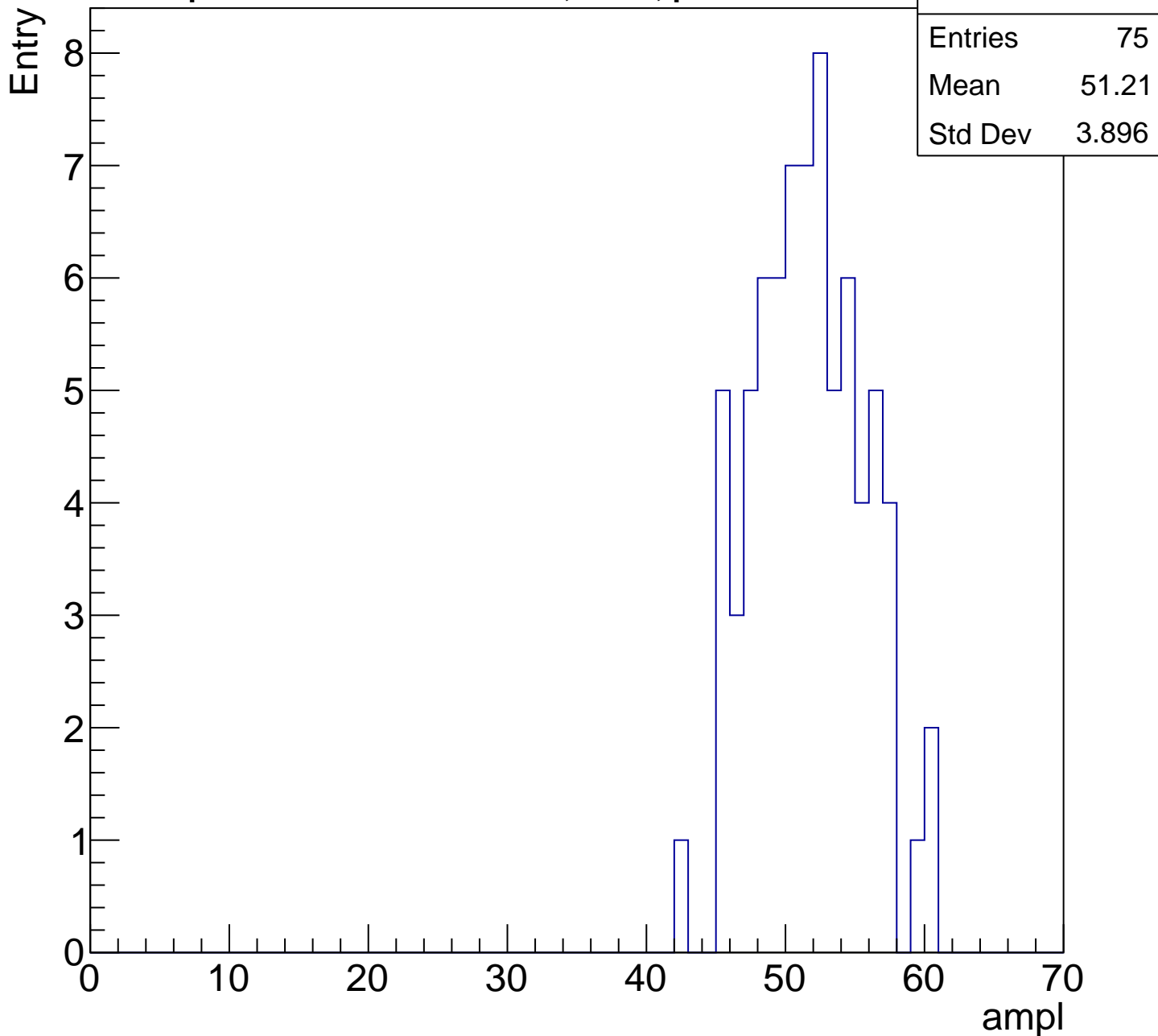
**Gaus mean : 44.2939**

**Gaus Width: 4.1889**



# B0L002S, U8-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

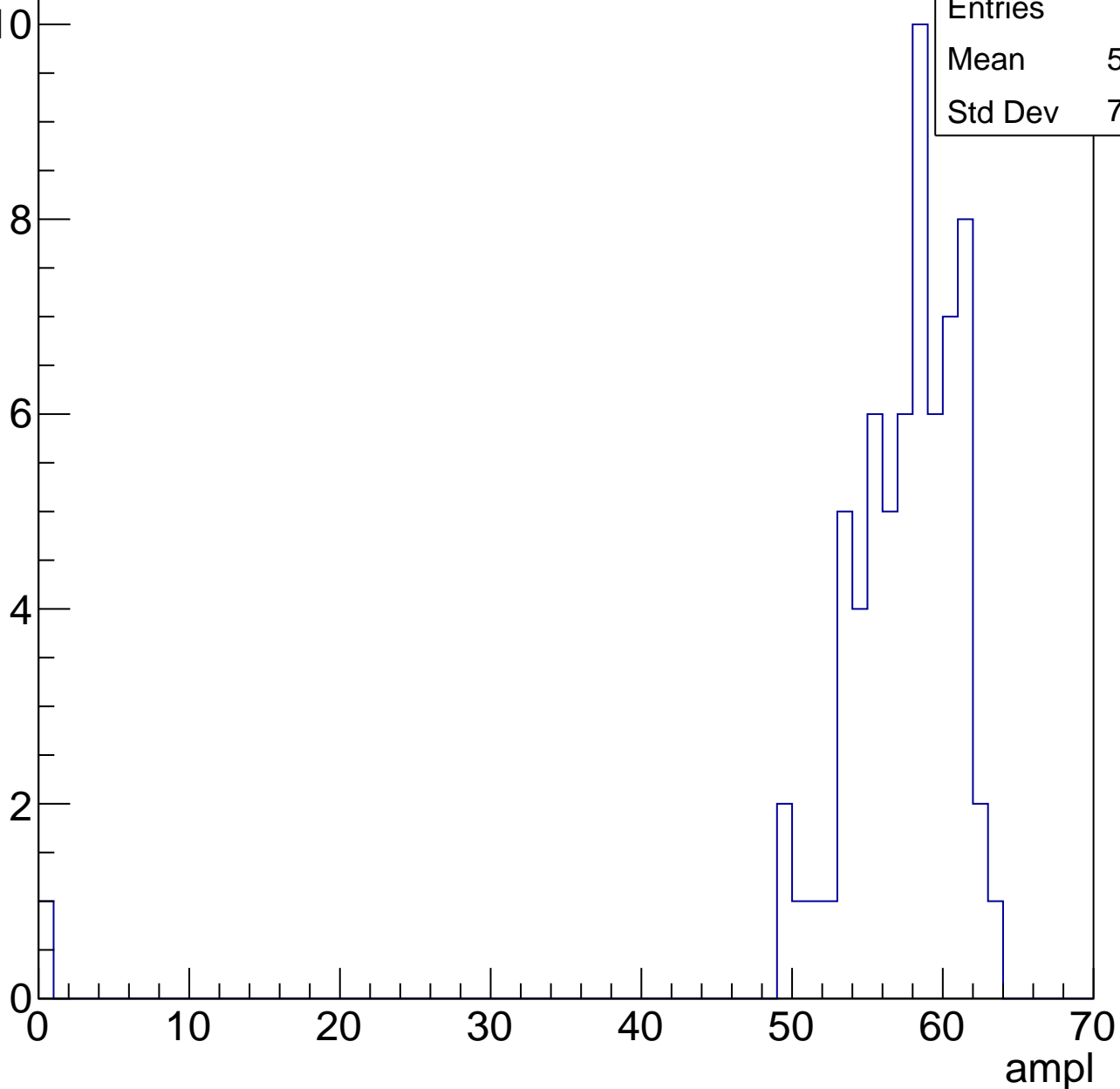


# B0L002S, U8-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	56.26
Std Dev	7.694



# B0L002S, U8-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	21
Mean	61.71
Std Dev	1.35

ampl

0

10

20

30

40

50

60

70

# B0L002S, U8-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch74, adc0

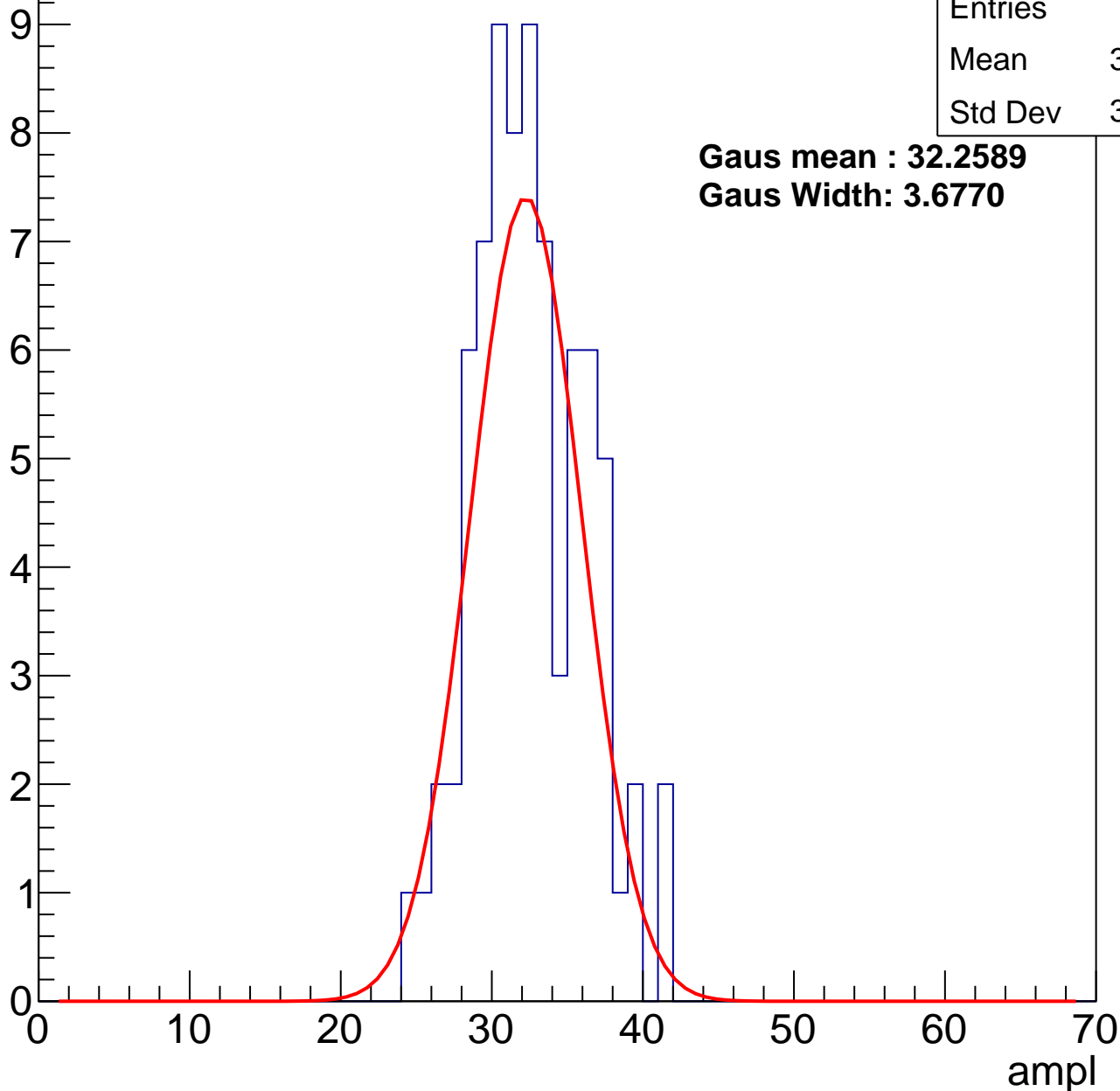
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	32.13
Std Dev	3.652

**Gaus mean : 32.2589**

**Gaus Width: 3.6770**



# B0L002S, U8-ch74, adc1

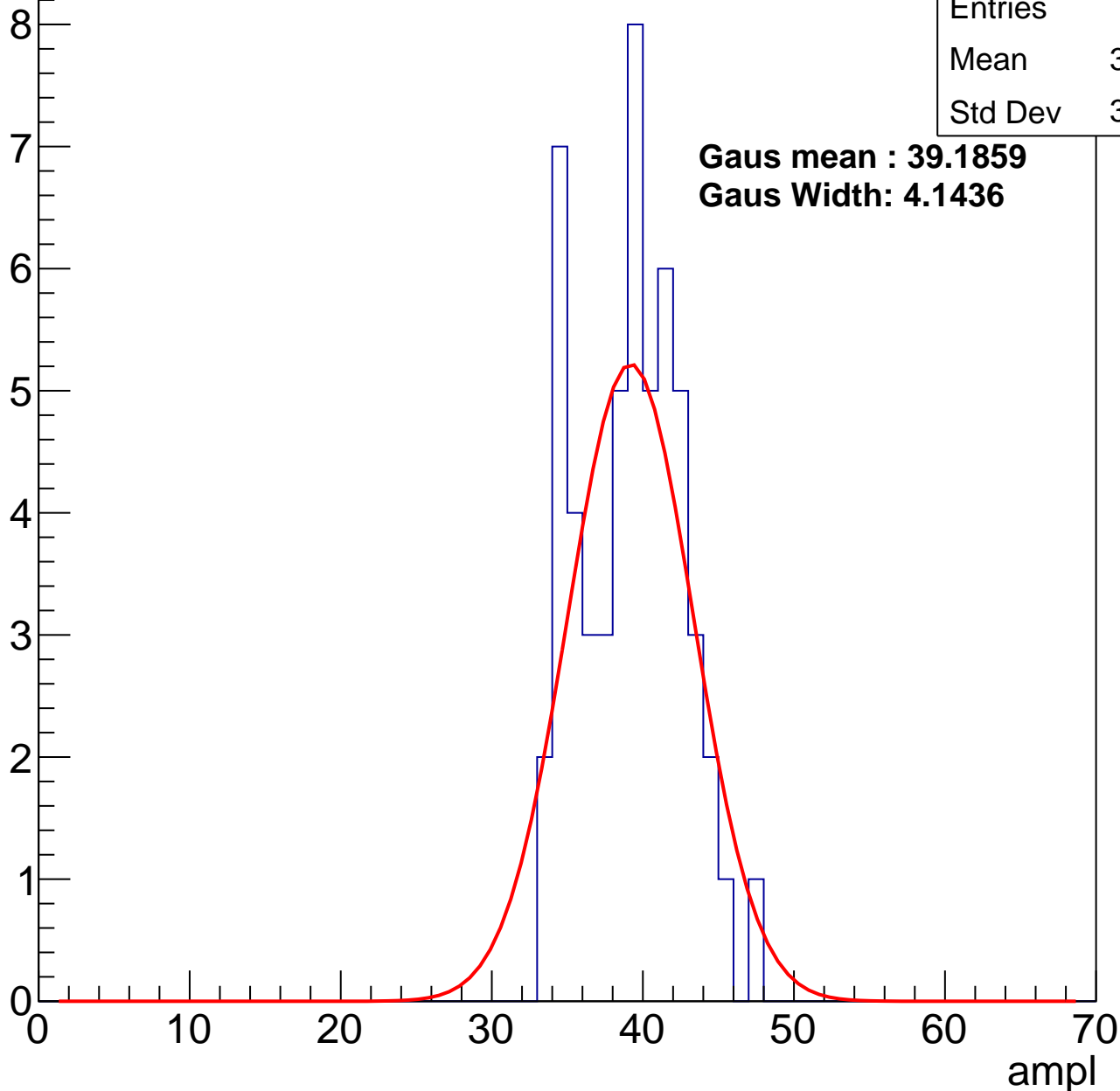
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	38.73
Std Dev	3.365

**Gaus mean : 39.1859**

**Gaus Width: 4.1436**



# B0L002S, U8-ch74, adc2

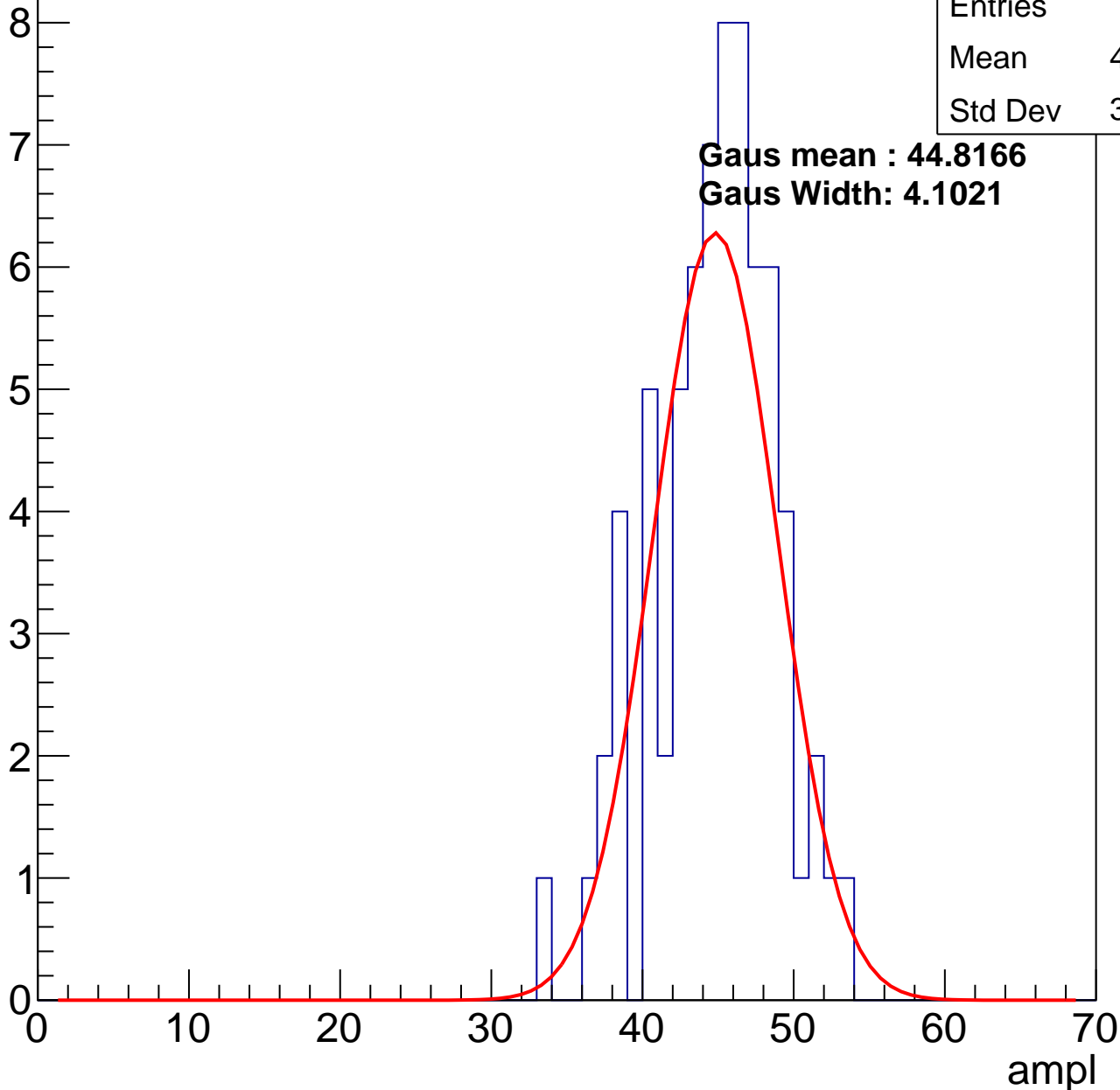
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.34
Std Dev	3.985

**Gaus mean : 44.8166**

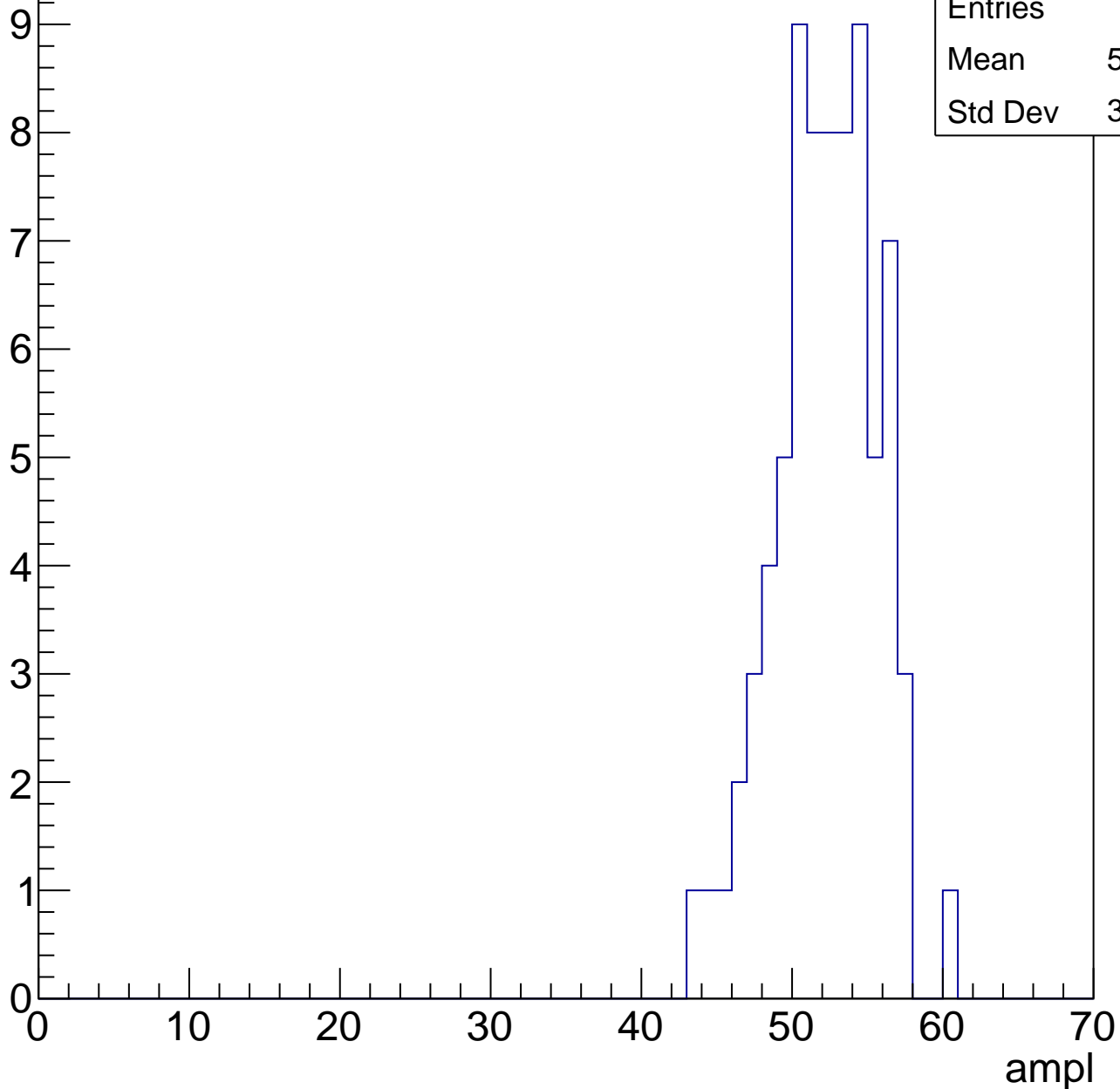
**Gaus Width: 4.1021**



# B0L002S, U8-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

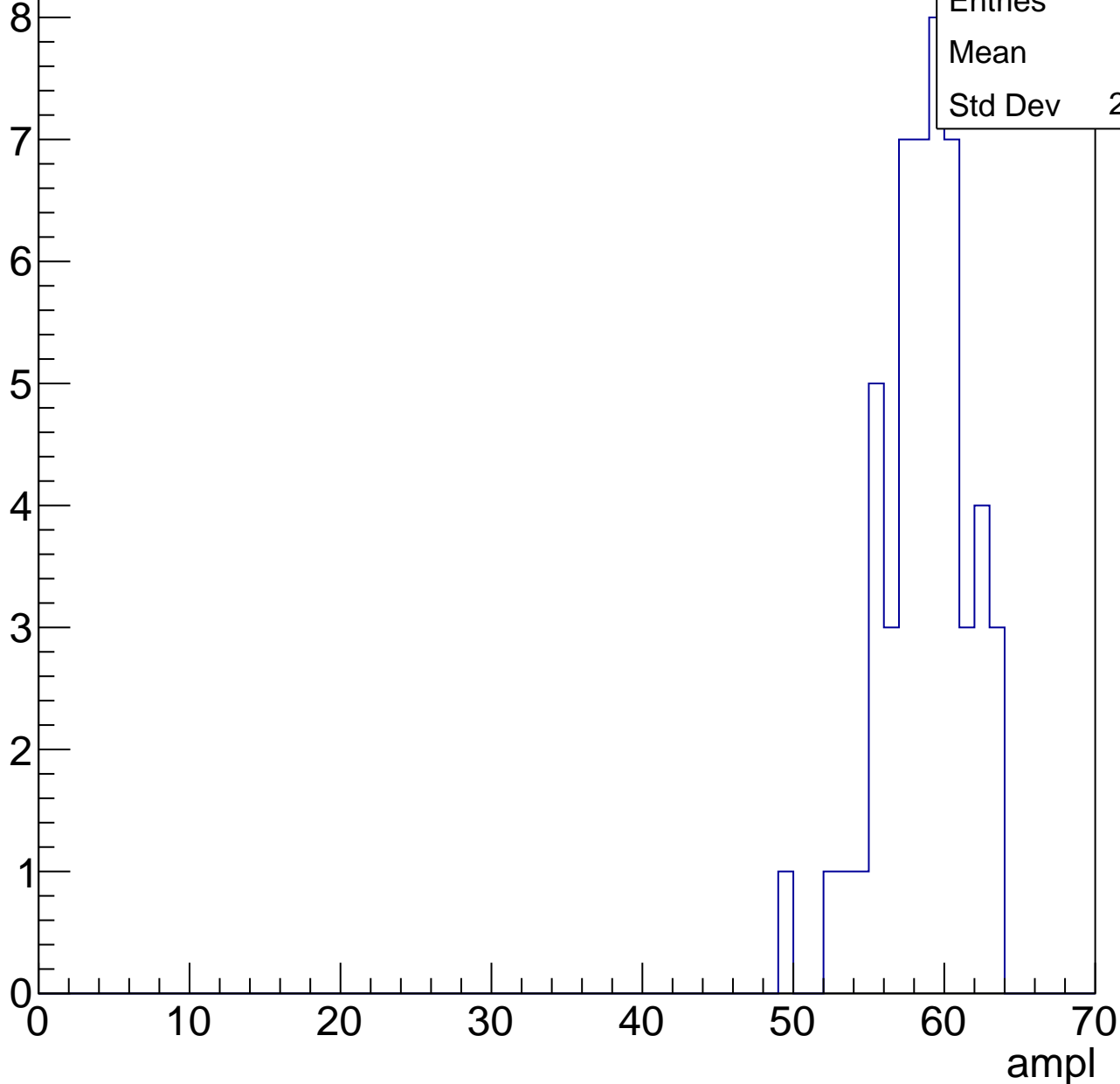


# B0L002S, U8-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	58.2
Std Dev	2.877

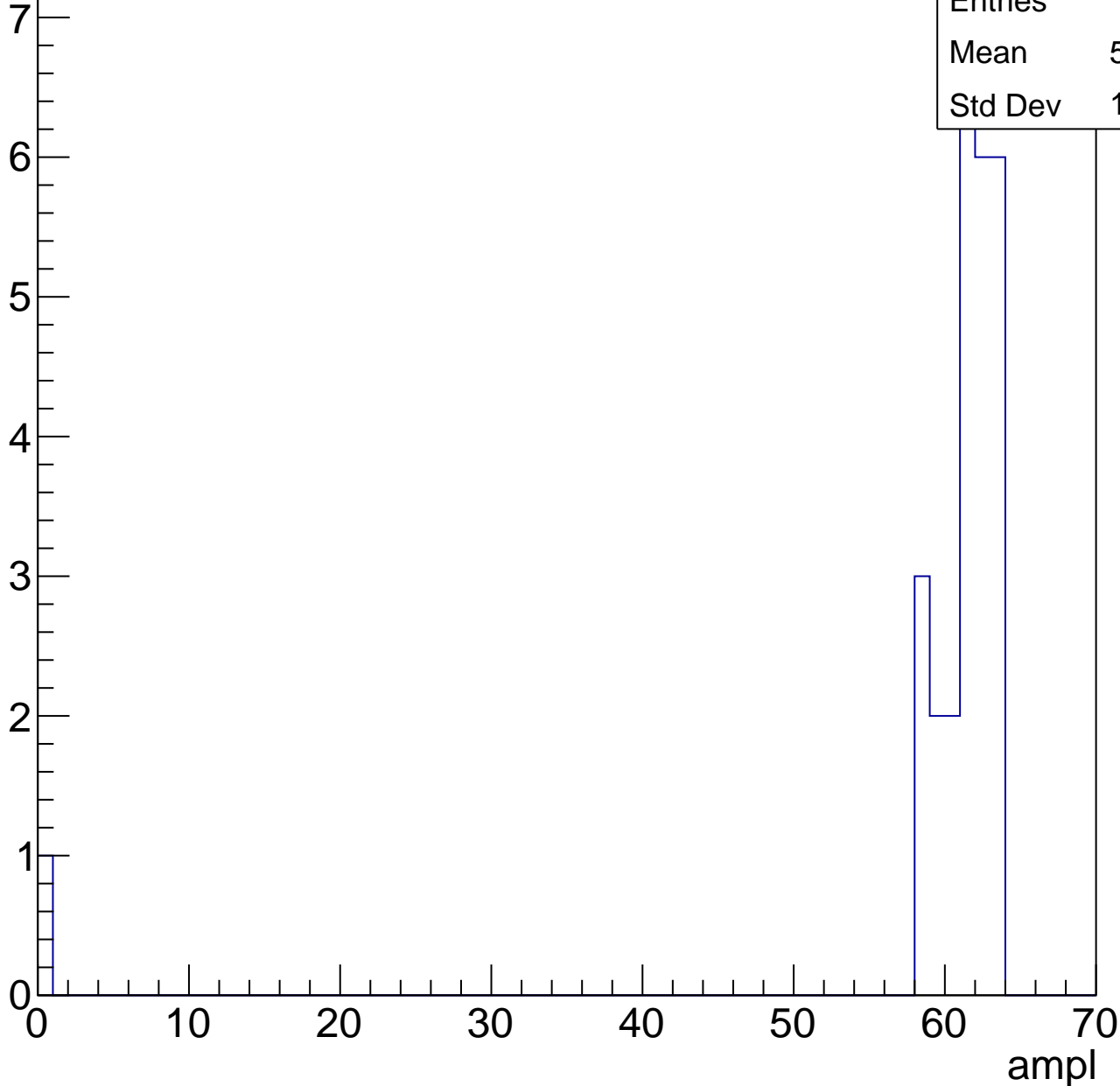


# B0L002S, U8-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.85
Std Dev	11.65



# B0L002S, U8-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L002S, U8-ch75, adc0

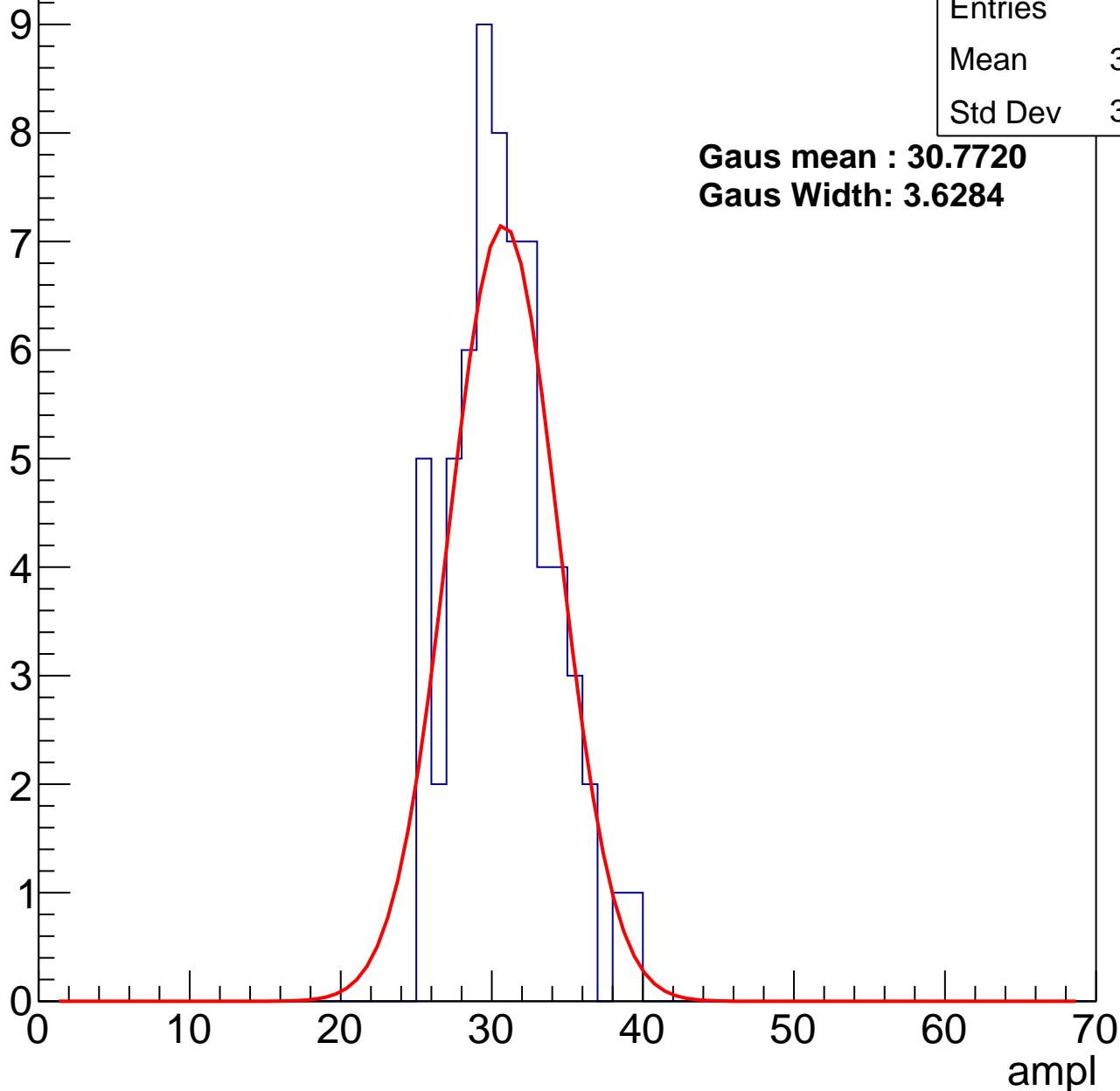
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.38
Std Dev	3.194

**Gaus mean : 30.7720**

**Gaus Width: 3.6284**



# B0L002S, U8-ch75, adc1

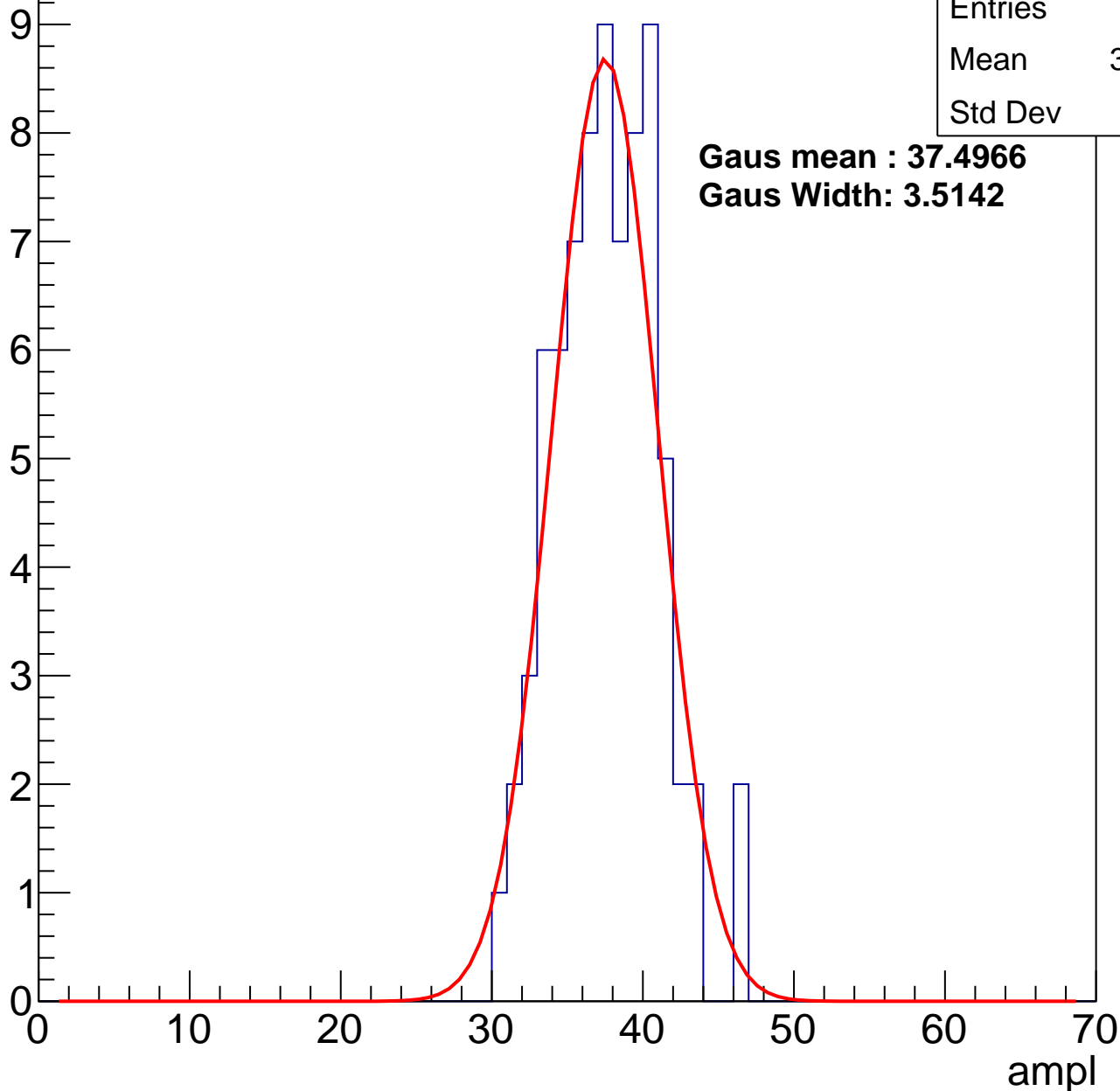
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37.16
Std Dev	3.35

**Gaus mean : 37.4966**

**Gaus Width: 3.5142**



# B0L002S, U8-ch75, adc2

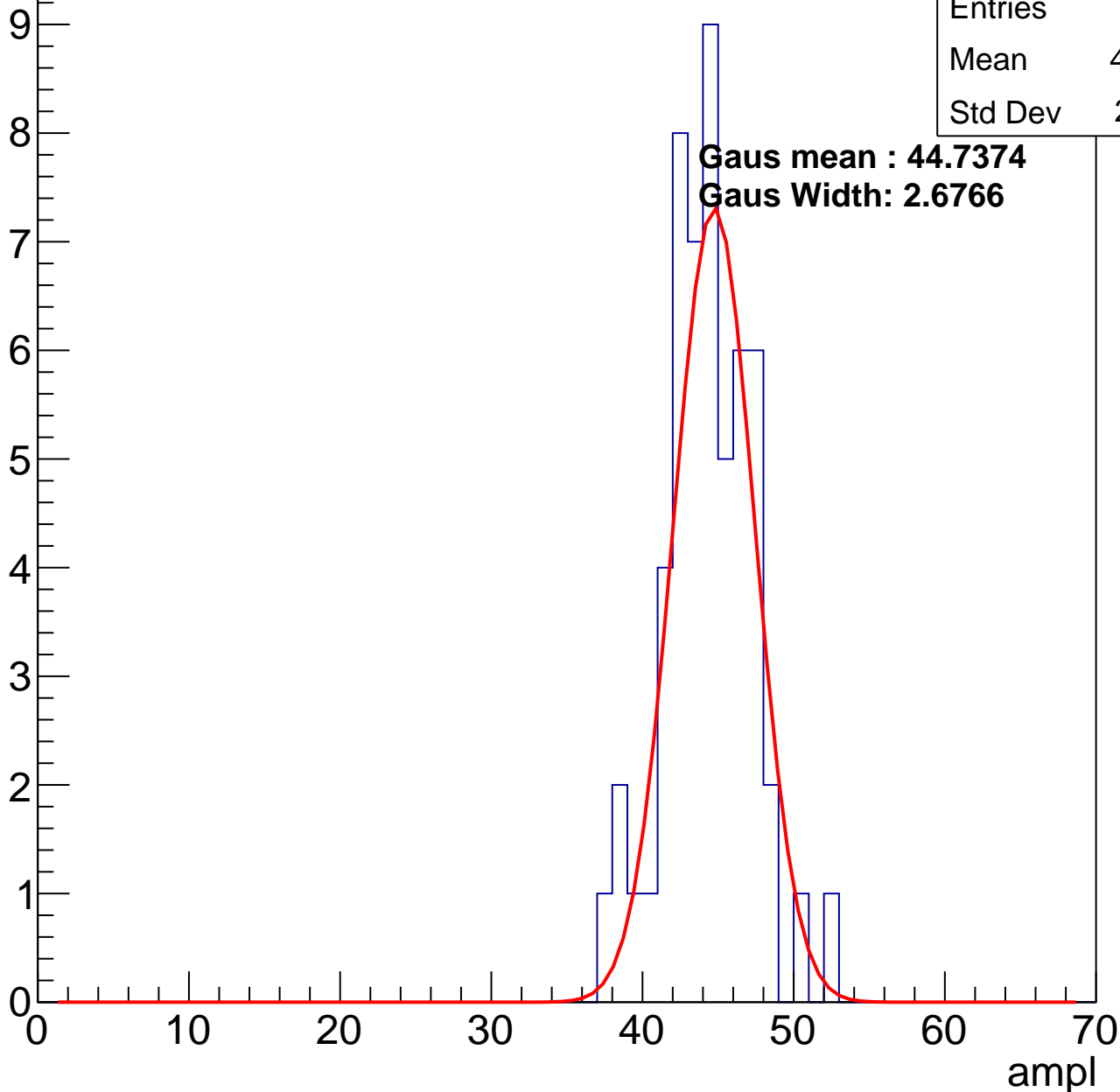
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.89
Std Dev	2.891

**Gaus mean : 44.7374**

**Gaus Width: 2.6766**

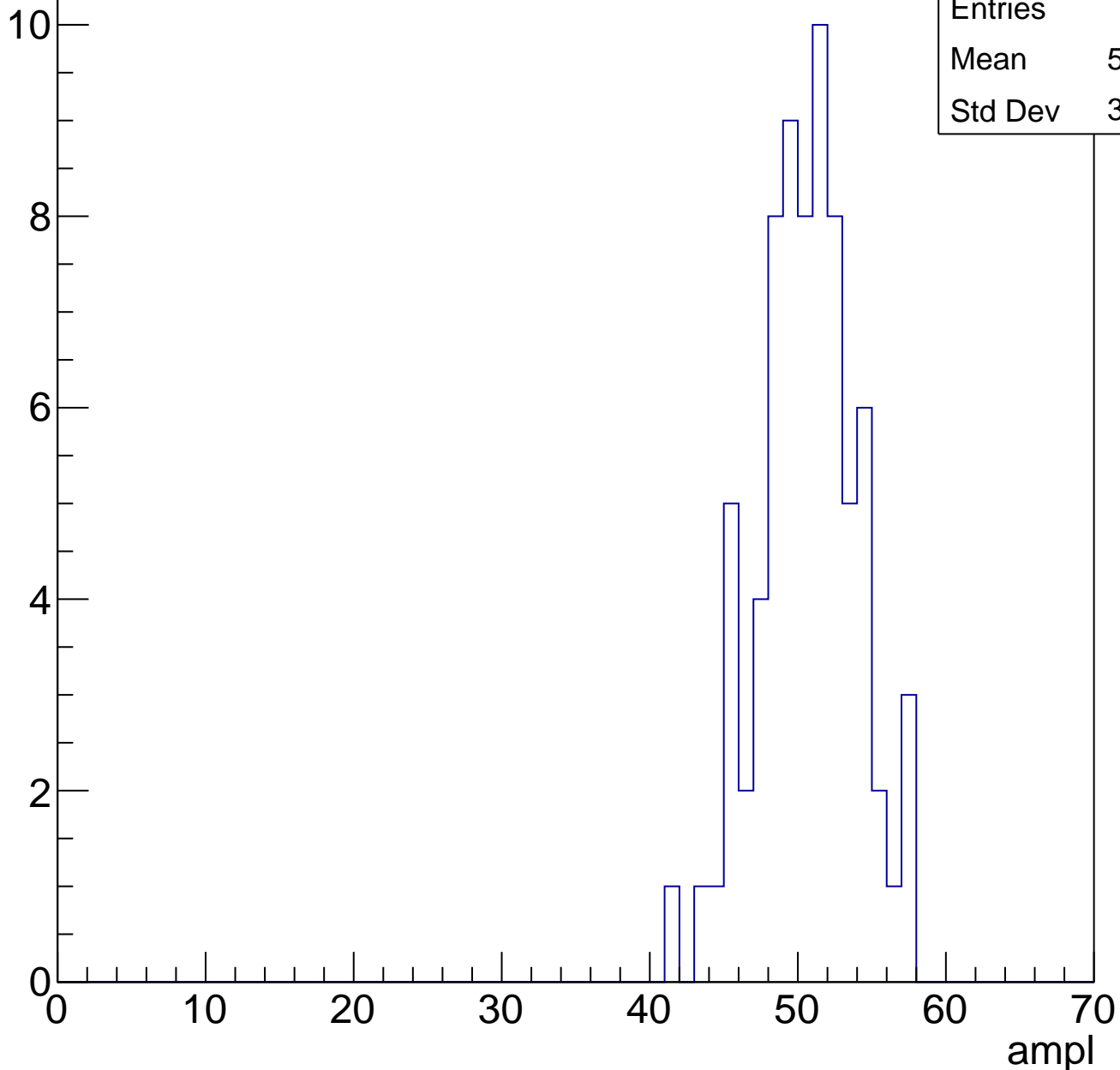


# B0L002S, U8-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	50.14
Std Dev	3.318

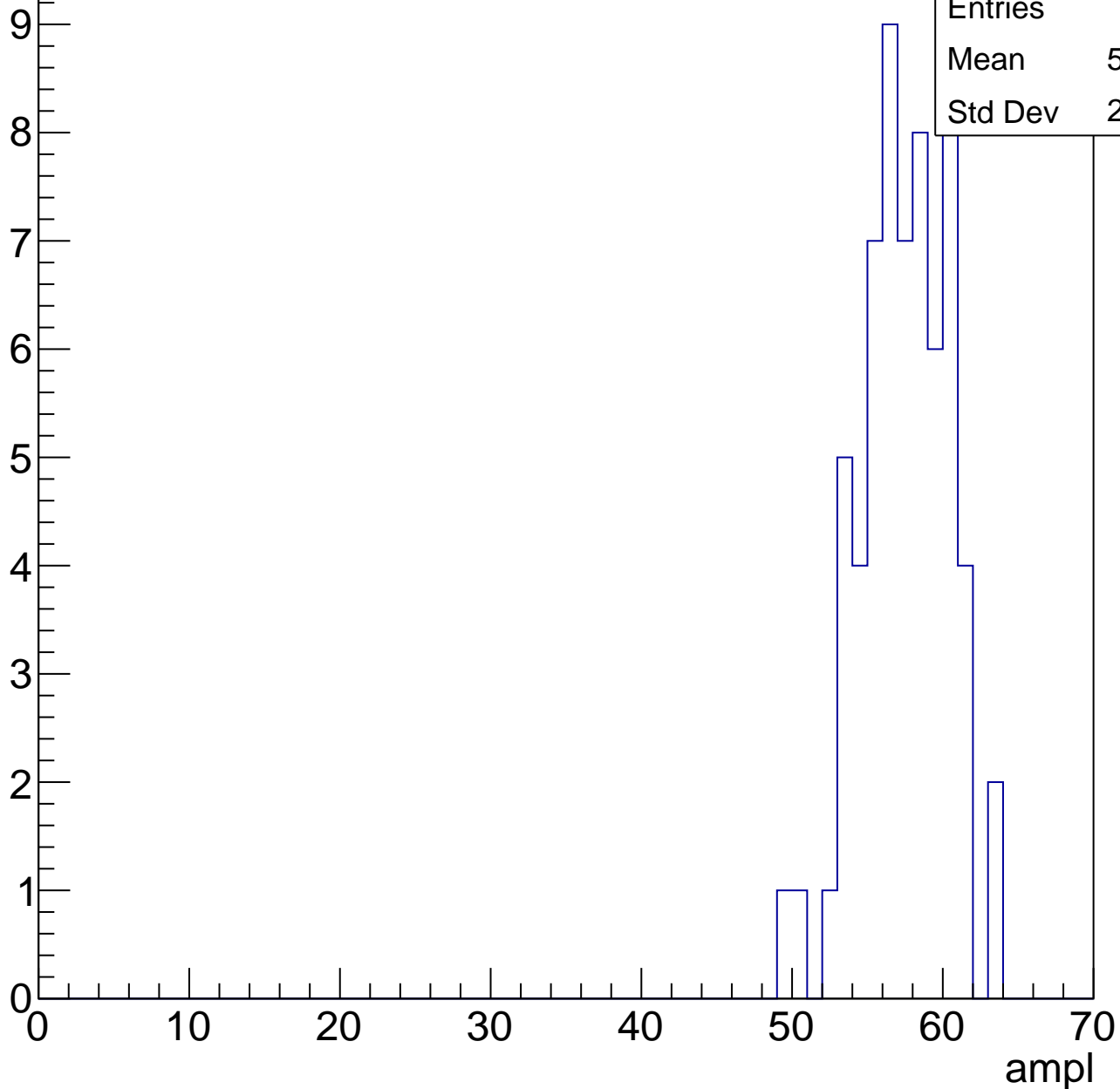
Entry



# B0L002S, U8-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

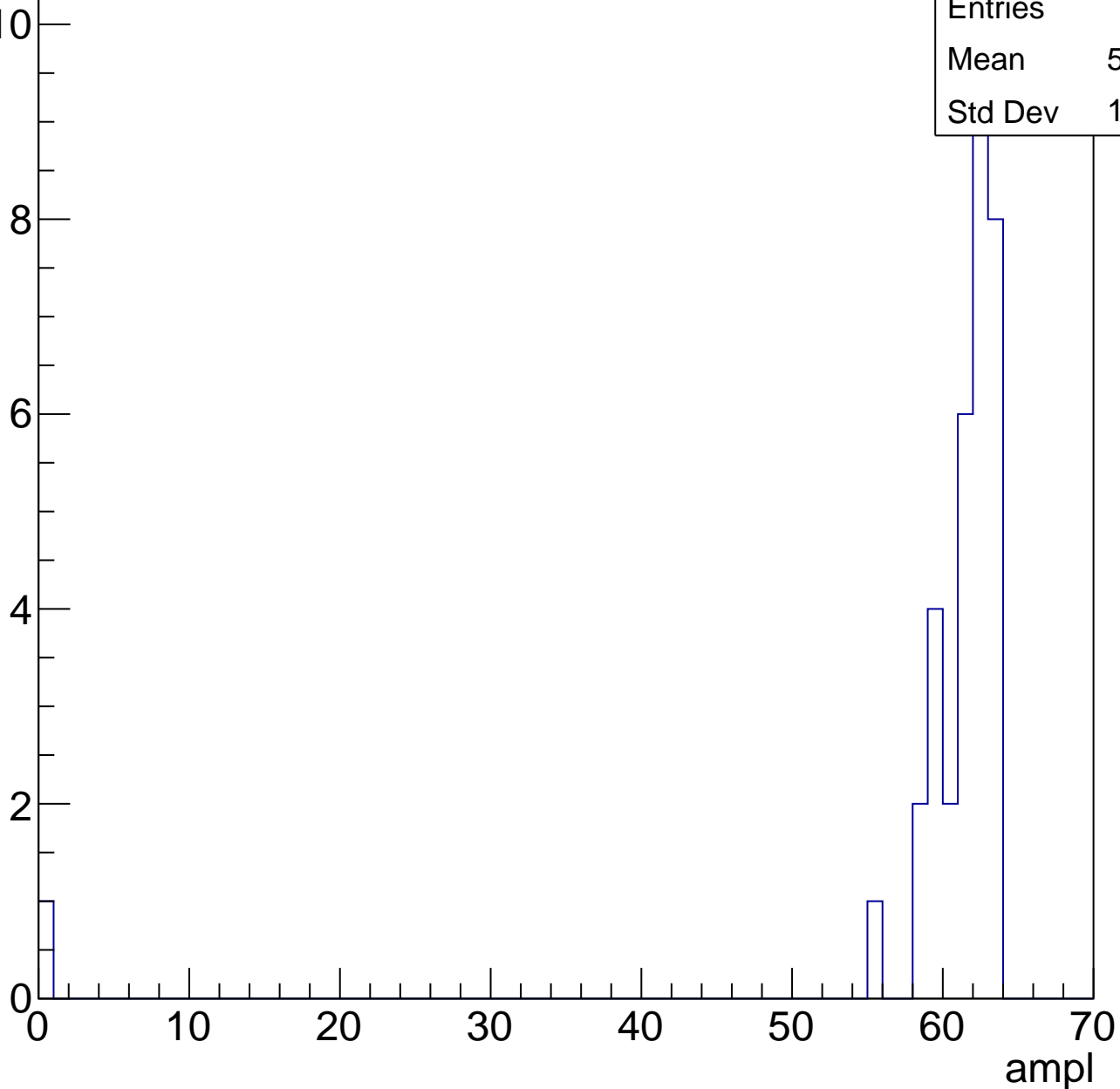


# B0L002S, U8-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.32
Std Dev	10.49



# B0L002S, U8-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch76, adc0

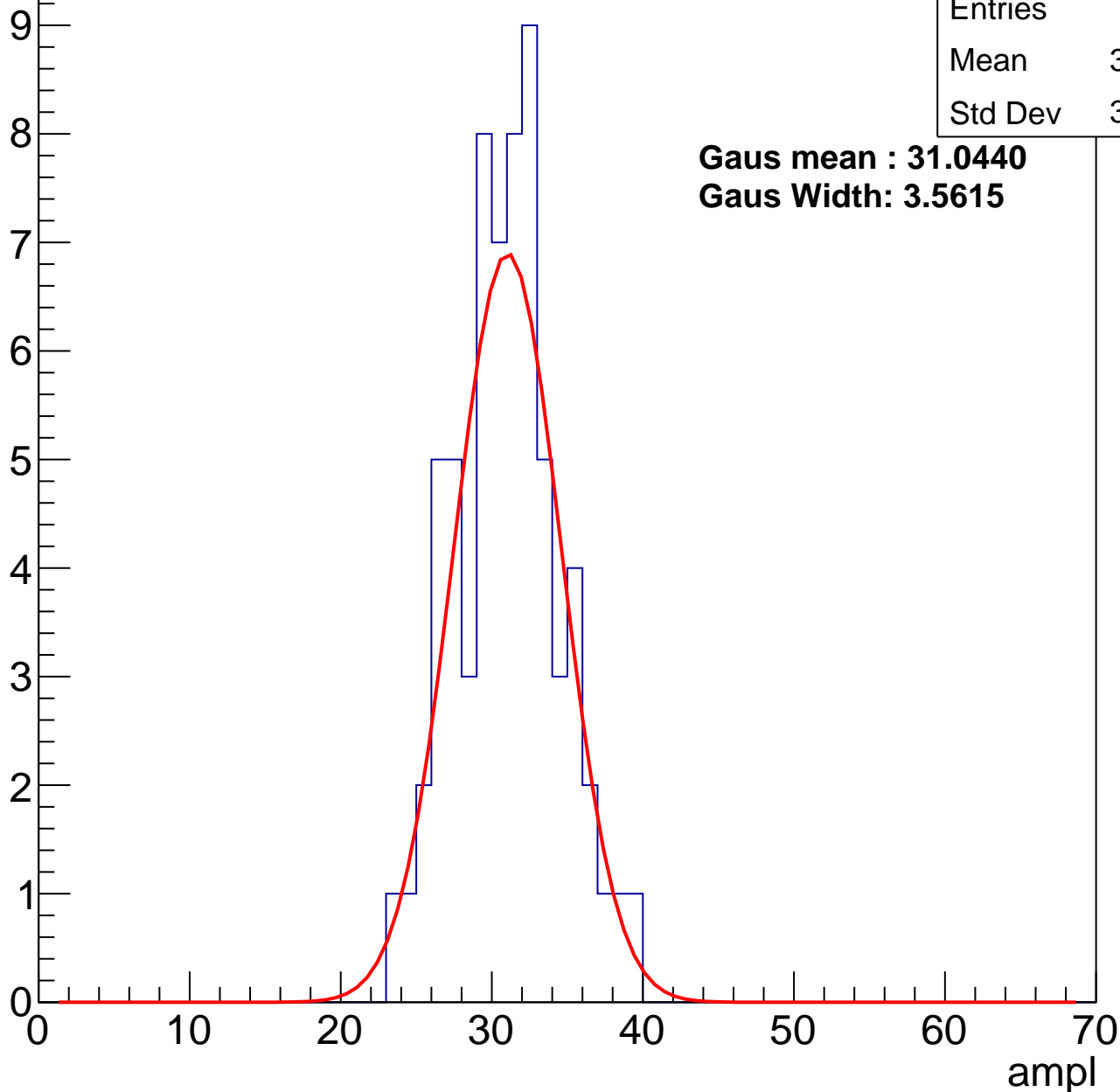
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.56
Std Dev	3.398

**Gaus mean : 31.0440**

**Gaus Width: 3.5615**



# B0L002S, U8-ch76, adc1

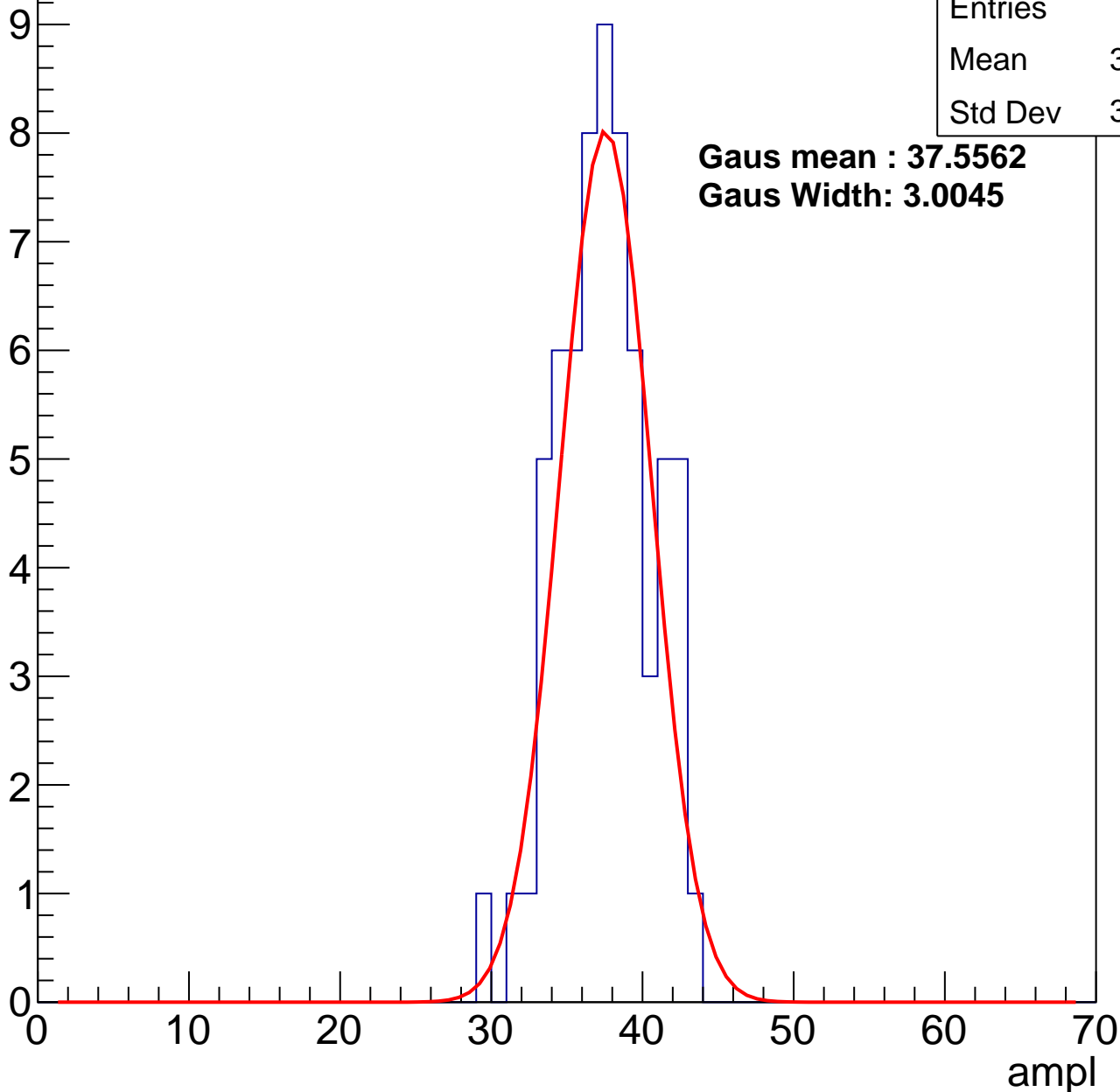
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.05
Std Dev	3.015

**Gaus mean : 37.5562**

**Gaus Width: 3.0045**



# B0L002S, U8-ch76, adc2

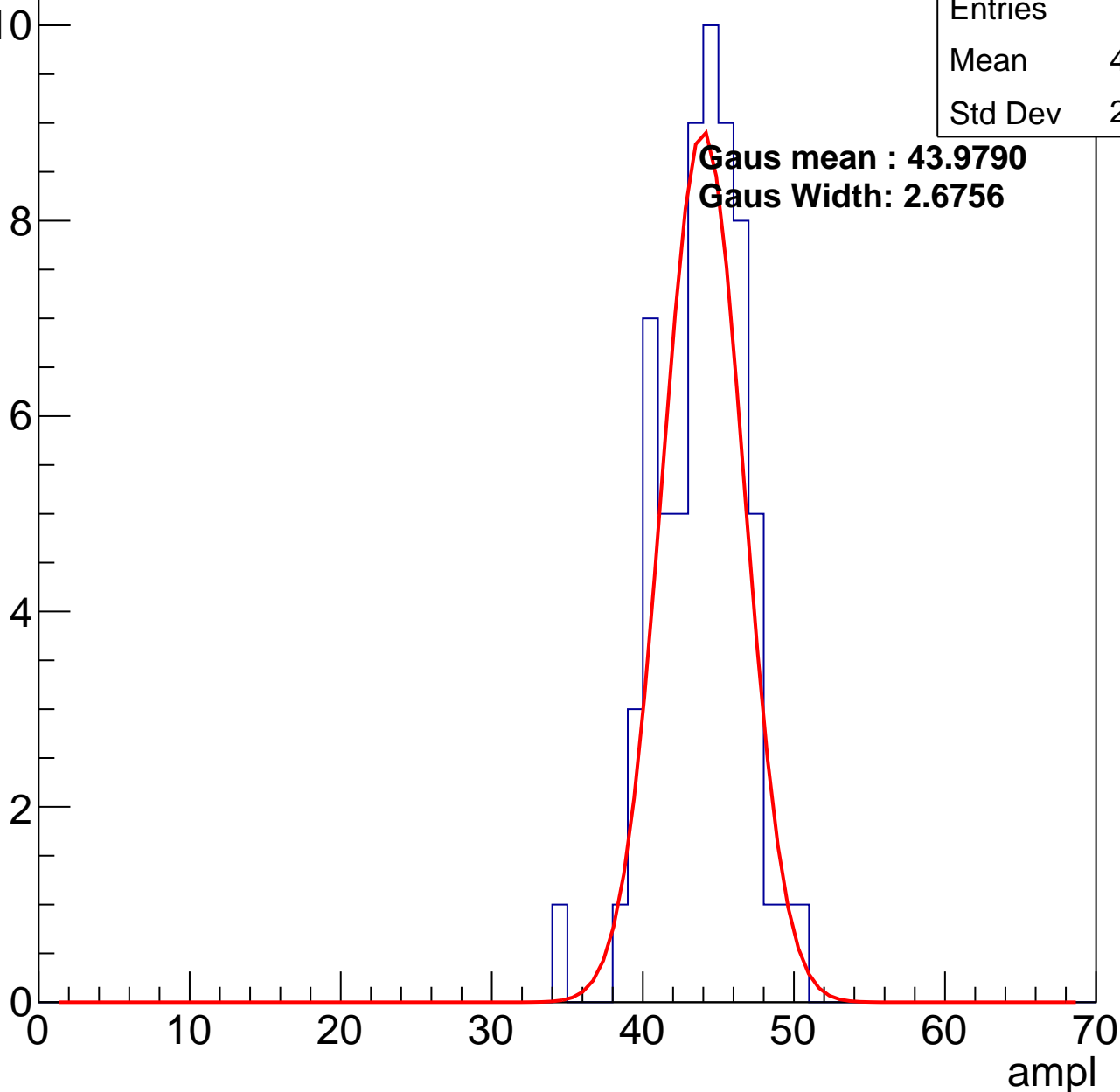
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.42
Std Dev	2.855

**Gaus mean : 43.9790**

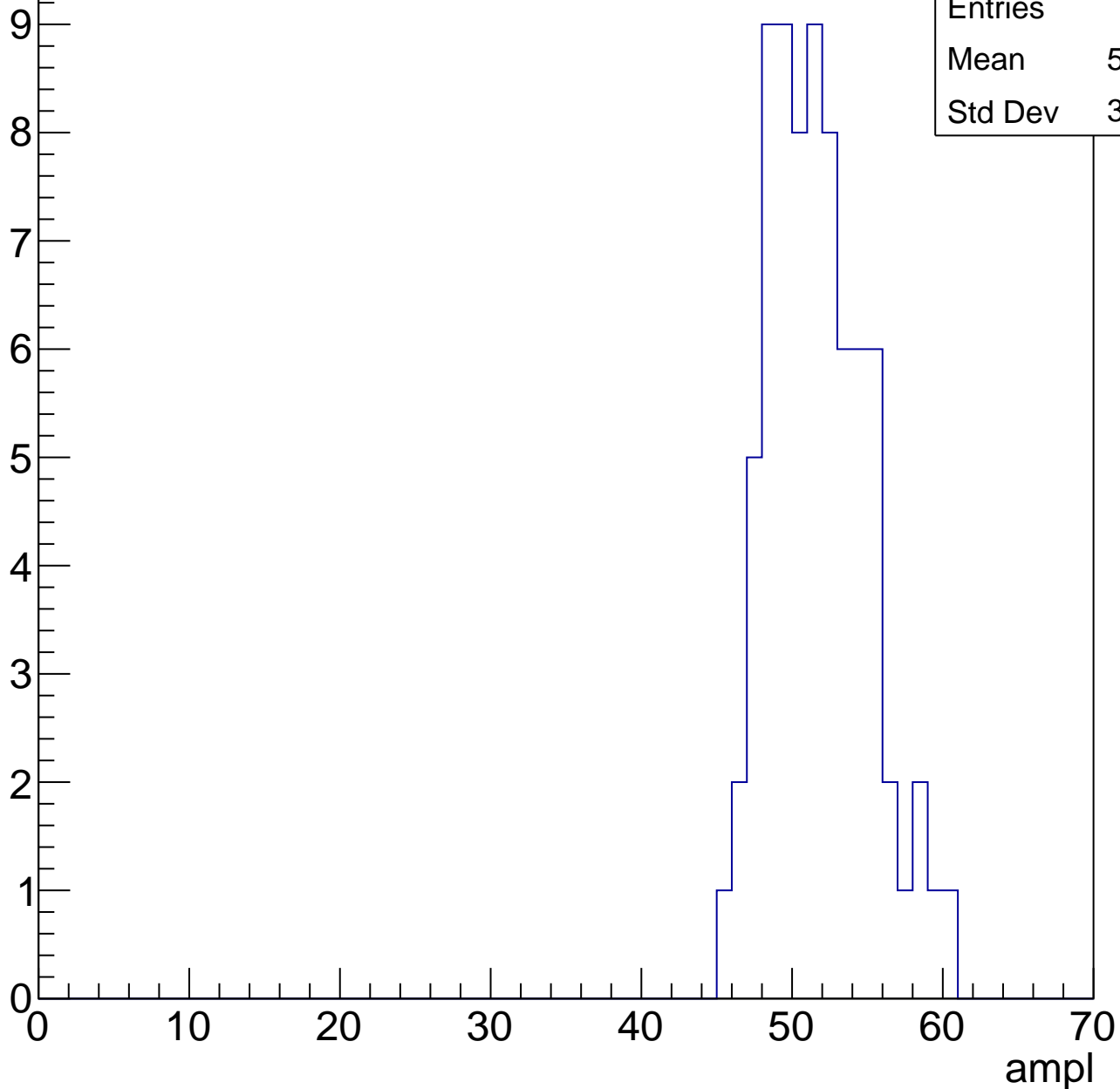
**Gaus Width: 2.6756**



# B0L002S, U8-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

6

5

4

3

2

1

0

Entries	43
Mean	56.74
Std Dev	3.02

10

20

30

40

50

60

70

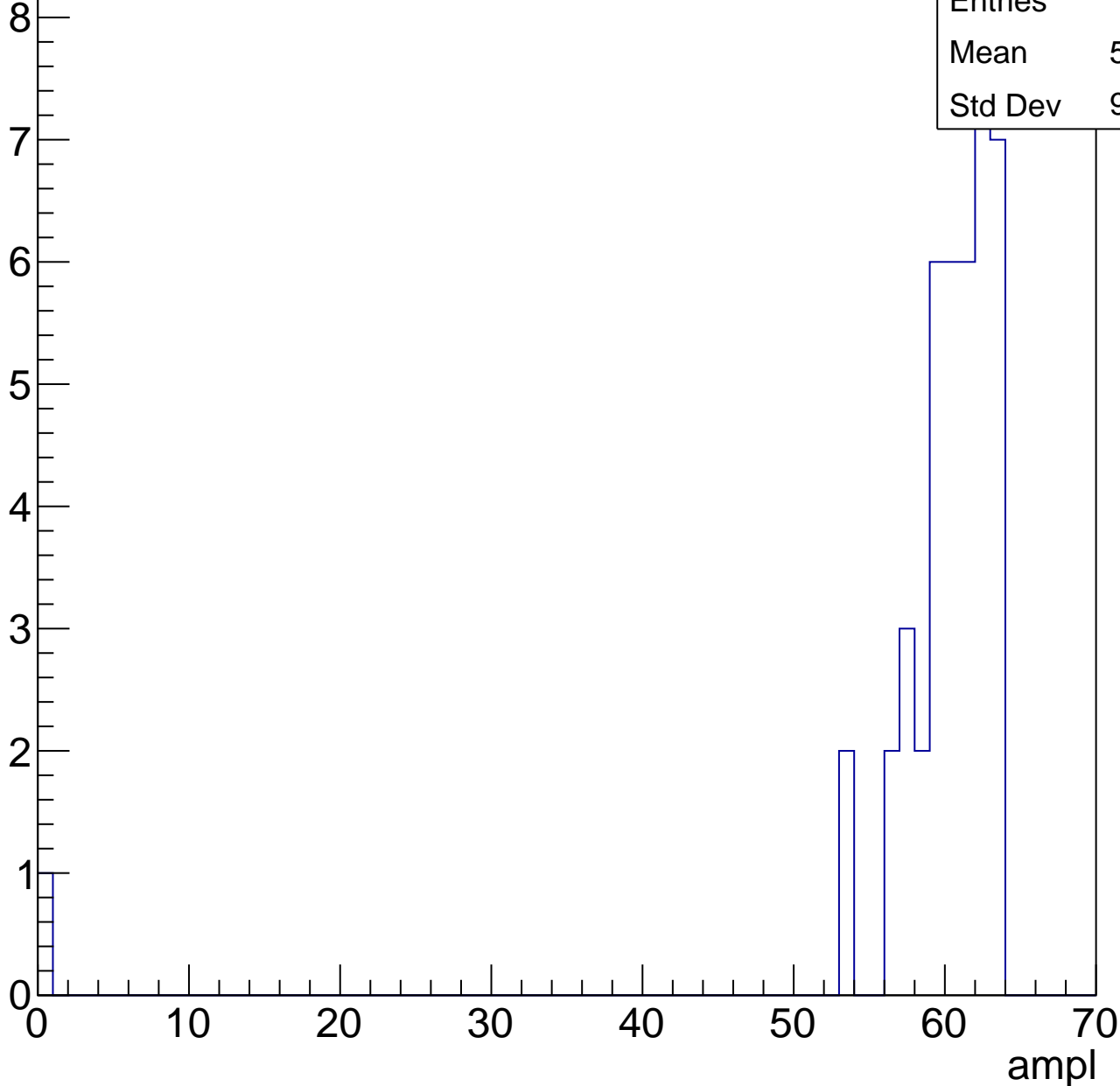
ampl

# B0L002S, U8-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.65
Std Dev	9.393



# B0L002S, U8-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	61.75
Std Dev	1.299



# B0L002S, U8-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch77, adc0

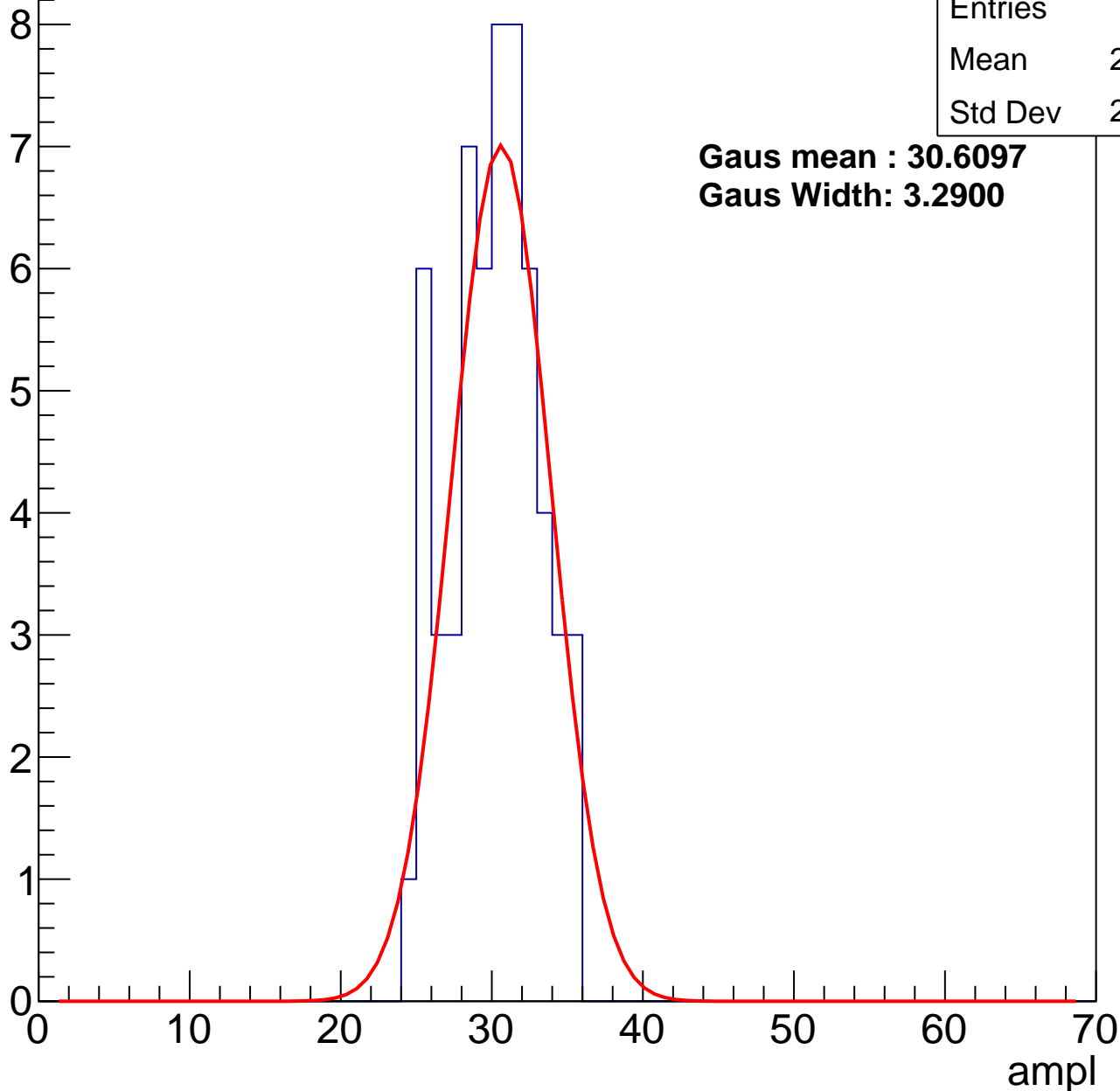
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.69
Std Dev	2.878

**Gaus mean : 30.6097**

**Gaus Width: 3.2900**



# B0L002S, U8-ch77, adc1

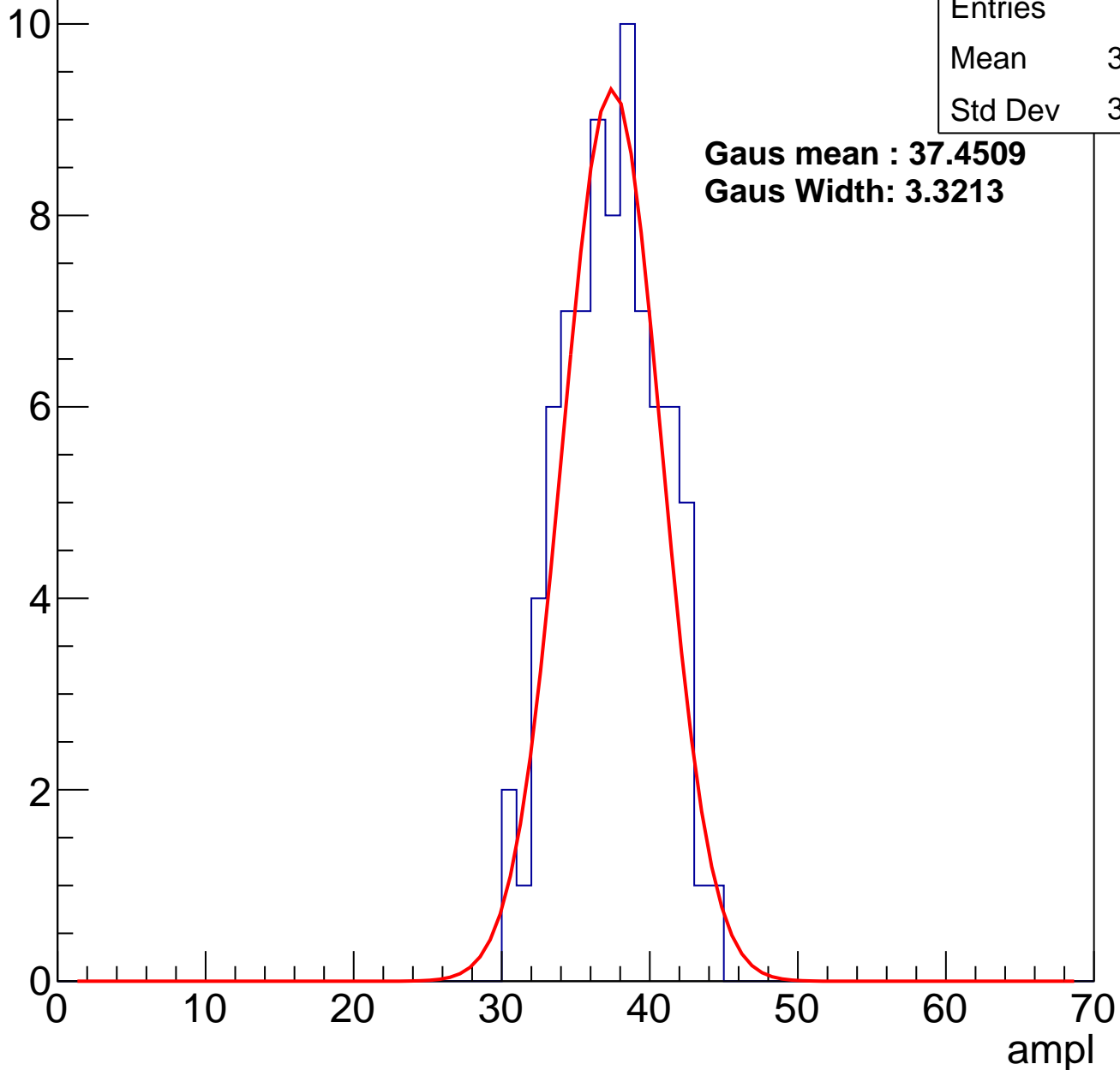
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	36.95
Std Dev	3.217

**Gaus mean : 37.4509**

**Gaus Width: 3.3213**

Entry



# B0L002S, U8-ch77, adc2

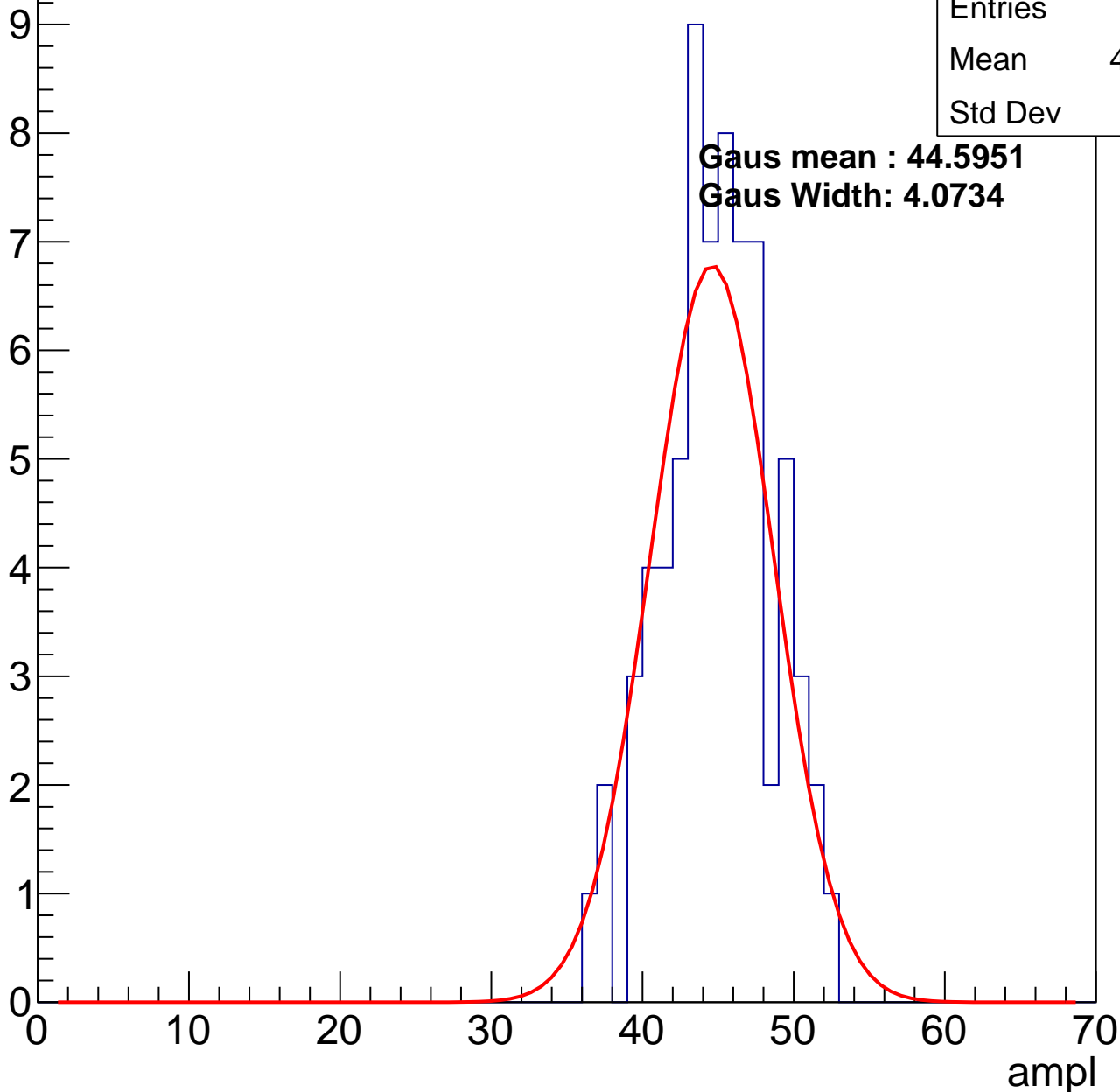
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.46
Std Dev	3.54

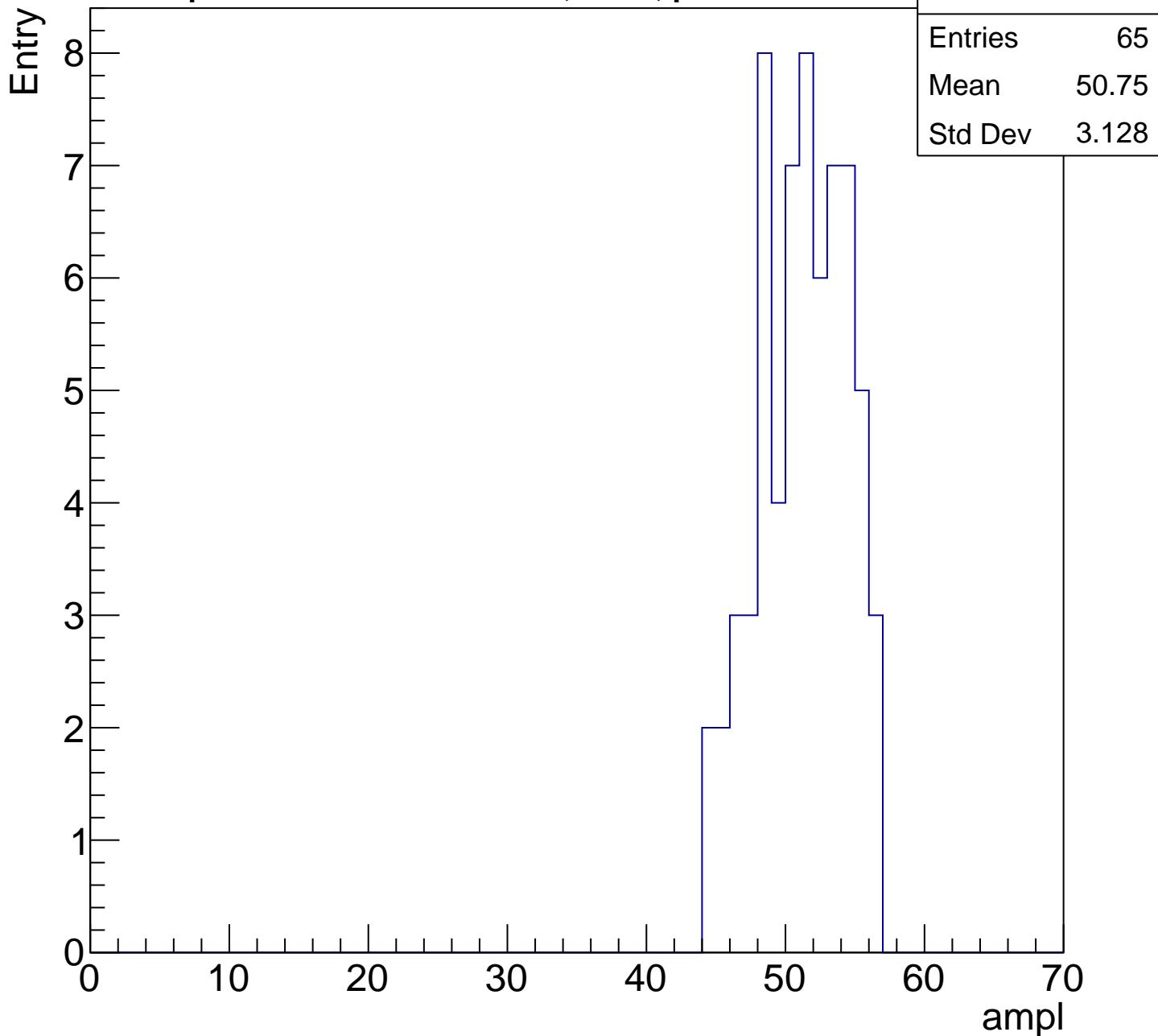
**Gaus mean : 44.5951**

**Gaus Width: 4.0734**



# B0L002S, U8-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

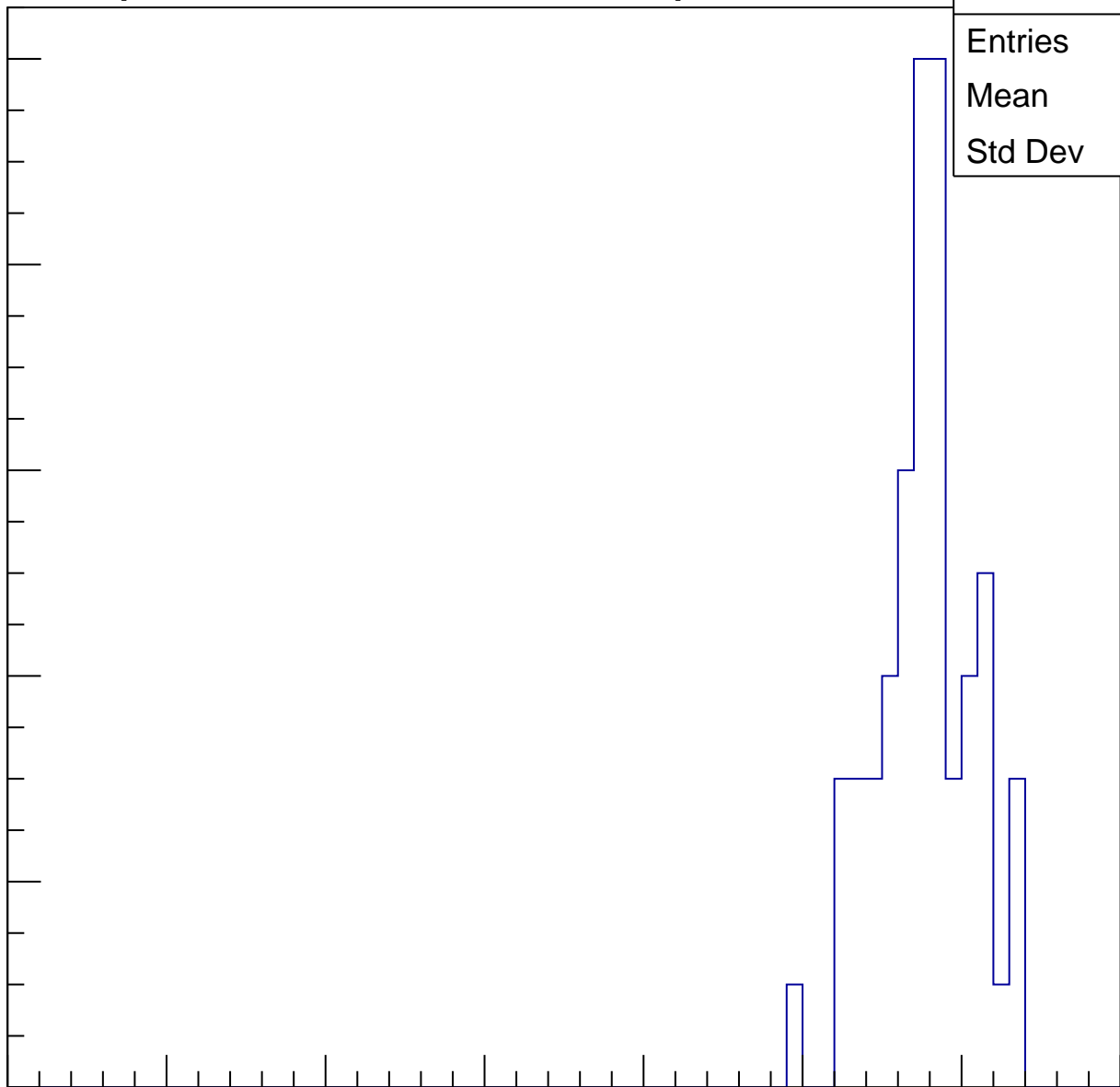
Entries	56
Mean	57.23
Std Dev	3

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

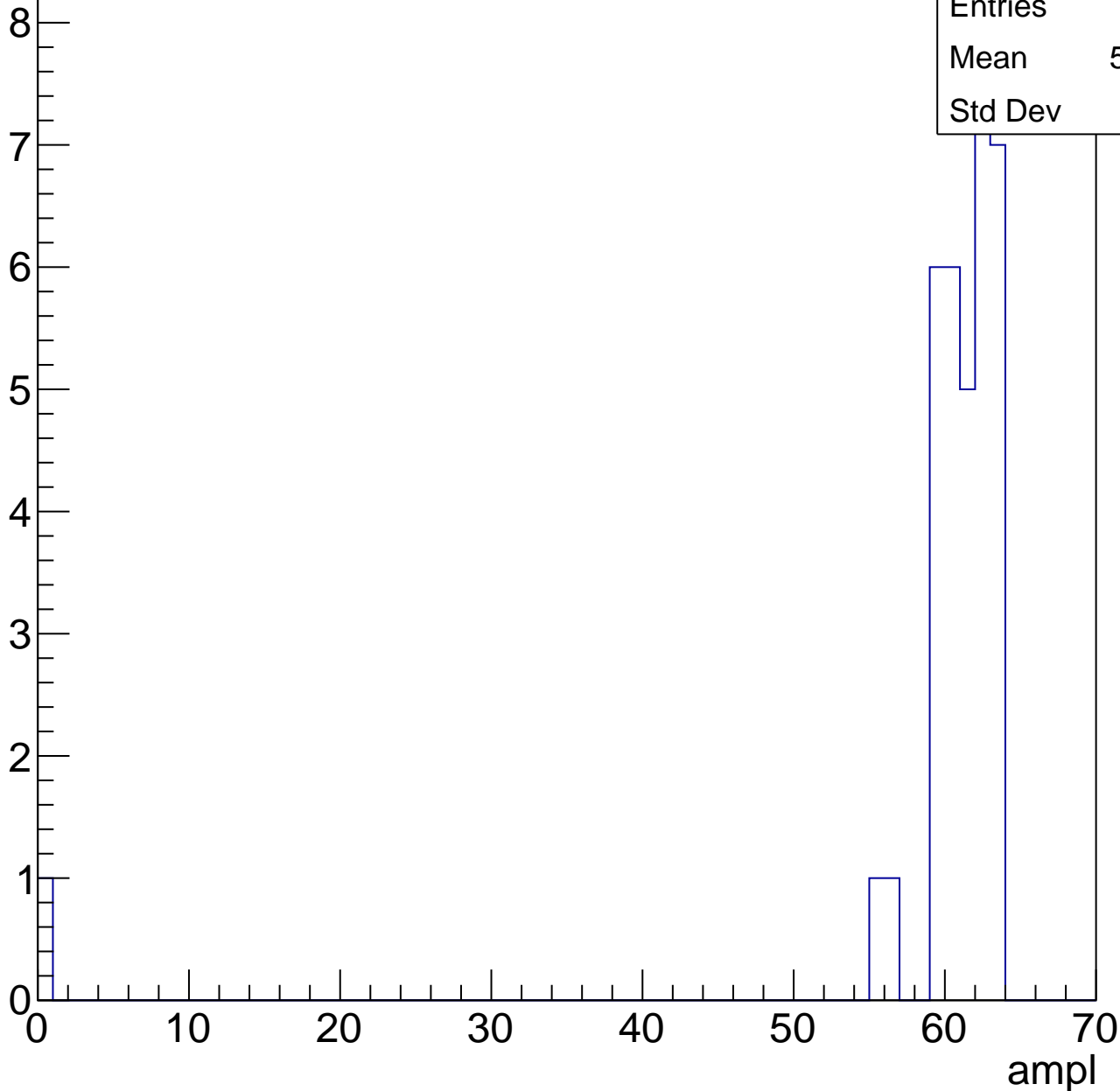


# B0L002S, U8-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

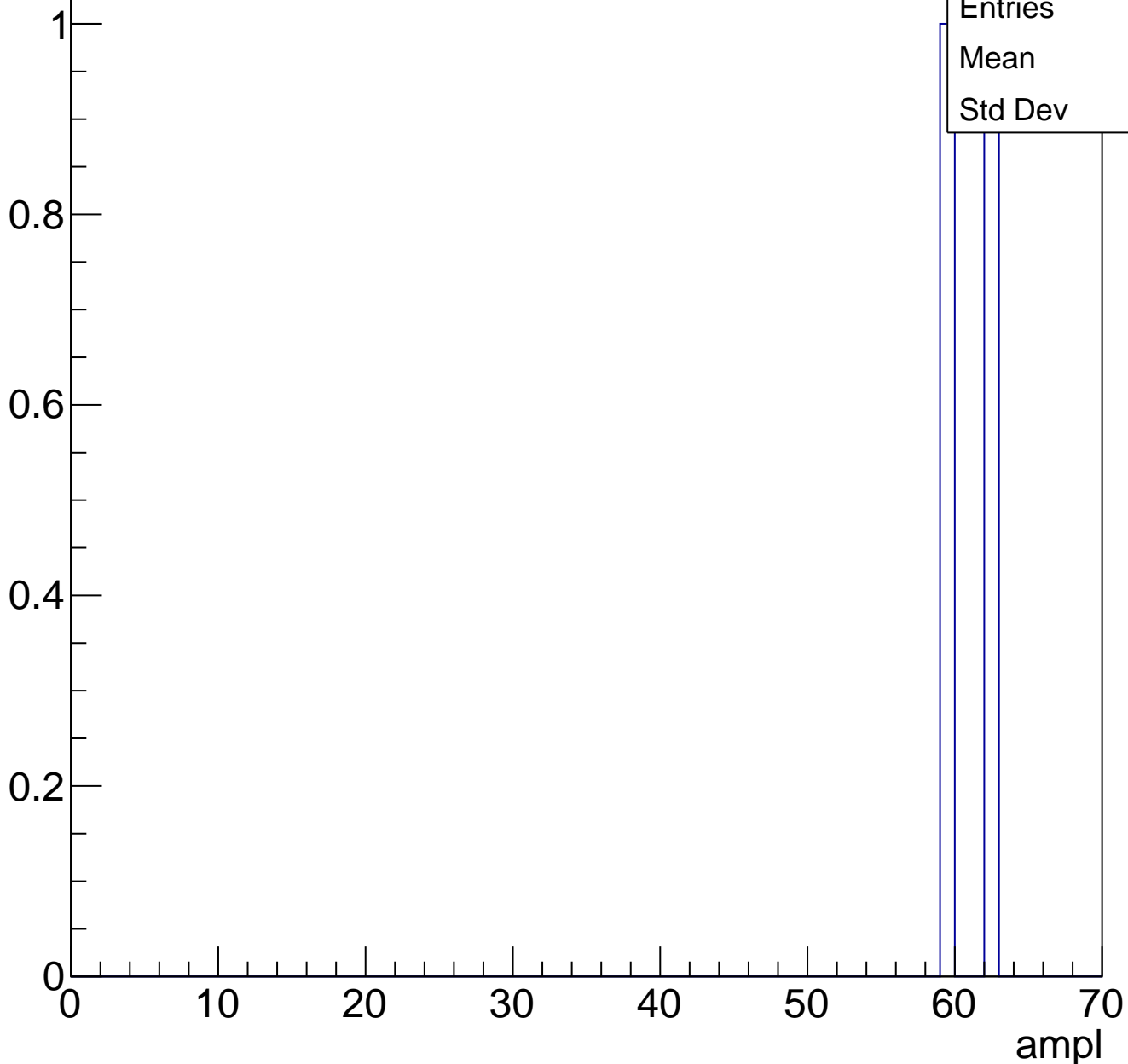
Entries	35
Mean	59.06
Std Dev	10.3



# B0L002S, U8-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	60.5
Std Dev	1.5



# B0L002S, U8-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch78, adc0

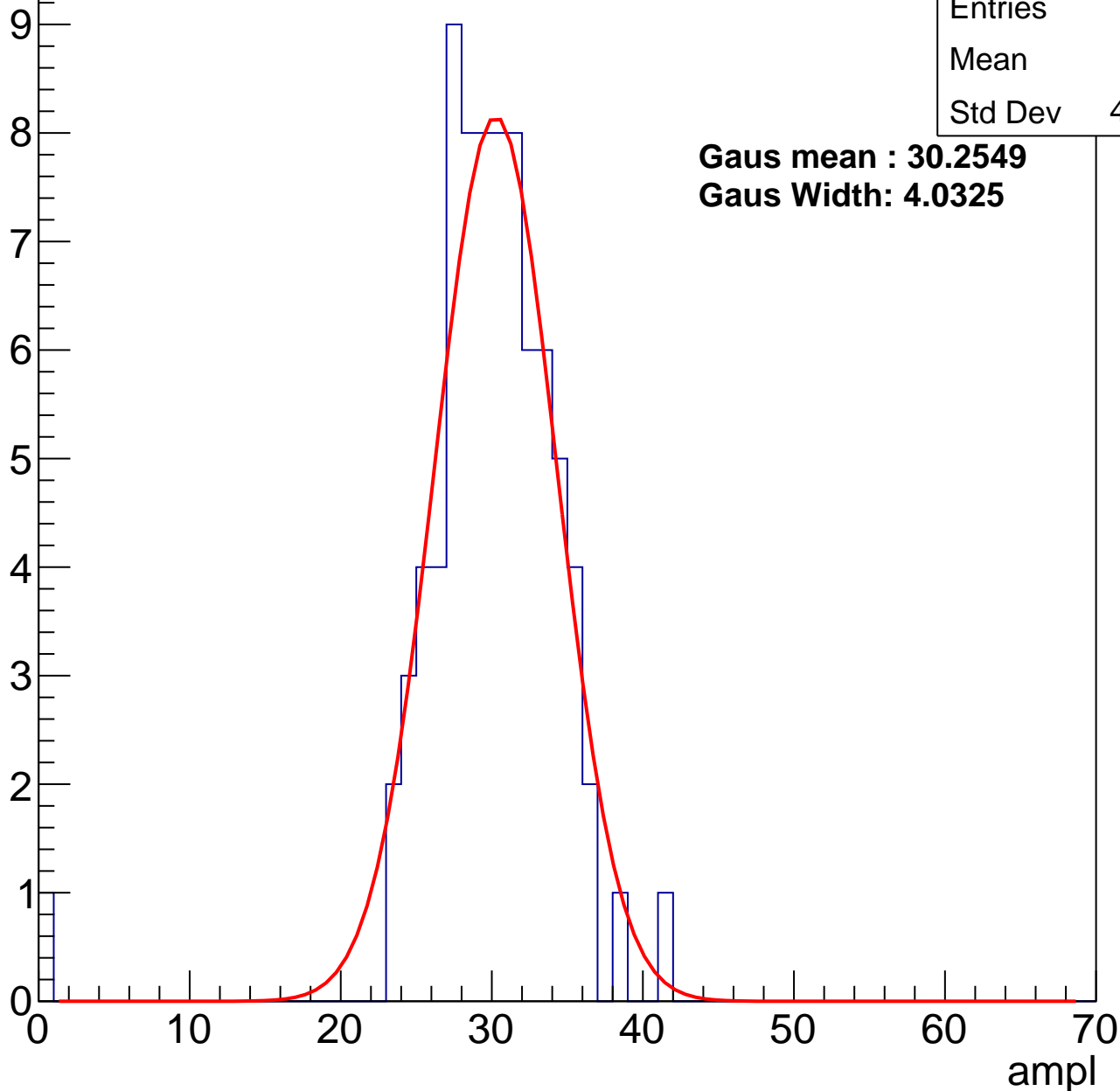
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	80
Mean	29.5
Std Dev	4.866

**Gaus mean : 30.2549**

**Gaus Width: 4.0325**



# B0L002S, U8-ch78, adc1

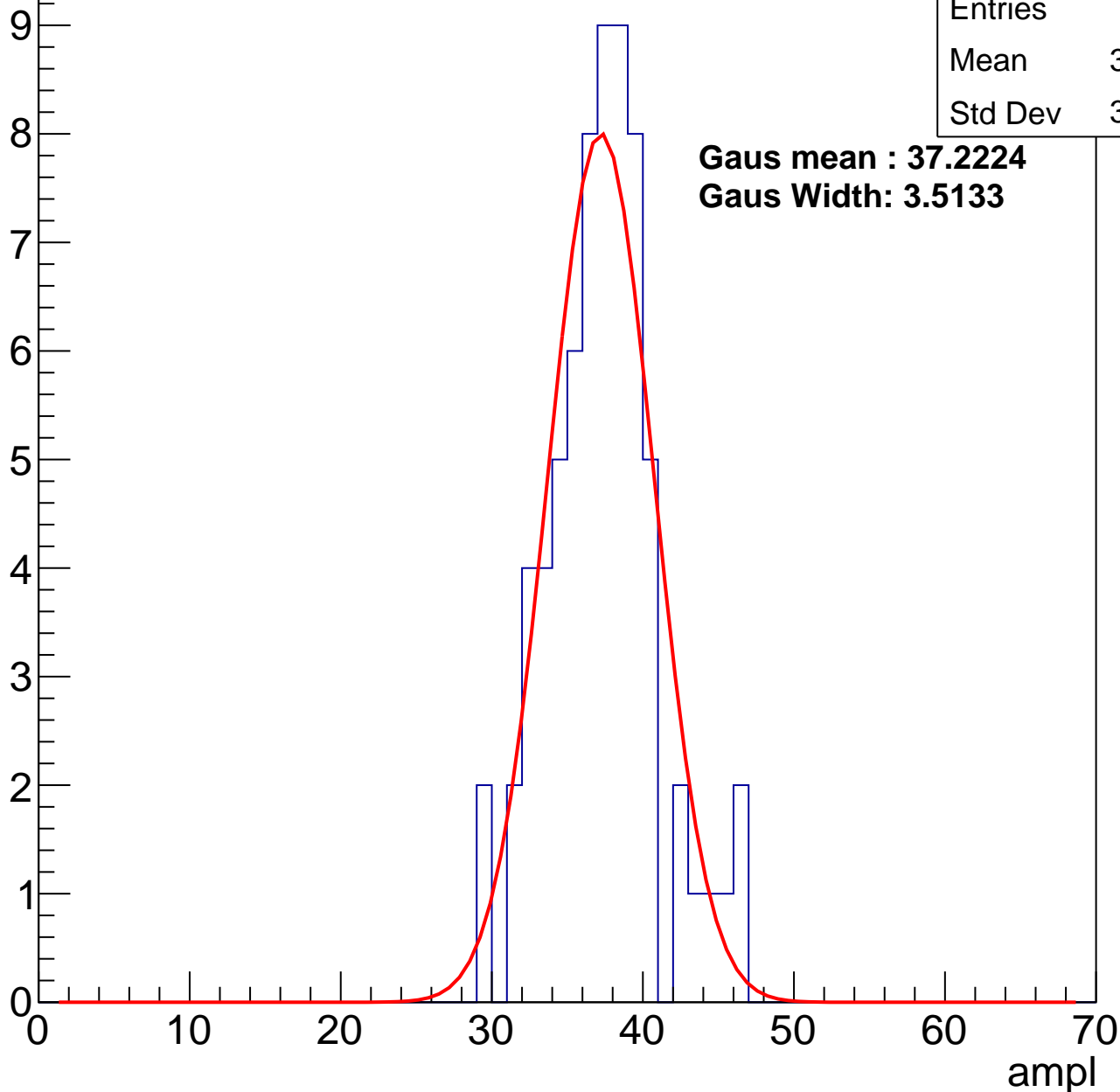
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.86
Std Dev	3.584

**Gaus mean : 37.2224**

**Gaus Width: 3.5133**



# B0L002S, U8-ch78, adc2

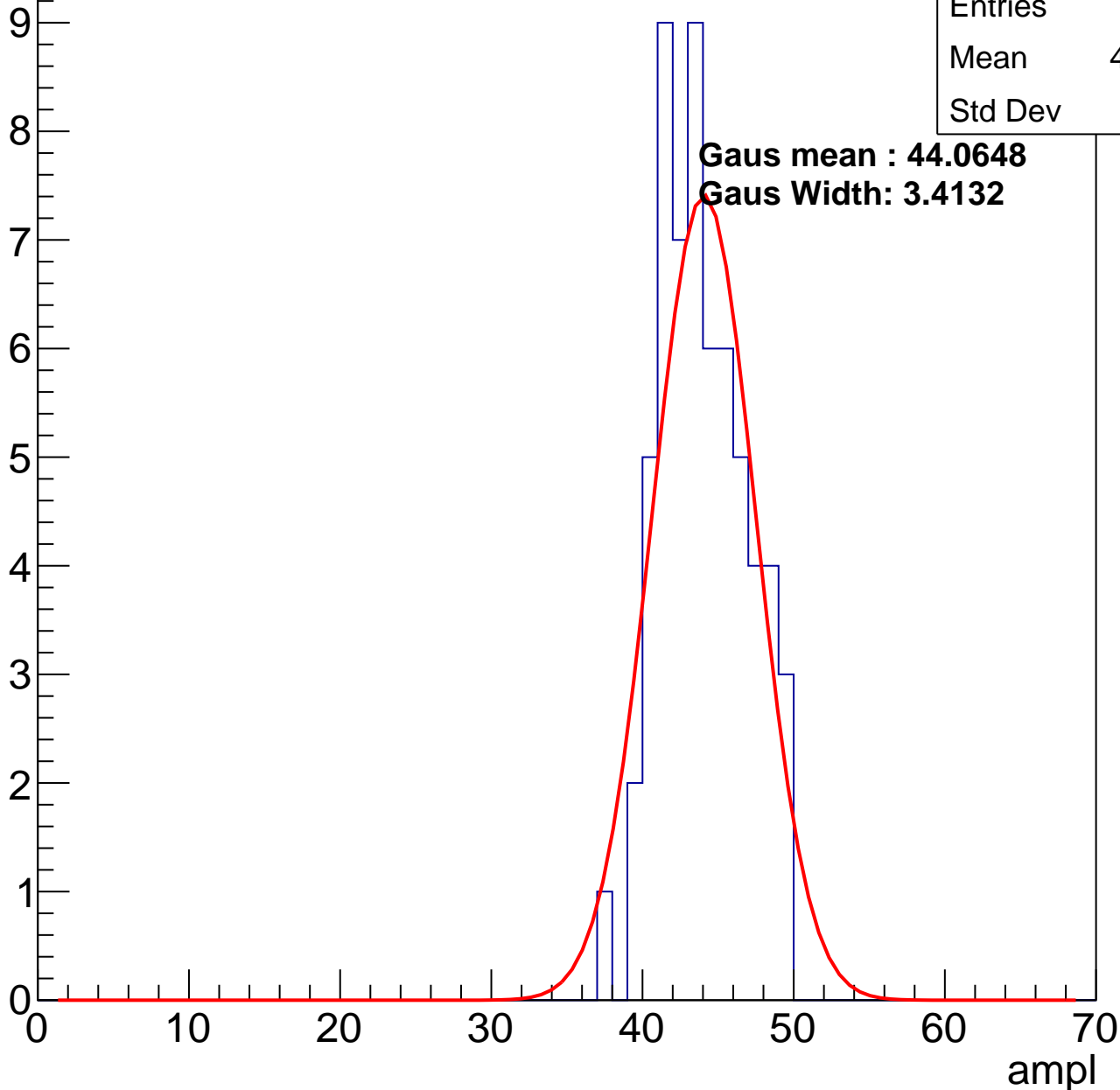
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.54
Std Dev	2.82

**Gaus mean : 44.0648**

**Gaus Width: 3.4132**



# B0L002S, U8-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

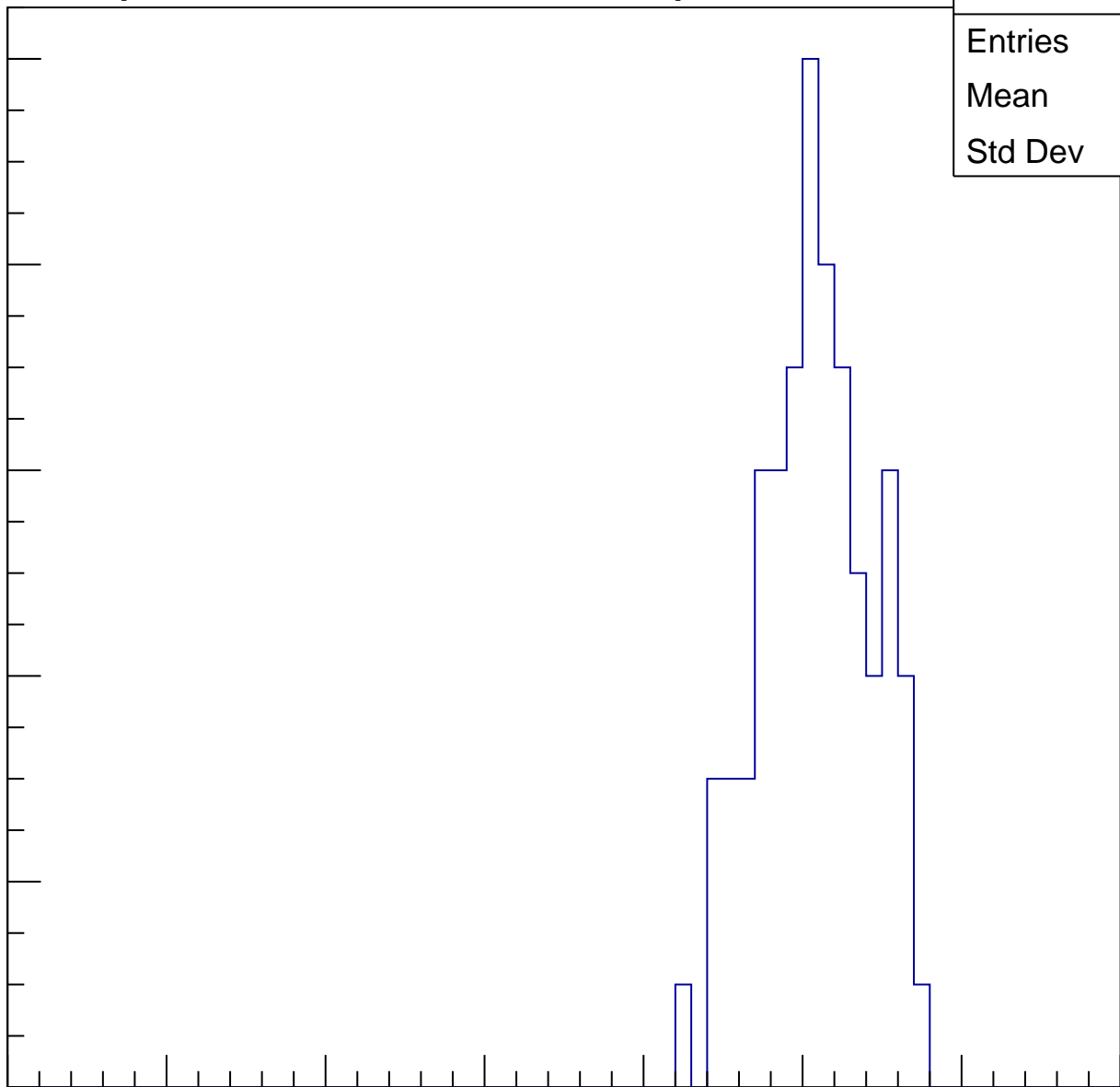
Entries	74
Mean	50.32
Std Dev	3.409

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch78, adc4

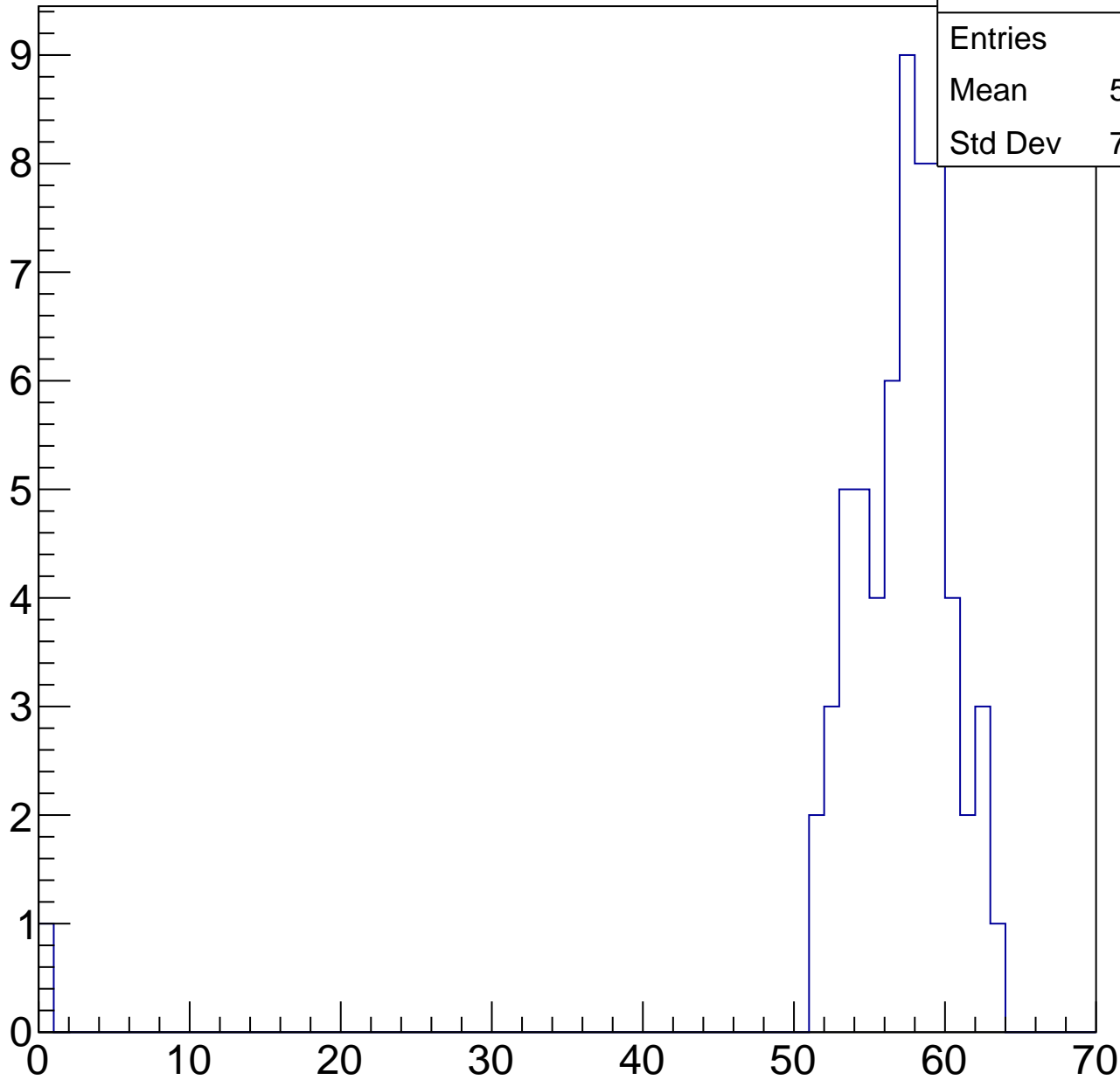
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	55.89
Std Dev	7.774

ampl

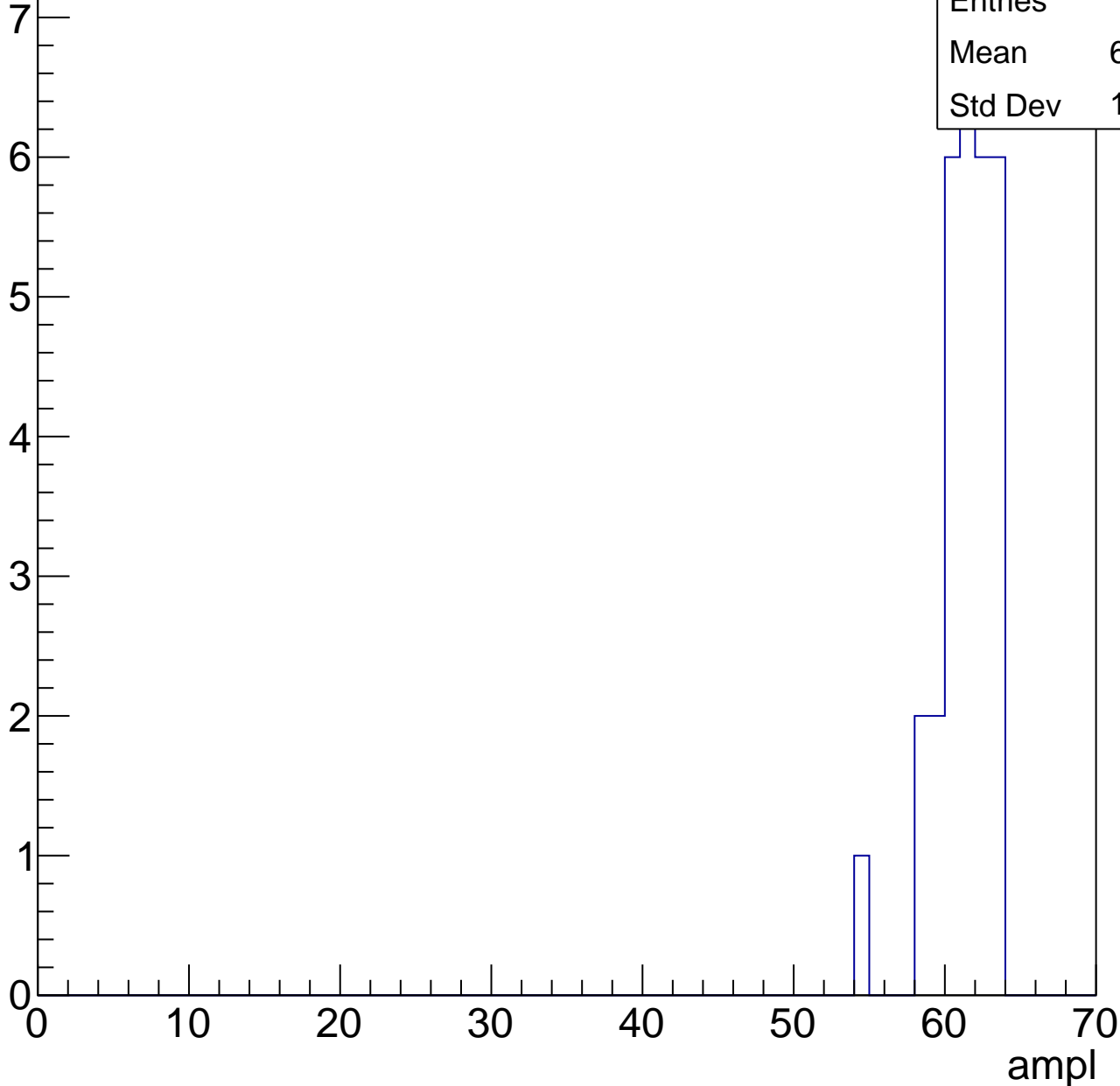


# B0L002S, U8-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.83
Std Dev	1.916



# B0L002S, U8-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch79, adc0

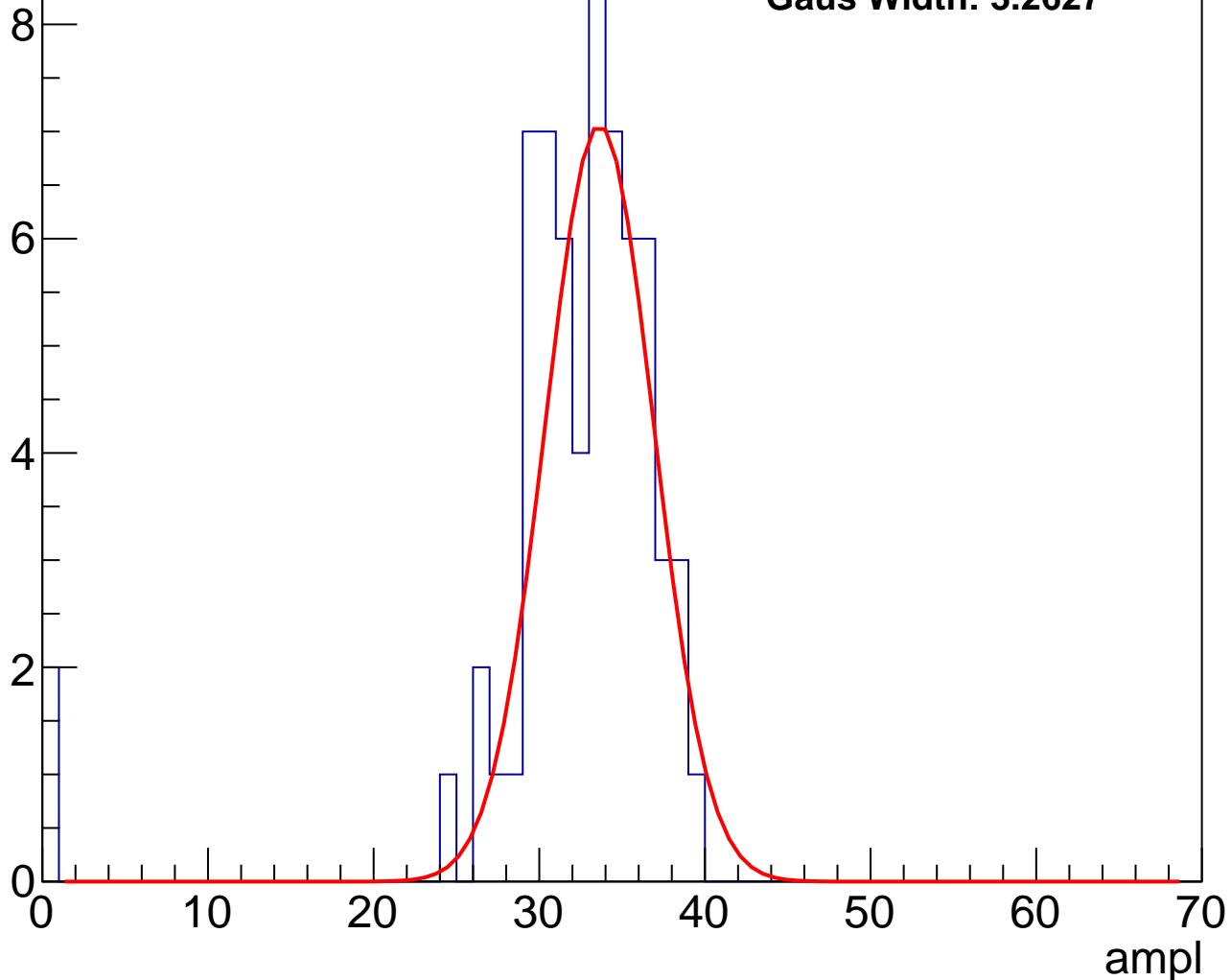
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	31.58
Std Dev	6.384

**Gaus mean : 33.6355**

**Gaus Width: 3.2627**



# B0L002S, U8-ch79, adc1

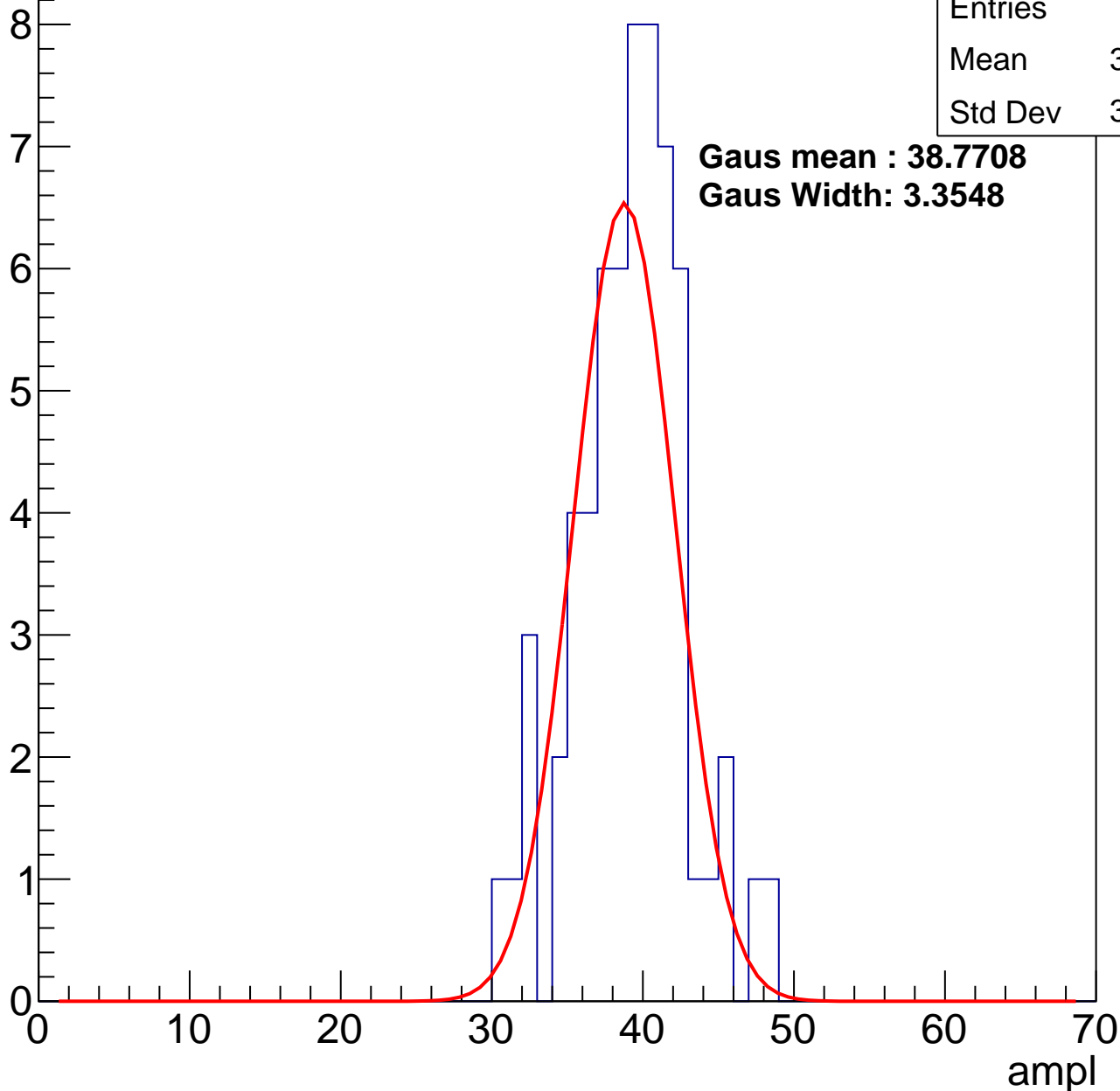
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.74
Std Dev	3.628

**Gaus mean : 38.7708**

**Gaus Width: 3.3548**



# B0L002S, U8-ch79, adc2

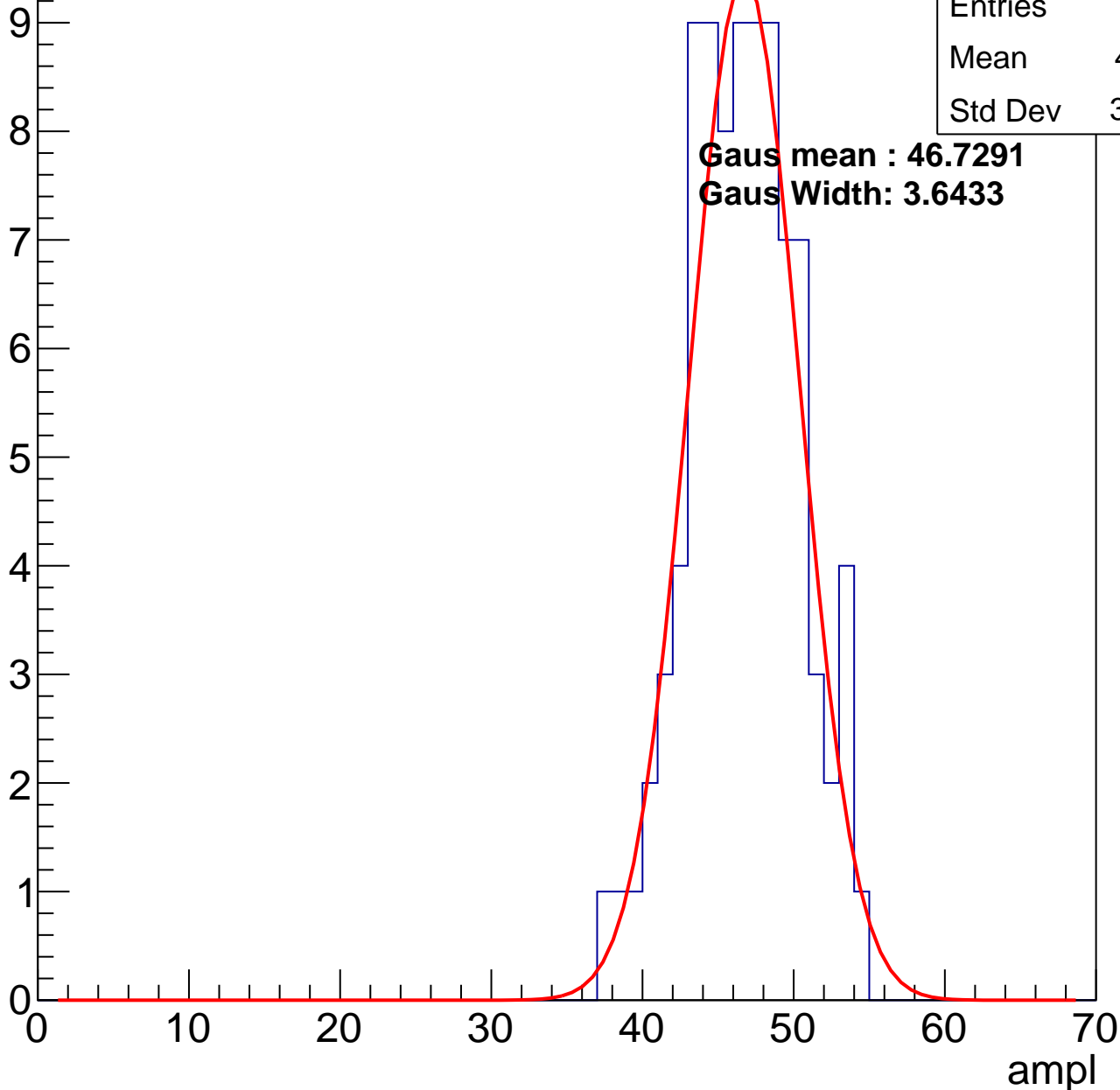
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	89
Mean	46.21
Std Dev	3.599

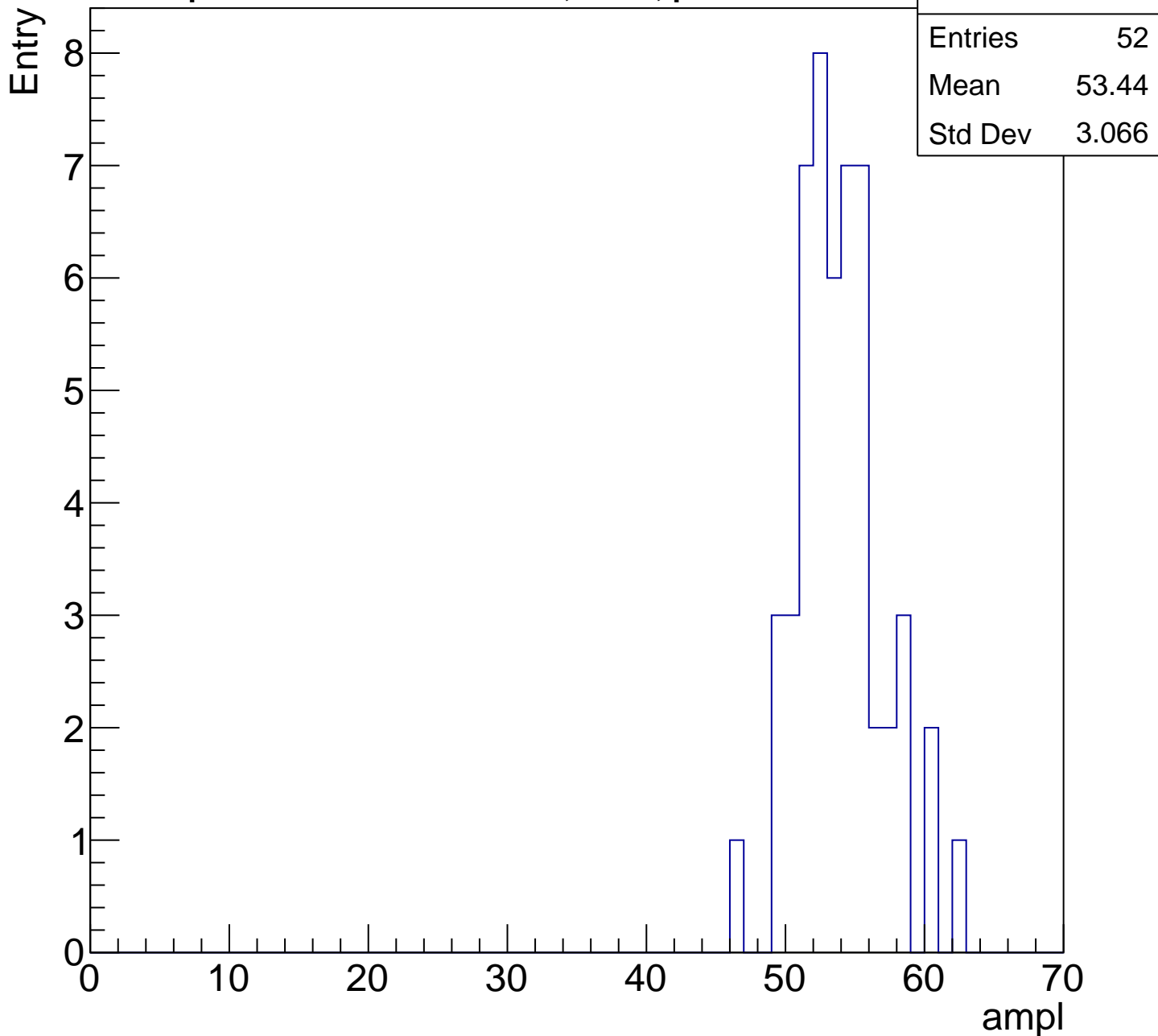
**Gaus mean : 46.7291**

**Gaus Width: 3.6433**



# B0L002S, U8-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

Entries 56

Mean 58.66

Std Dev 2.473

8

6

4

2

0

0

10

20

30

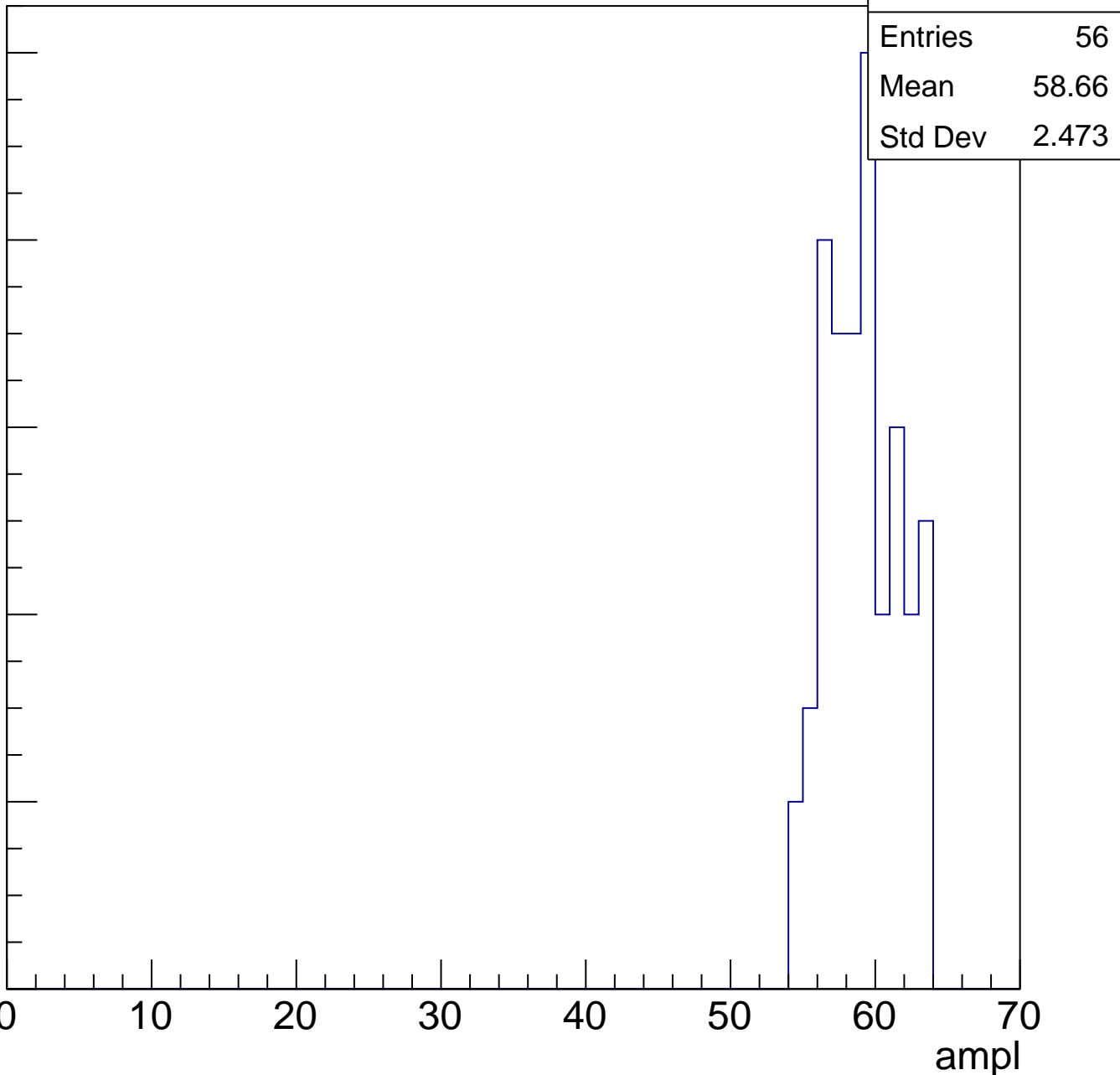
40

50

60

70

ampl

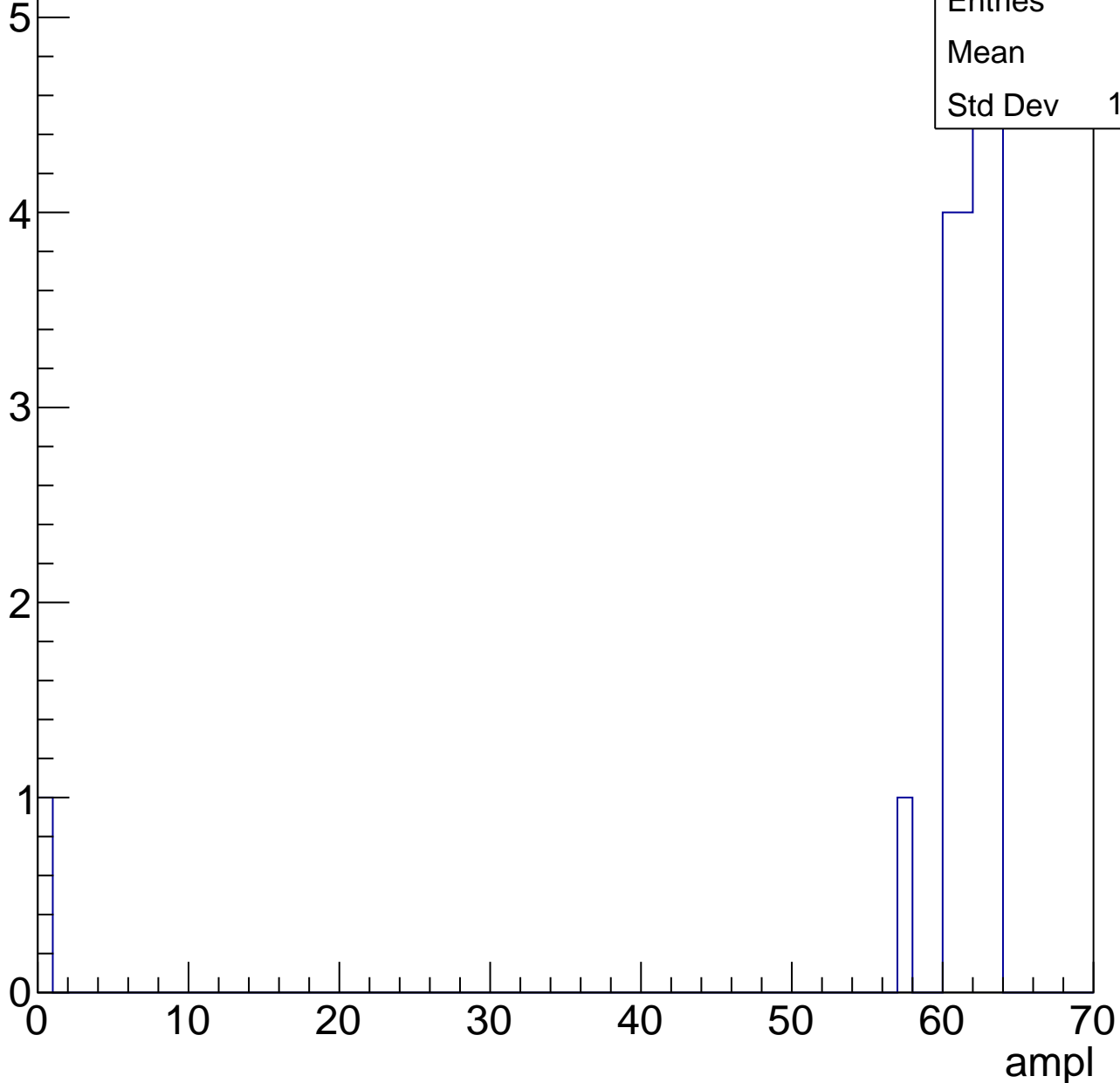


# B0L002S, U8-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.3
Std Dev	13.45



# B0L002S, U8-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch80, adc0

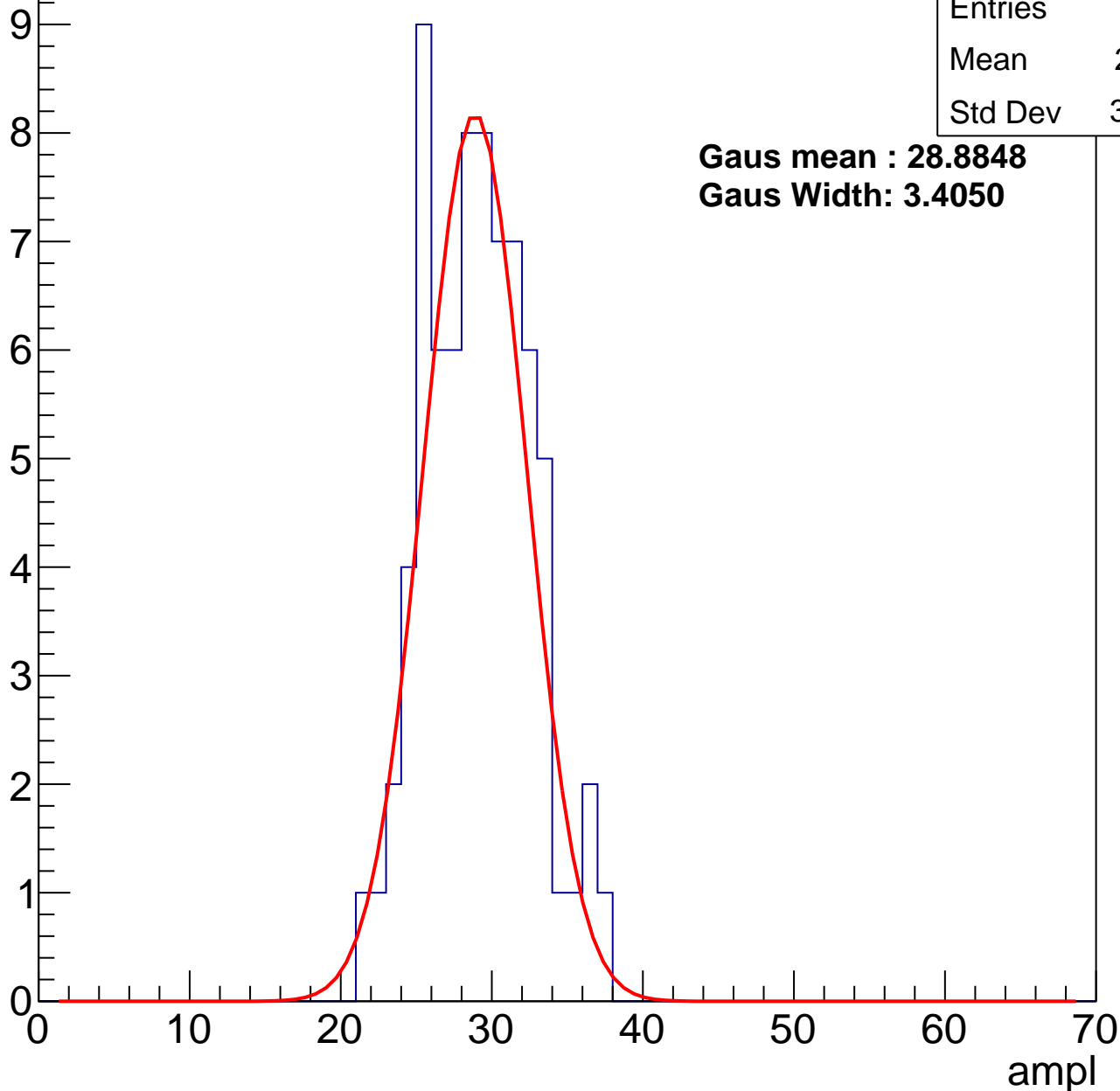
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	28.61
Std Dev	3.456

**Gaus mean : 28.8848**

**Gaus Width: 3.4050**



# B0L002S, U8-ch80, adc1

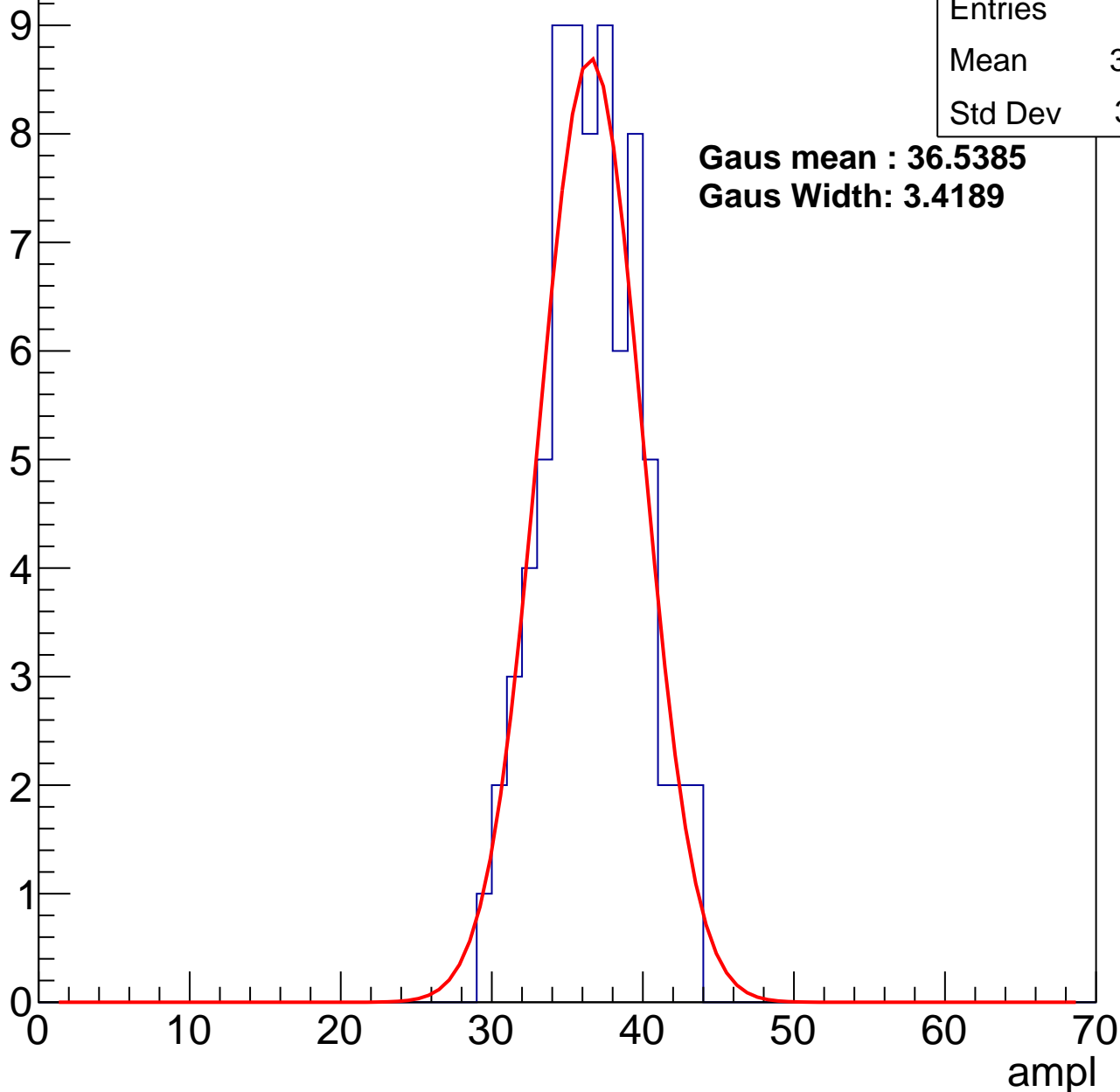
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.12
Std Dev	3.171

**Gaus mean : 36.5385**

**Gaus Width: 3.4189**



# B0L002S, U8-ch80, adc2

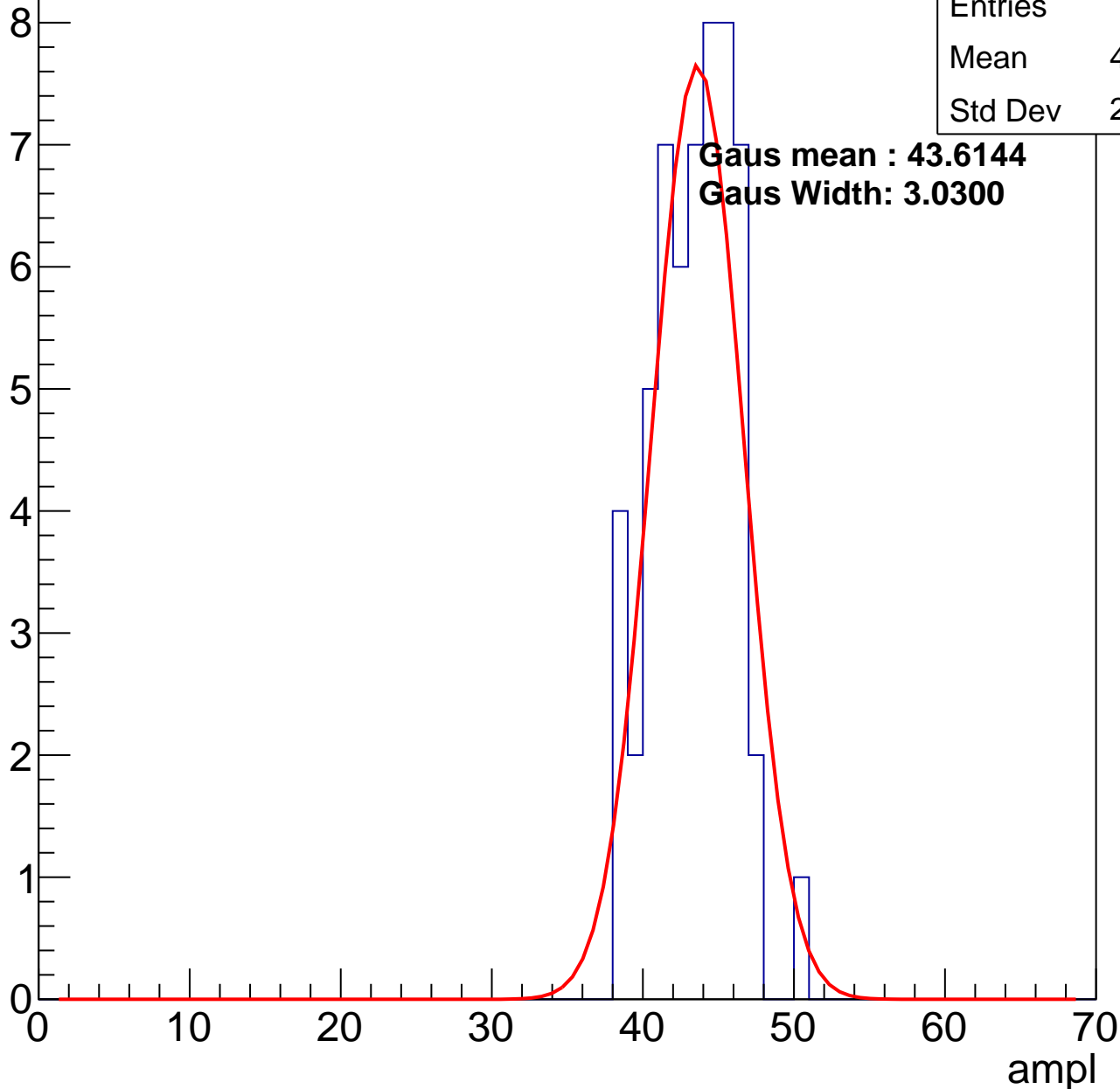
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.95
Std Dev	2.632

**Gaus mean : 43.6144**

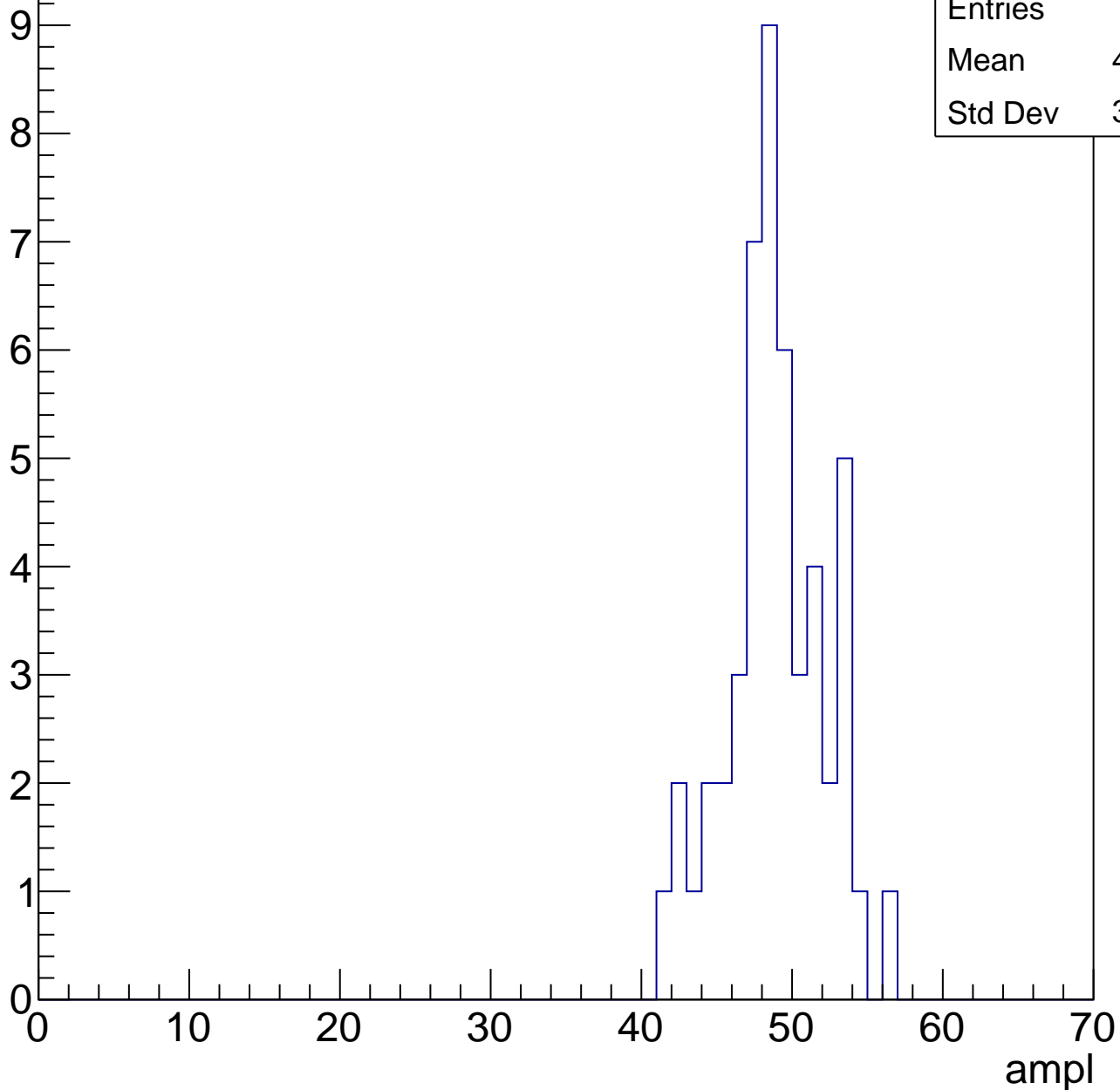
**Gaus Width: 3.0300**



# B0L002S, U8-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

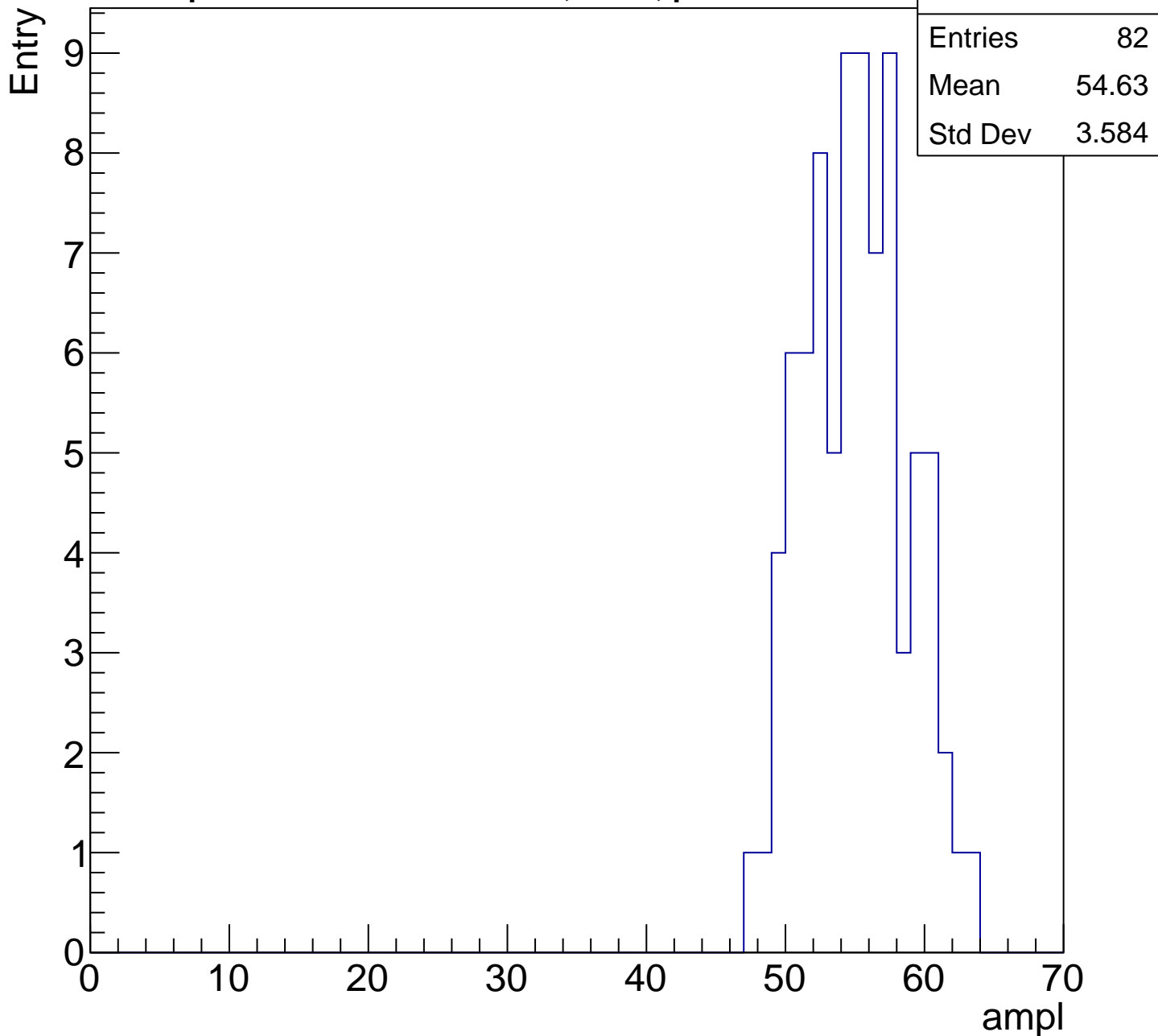
Entry



Entries	49
Mean	48.41
Std Dev	3.251

# B0L002S, U8-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

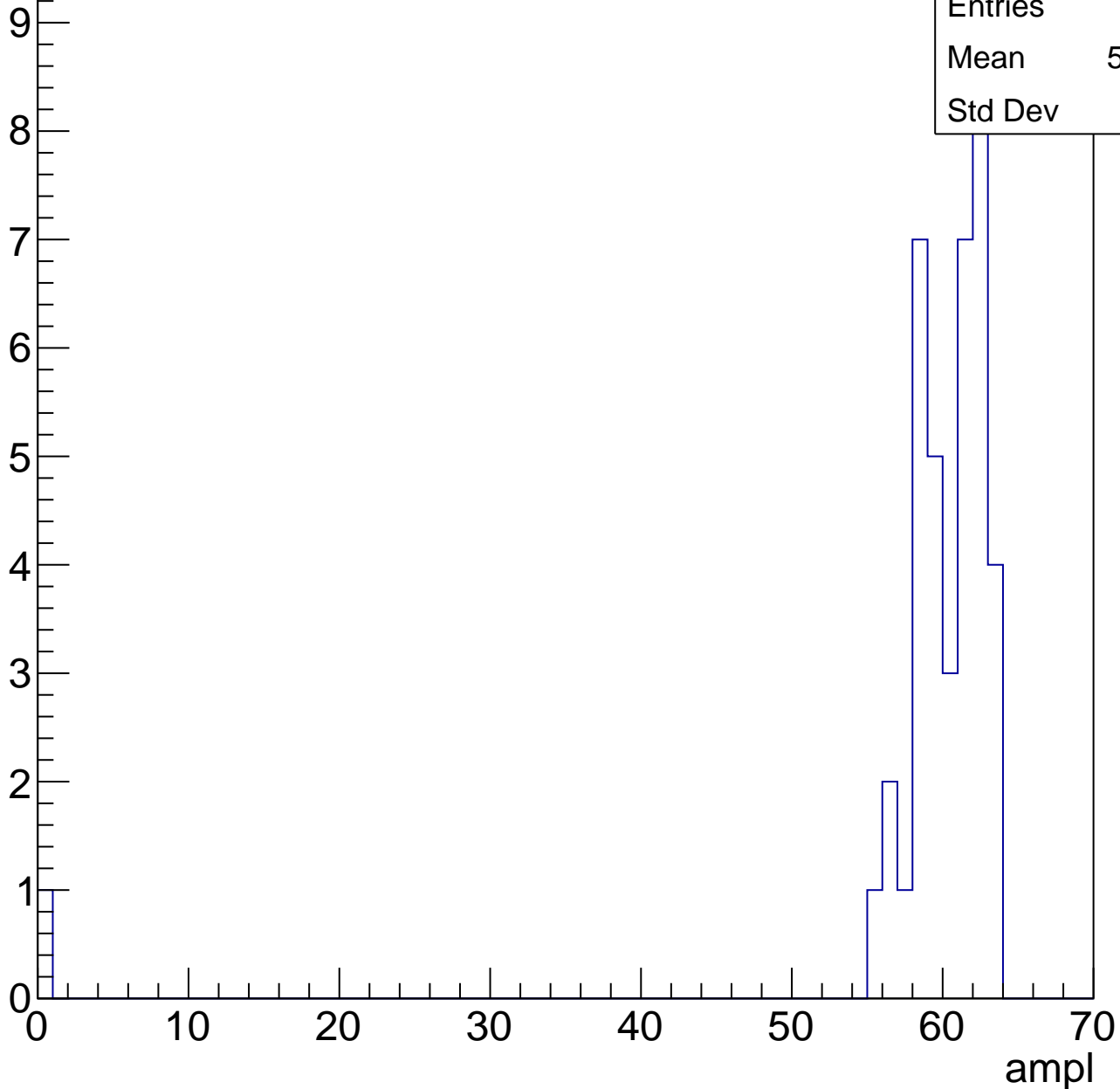


# B0L002S, U8-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.55
Std Dev	9.61

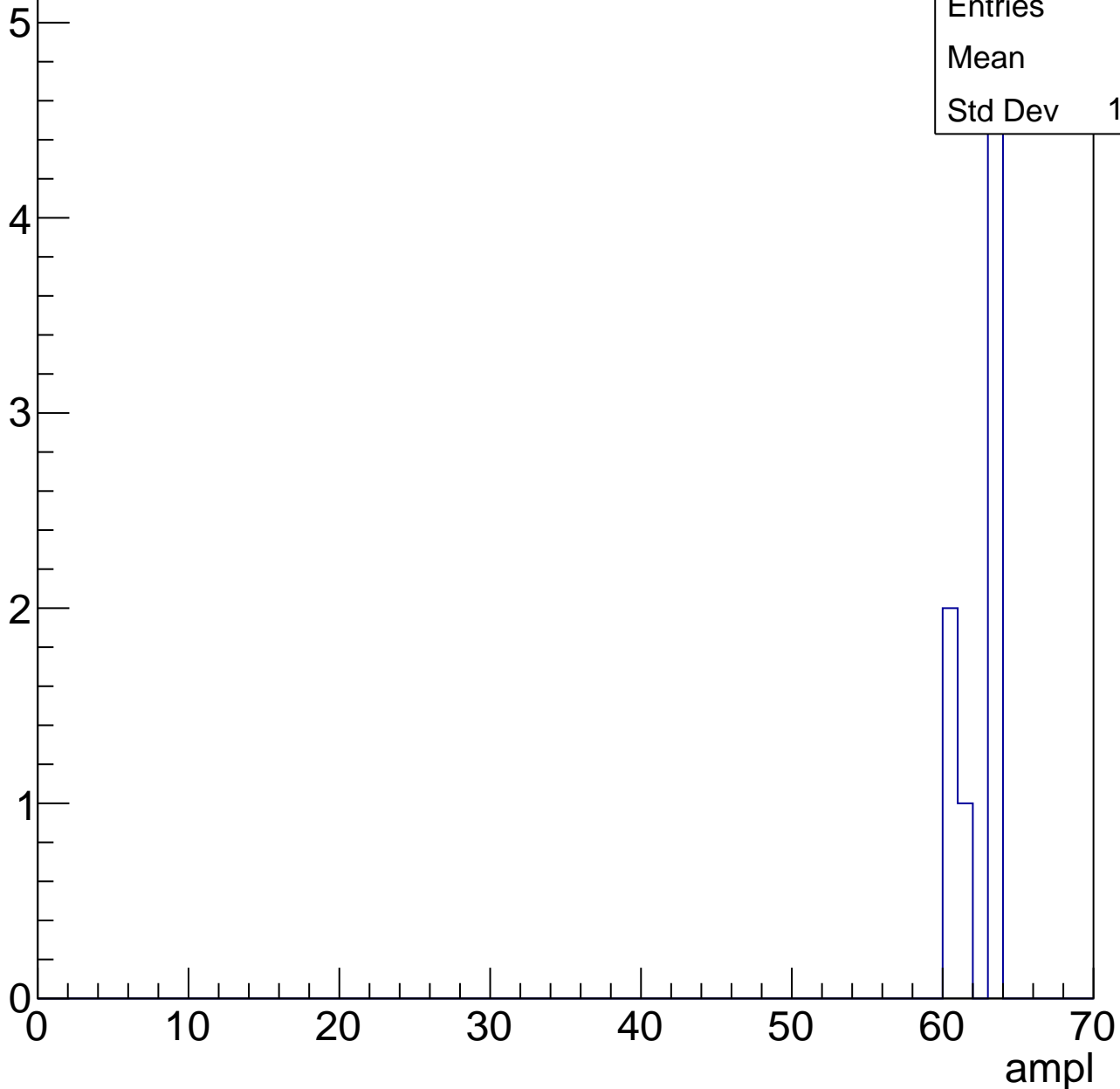


# B0L002S, U8-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	8
Mean	62
Std Dev	1.323





# B0L002S, U8-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch81, adc0

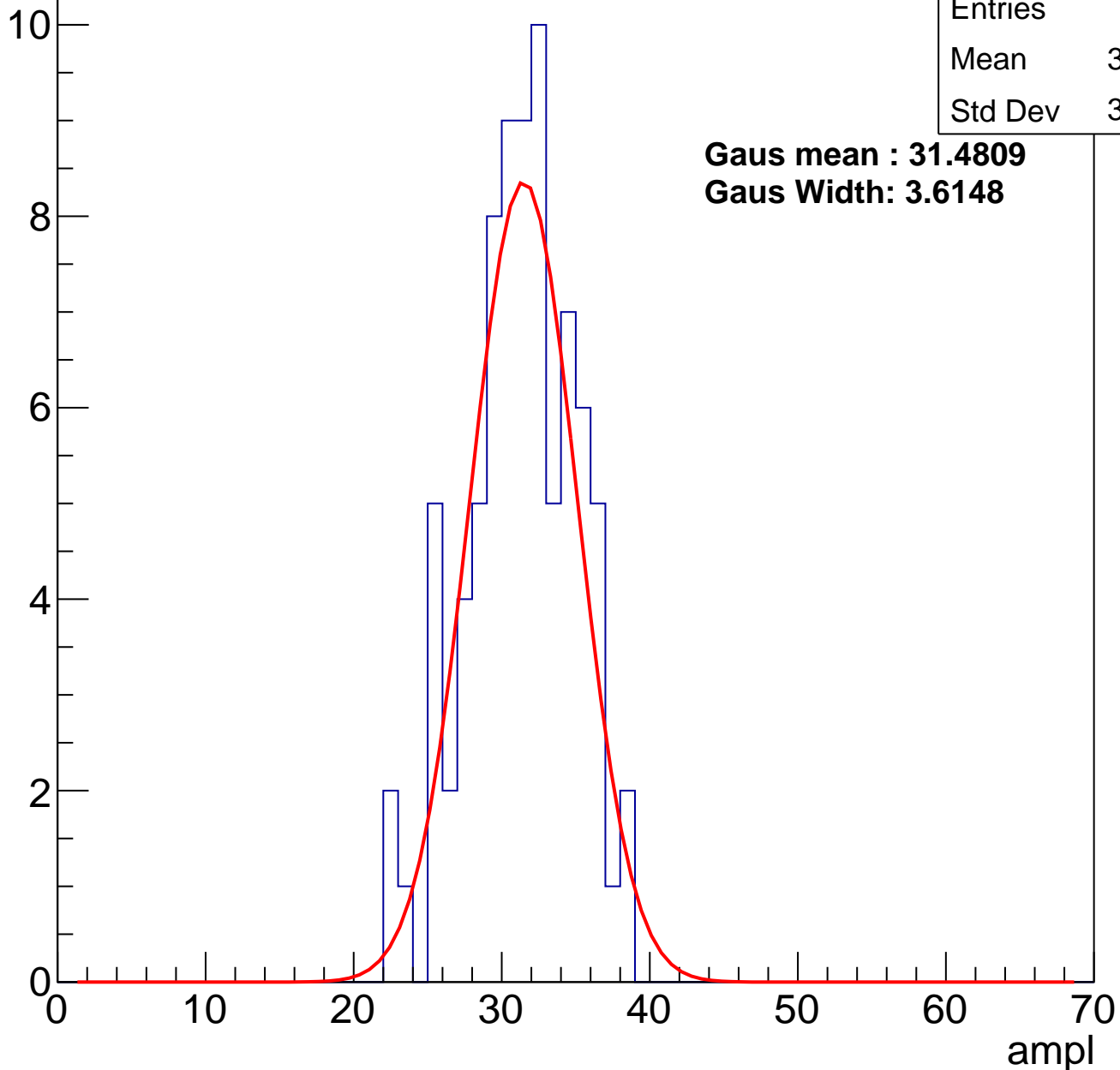
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	81
Mean	30.85
Std Dev	3.604

**Gaus mean : 31.4809**

**Gaus Width: 3.6148**

Entry



# B0L002S, U8-ch81, adc1

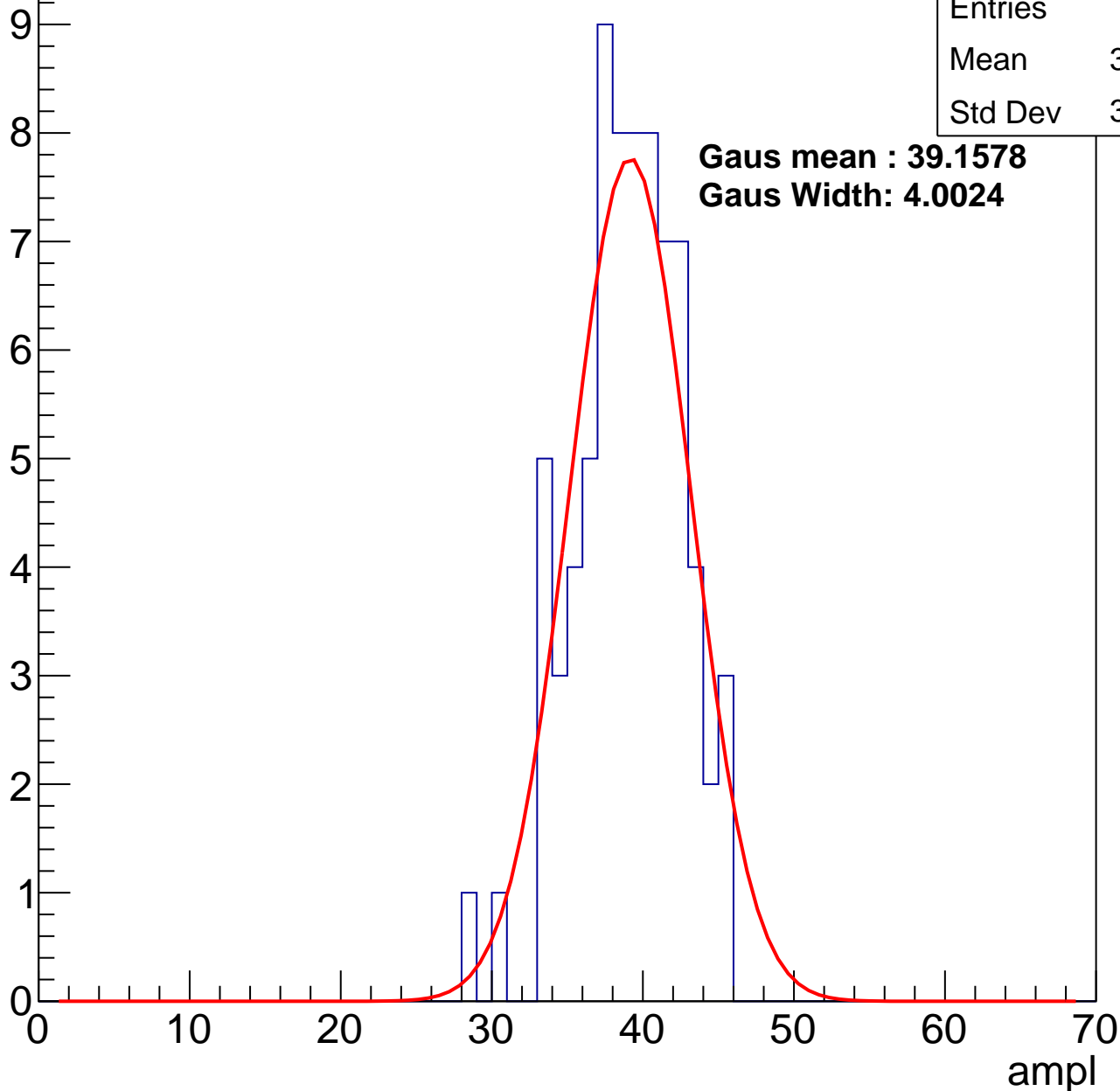
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.53
Std Dev	3.492

**Gaus mean : 39.1578**

**Gaus Width: 4.0024**



# B0L002S, U8-ch81, adc2

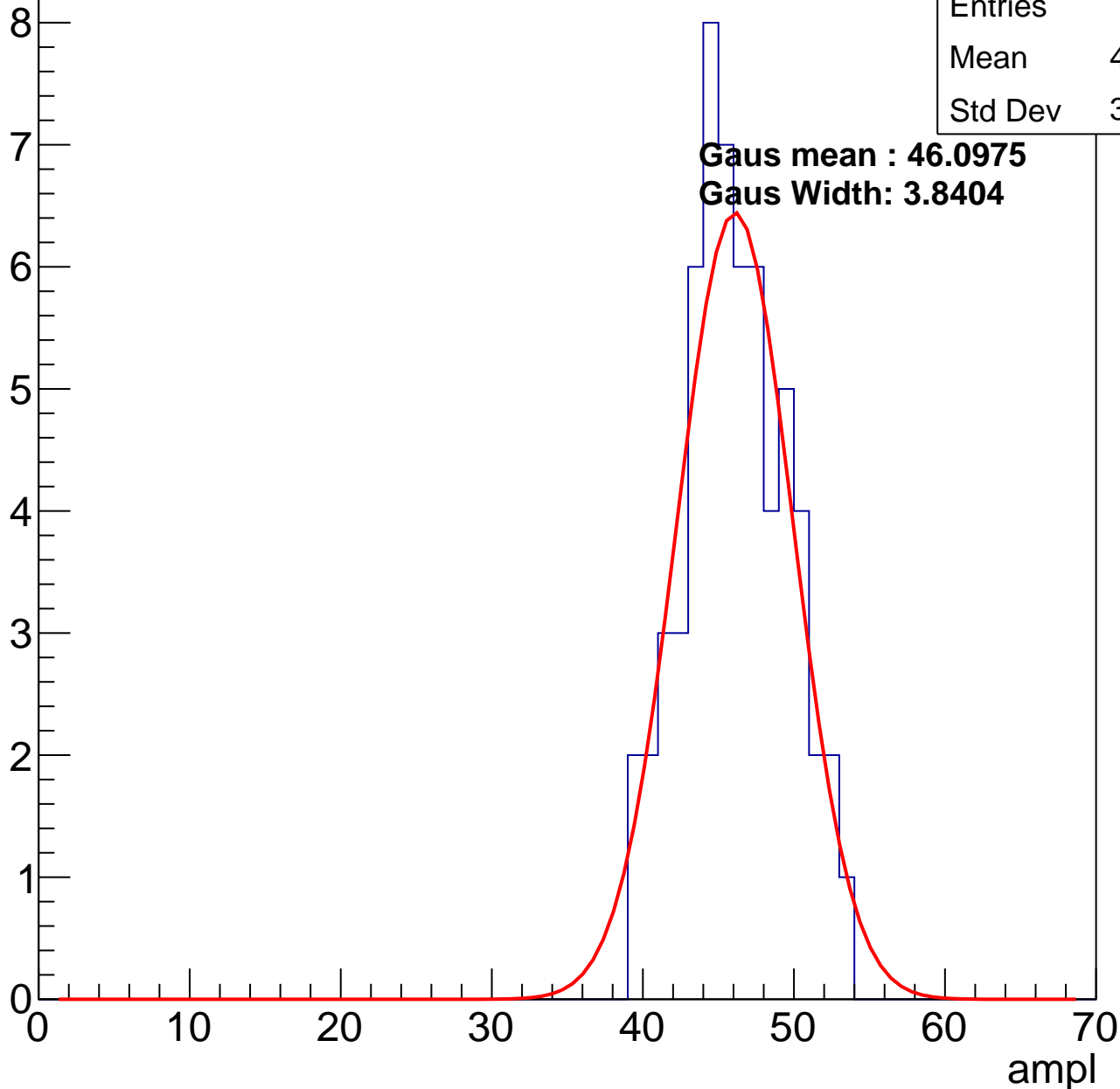
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.67
Std Dev	3.342

**Gaus mean : 46.0975**

**Gaus Width: 3.8404**

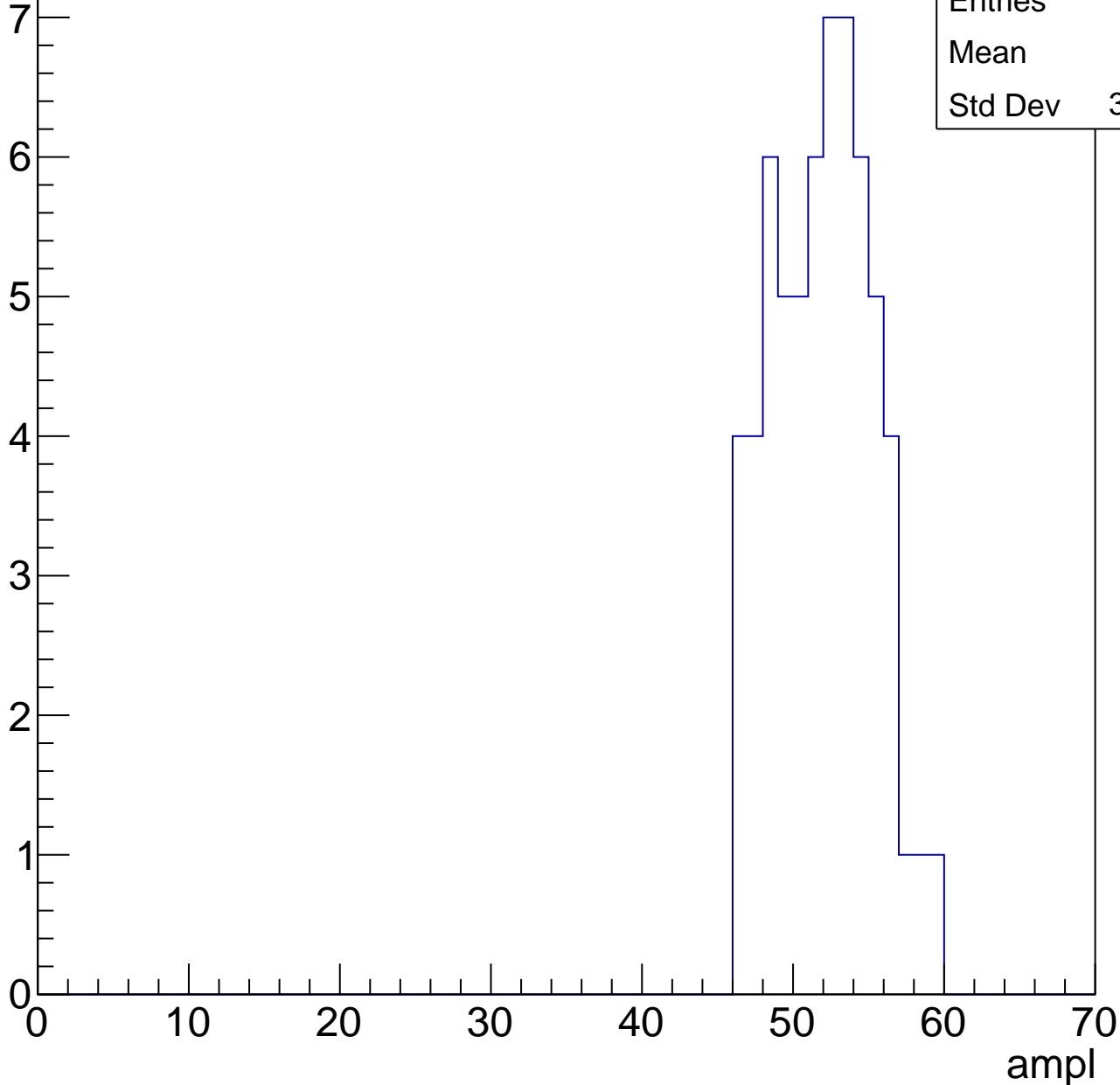


# B0L002S, U8-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.5
Std Dev	3.227

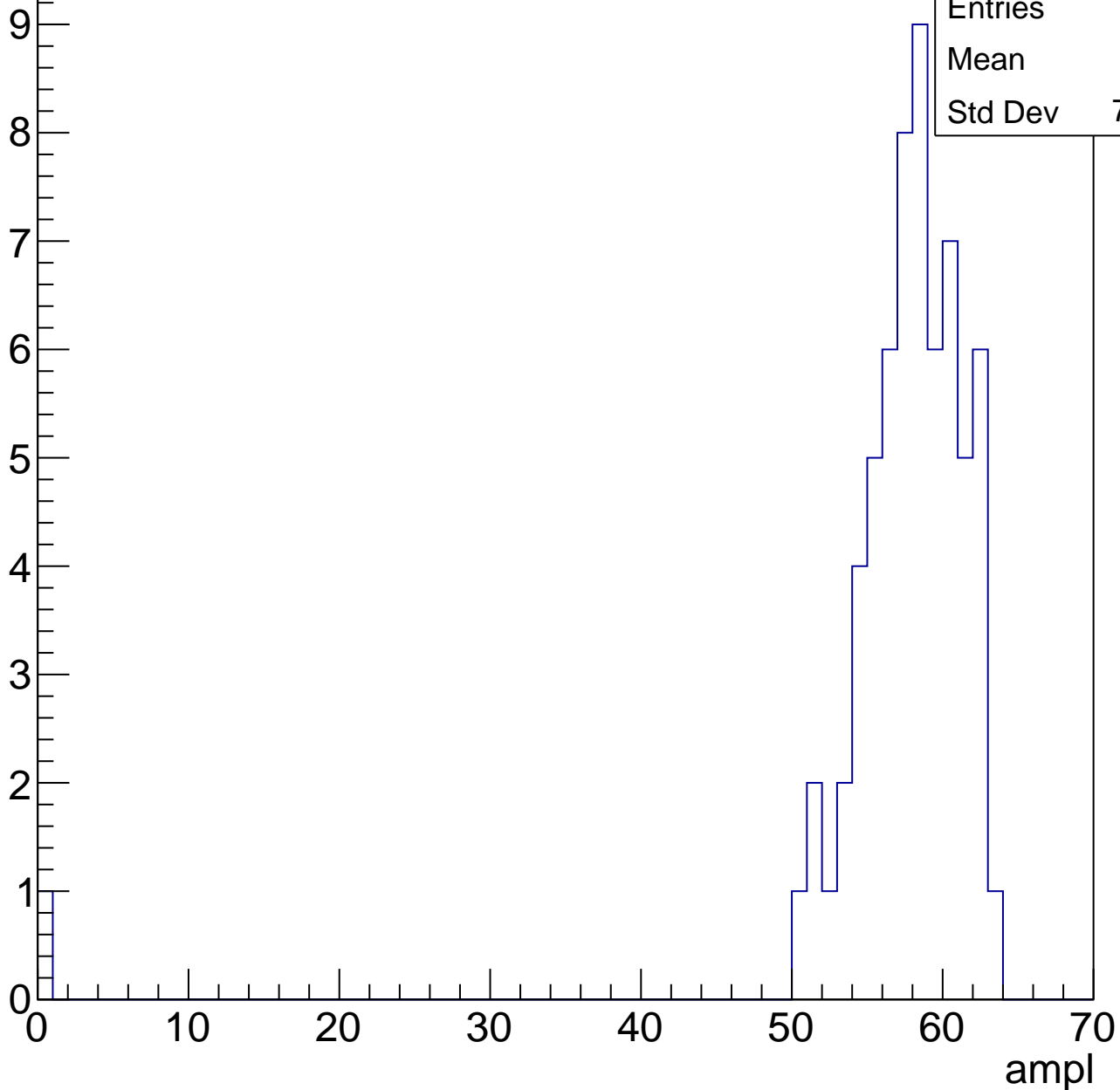


# B0L002S, U8-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	56.7
Std Dev	7.751

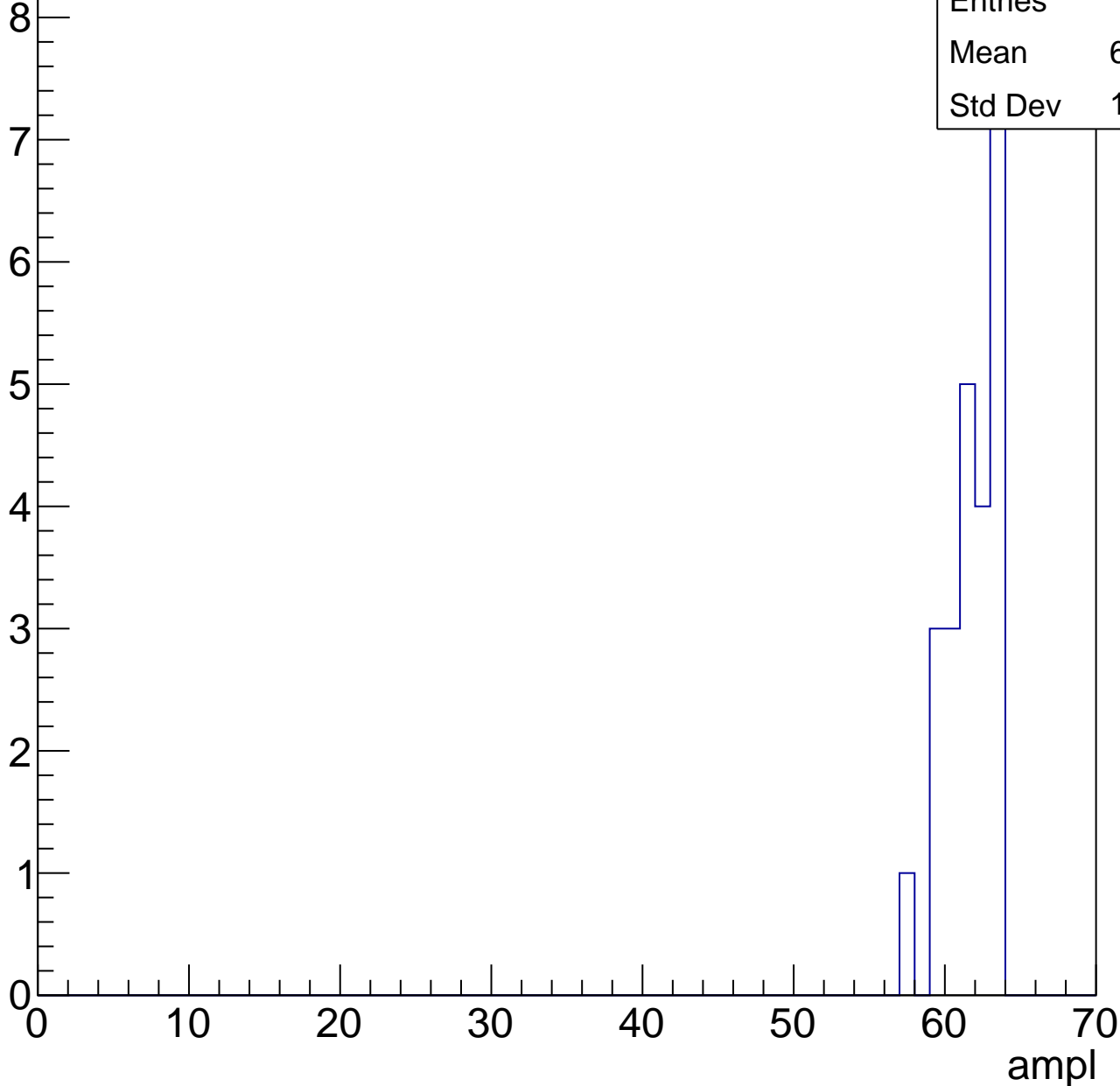


# B0L002S, U8-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.29
Std Dev	1.645



# B0L002S, U8-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0



# B0L002S, U8-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch82, adc0

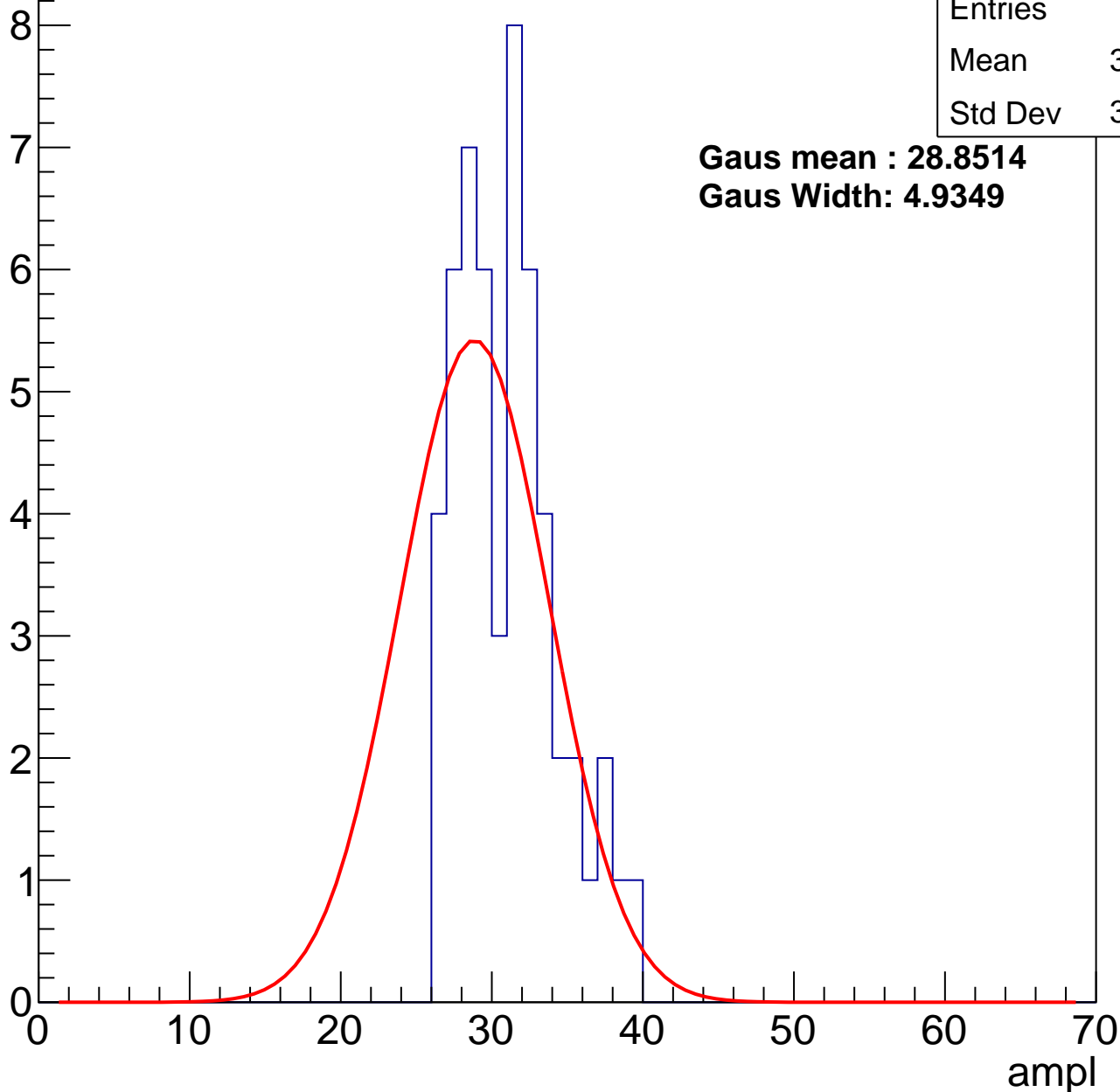
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	30.62
Std Dev	3.252

**Gaus mean : 28.8514**

**Gaus Width: 4.9349**



# B0L002S, U8-ch82, adc1

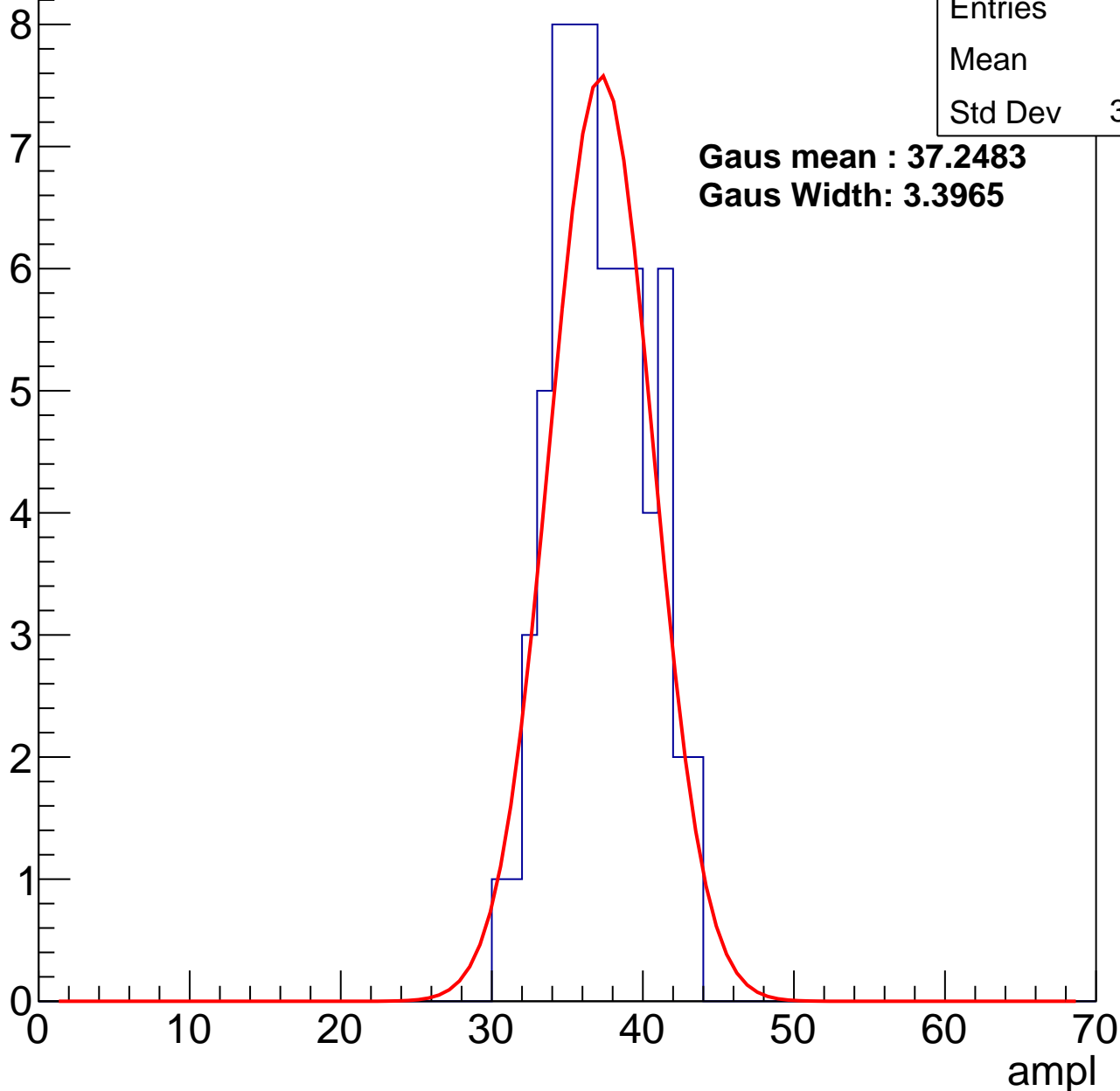
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.7
Std Dev	3.089

**Gaus mean : 37.2483**

**Gaus Width: 3.3965**



# B0L002S, U8-ch82, adc2

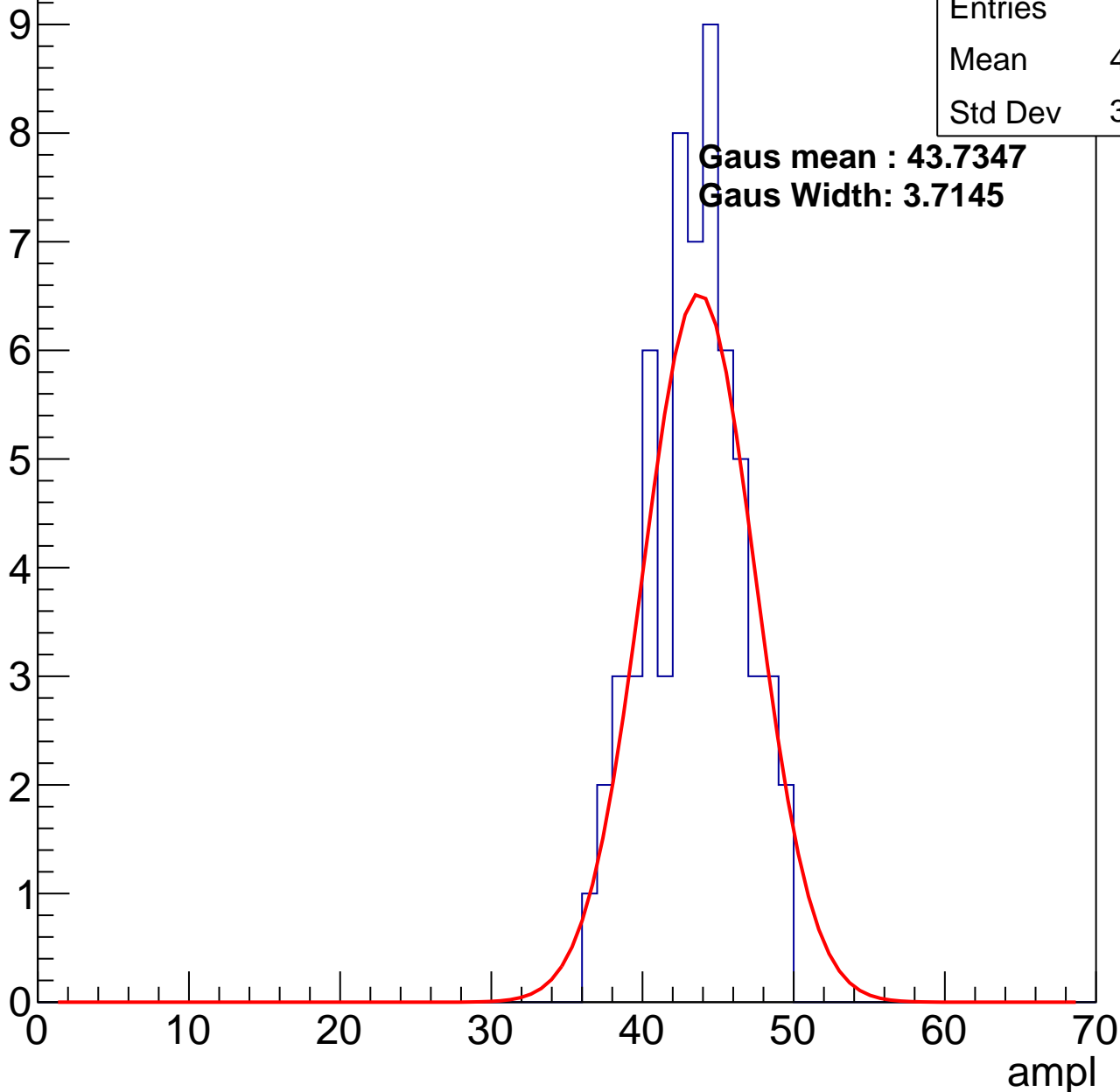
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.95
Std Dev	3.112

**Gaus mean : 43.7347**

**Gaus Width: 3.7145**



# B0L002S, U8-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

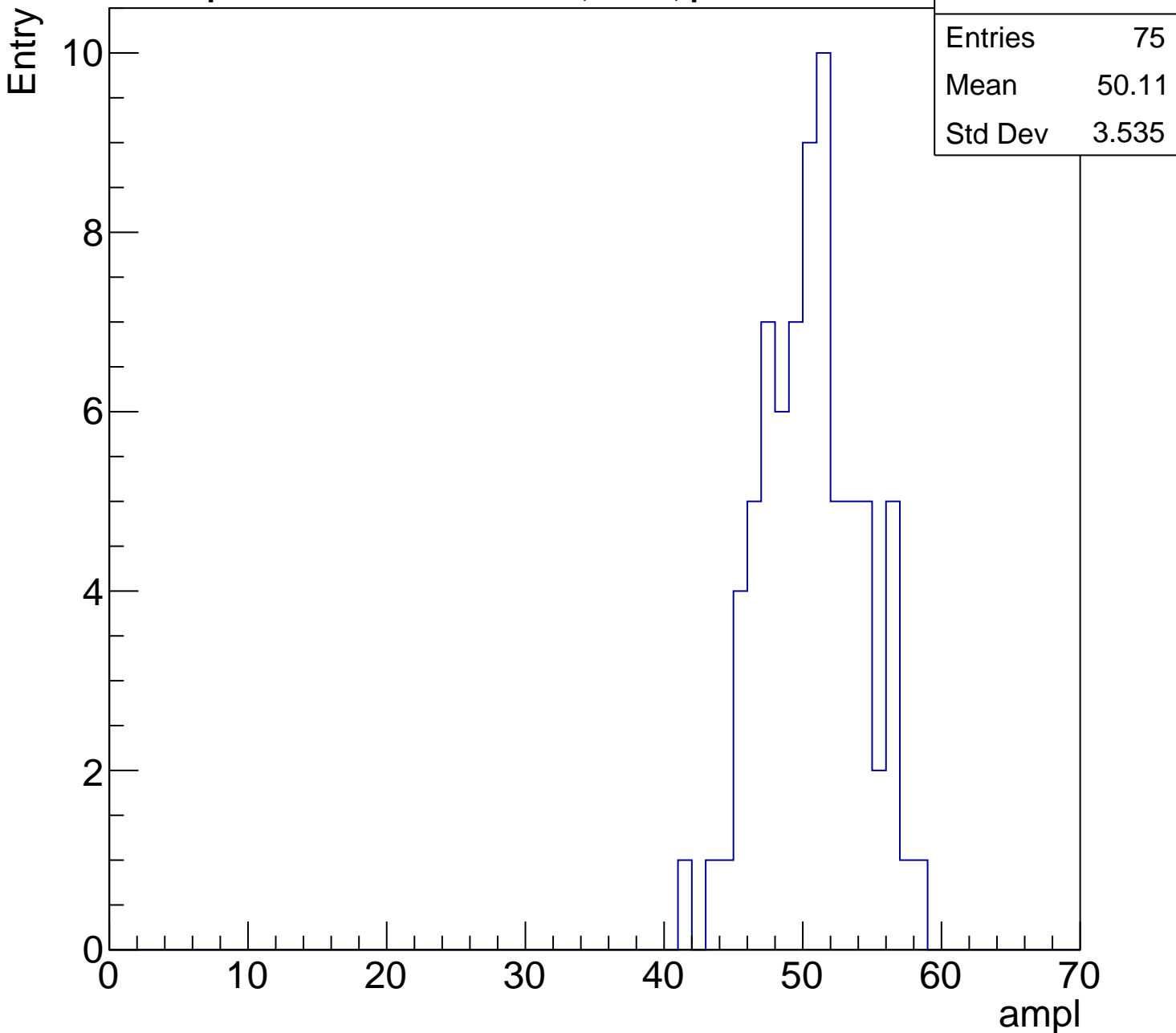
Entries	75
Mean	50.11
Std Dev	3.535

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

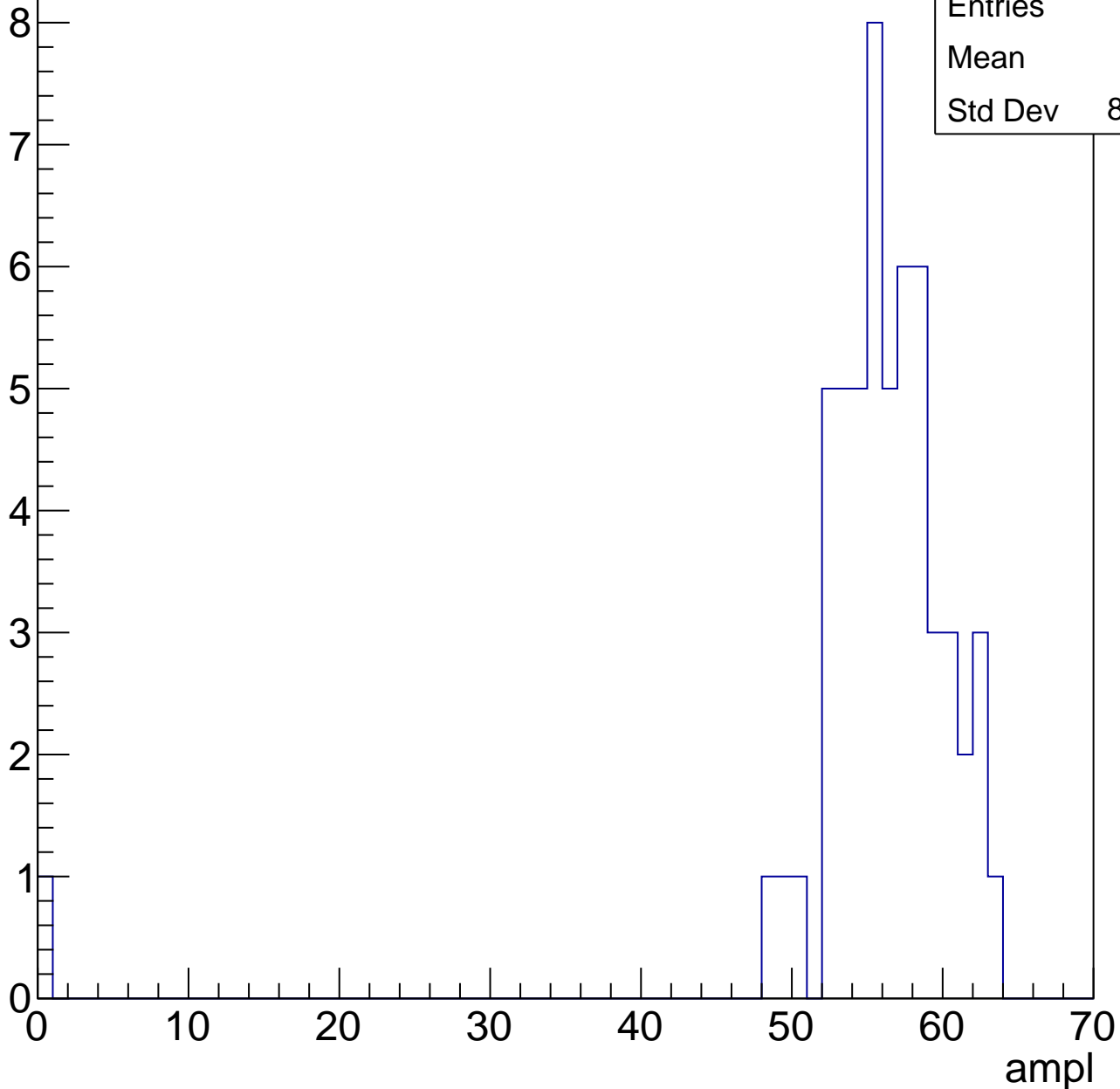


# B0L002S, U8-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

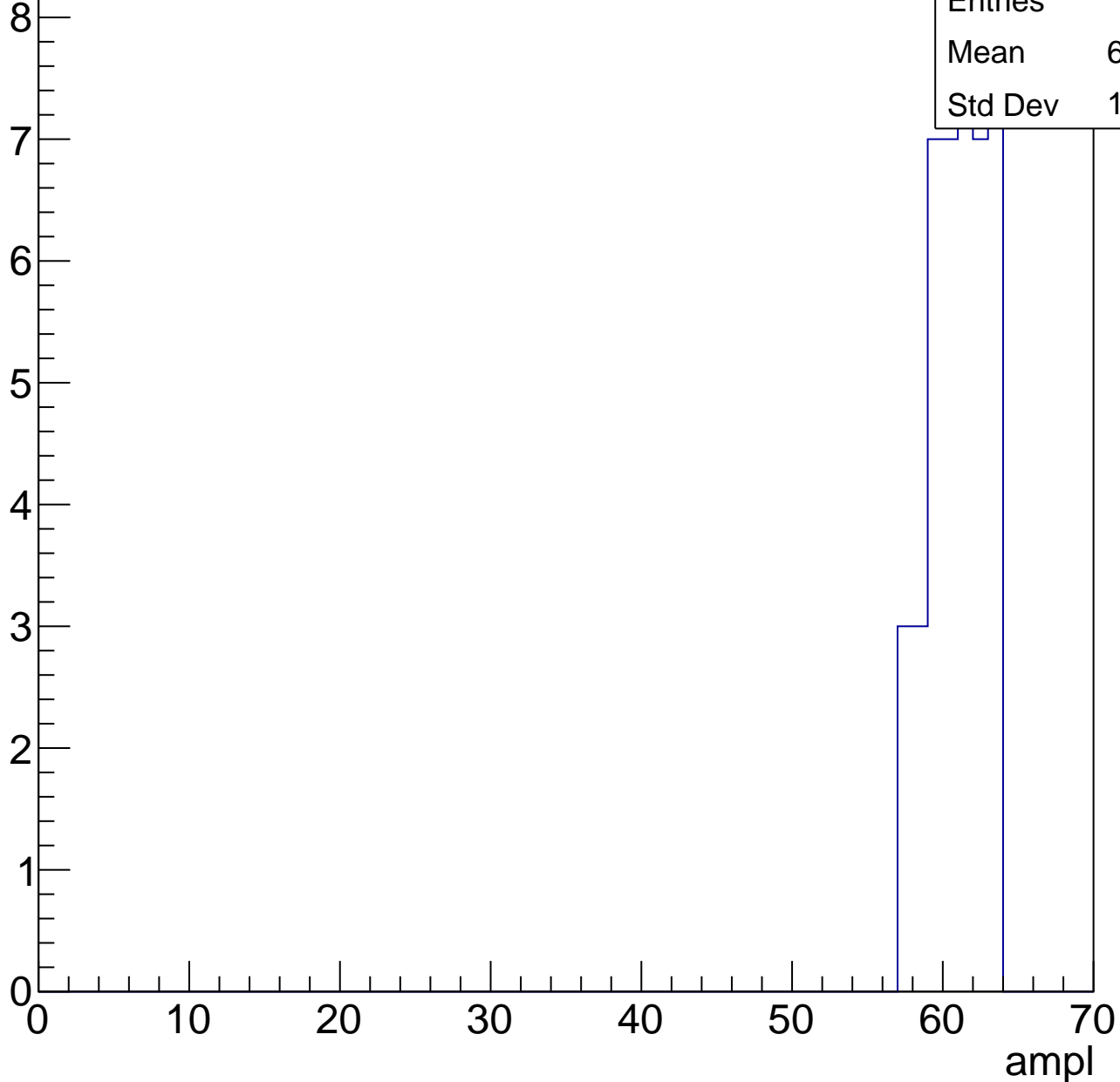
Entries	56
Mean	55
Std Dev	8.122



# B0L002S, U8-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	43
Mean	60.56
Std Dev	1.808

# B0L002S, U8-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L002S, U8-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L002S, U8-ch83, adc0

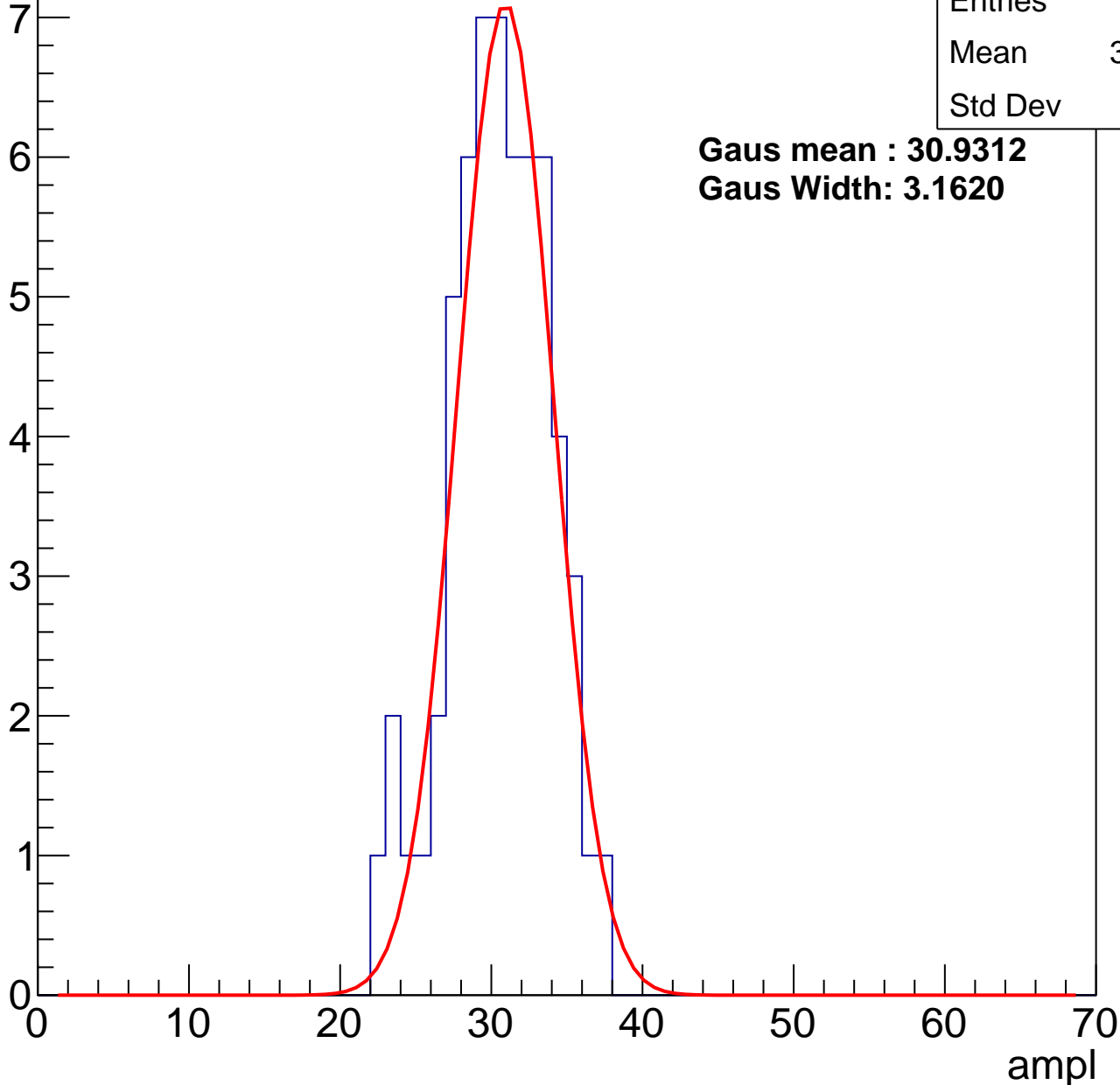
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.08
Std Dev	3.29

**Gaus mean : 30.9312**

**Gaus Width: 3.1620**



# B0L002S, U8-ch83, adc1

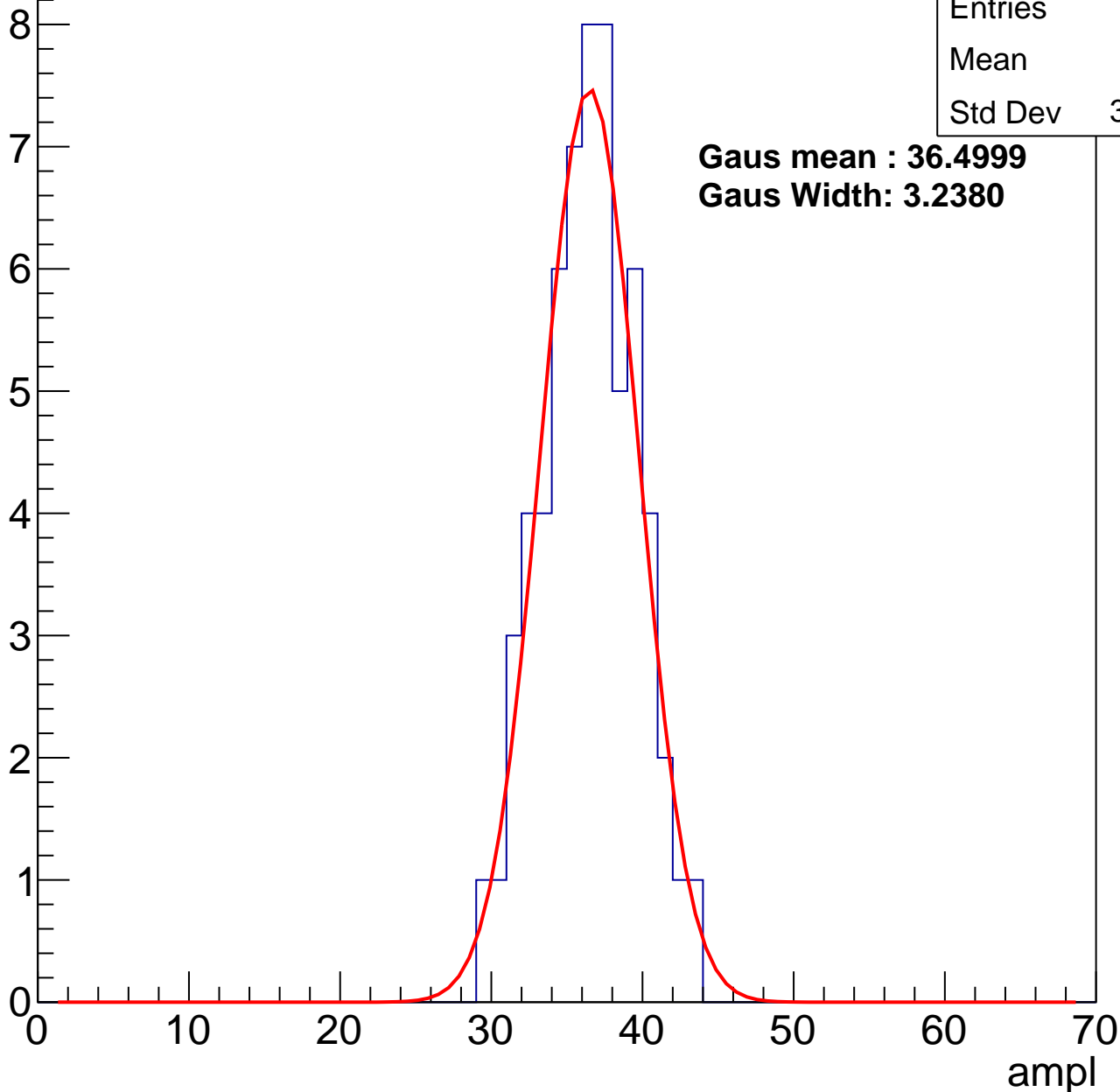
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36
Std Dev	3.062

**Gaus mean : 36.4999**

**Gaus Width: 3.2380**



# B0L002S, U8-ch83, adc2

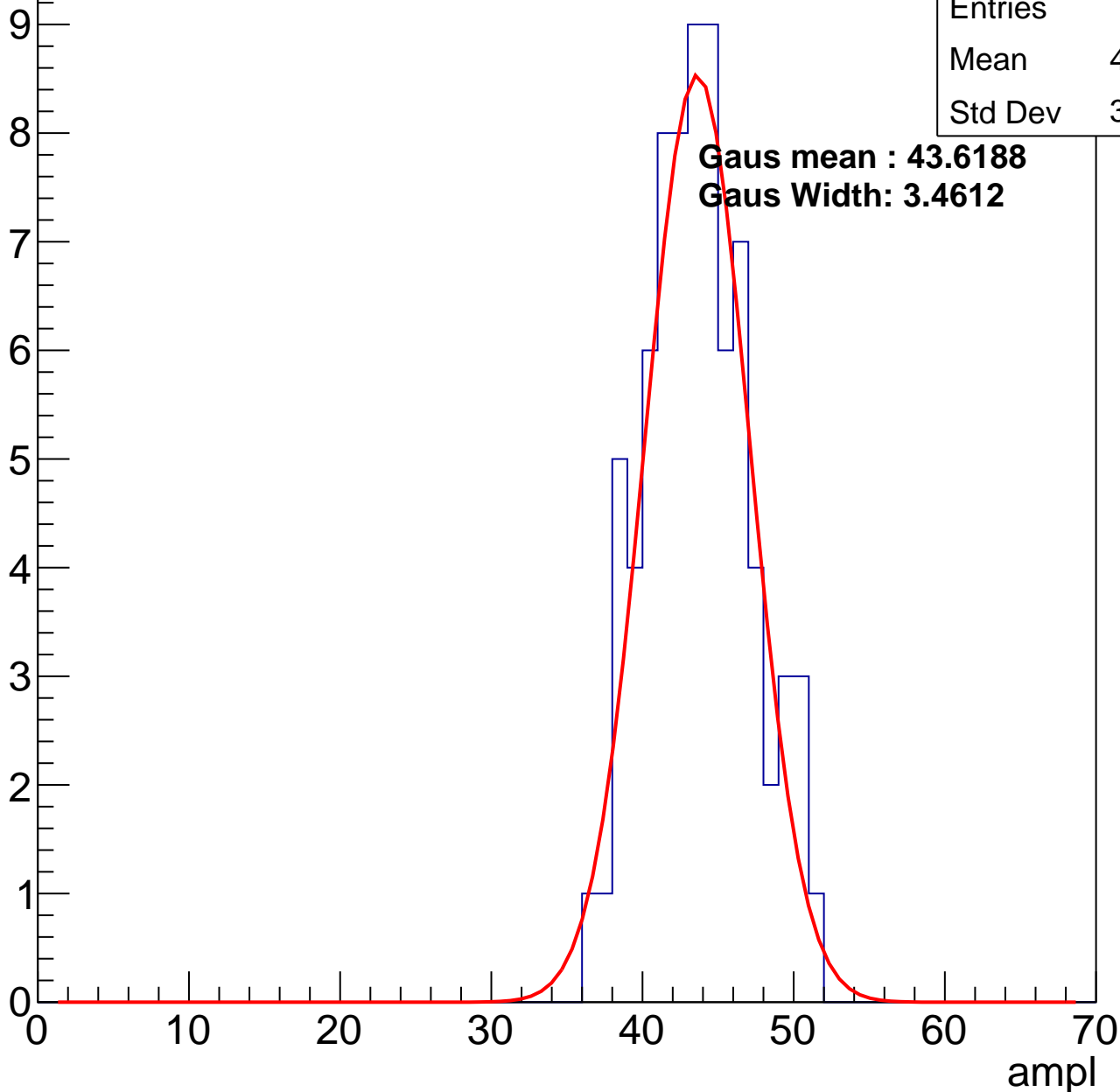
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.25
Std Dev	3.404

**Gaus mean : 43.6188**

**Gaus Width: 3.4612**

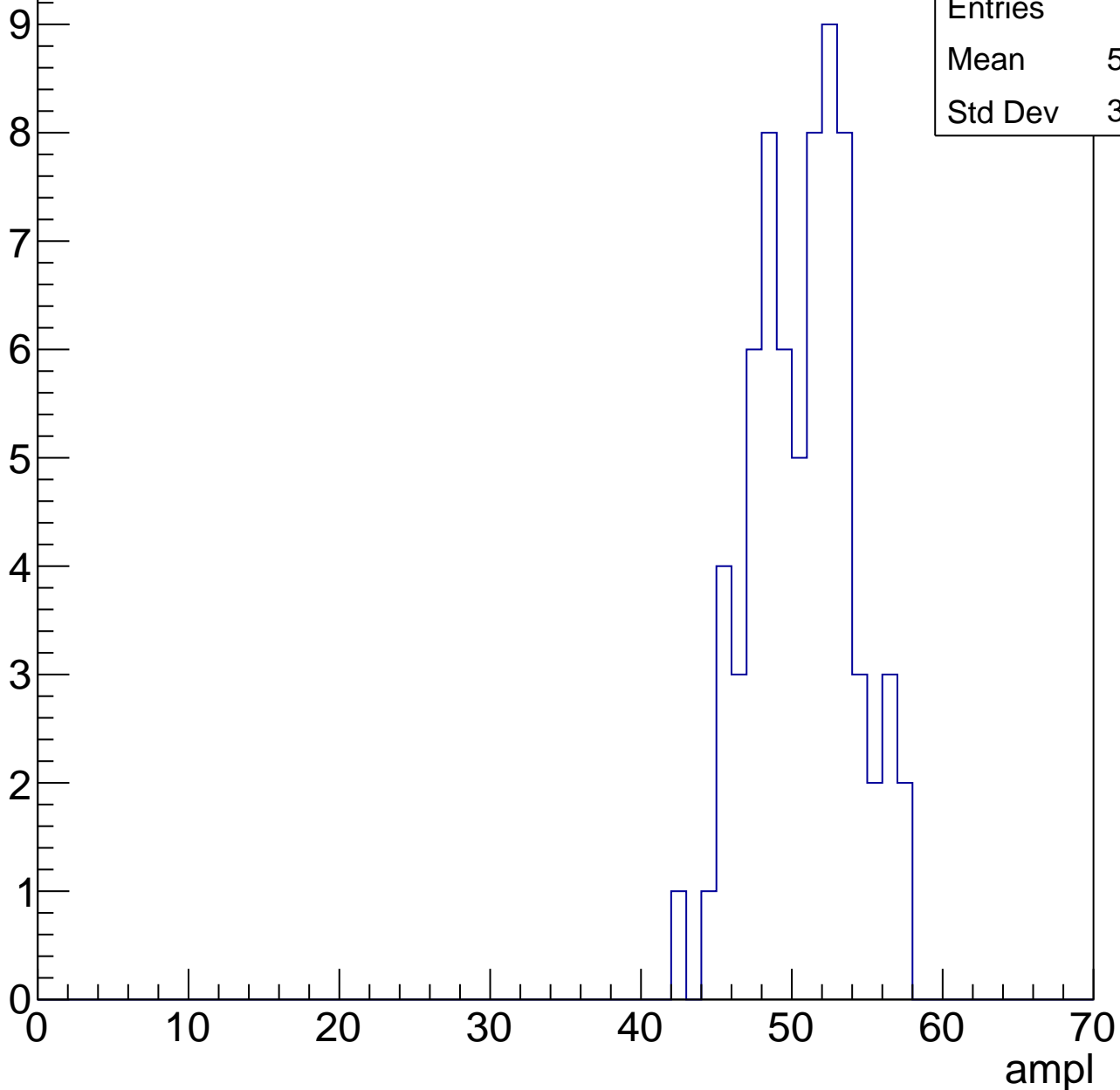


# B0L002S, U8-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.26
Std Dev	3.309

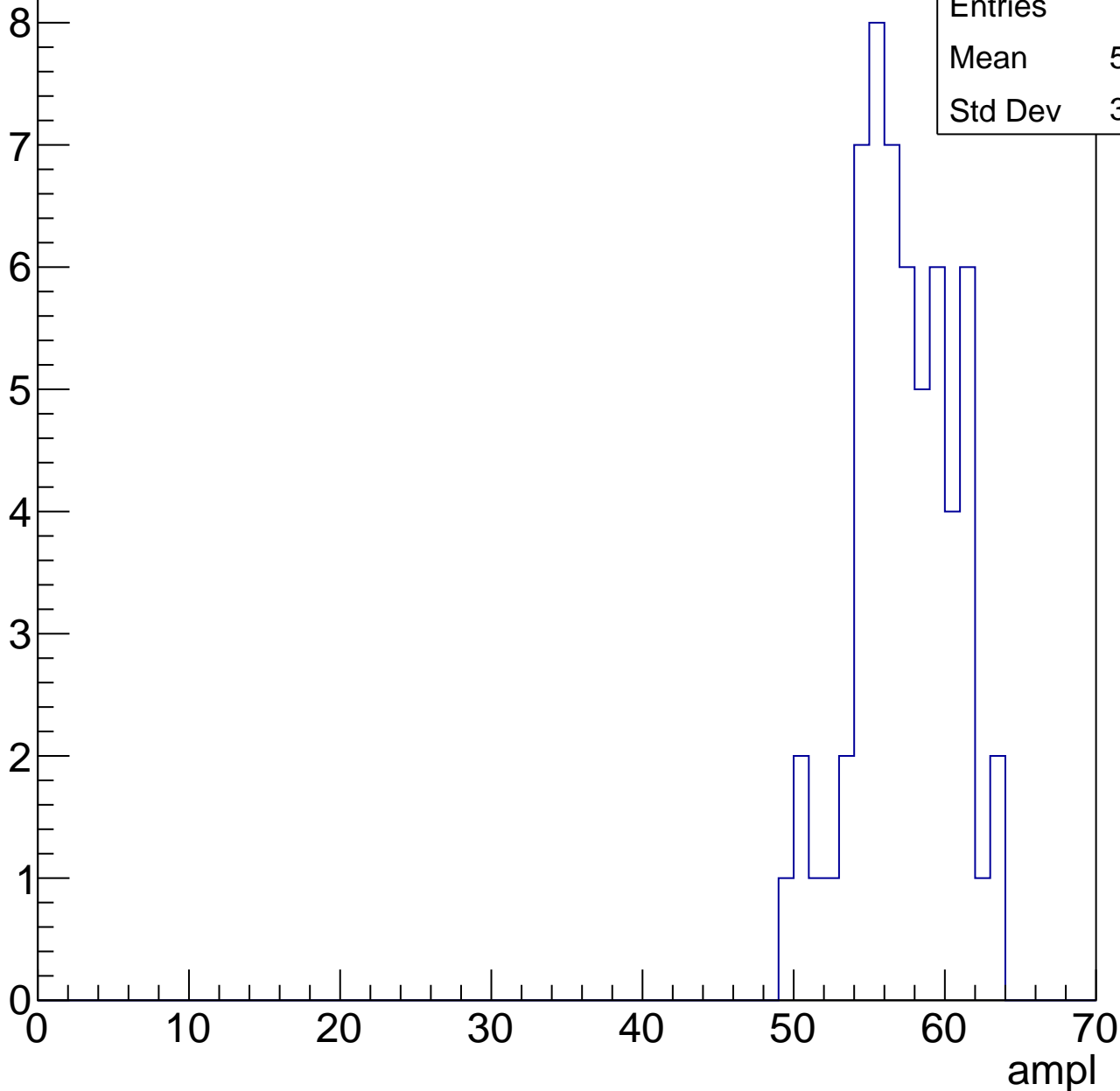


# B0L002S, U8-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.75
Std Dev	3.219

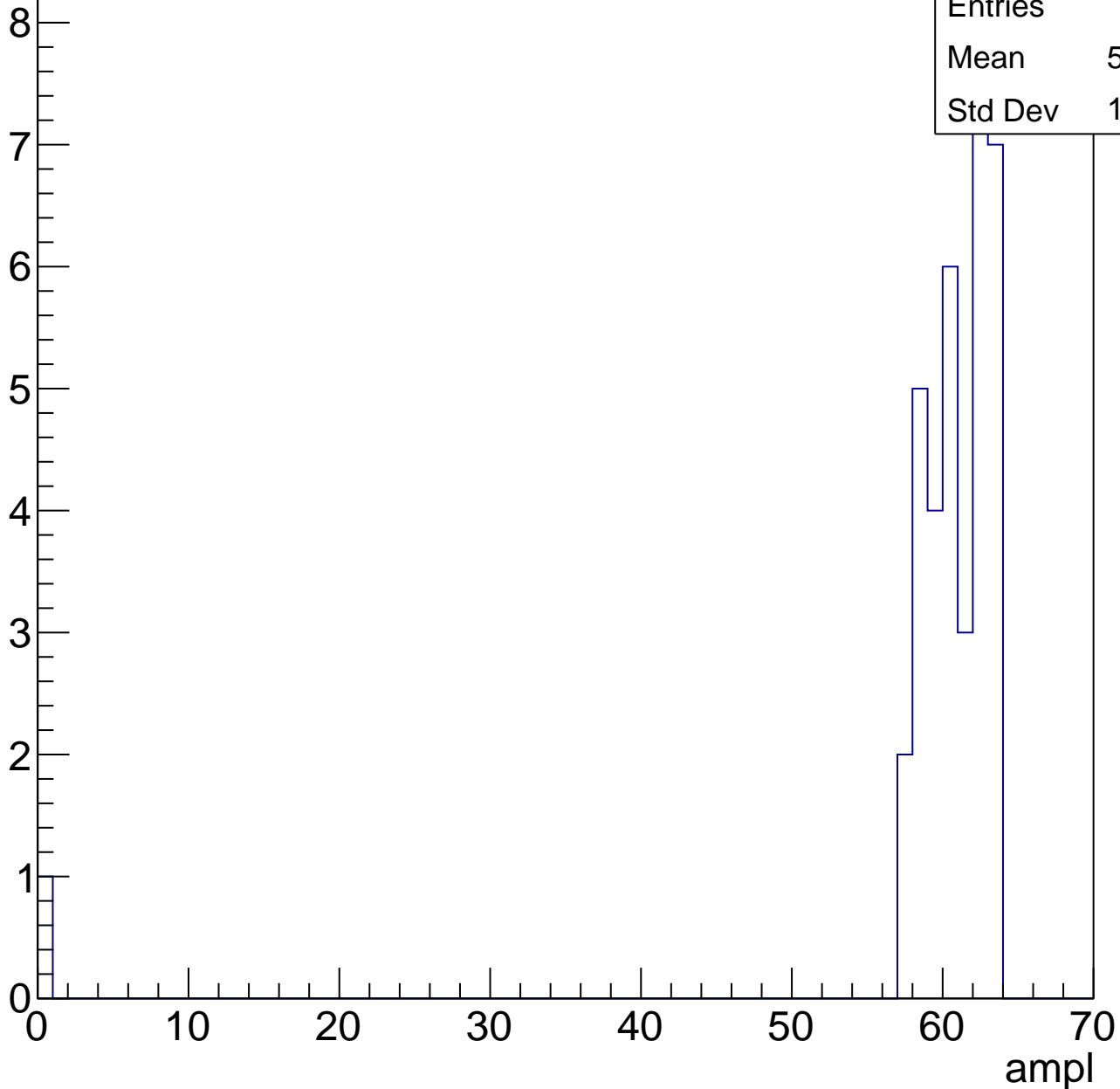


# B0L002S, U8-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.89
Std Dev	10.13



# B0L002S, U8-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch84, adc0

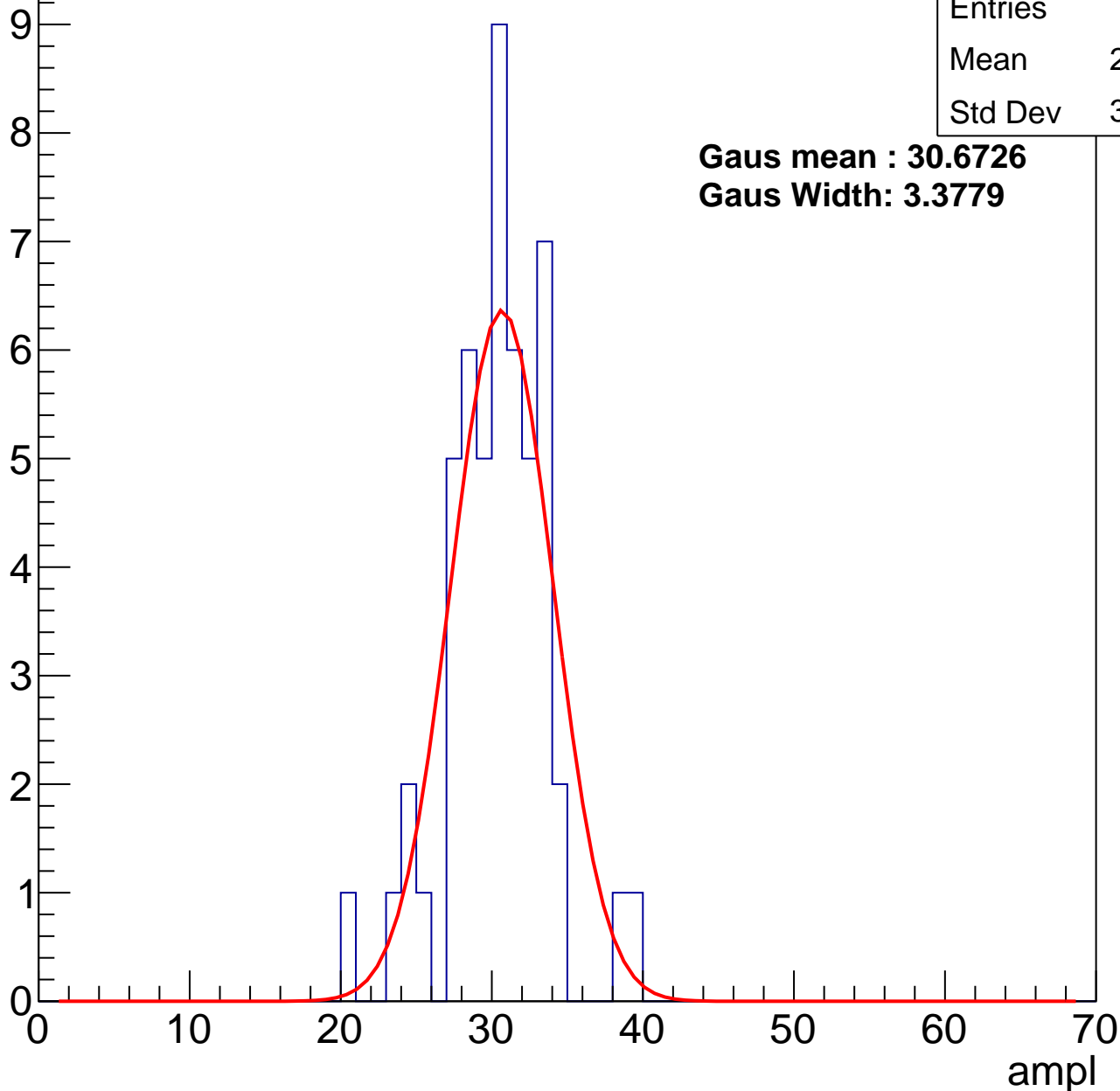
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	29.92
Std Dev	3.356

**Gaus mean : 30.6726**

**Gaus Width: 3.3779**



# B0L002S, U8-ch84, adc1

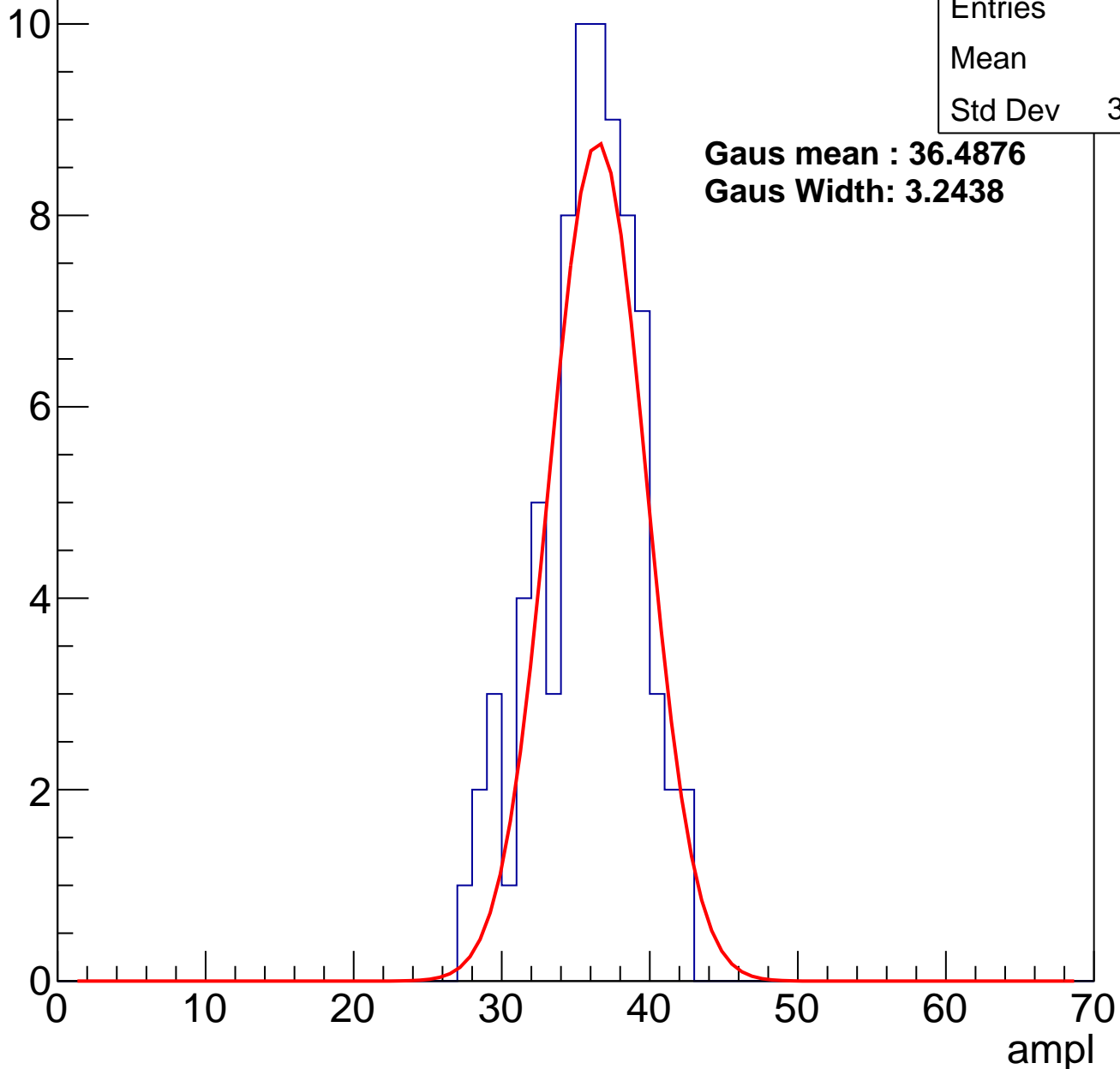
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	35.4
Std Dev	3.372

**Gaus mean : 36.4876**

**Gaus Width: 3.2438**

Entry



# B0L002S, U8-ch84, adc2

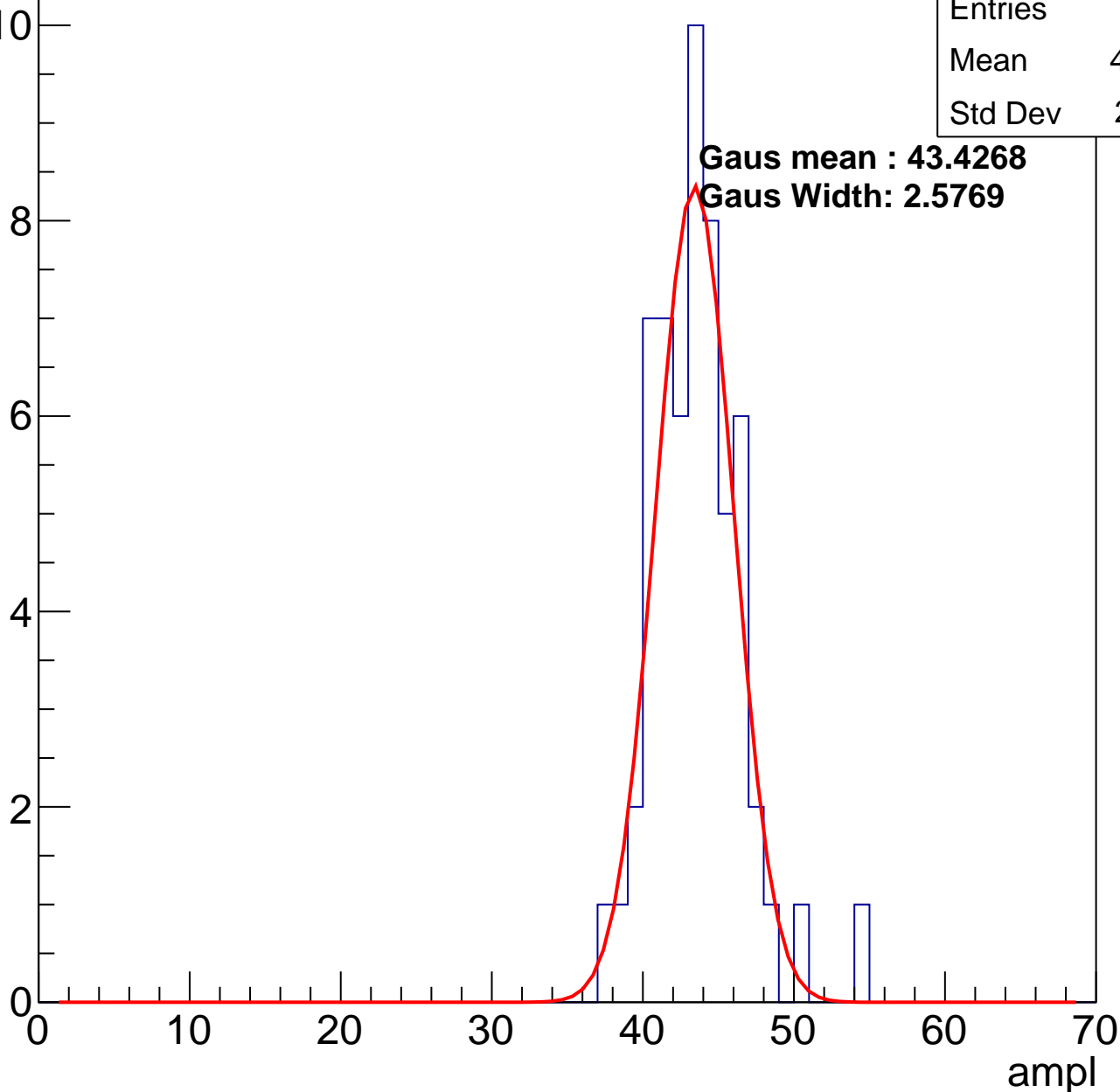
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.12
Std Dev	2.931

**Gaus mean : 43.4268**

**Gaus Width: 2.5769**



# B0L002S, U8-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

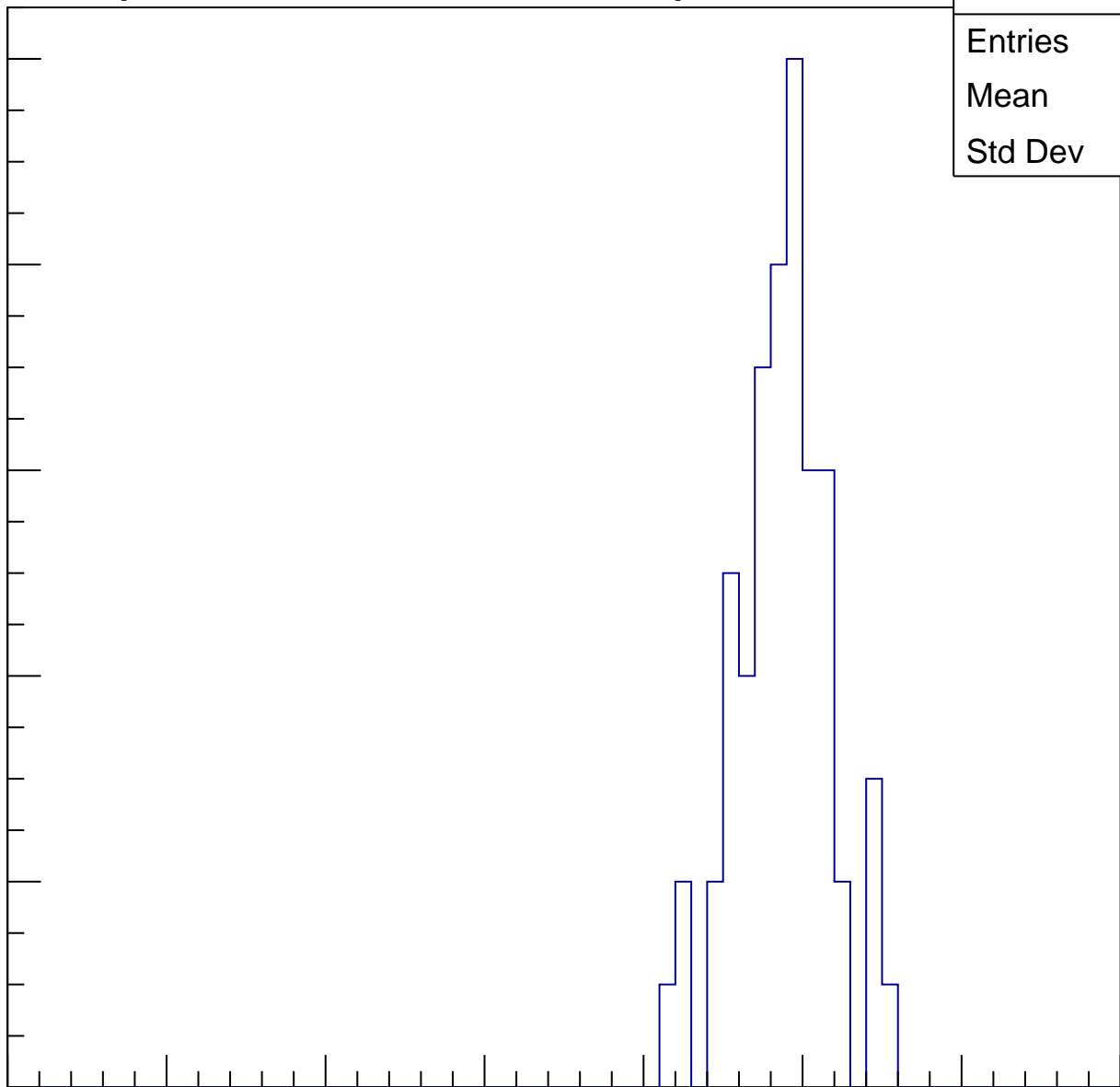
Entries	57
Mean	48.28
Std Dev	2.942

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

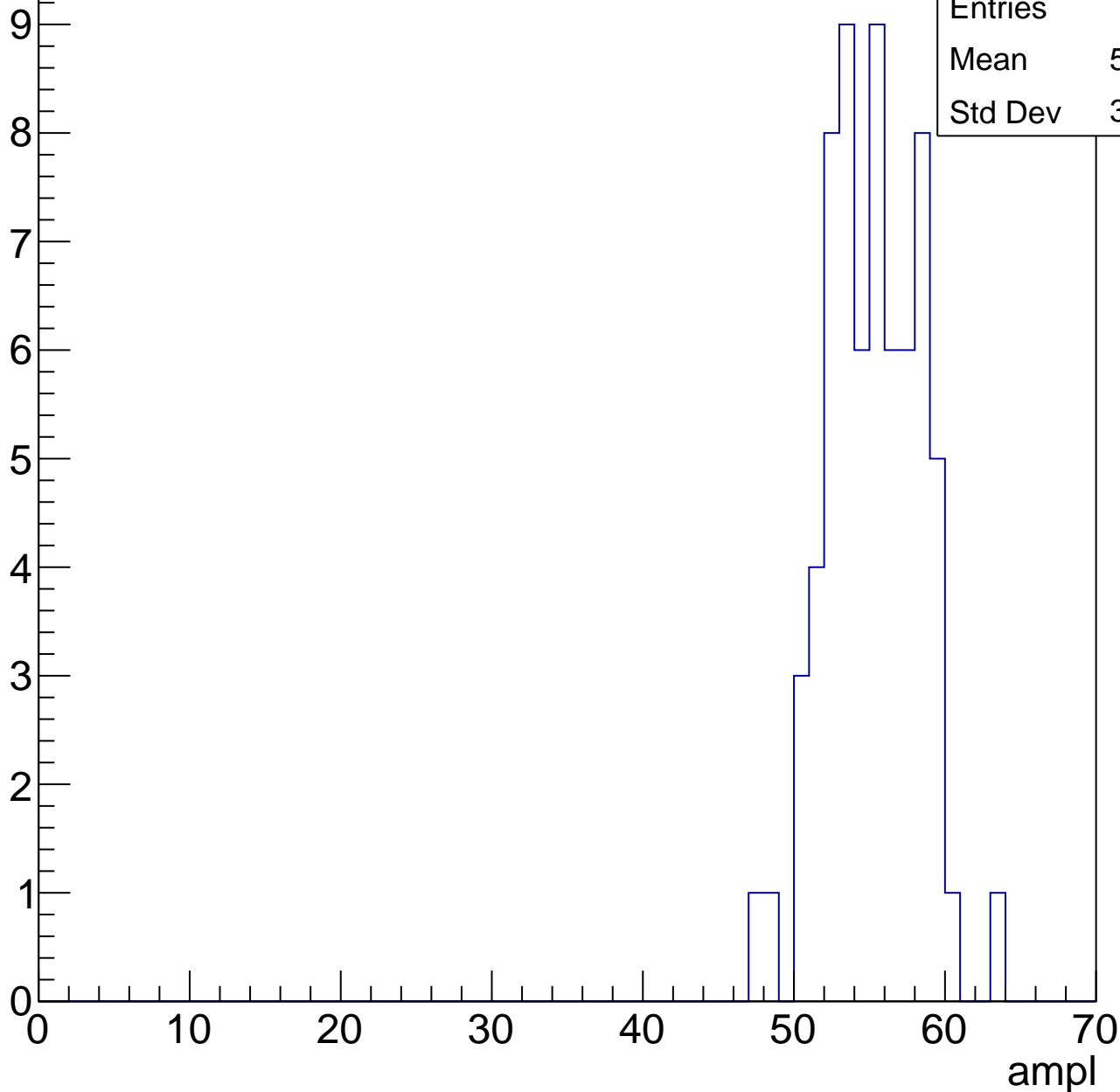
ampl



# B0L002S, U8-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

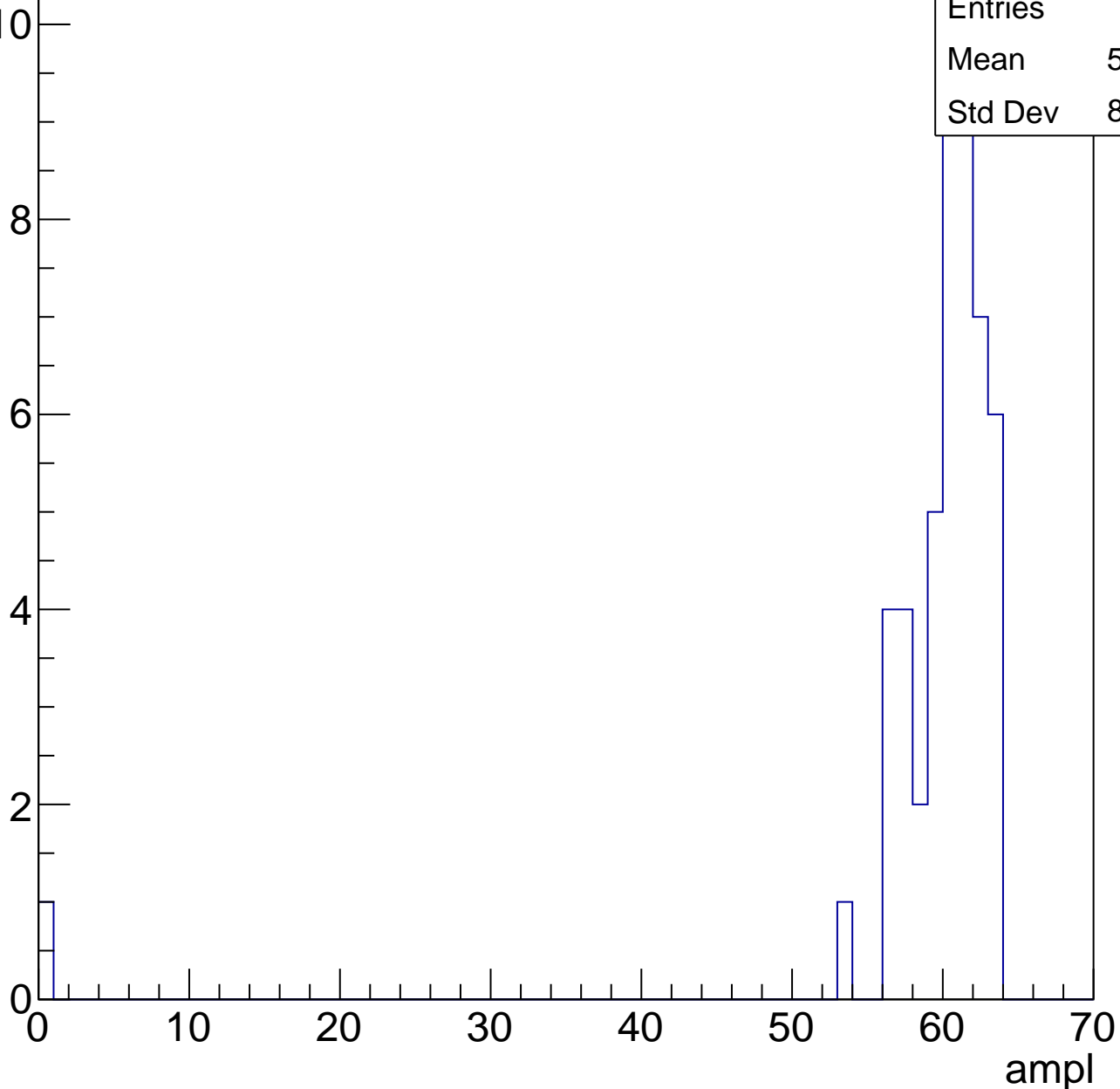


# B0L002S, U8-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.73
Std Dev	8.778



# B0L002S, U8-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch85, adc0

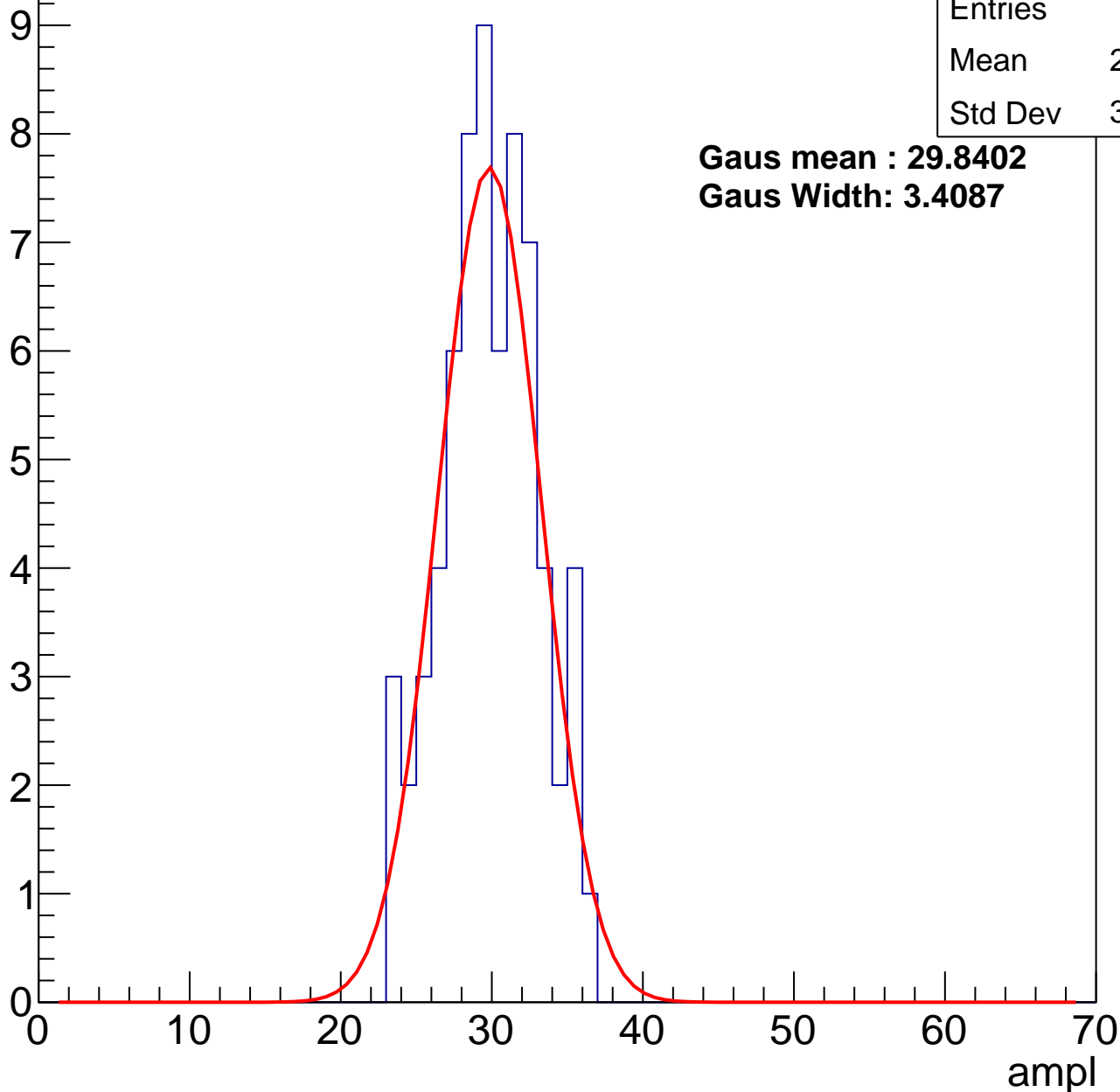
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.42
Std Dev	3.163

**Gaus mean : 29.8402**

**Gaus Width: 3.4087**



# B0L002S, U8-ch85, adc1

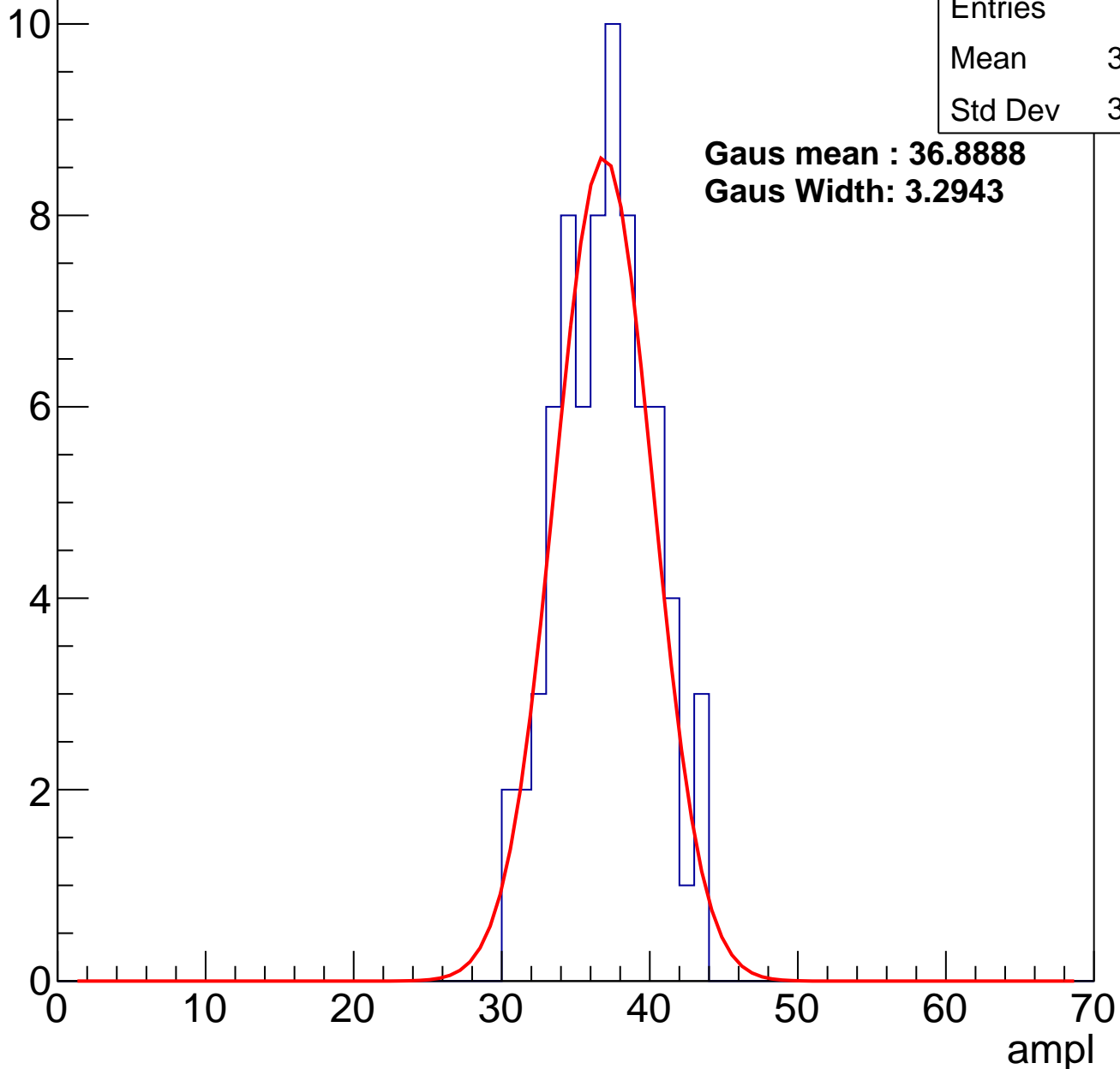
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	36.56
Std Dev	3.127

**Gaus mean : 36.8888**

**Gaus Width: 3.2943**

Entry



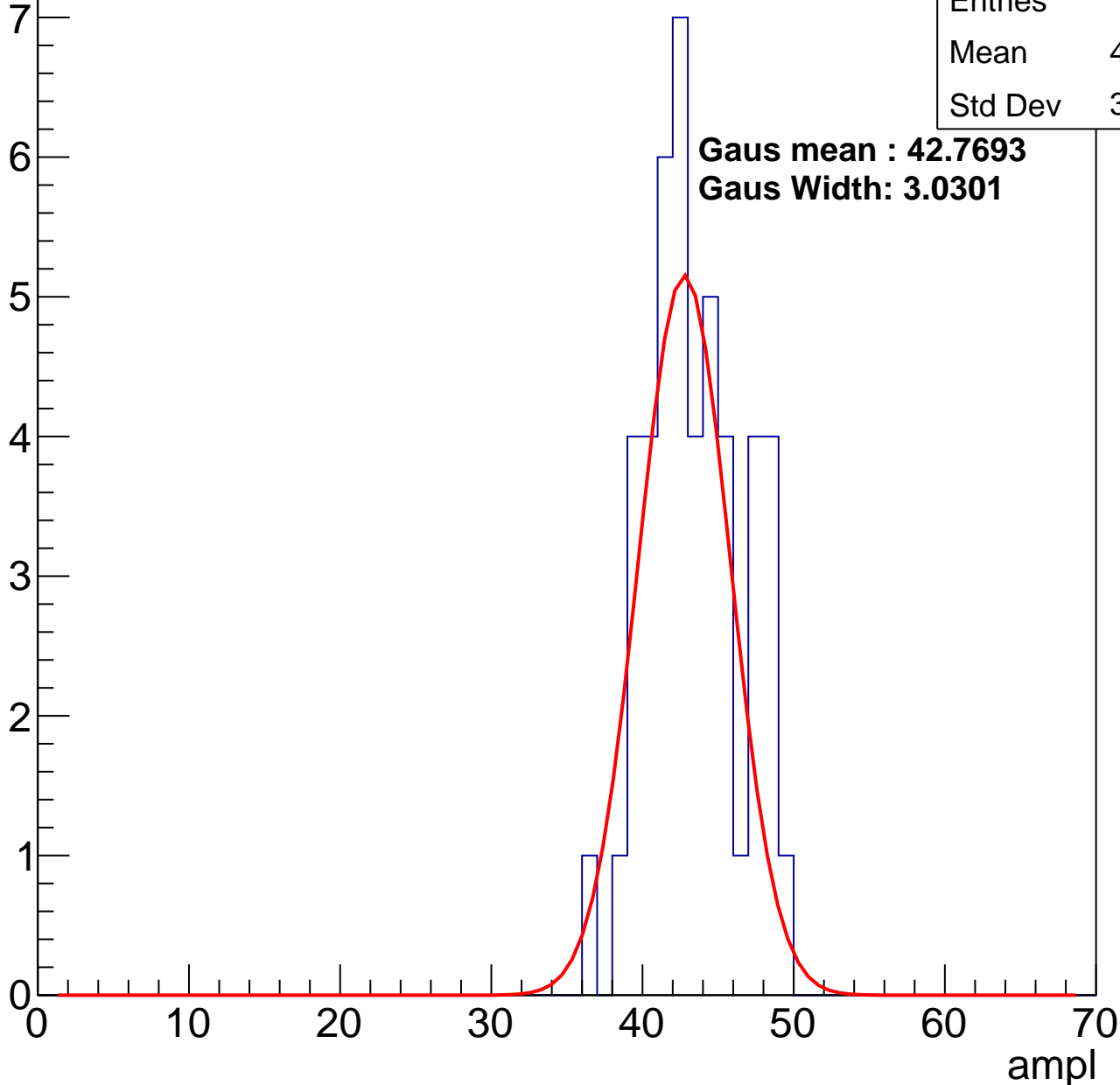
# B0L002S, U8-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	42.98
Std Dev	3.075

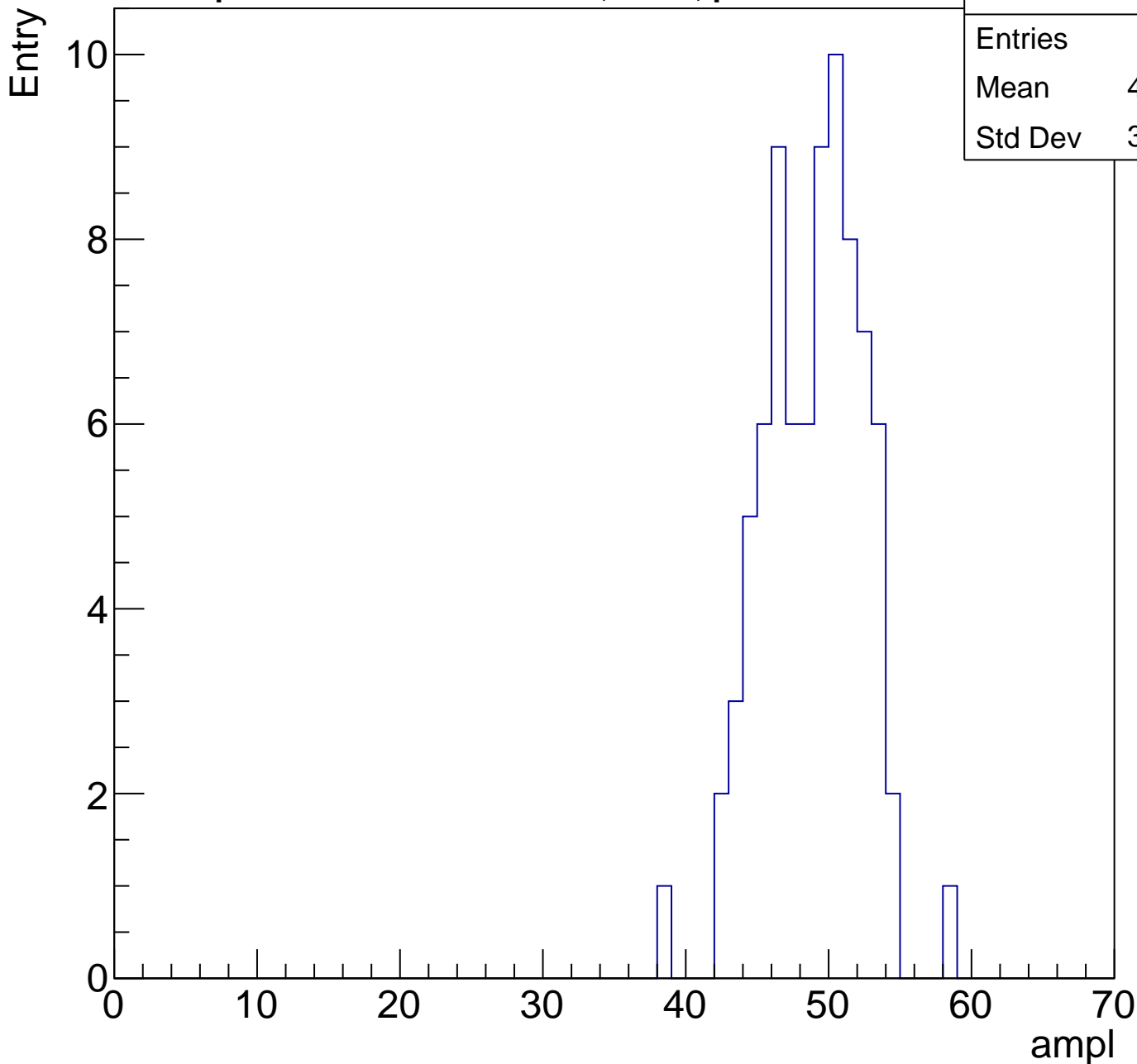
**Gaus mean : 42.7693**  
**Gaus Width: 3.0301**



# B0L002S, U8-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	81
Mean	48.42
Std Dev	3.449

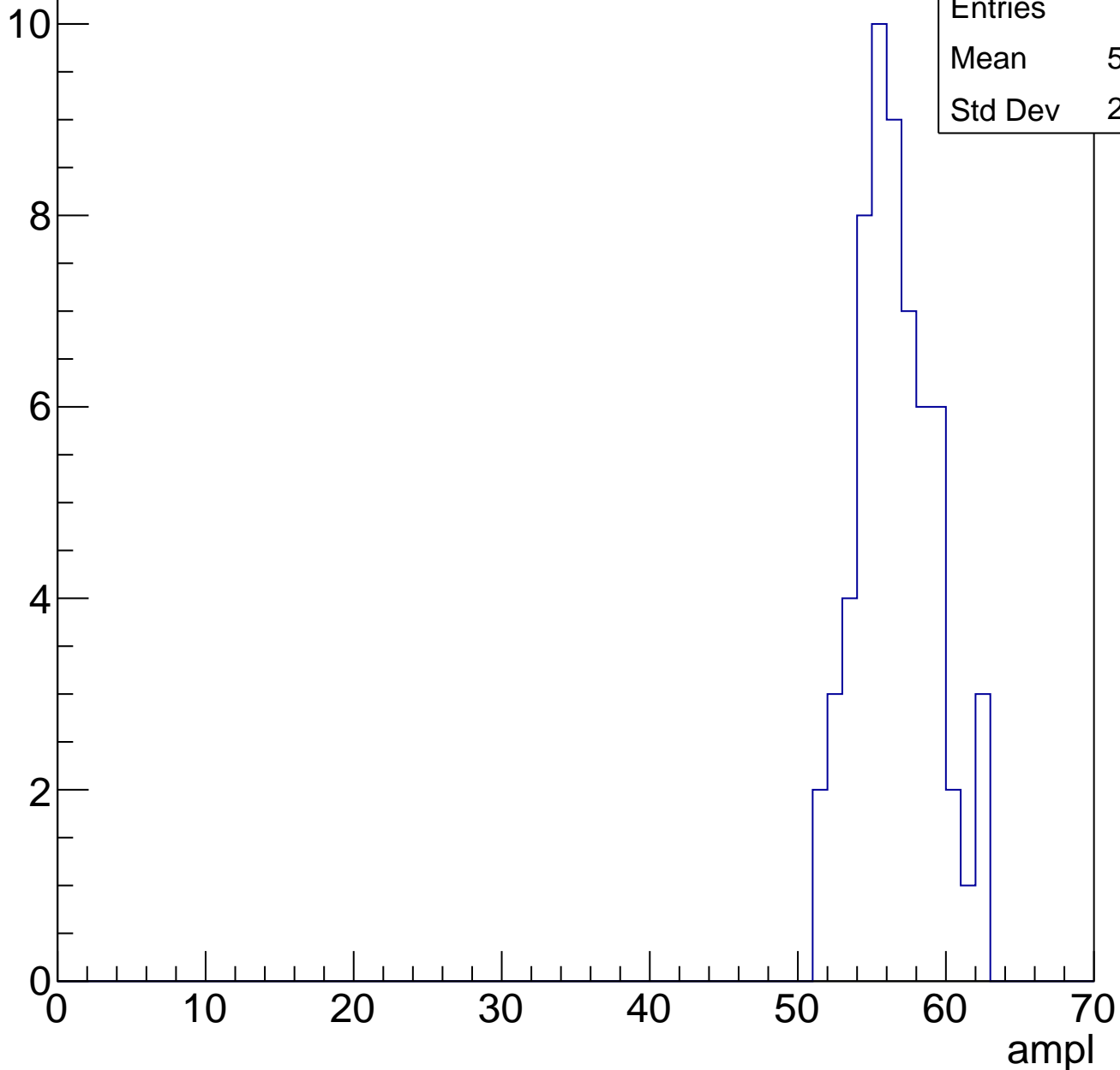


# B0L002S, U8-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	56.13
Std Dev	2.639

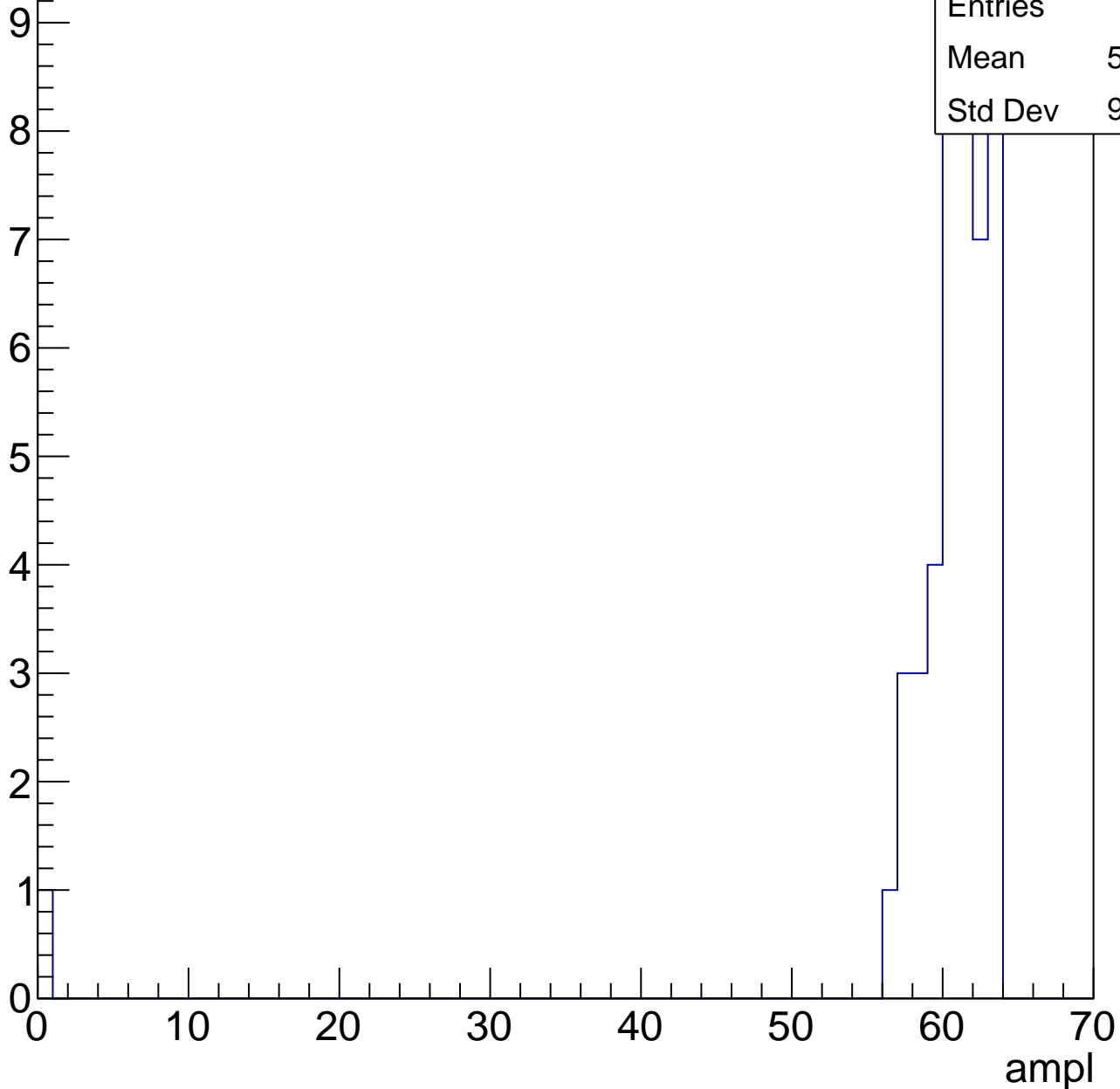
Entry



# B0L002S, U8-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	44
Mean	59.23
Std Dev	9.232

# B0L002S, U8-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	30.43
Std Dev	4.881

**Gaus mean : 31.0720**

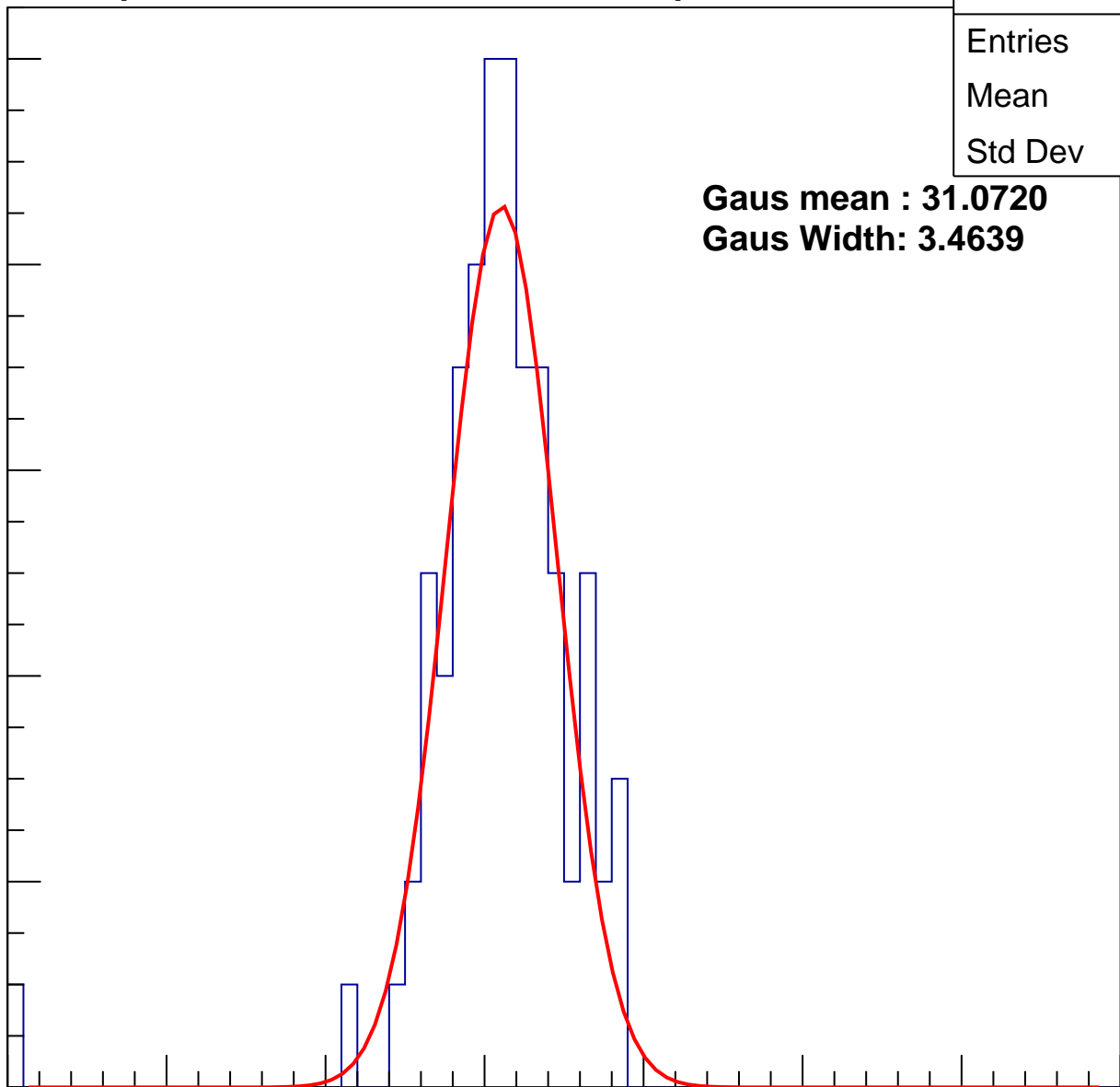
**Gaus Width: 3.4639**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch86, adc1

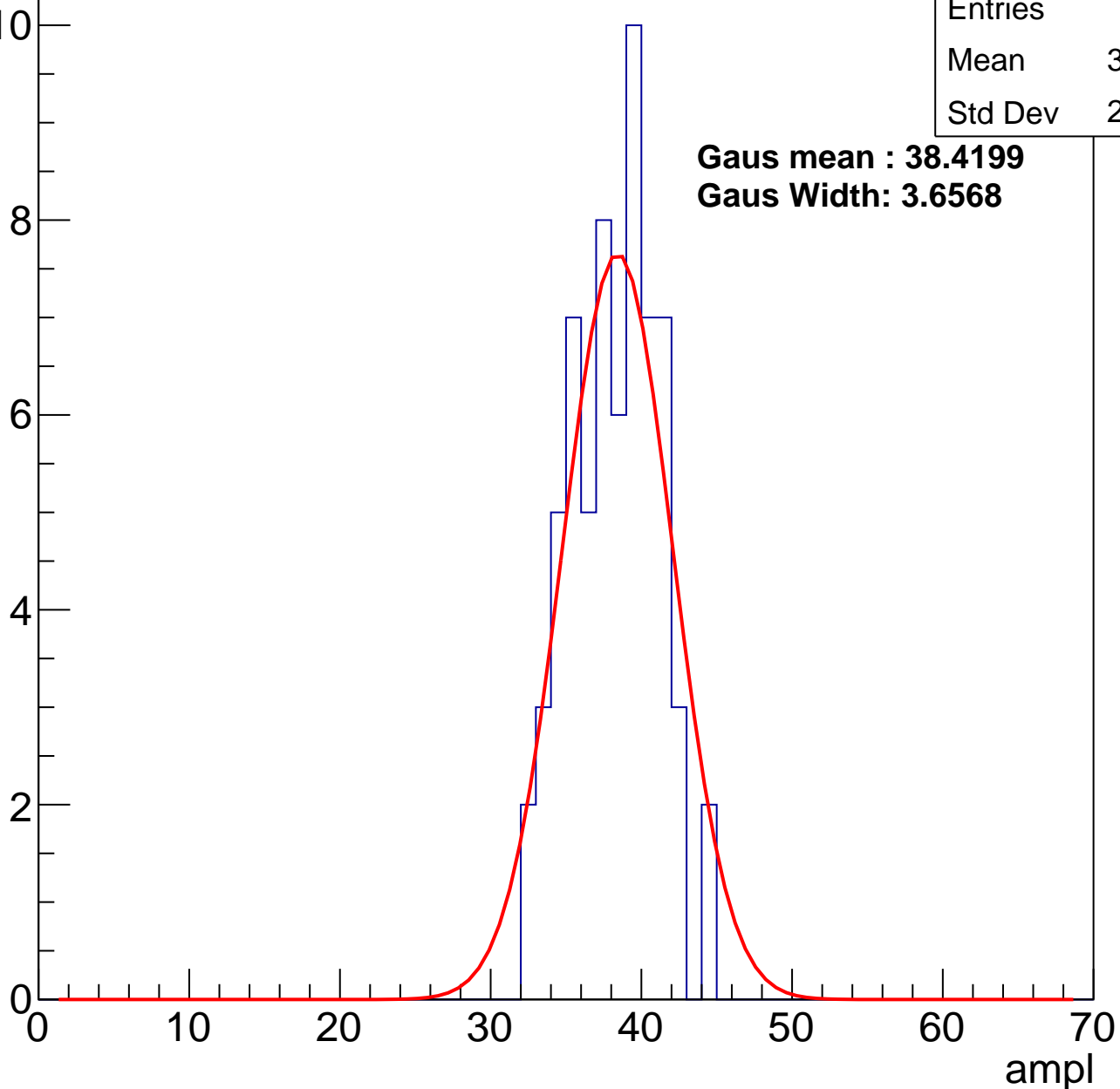
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.74
Std Dev	2.868

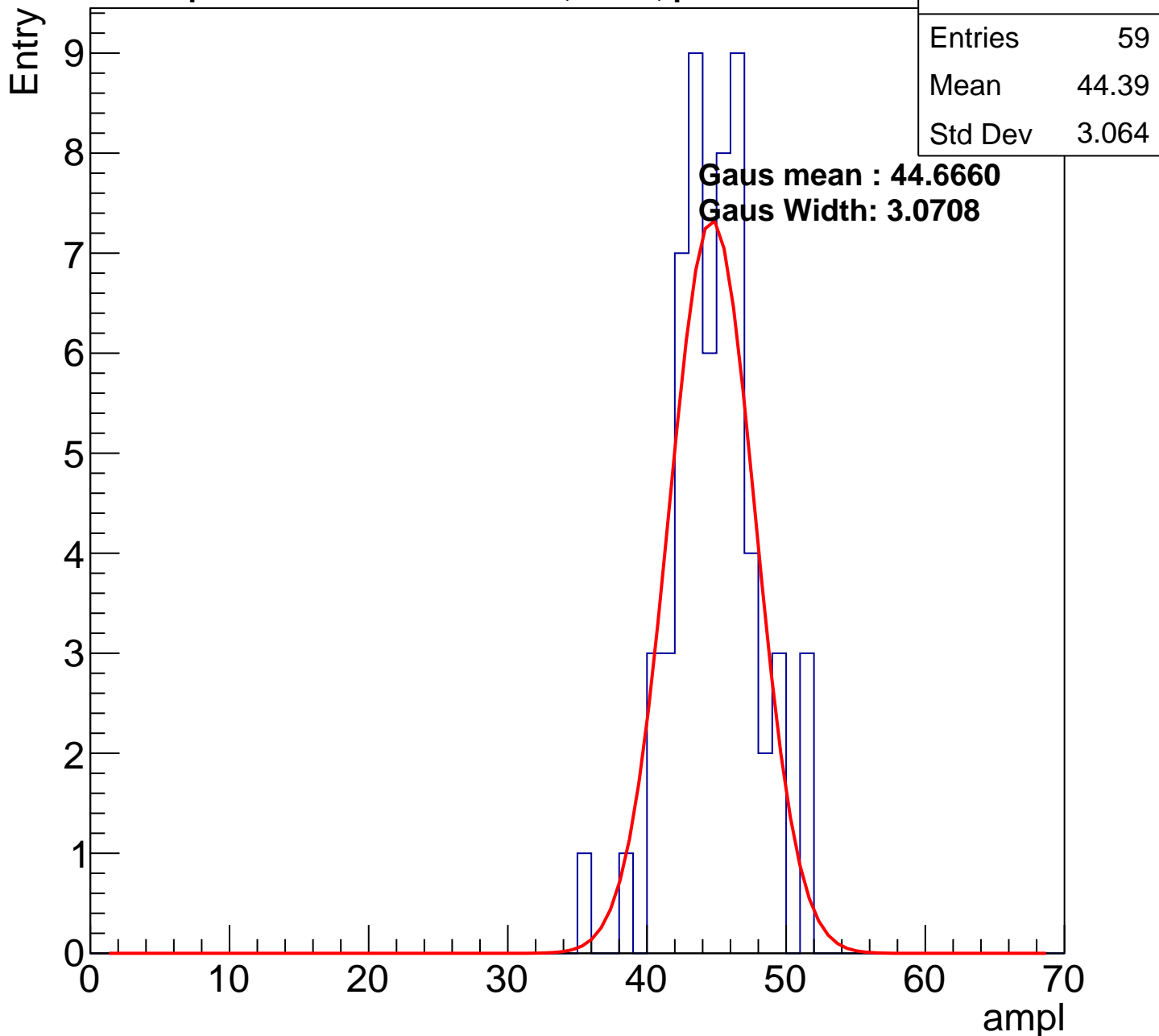
**Gaus mean : 38.4199**

**Gaus Width: 3.6568**



# B0L002S, U8-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

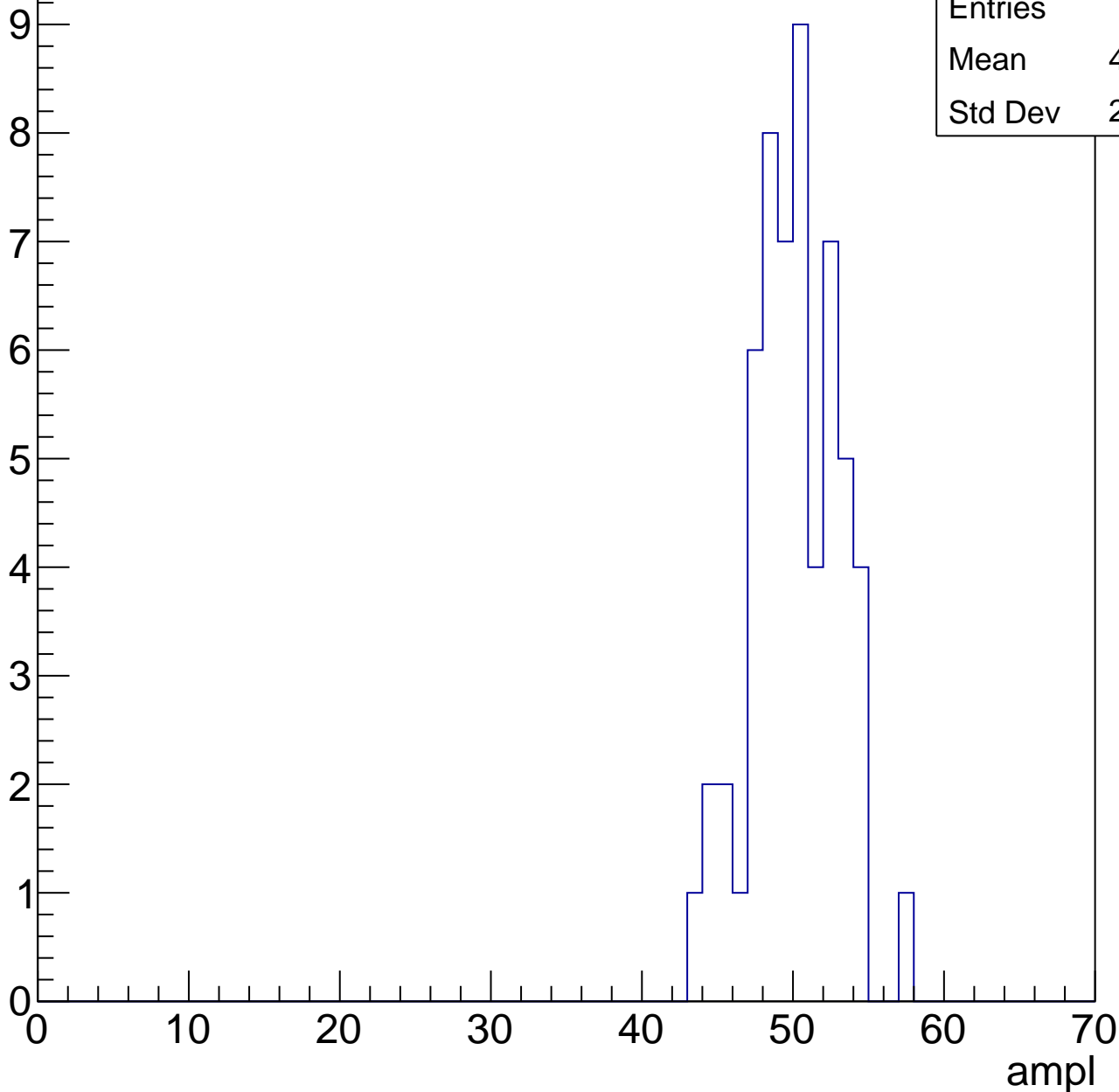


# B0L002S, U8-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.68
Std Dev	2.854



# B0L002S, U8-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	56.71
Std Dev	3.076

Entry

10

8

6

4

2

0

0

10

20

30

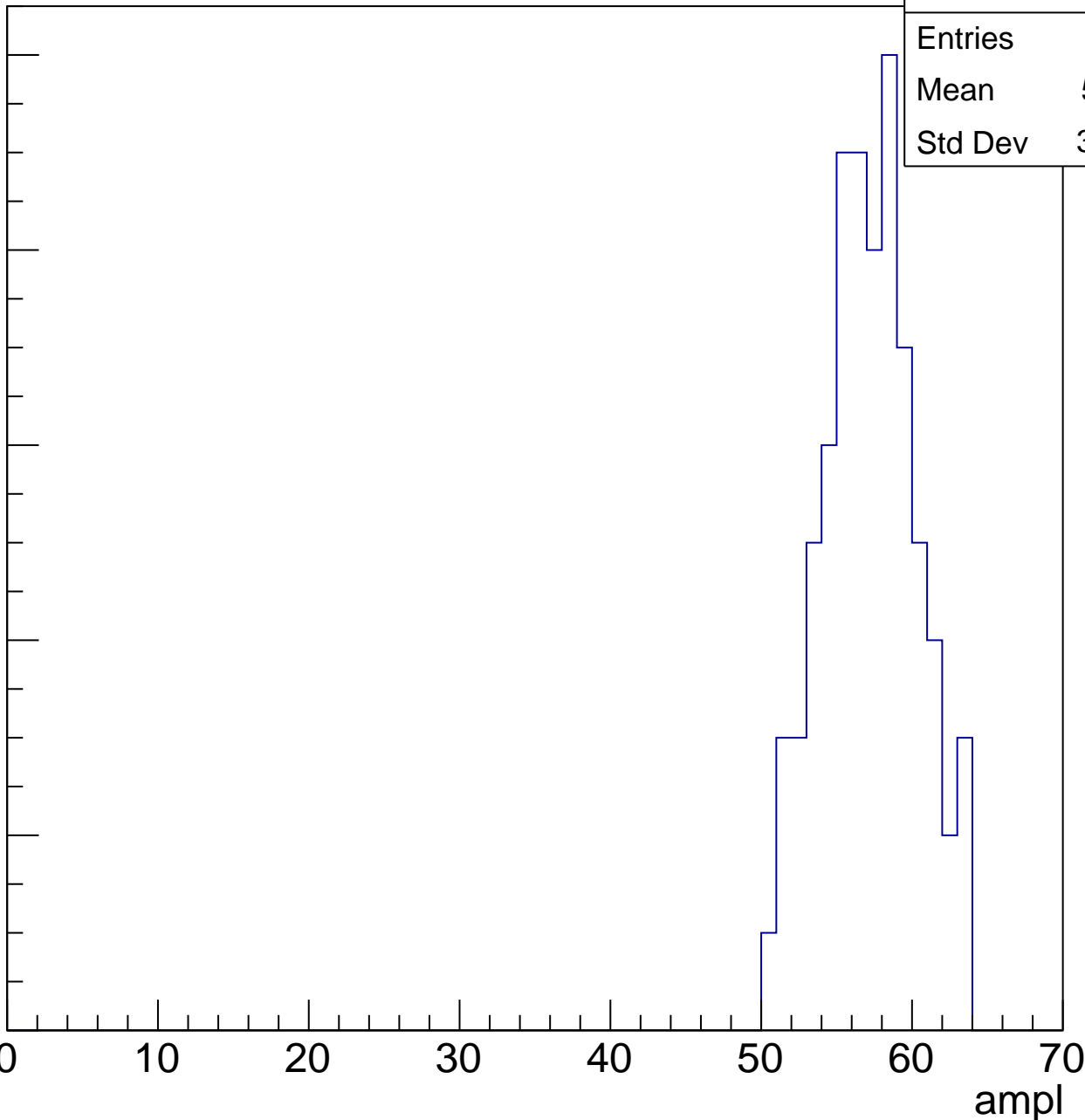
40

50

60

70

ampl

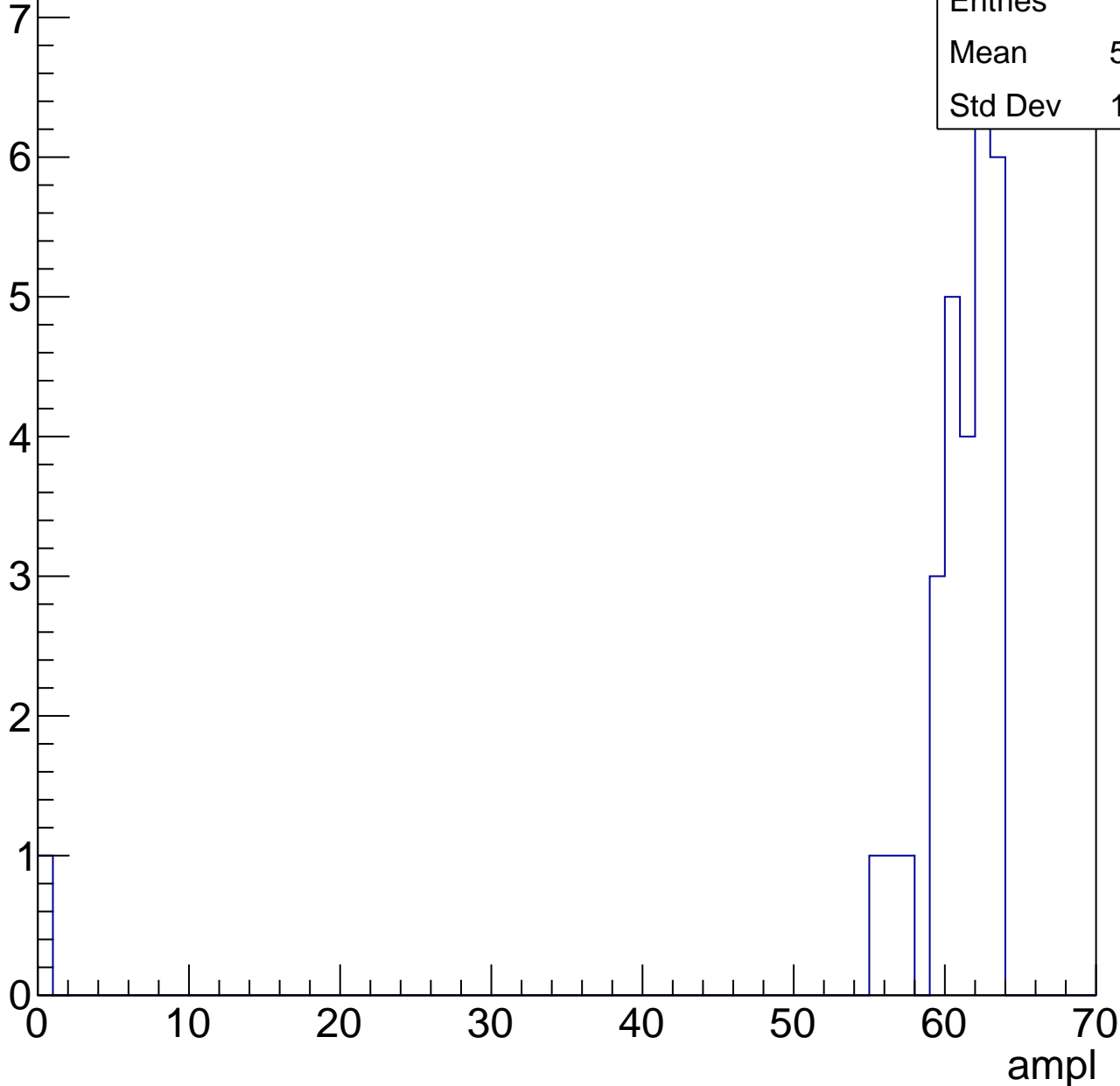


# B0L002S, U8-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.66
Std Dev	11.27



# B0L002S, U8-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

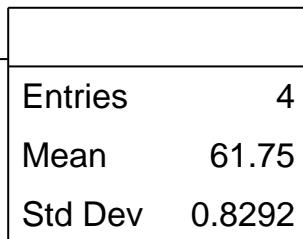
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.75
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl





# B0L002S, U8-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch87, adc0

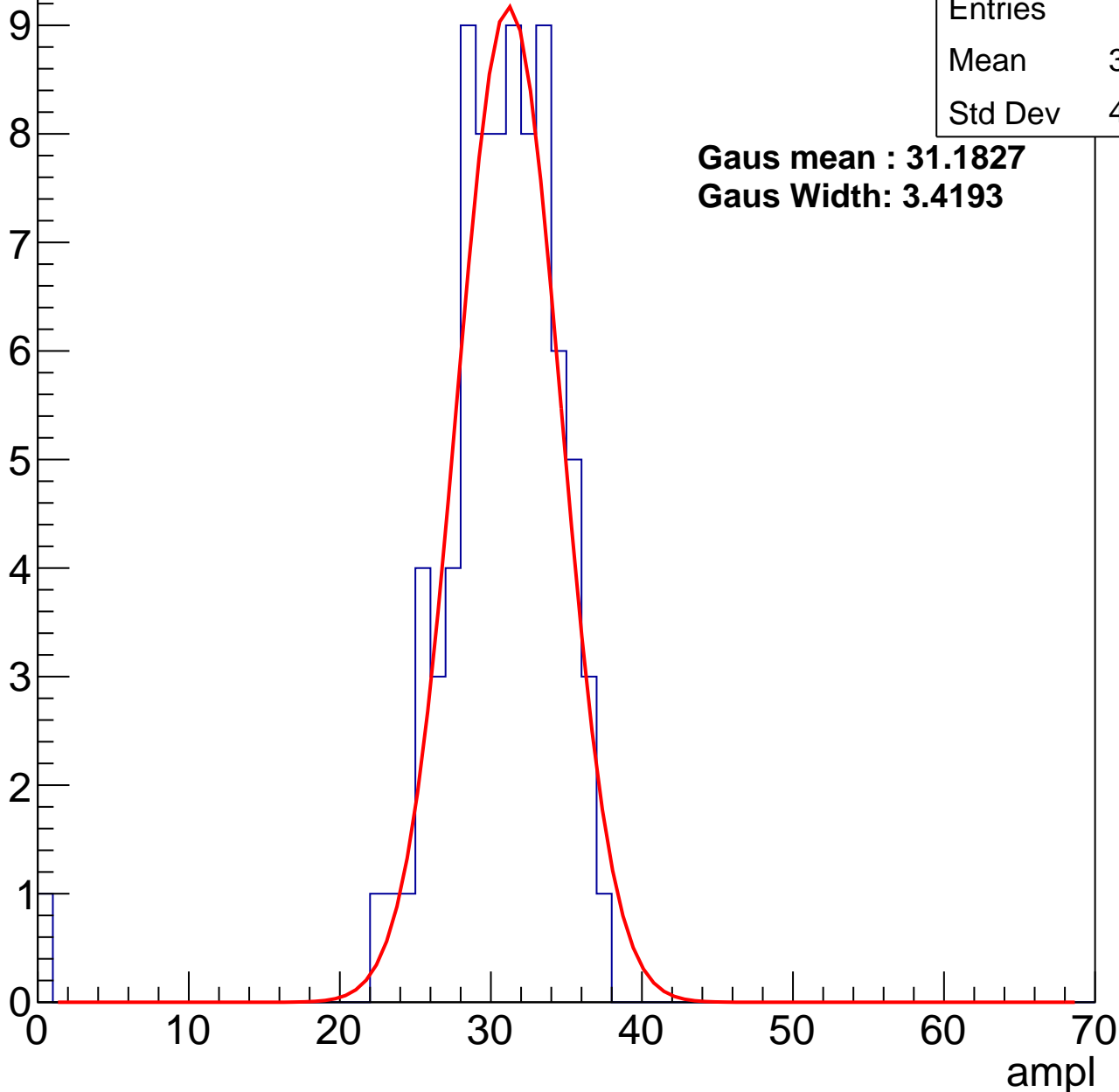
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	30.06
Std Dev	4.683

**Gaus mean : 31.1827**

**Gaus Width: 3.4193**

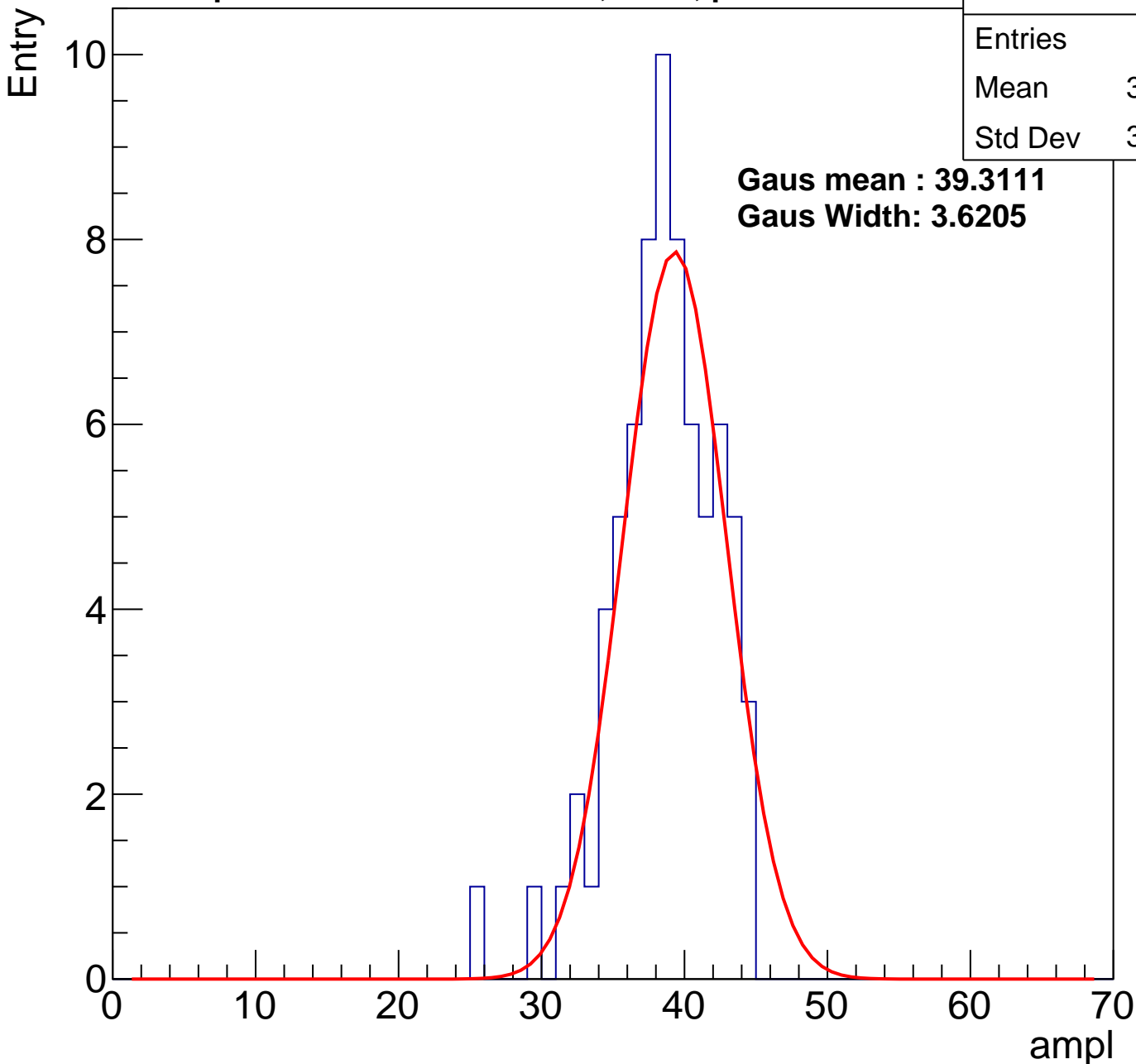


# B0L002S, U8-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	38.07
Std Dev	3.618

**Gaus mean : 39.3111**  
**Gaus Width: 3.6205**



# B0L002S, U8-ch87, adc2

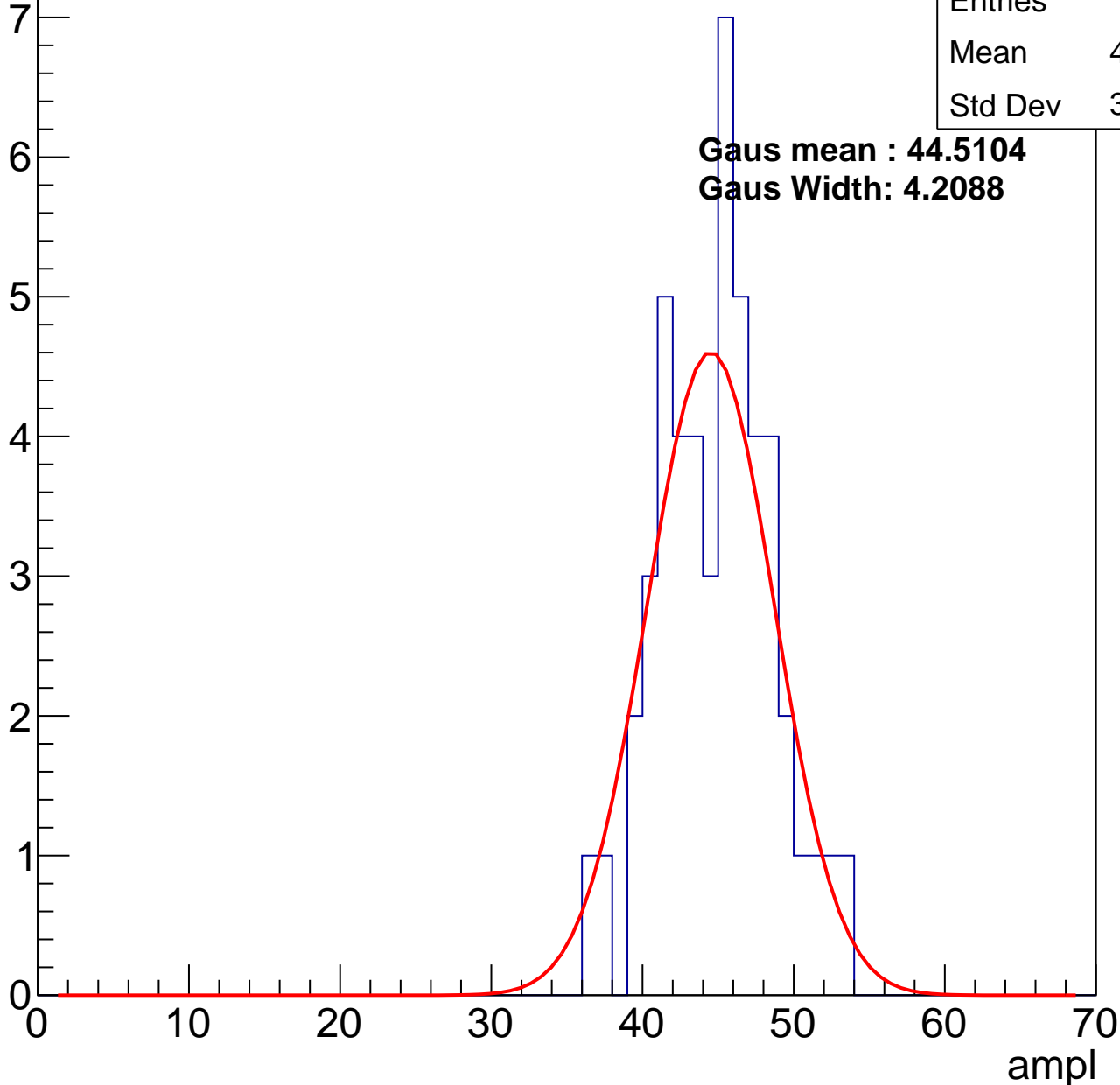
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	44.43
Std Dev	3.709

**Gaus mean : 44.5104**

**Gaus Width: 4.2088**

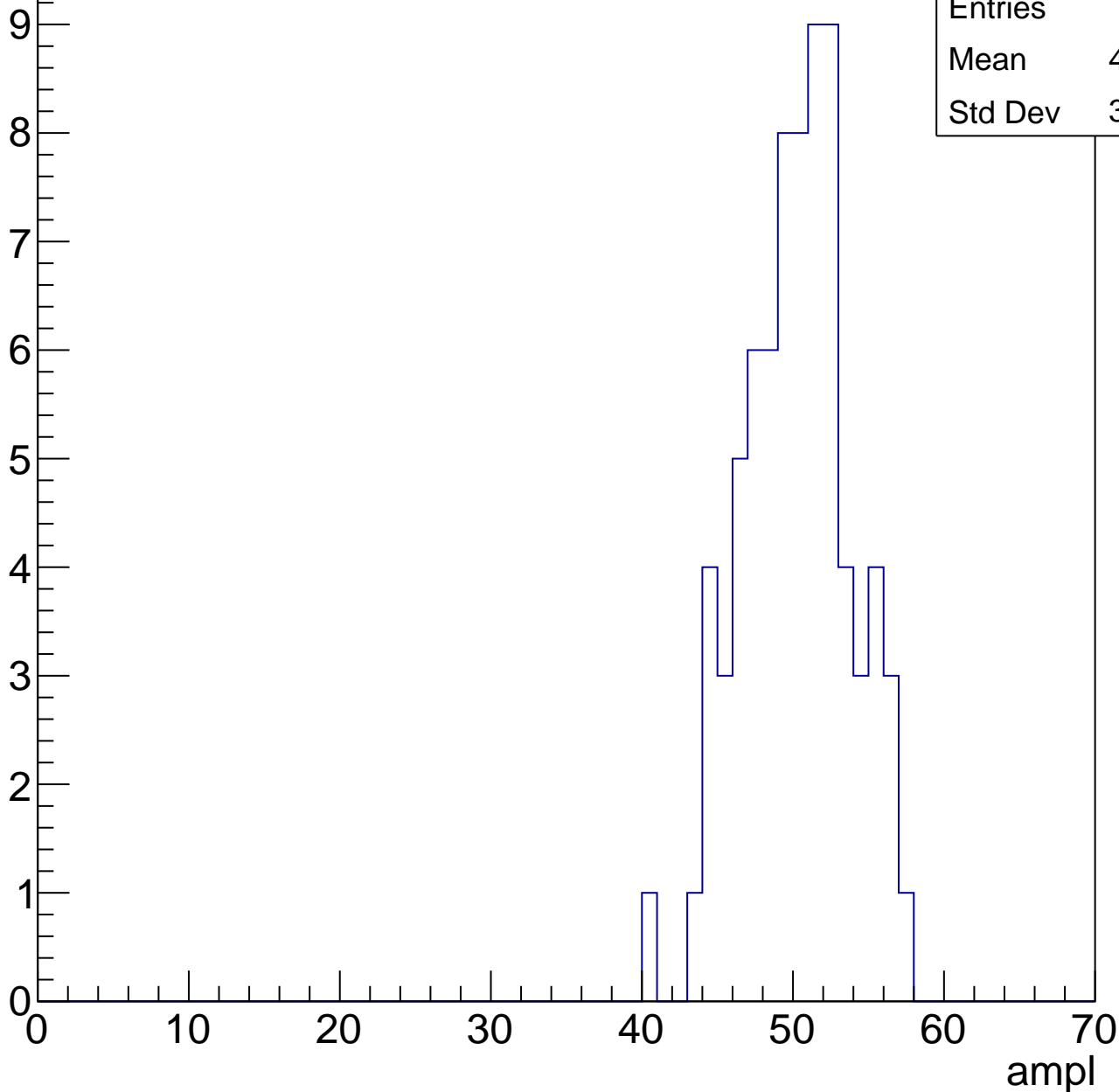


# B0L002S, U8-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	49.76
Std Dev	3.494

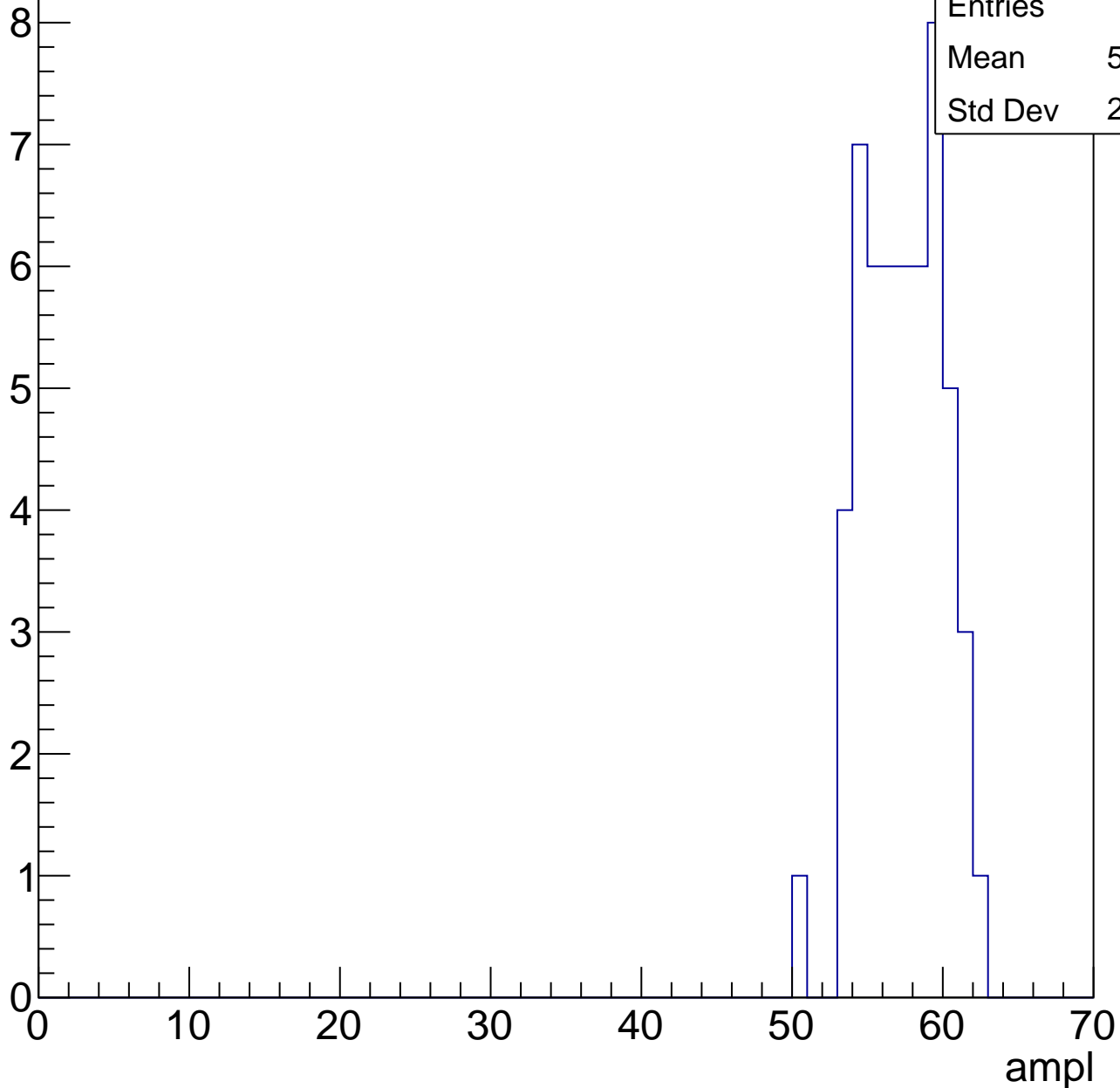


# B0L002S, U8-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.85
Std Dev	2.609

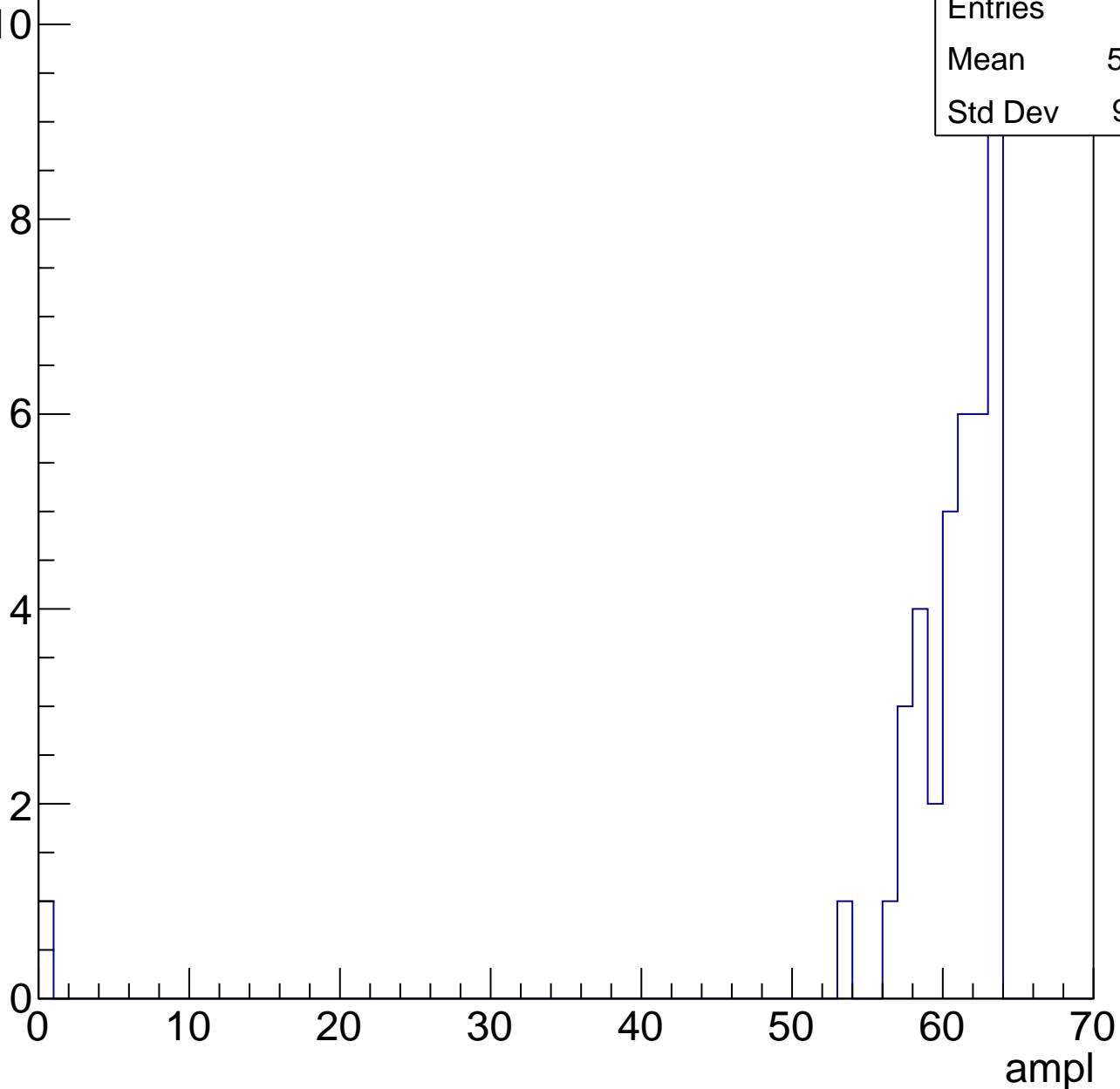


# B0L002S, U8-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.92
Std Dev	9.851



# B0L002S, U8-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	61.75
Std Dev	0.433



# B0L002S, U8-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch88, adc0

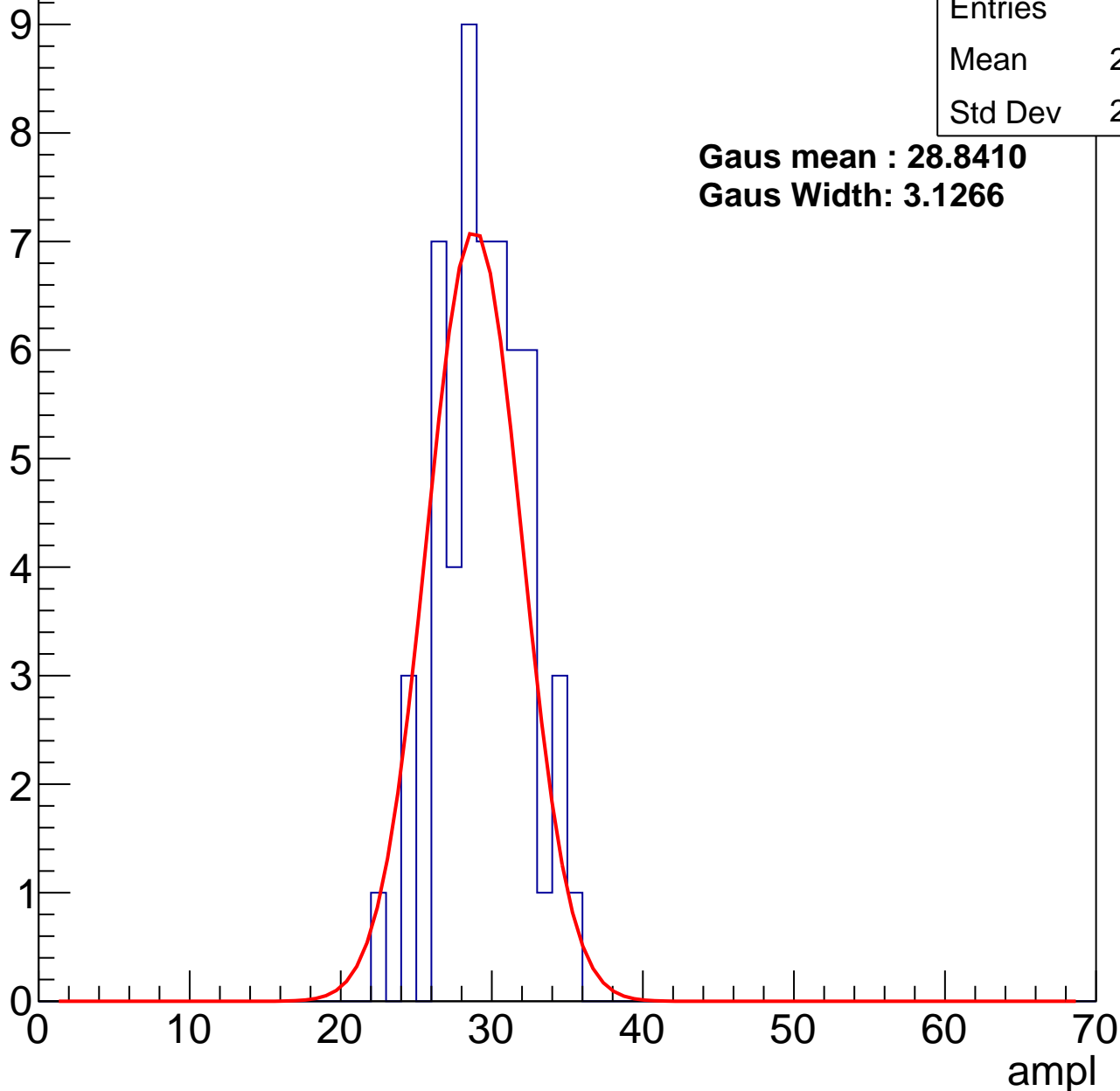
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	29.04
Std Dev	2.776

**Gaus mean : 28.8410**

**Gaus Width: 3.1266**



# B0L002S, U8-ch88, adc1

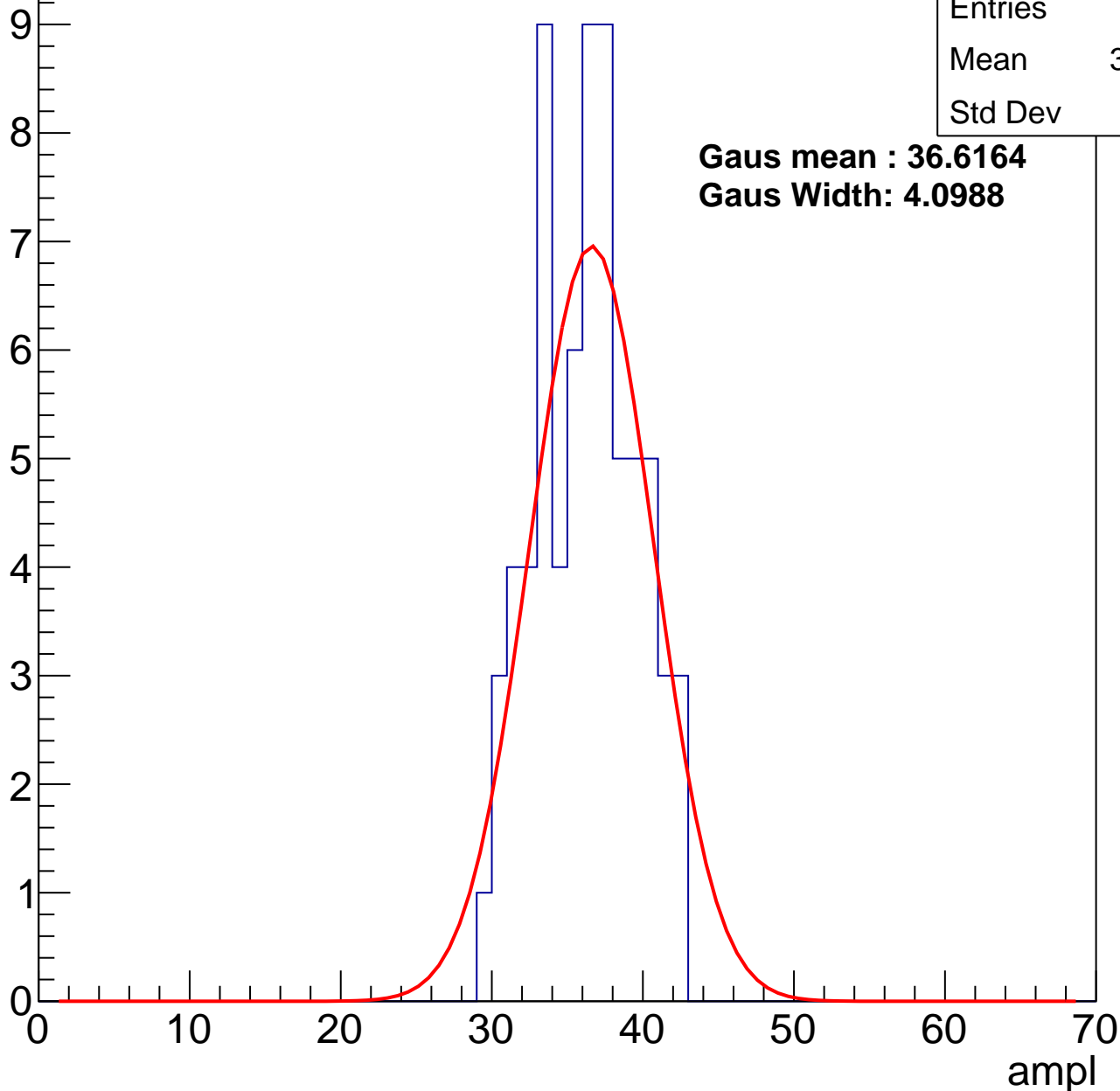
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.79
Std Dev	3.29

**Gaus mean : 36.6164**

**Gaus Width: 4.0988**



# B0L002S, U8-ch88, adc2

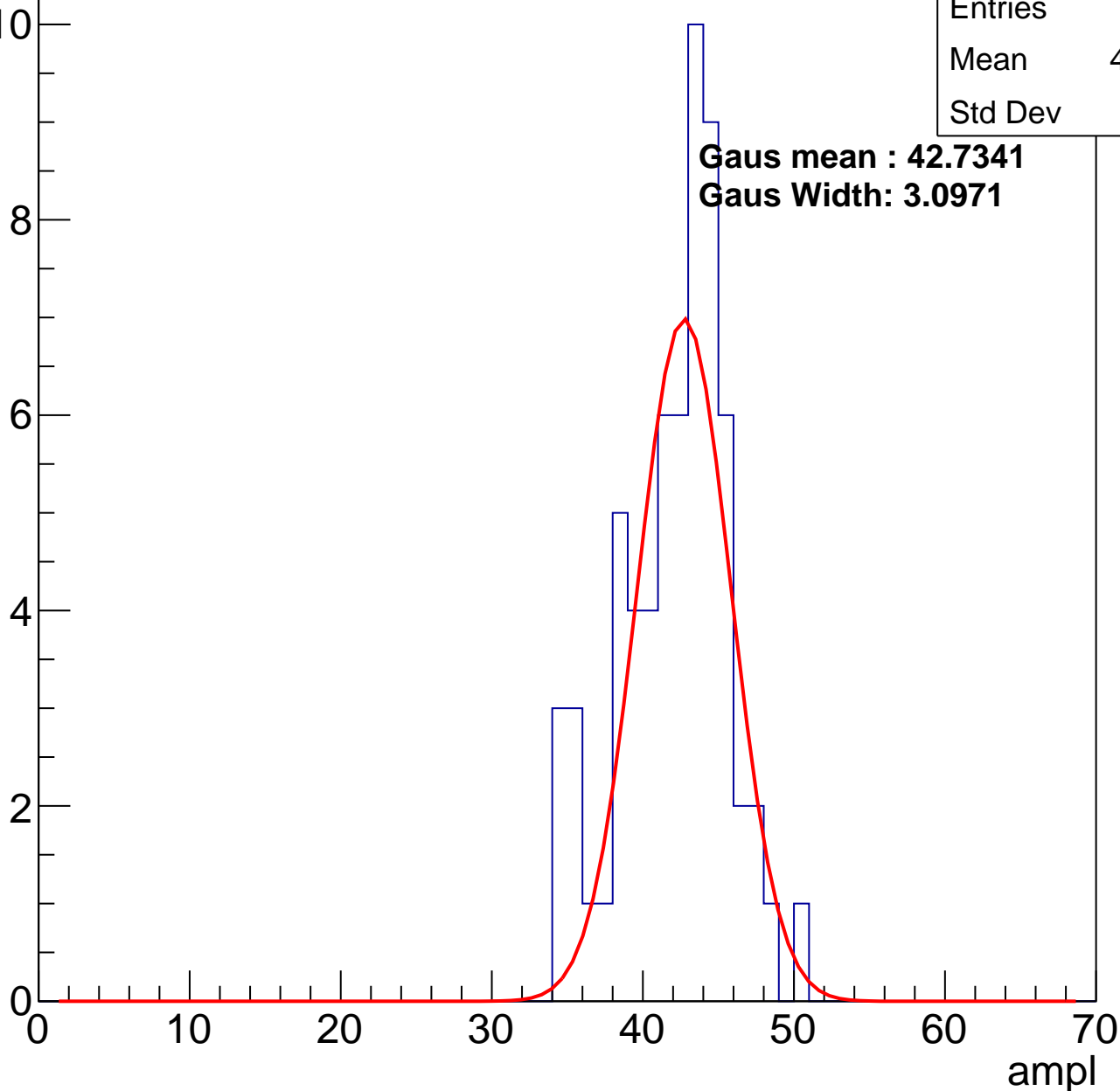
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	41.62
Std Dev	3.56

**Gaus mean : 42.7341**

**Gaus Width: 3.0971**

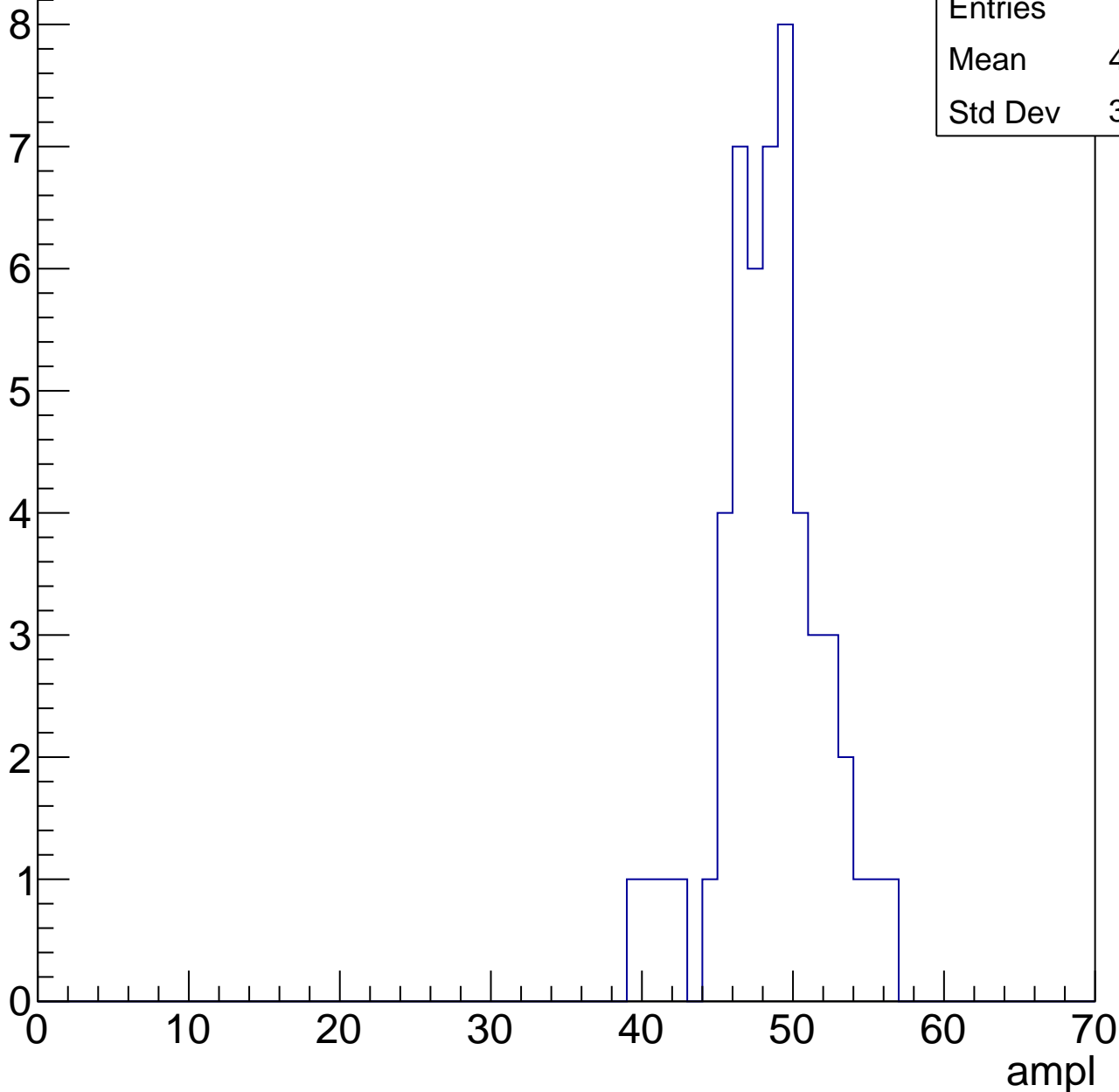


# B0L002S, U8-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

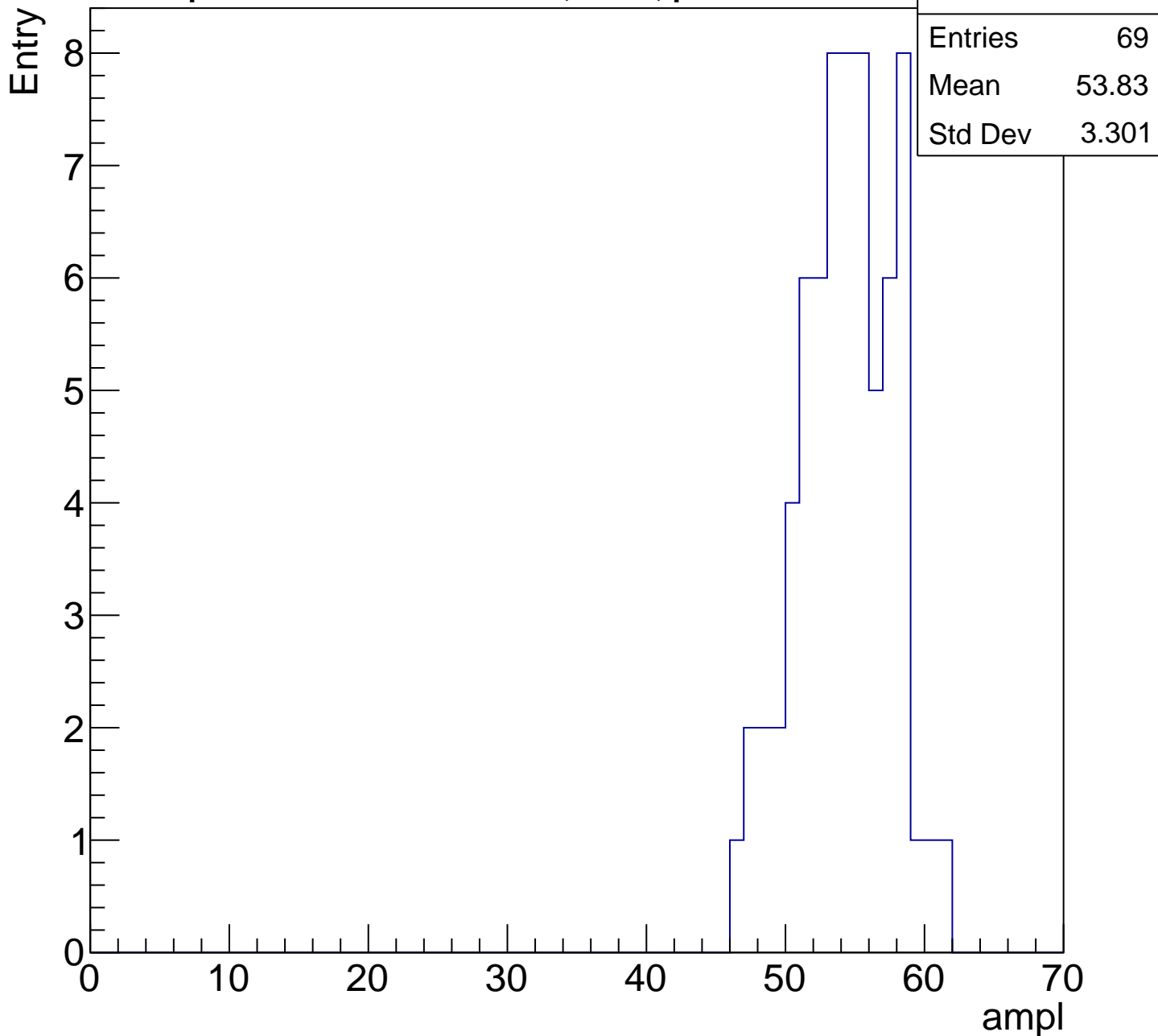
Entry

Entries	52
Mean	48.04
Std Dev	3.436



# B0L002S, U8-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U8-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

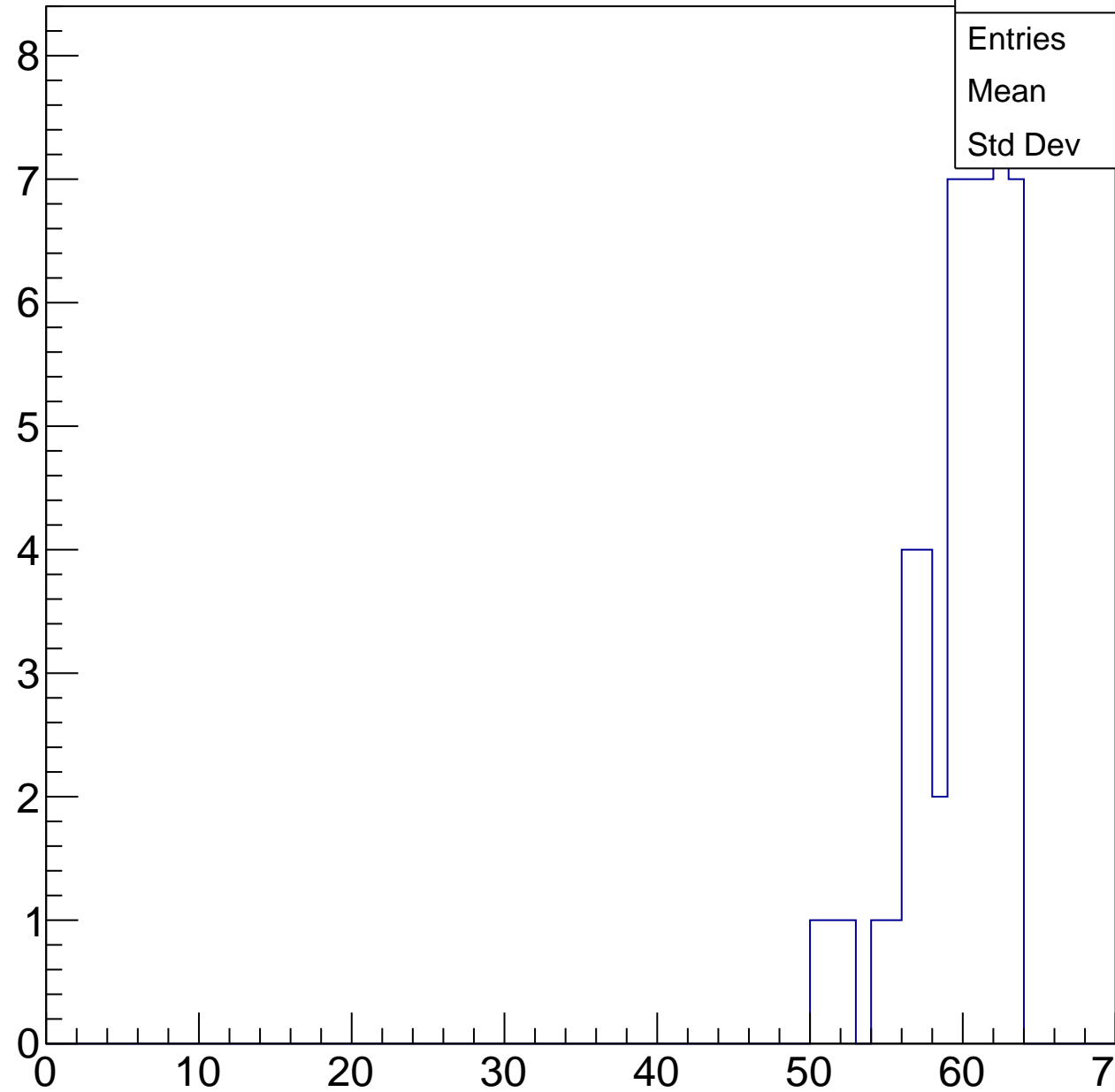
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.35
Std Dev	3.136

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	10
Mean	54.7
Std Dev	18.28

ampl



# B0L002S, U8-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch89, adc0

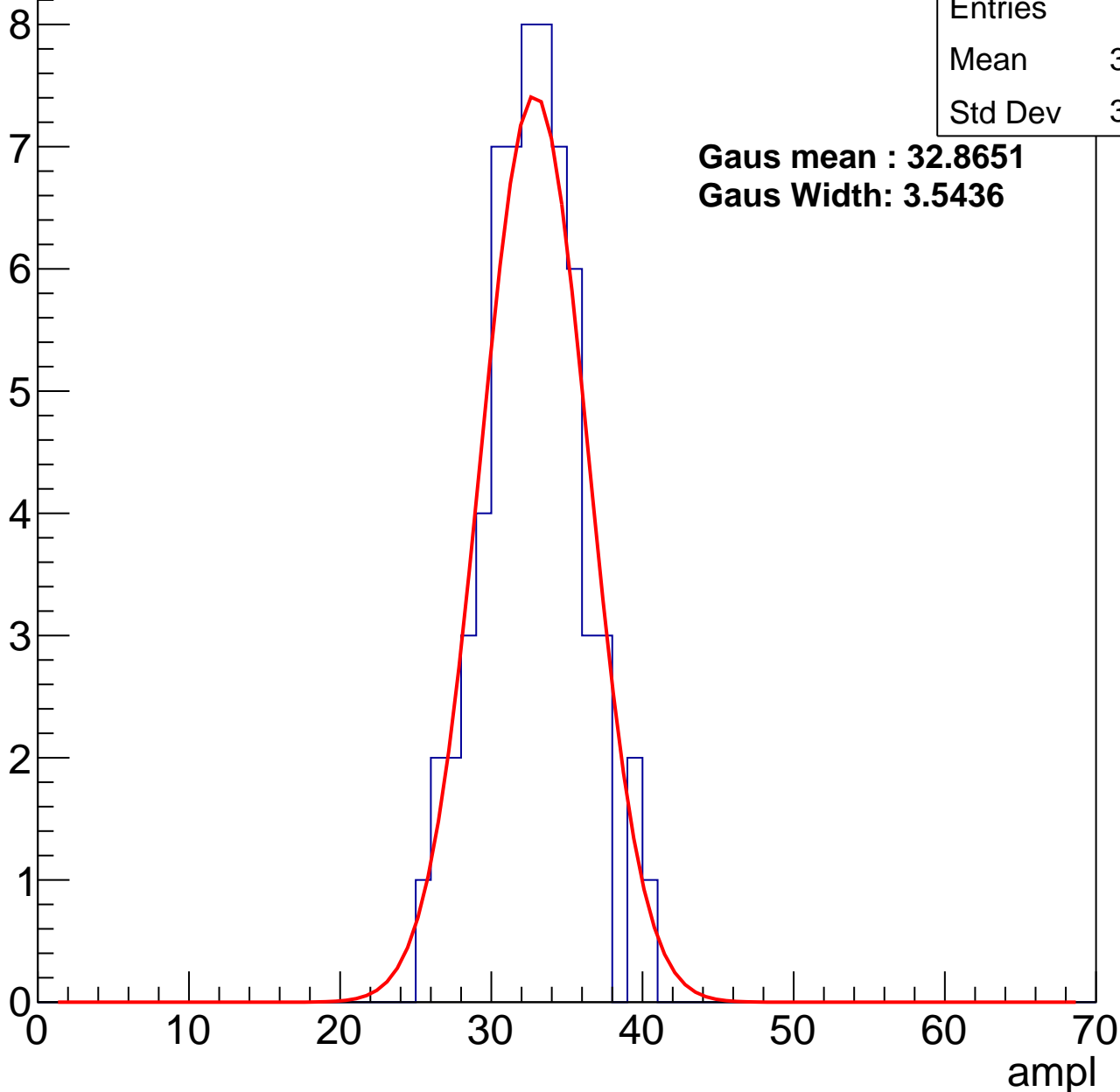
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	32.23
Std Dev	3.215

**Gaus mean : 32.8651**

**Gaus Width: 3.5436**



# B0L002S, U8-ch89, adc1

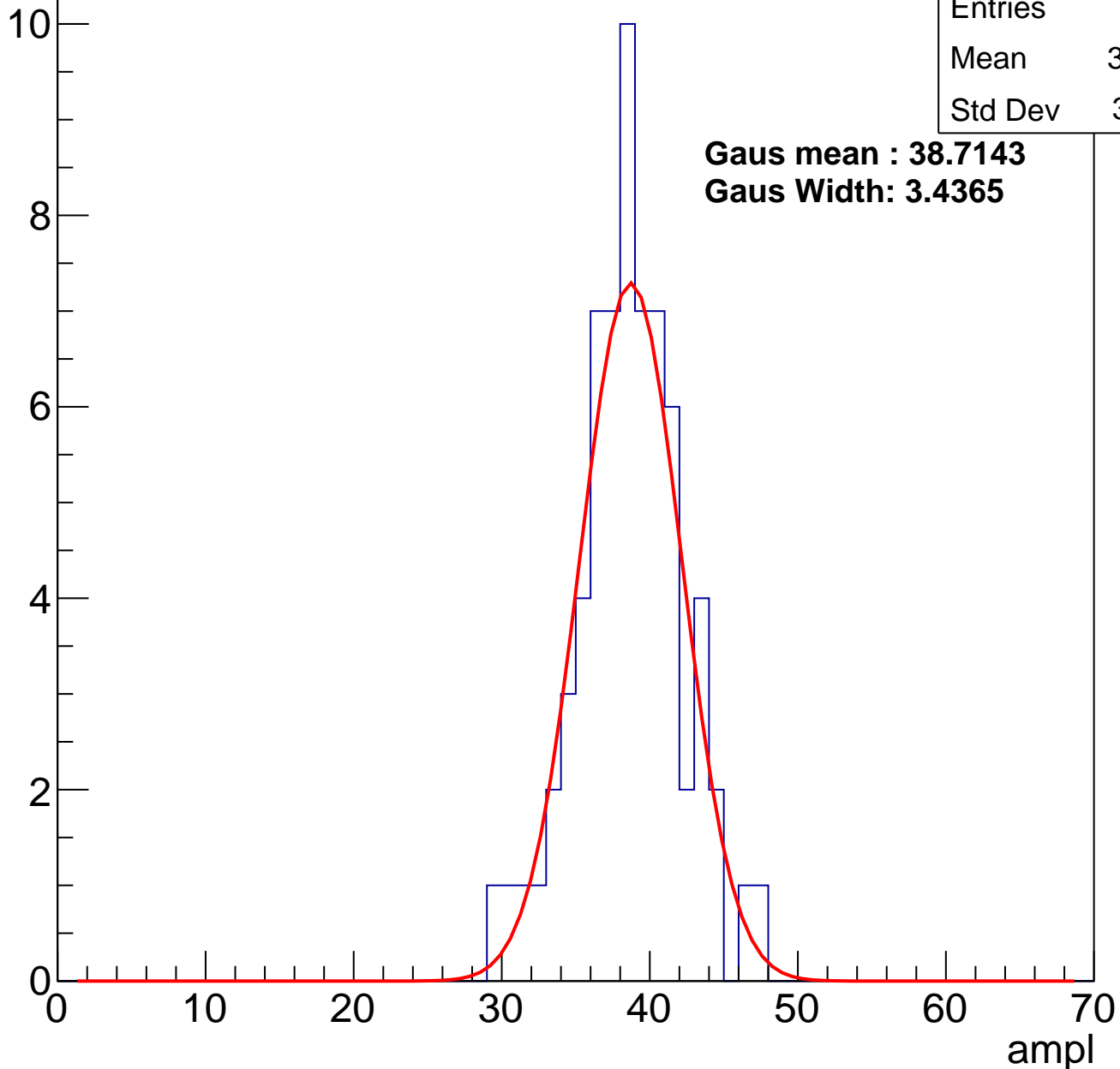
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	38.16
Std Dev	3.531

**Gaus mean : 38.7143**

**Gaus Width: 3.4365**

Entry



# B0L002S, U8-ch89, adc2

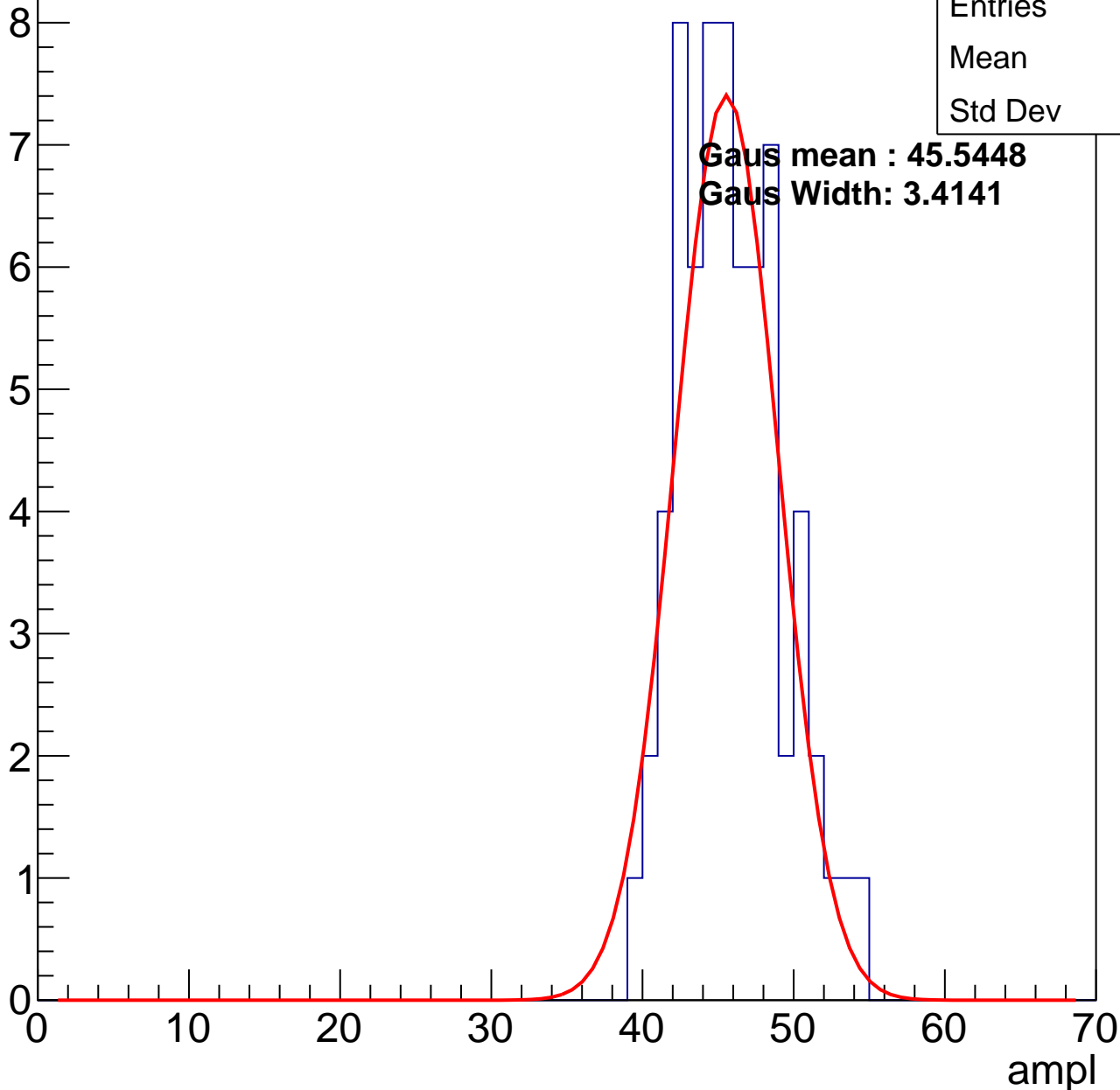
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	45.4
Std Dev	3.31

**Gaus mean : 45.5448**

**Gaus Width: 3.4141**

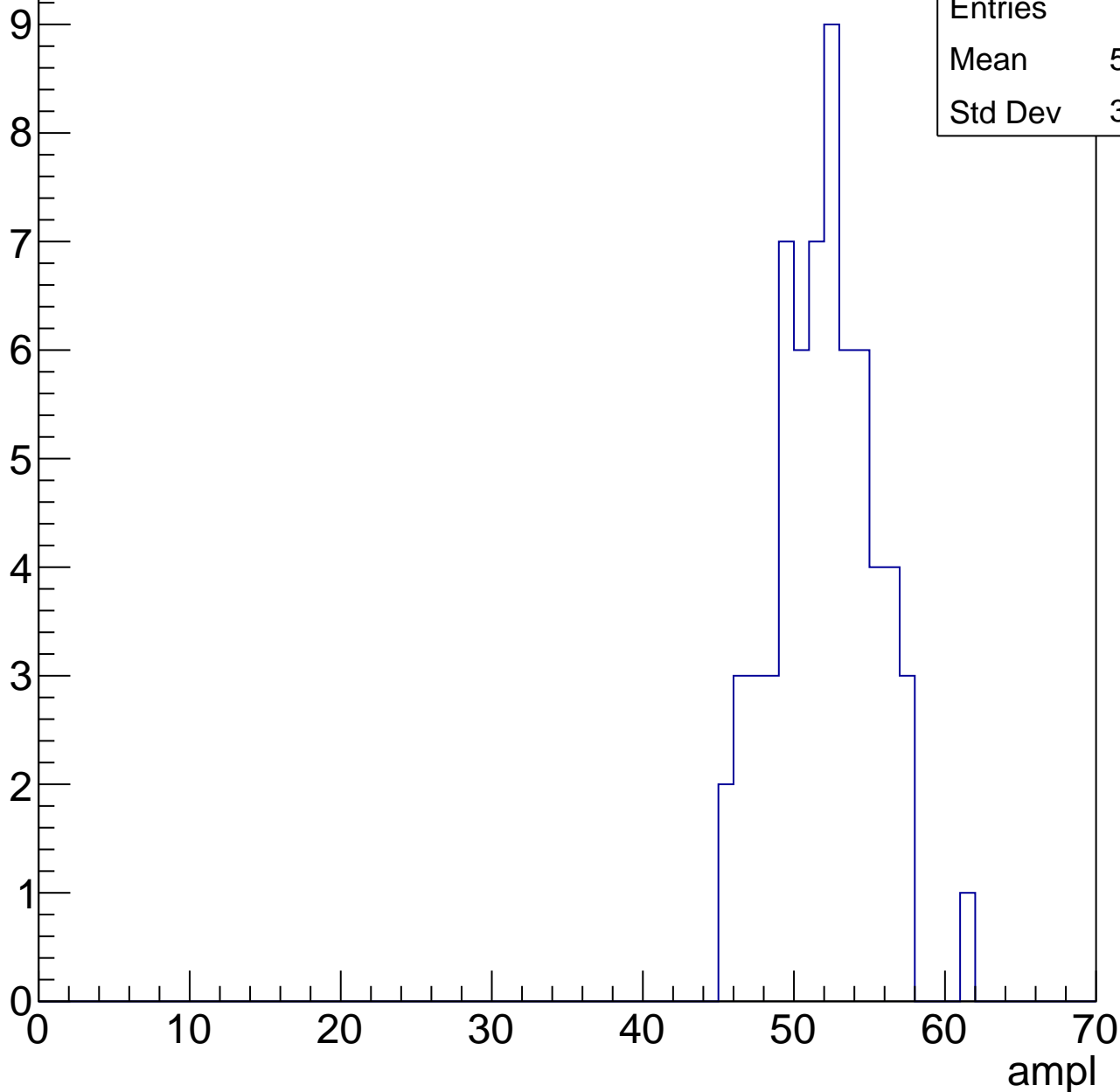


# B0L002S, U8-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

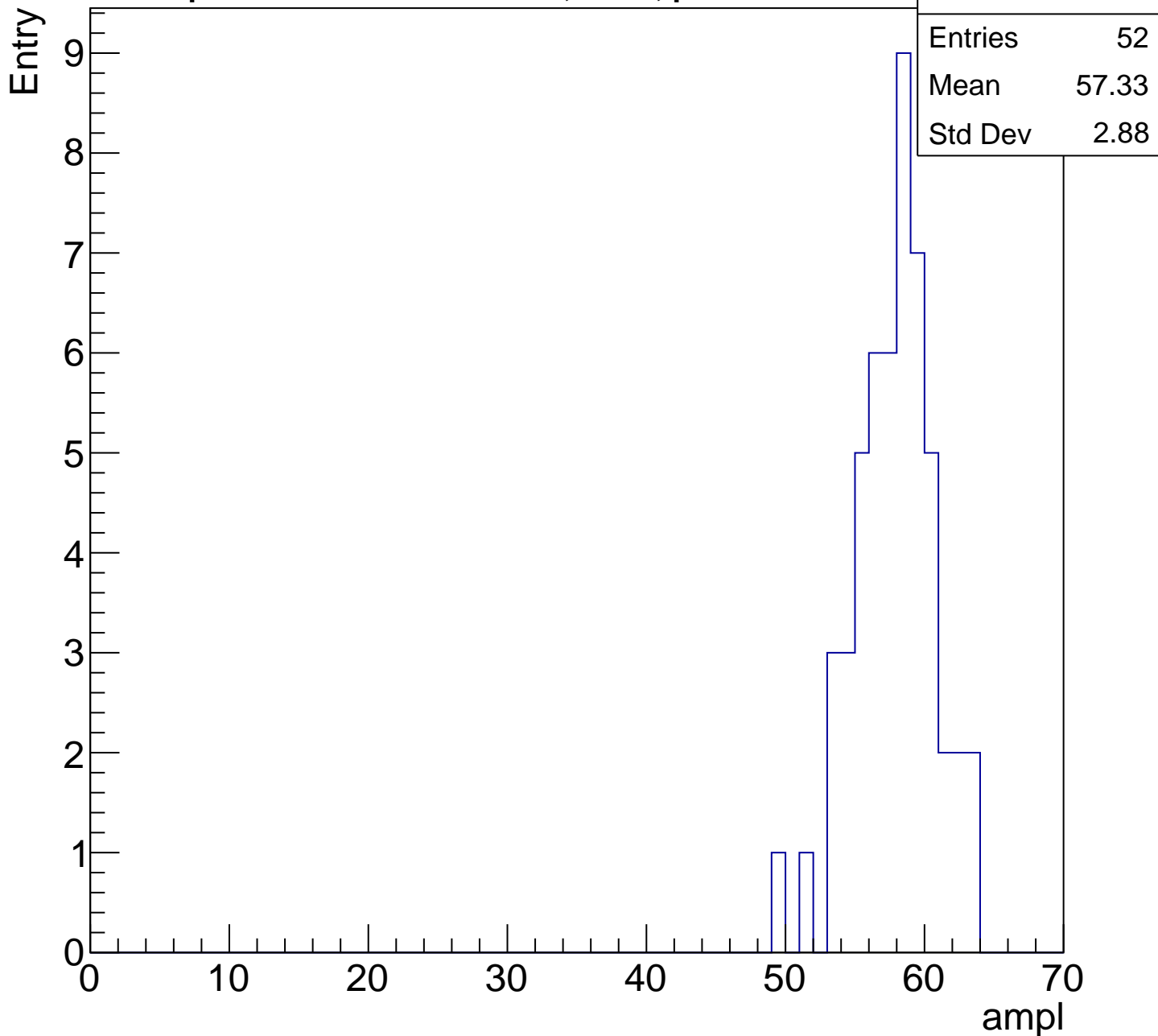
Entry

Entries	64
Mean	51.55
Std Dev	3.297



# B0L002S, U8-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

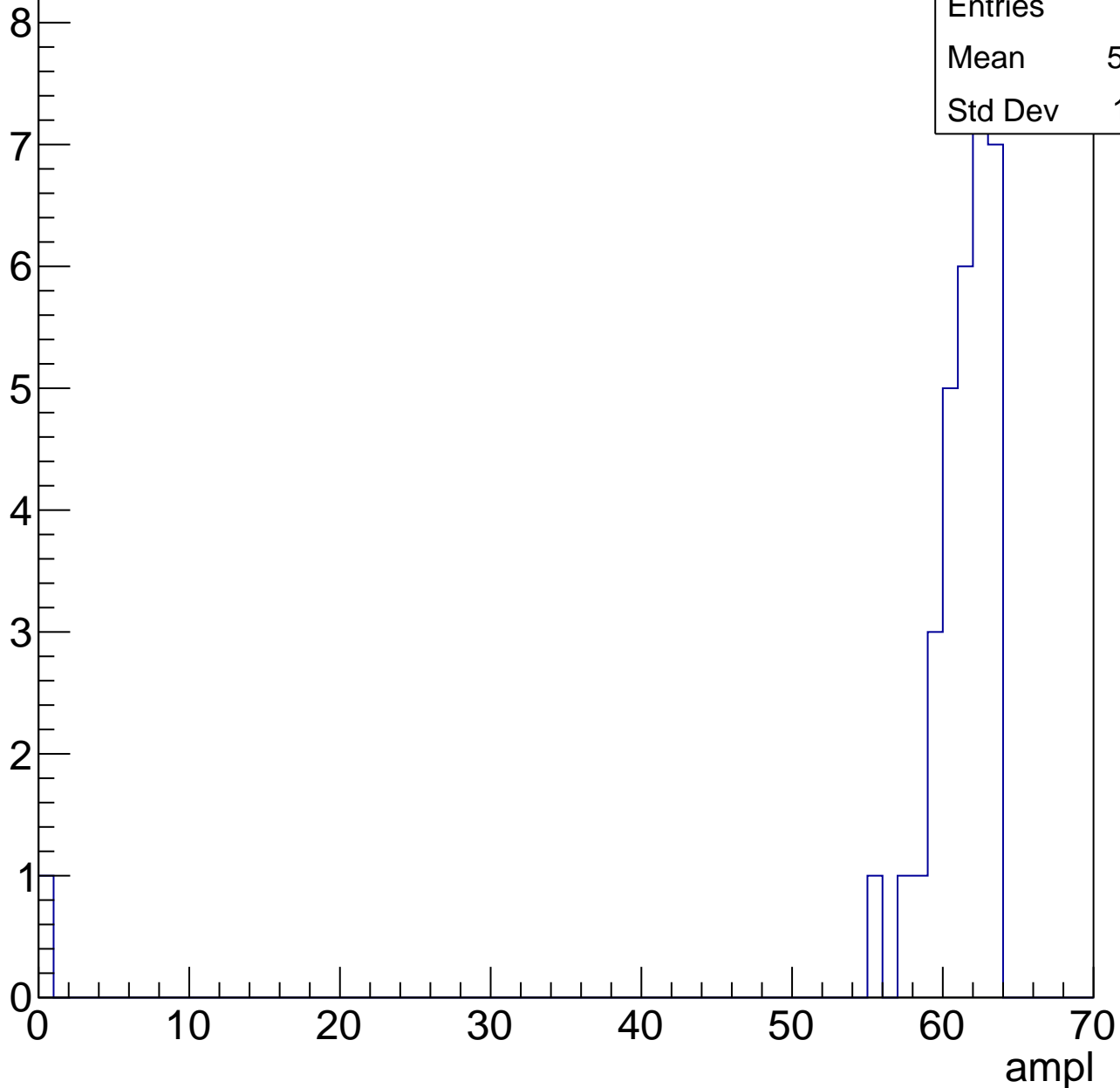


# B0L002S, U8-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59.09
Std Dev	10.61



# B0L002S, U8-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch90, adc0

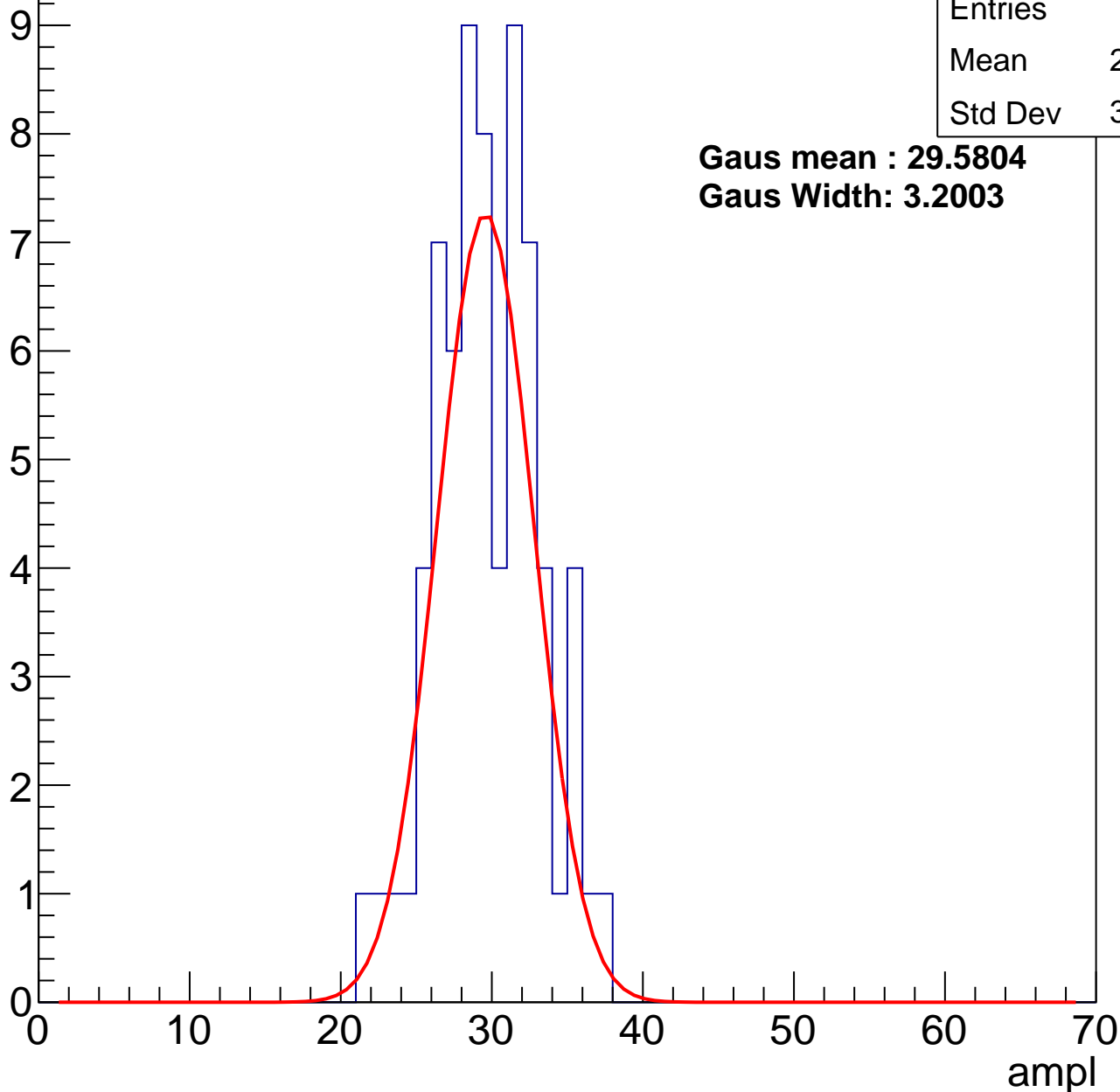
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.28
Std Dev	3.357

**Gaus mean : 29.5804**

**Gaus Width: 3.2003**



# B0L002S, U8-ch90, adc1

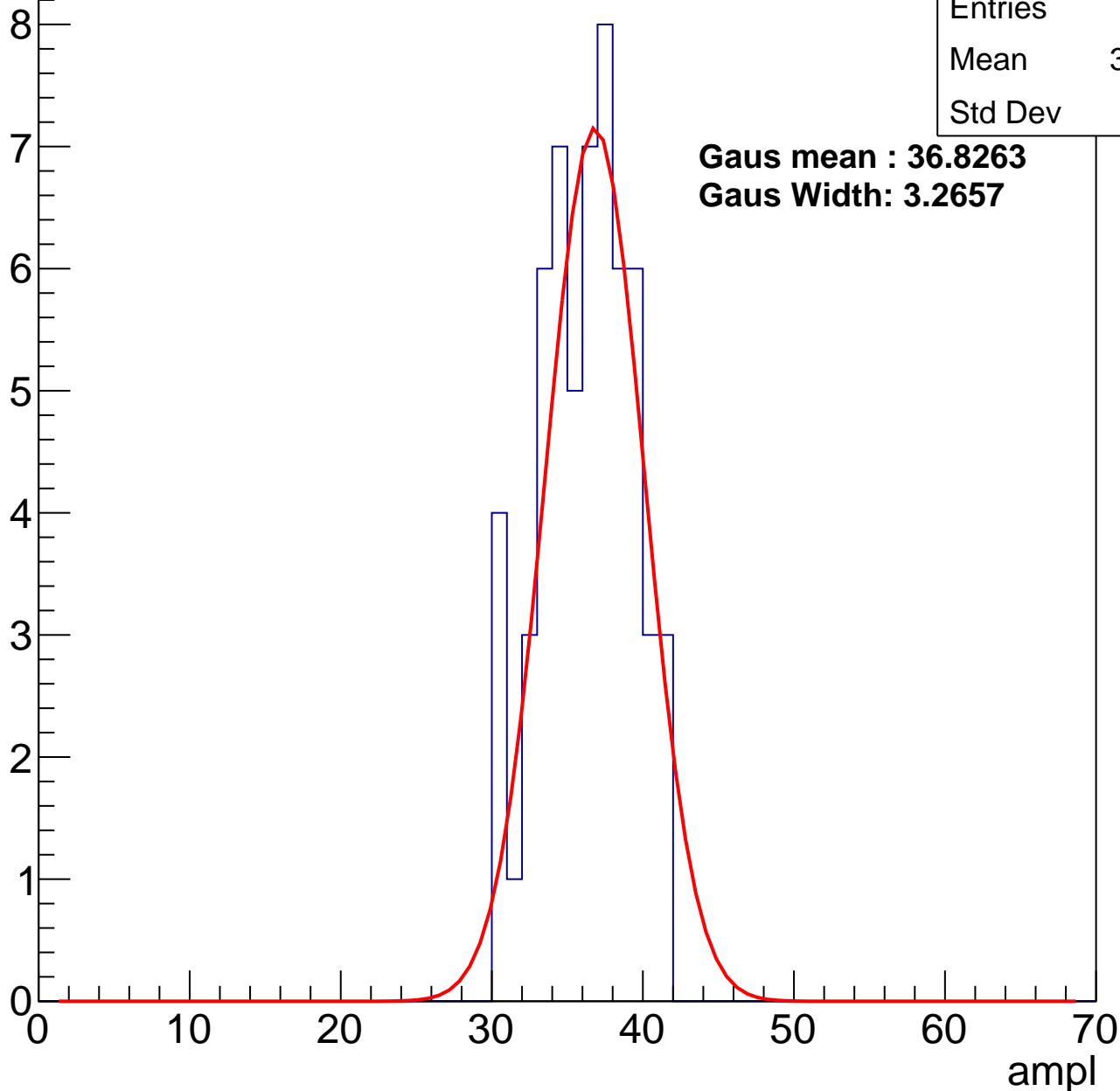
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.78
Std Dev	2.94

**Gaus mean : 36.8263**

**Gaus Width: 3.2657**



# B0L002S, U8-ch90, adc2

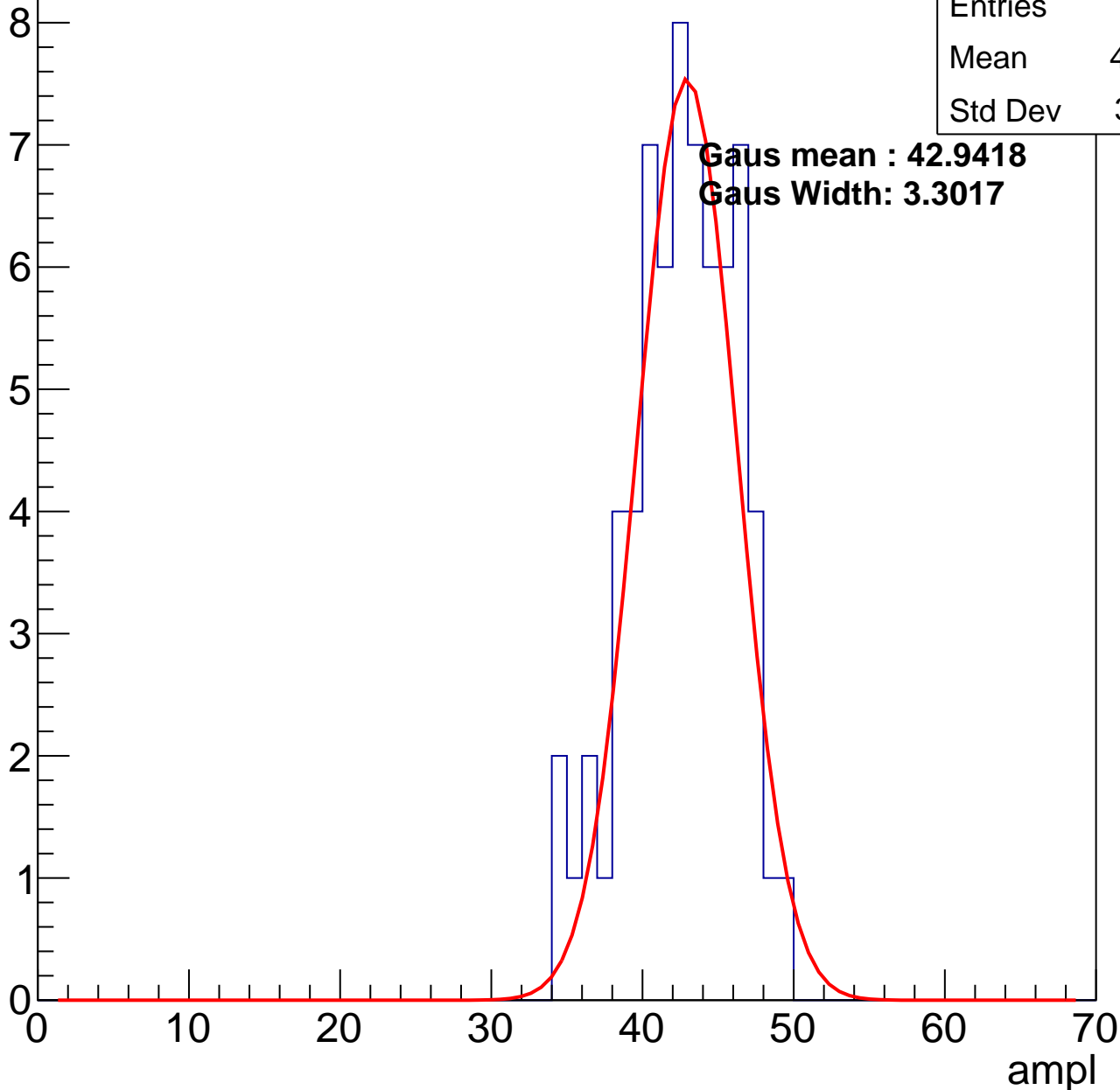
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.15
Std Dev	3.431

**Gaus mean : 42.9418**

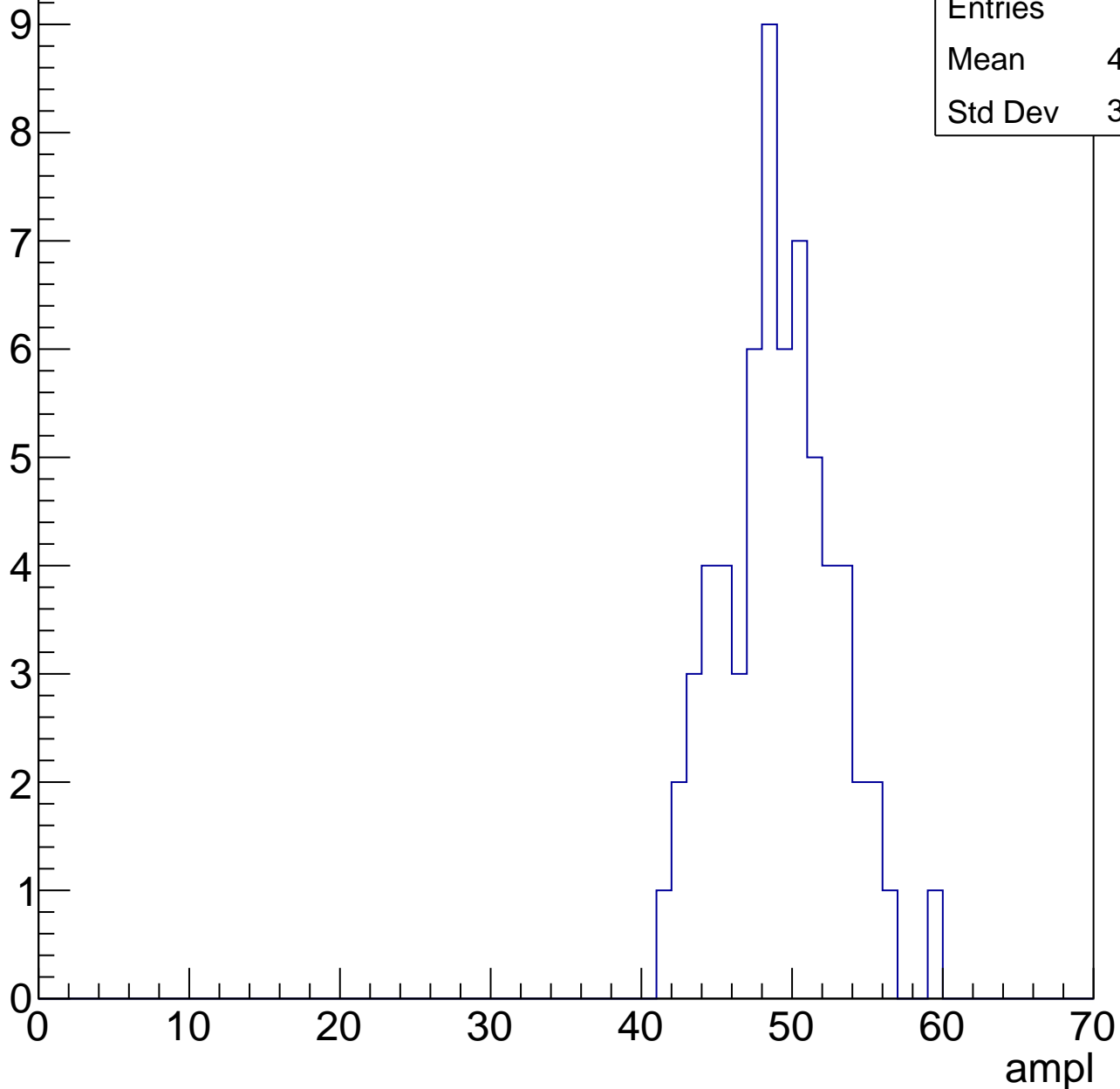
**Gaus Width: 3.3017**



# B0L002S, U8-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

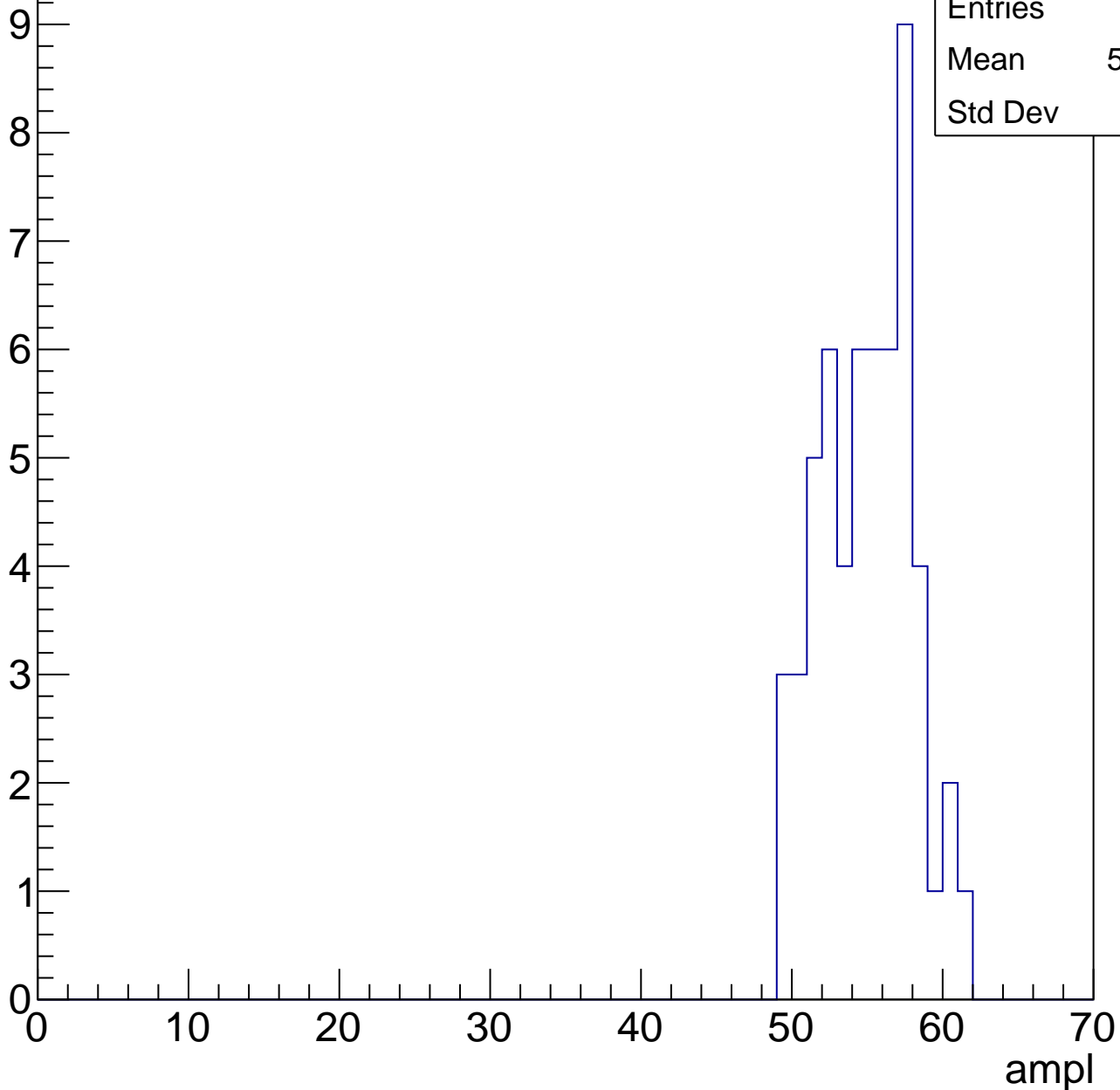


# B0L002S, U8-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

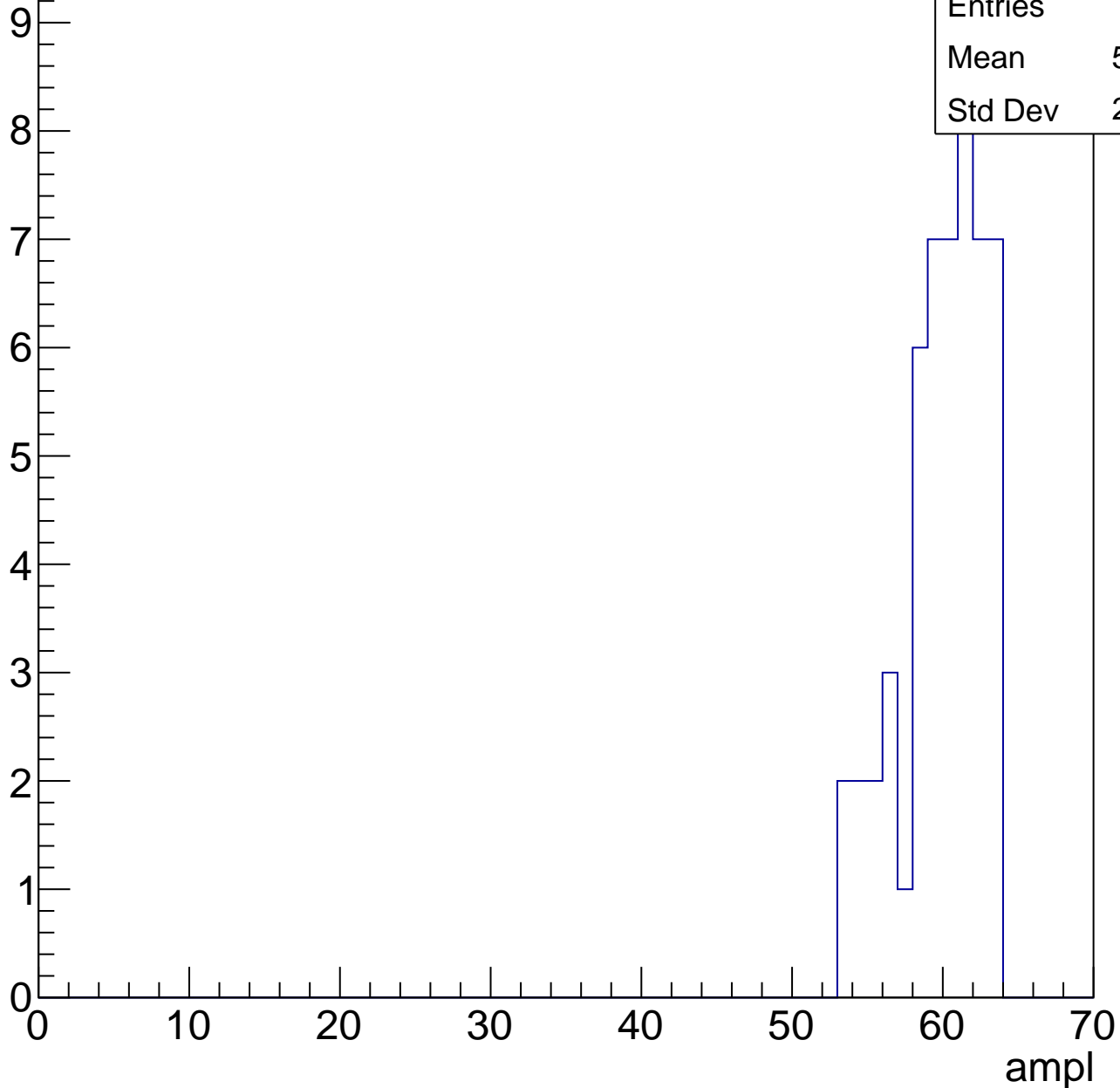
Entries	56
Mean	54.48
Std Dev	3



# B0L002S, U8-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

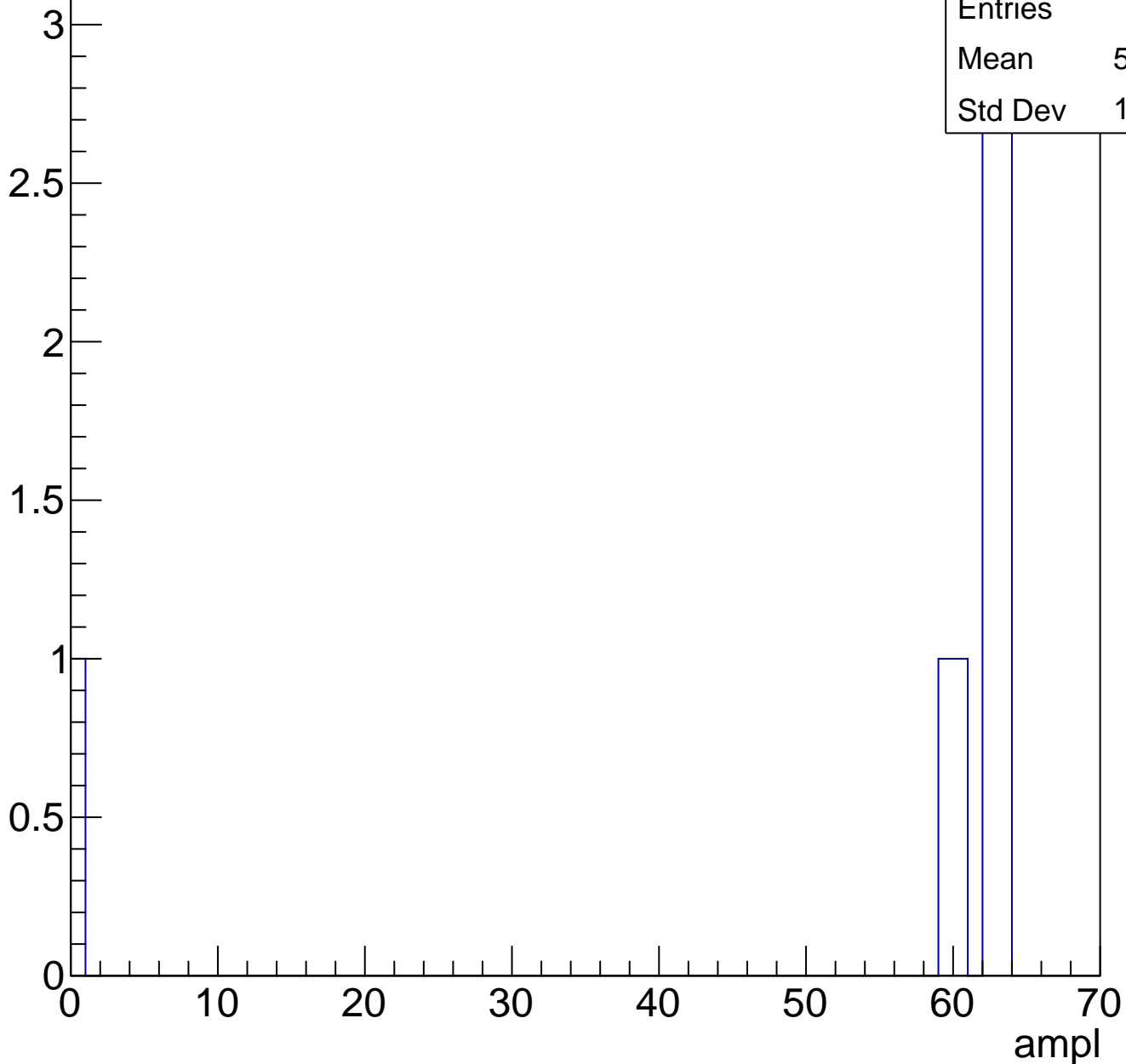
Entry



# B0L002S, U8-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch91, adc0

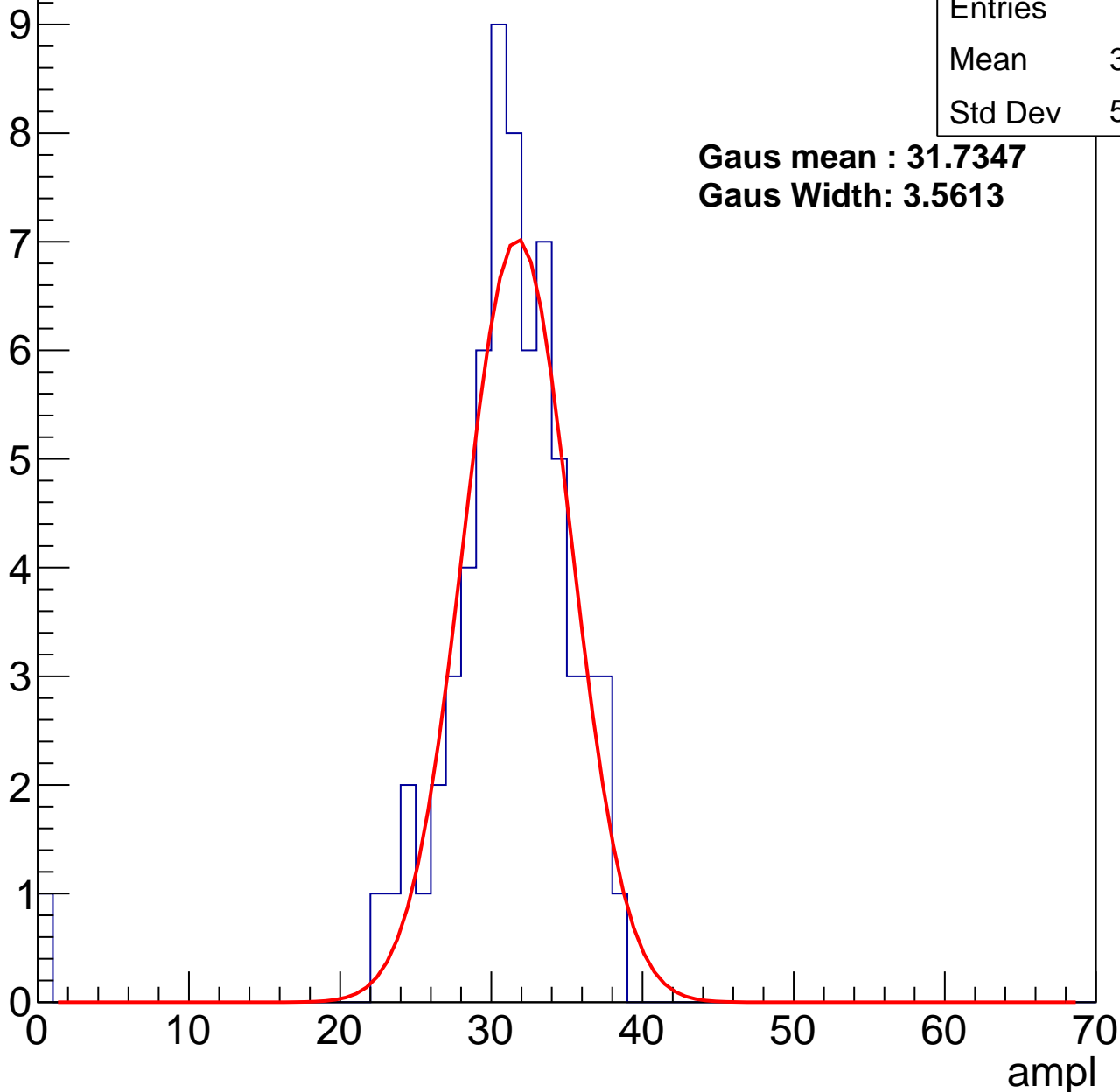
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.45
Std Dev	5.144

**Gaus mean : 31.7347**

**Gaus Width: 3.5613**



# B0L002S, U8-ch91, adc1

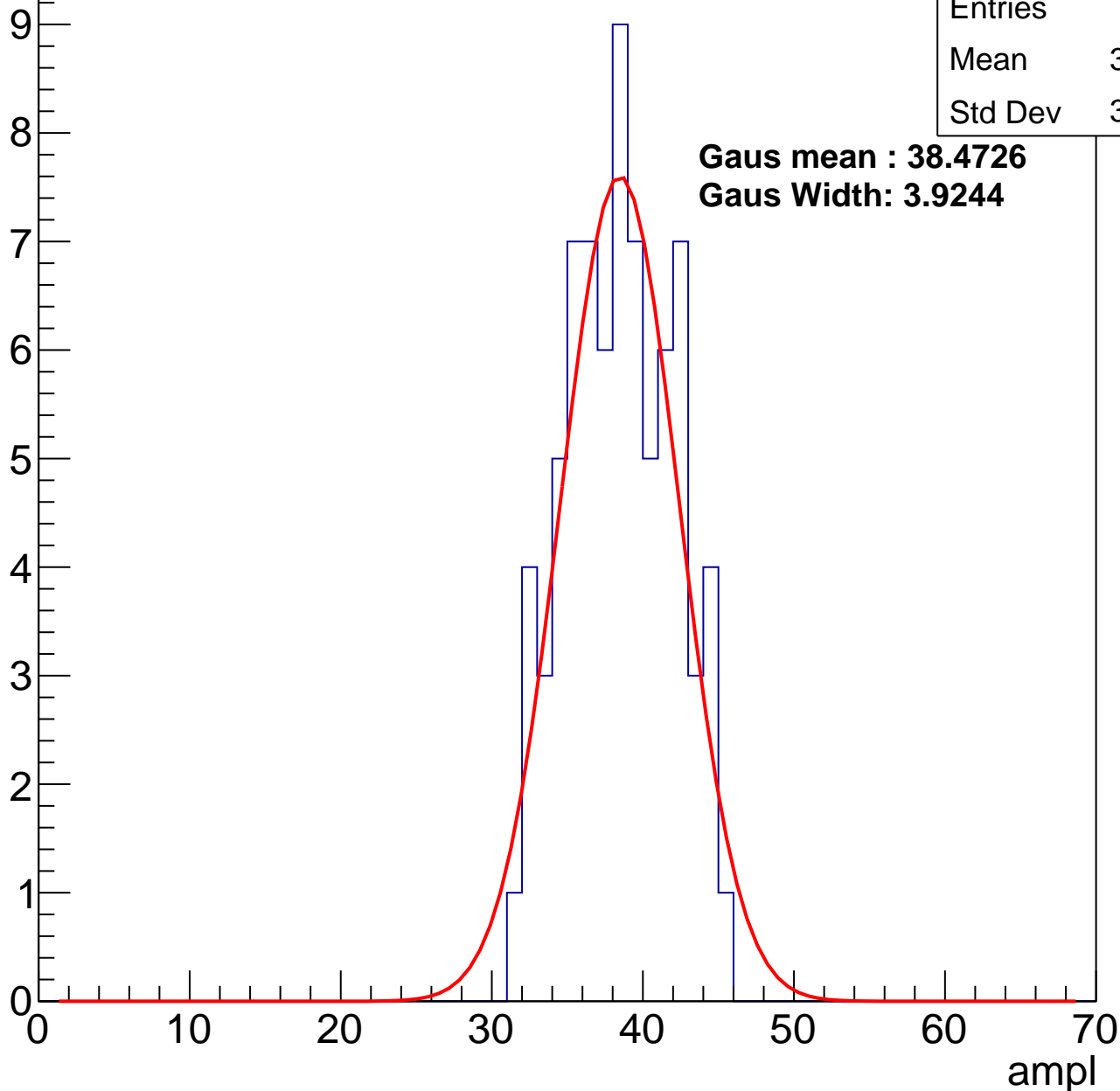
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.03
Std Dev	3.476

**Gaus mean : 38.4726**

**Gaus Width: 3.9244**



# B0L002S, U8-ch91, adc2

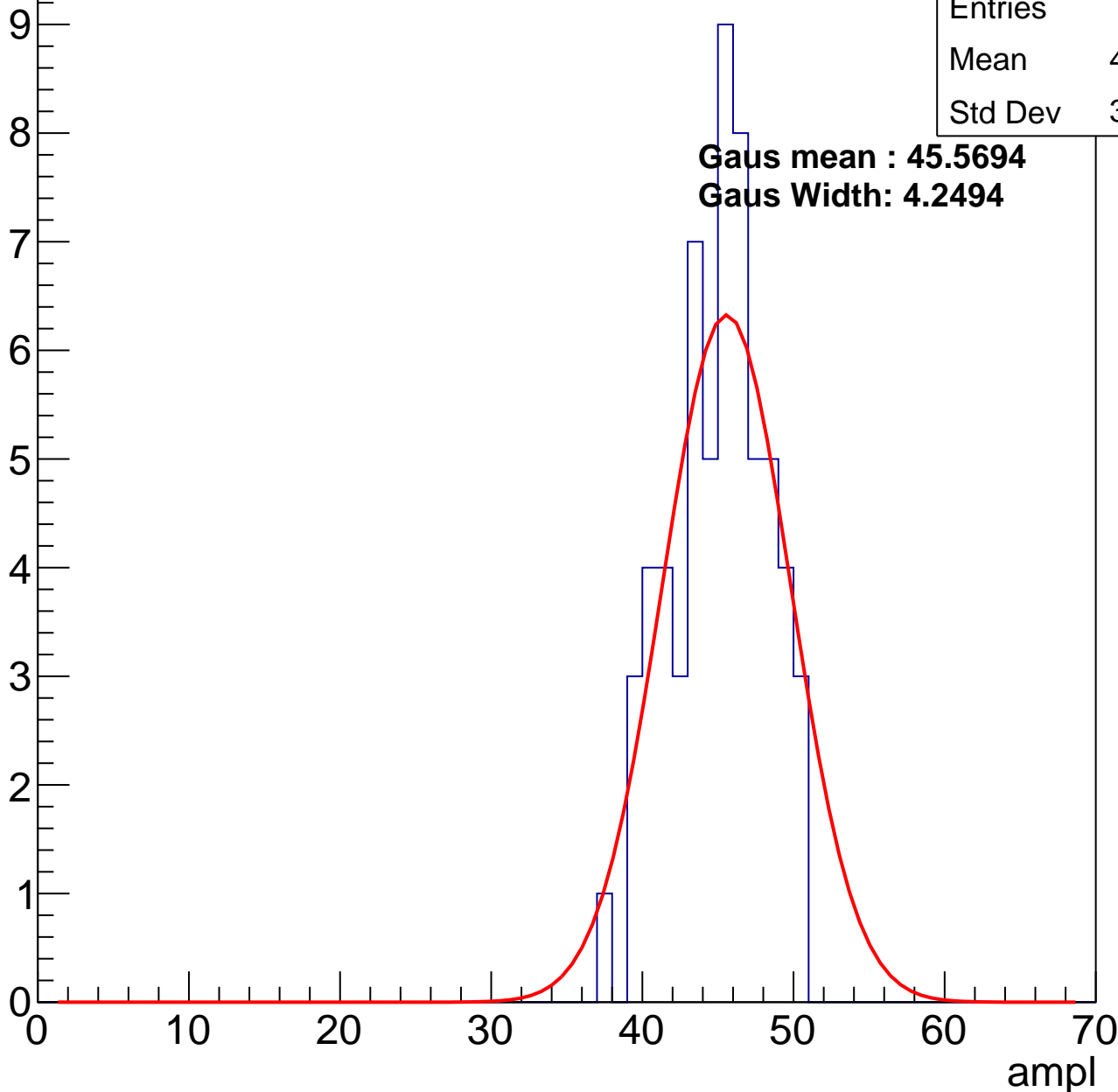
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.57
Std Dev	3.128

**Gaus mean : 45.5694**

**Gaus Width: 4.2494**

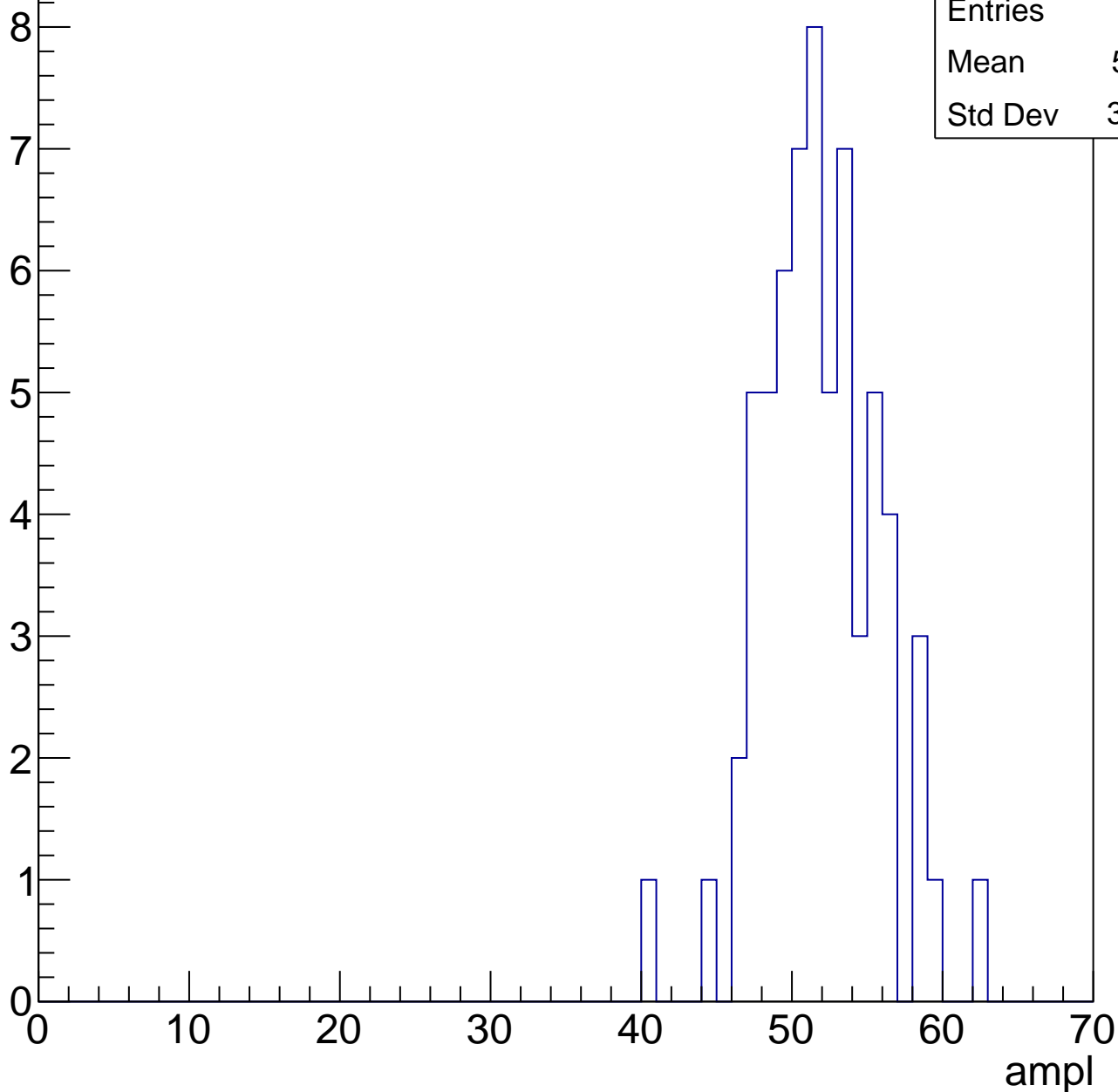


# B0L002S, U8-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.41
Std Dev	3.827

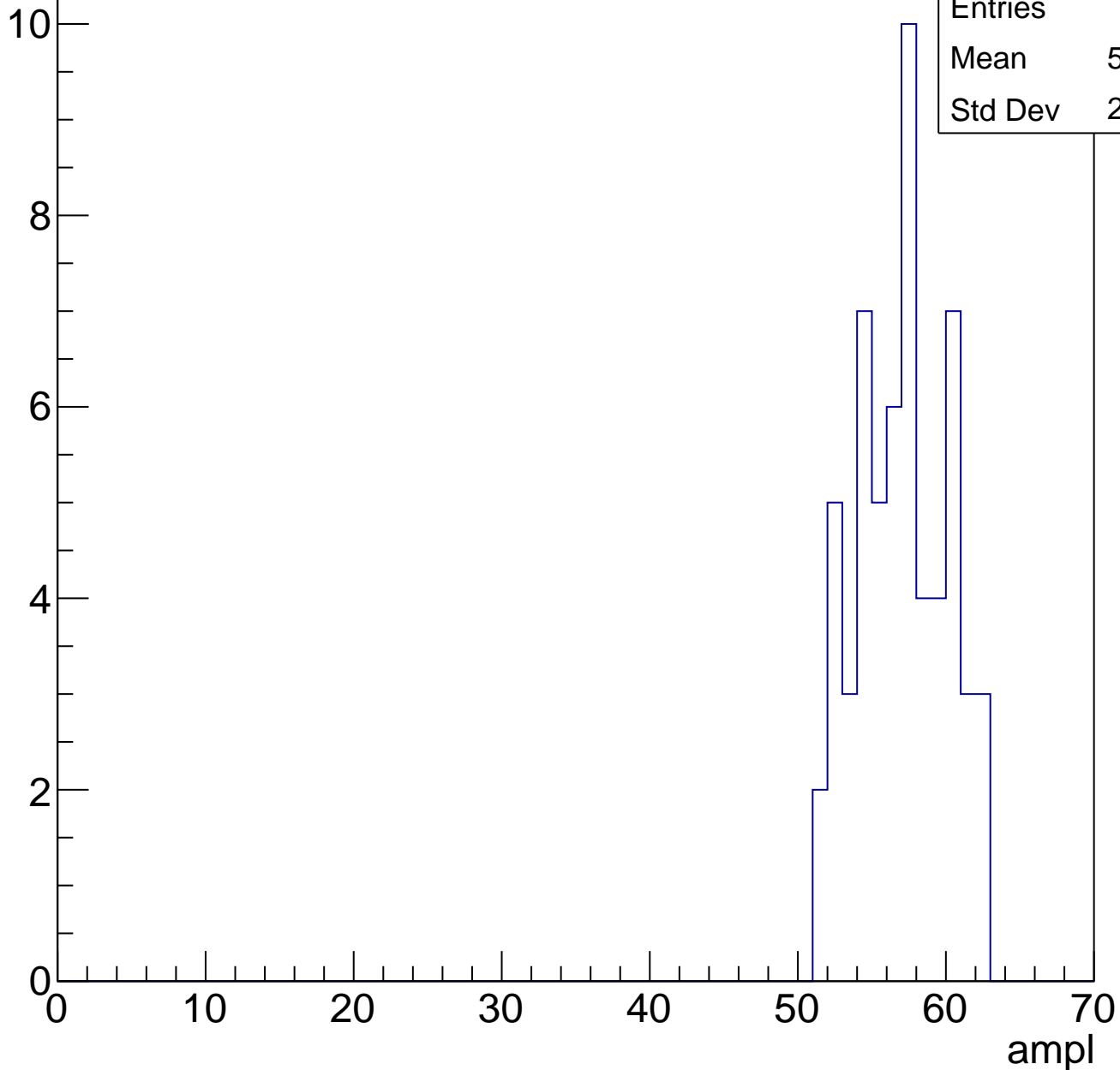


# B0L002S, U8-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	59
Mean	56.56
Std Dev	2.993

Entry

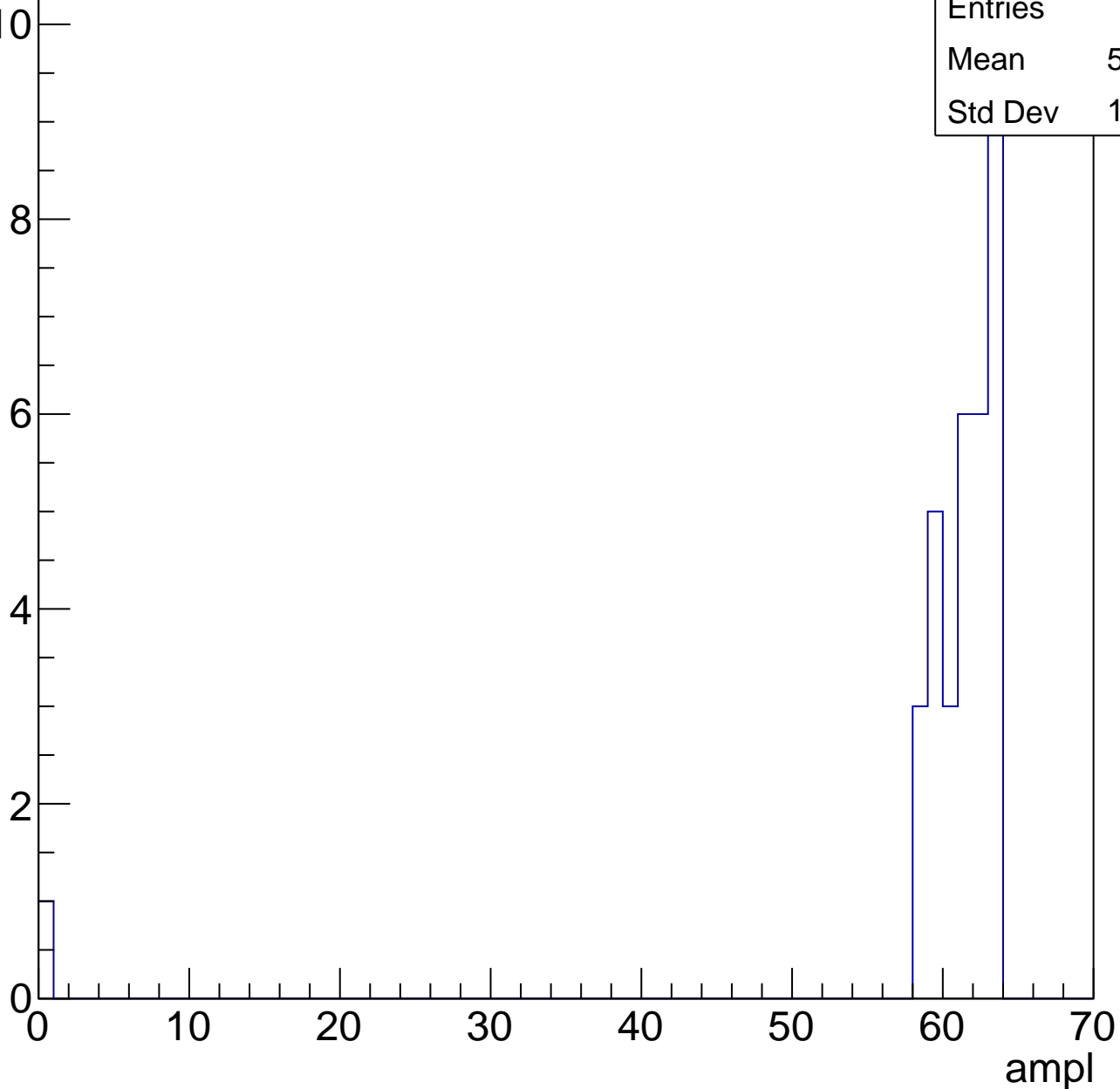


# B0L002S, U8-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.32
Std Dev	10.46



# B0L002S, U8-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch92, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	29.82
Std Dev	3.842

**Gaus mean : 30.4571**

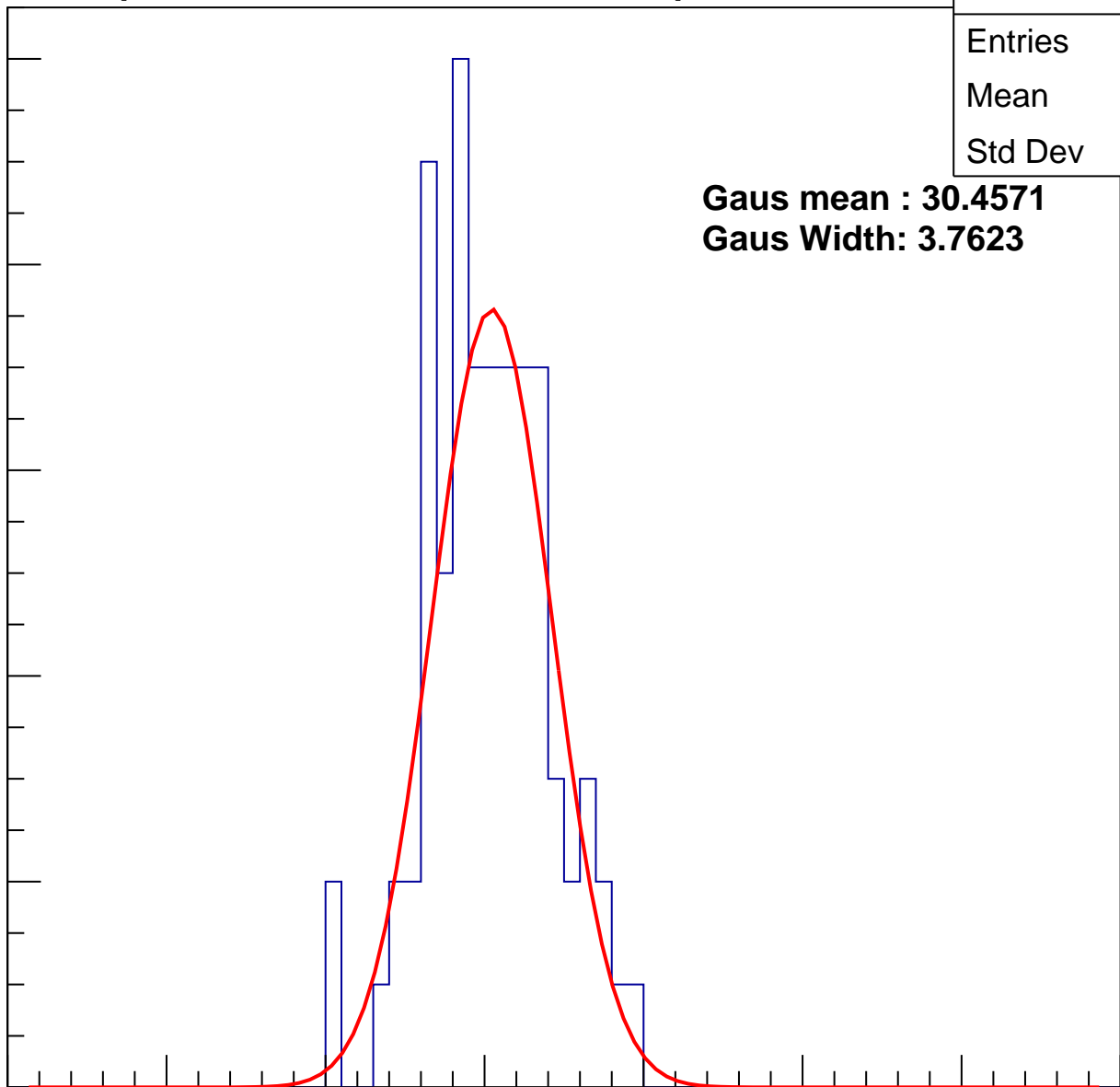
**Gaus Width: 3.7623**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch92, adc1

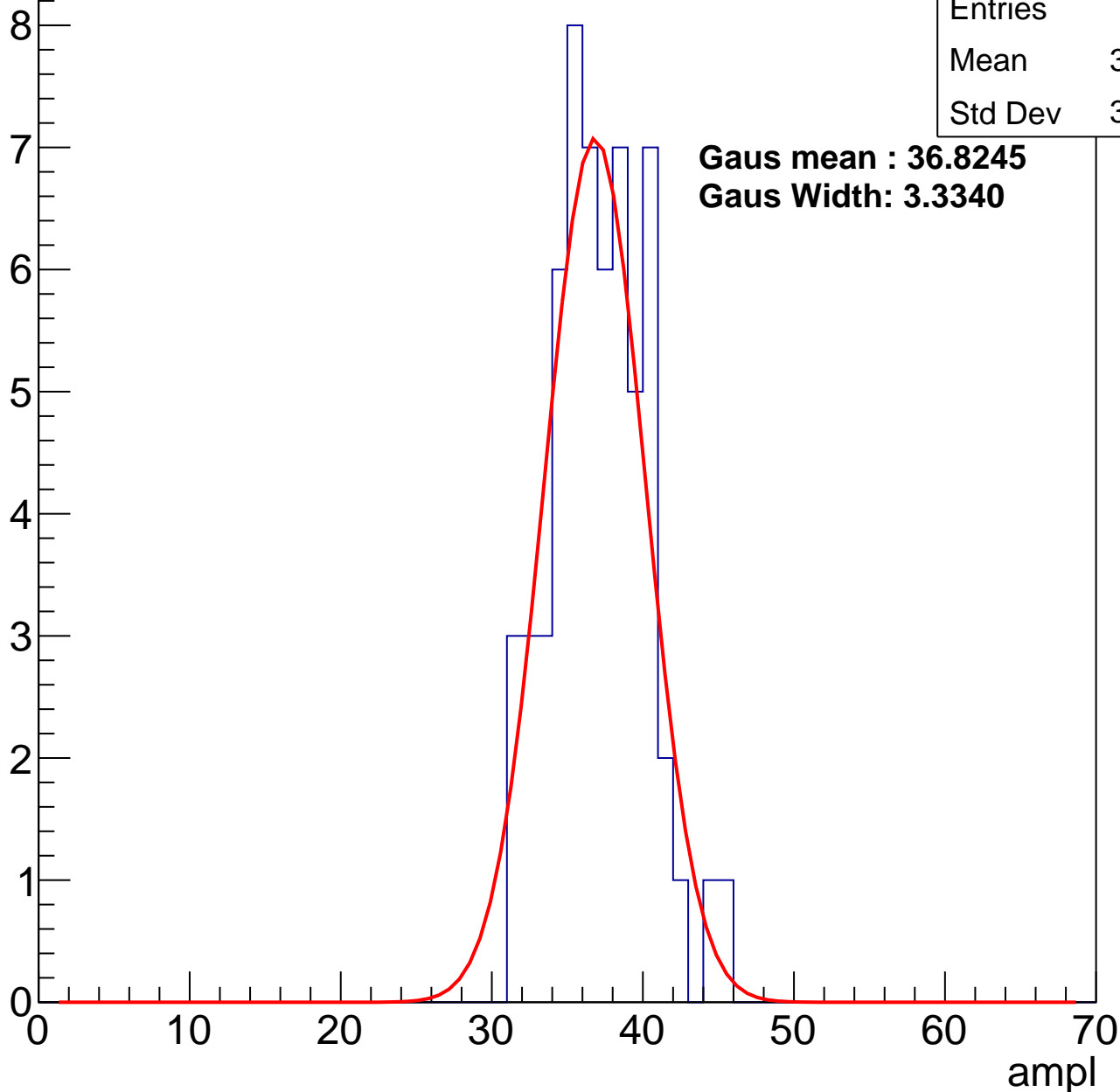
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.67
Std Dev	3.102

**Gaus mean : 36.8245**

**Gaus Width: 3.3340**



# B0L002S, U8-ch92, adc2

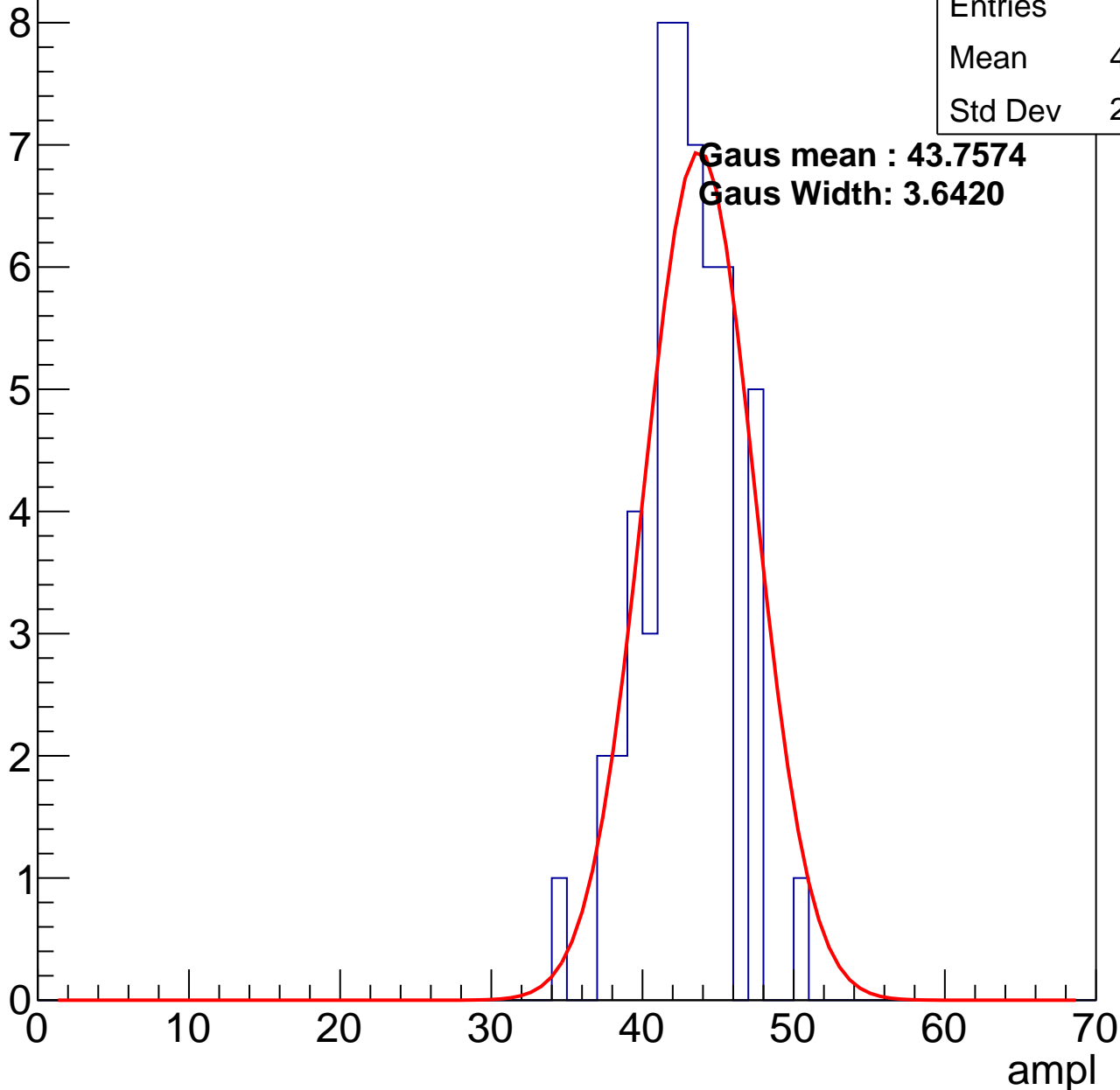
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.34
Std Dev	2.978

**Gaus mean : 43.7574**

**Gaus Width: 3.6420**

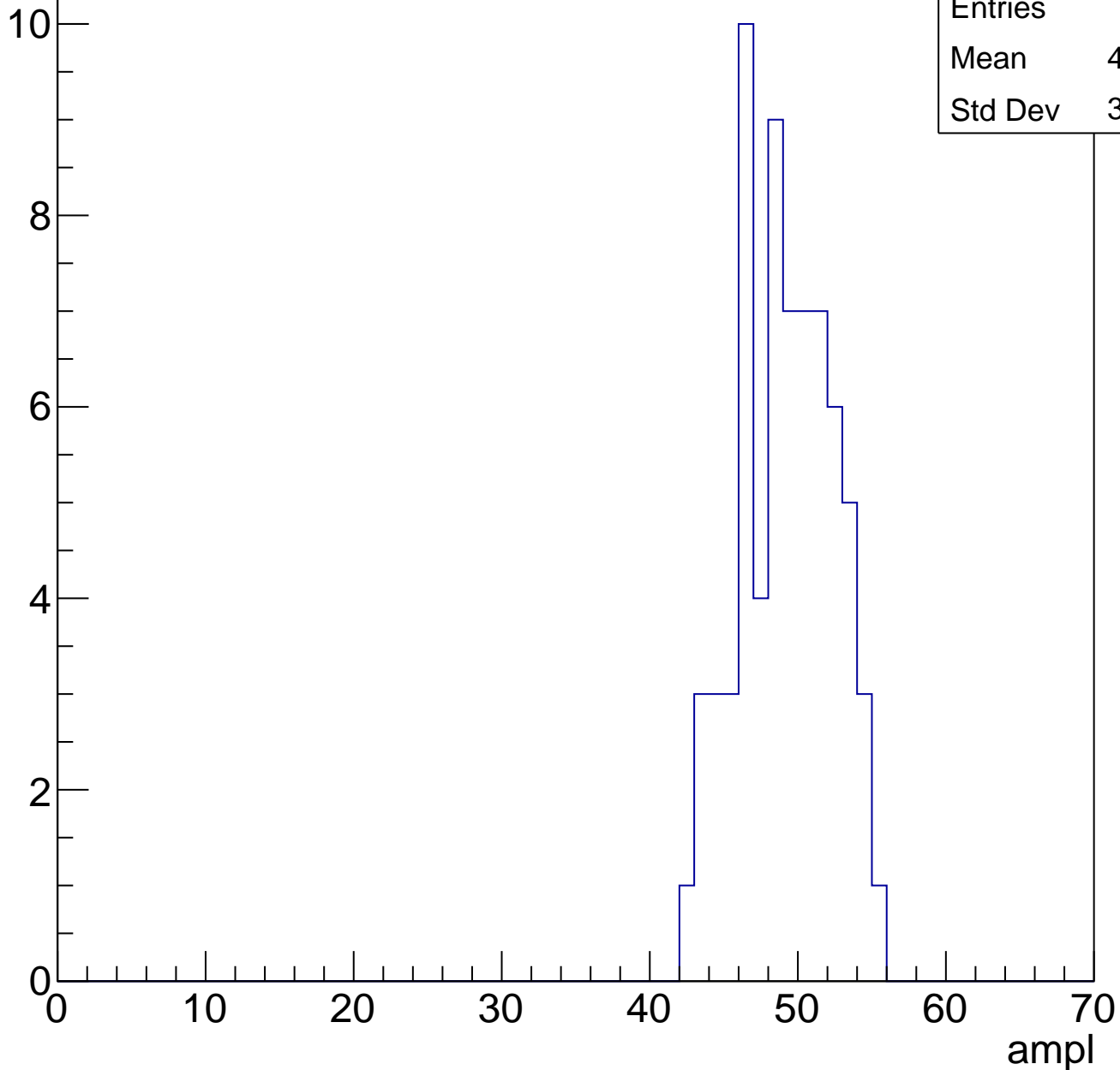


# B0L002S, U8-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	69
Mean	48.72
Std Dev	3.116

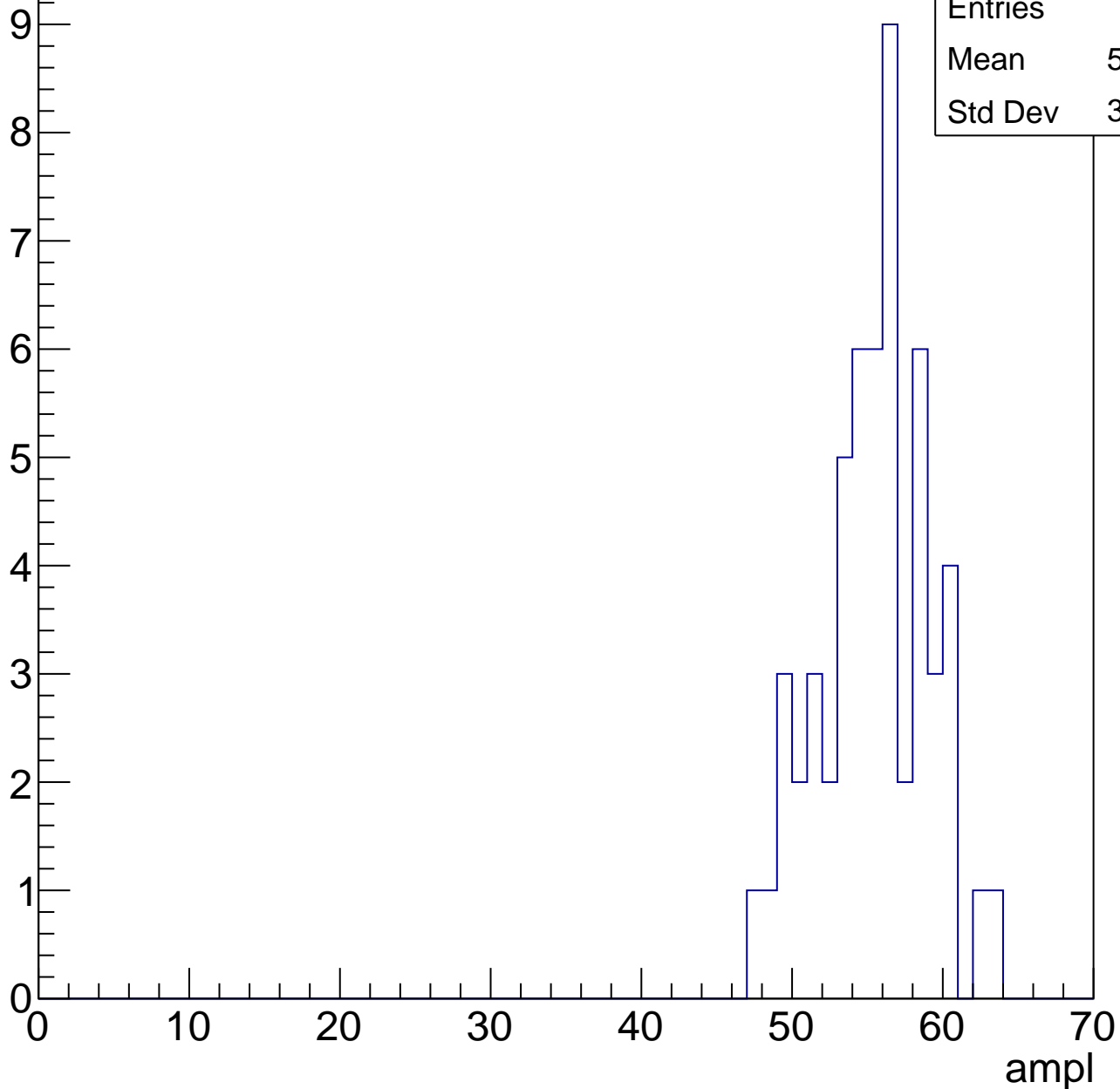
Entry



# B0L002S, U8-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



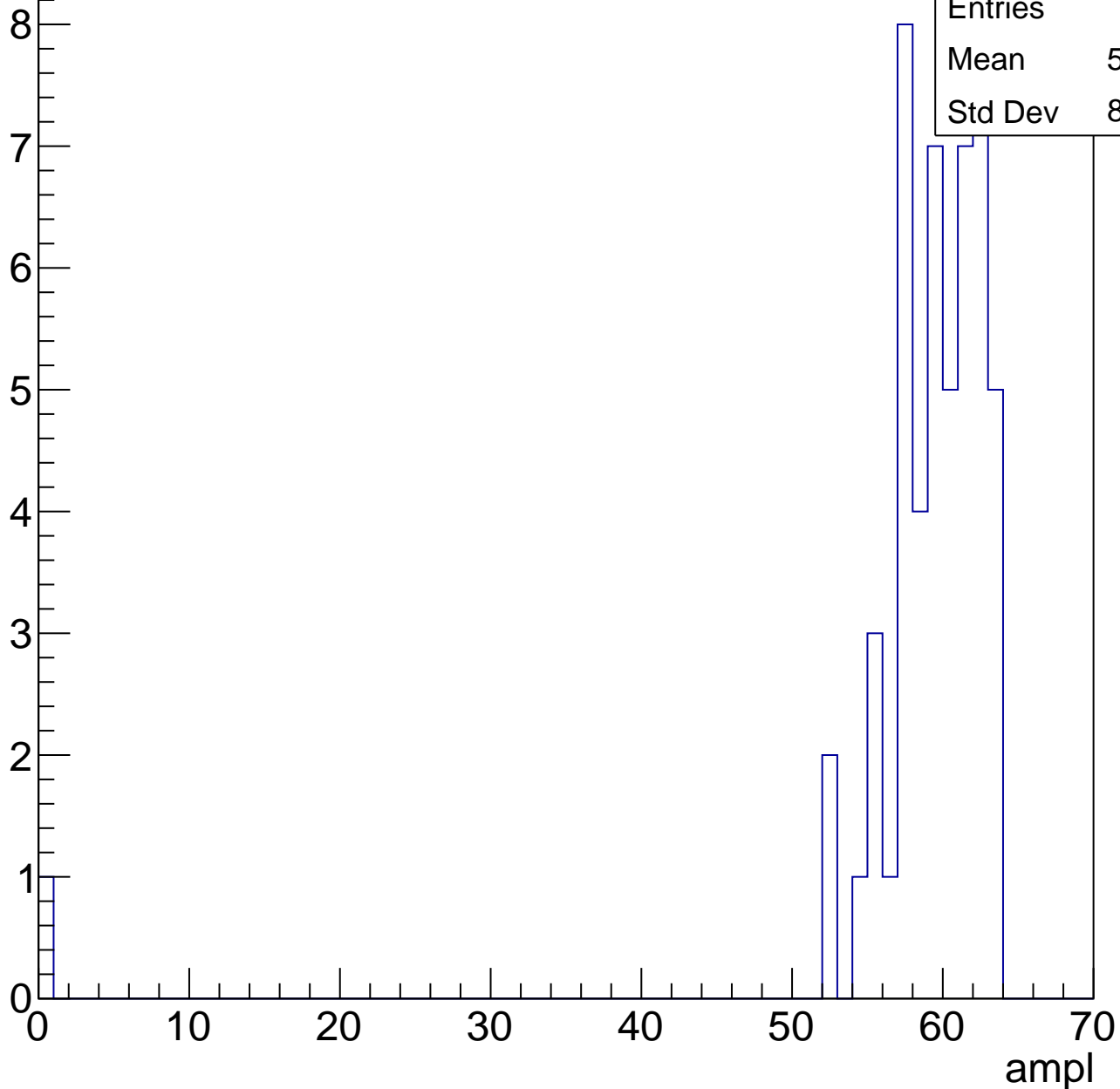
Entries	55
Mean	55.02
Std Dev	3.555

# B0L002S, U8-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

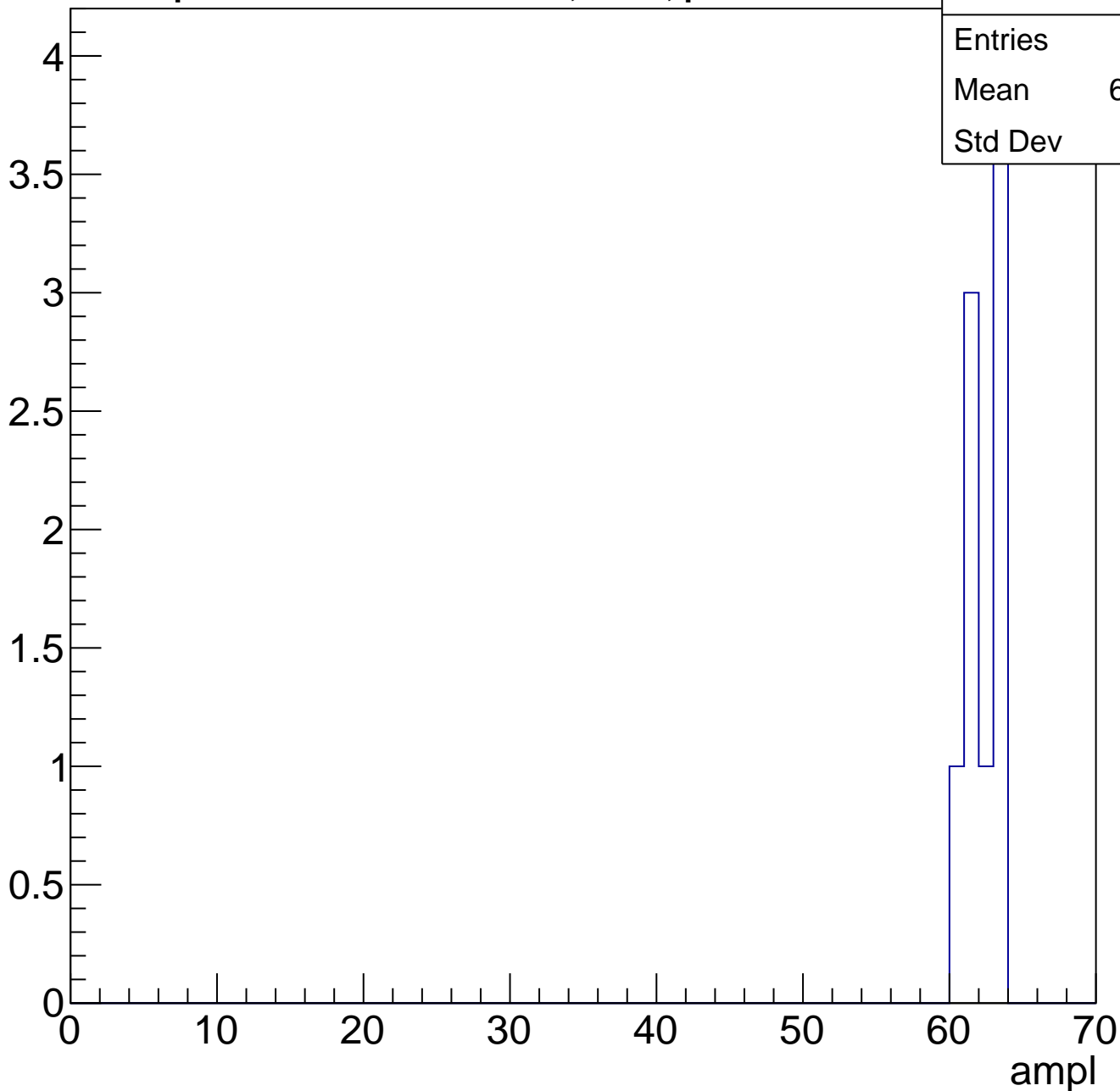
Entries	52
Mean	58.04
Std Dev	8.587



# B0L002S, U8-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch93, adc0

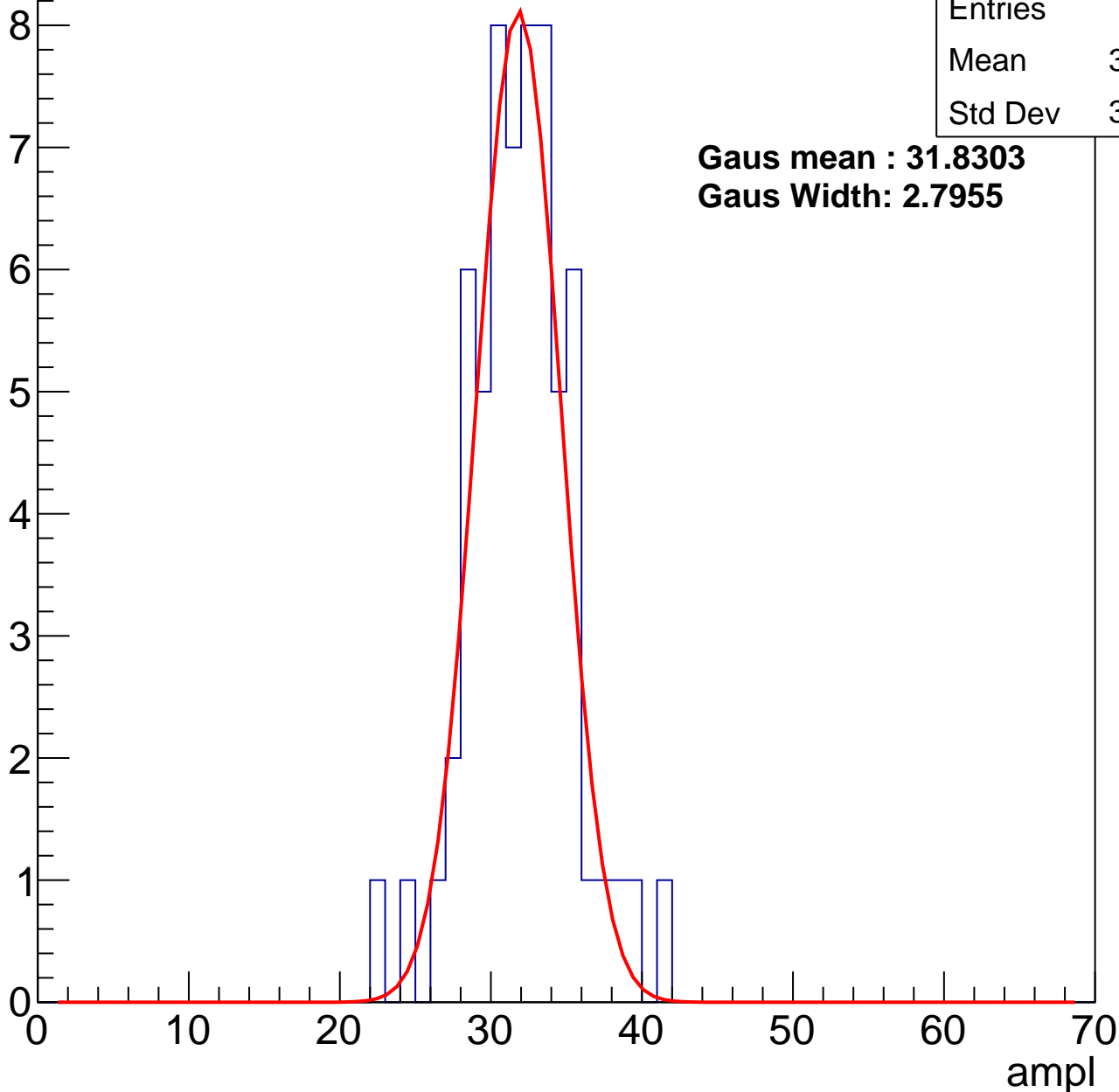
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.54
Std Dev	3.347

**Gaus mean : 31.8303**

**Gaus Width: 2.7955**



# B0L002S, U8-ch93, adc1

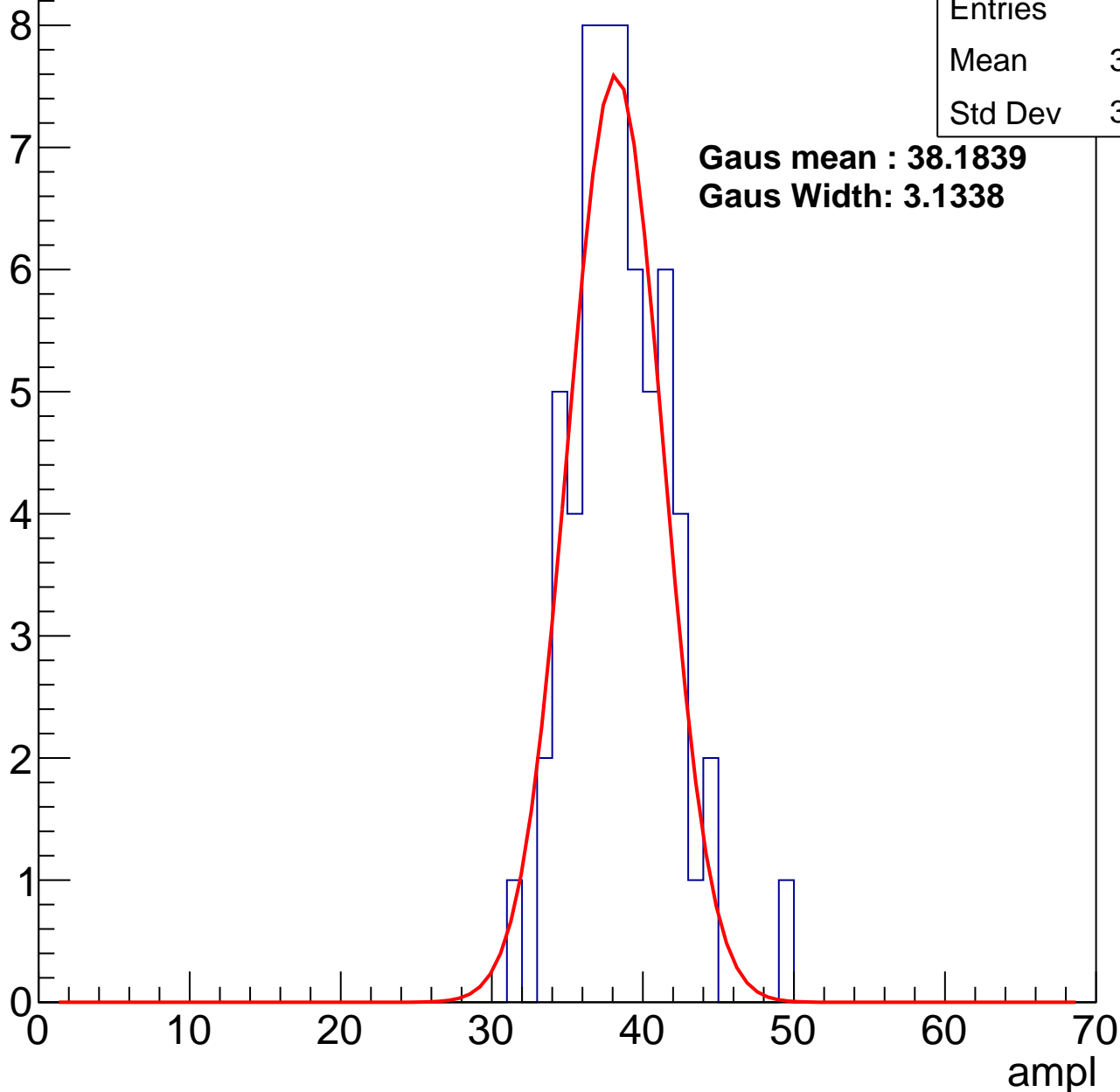
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.08
Std Dev	3.179

**Gaus mean : 38.1839**

**Gaus Width: 3.1338**



# B0L002S, U8-ch93, adc2

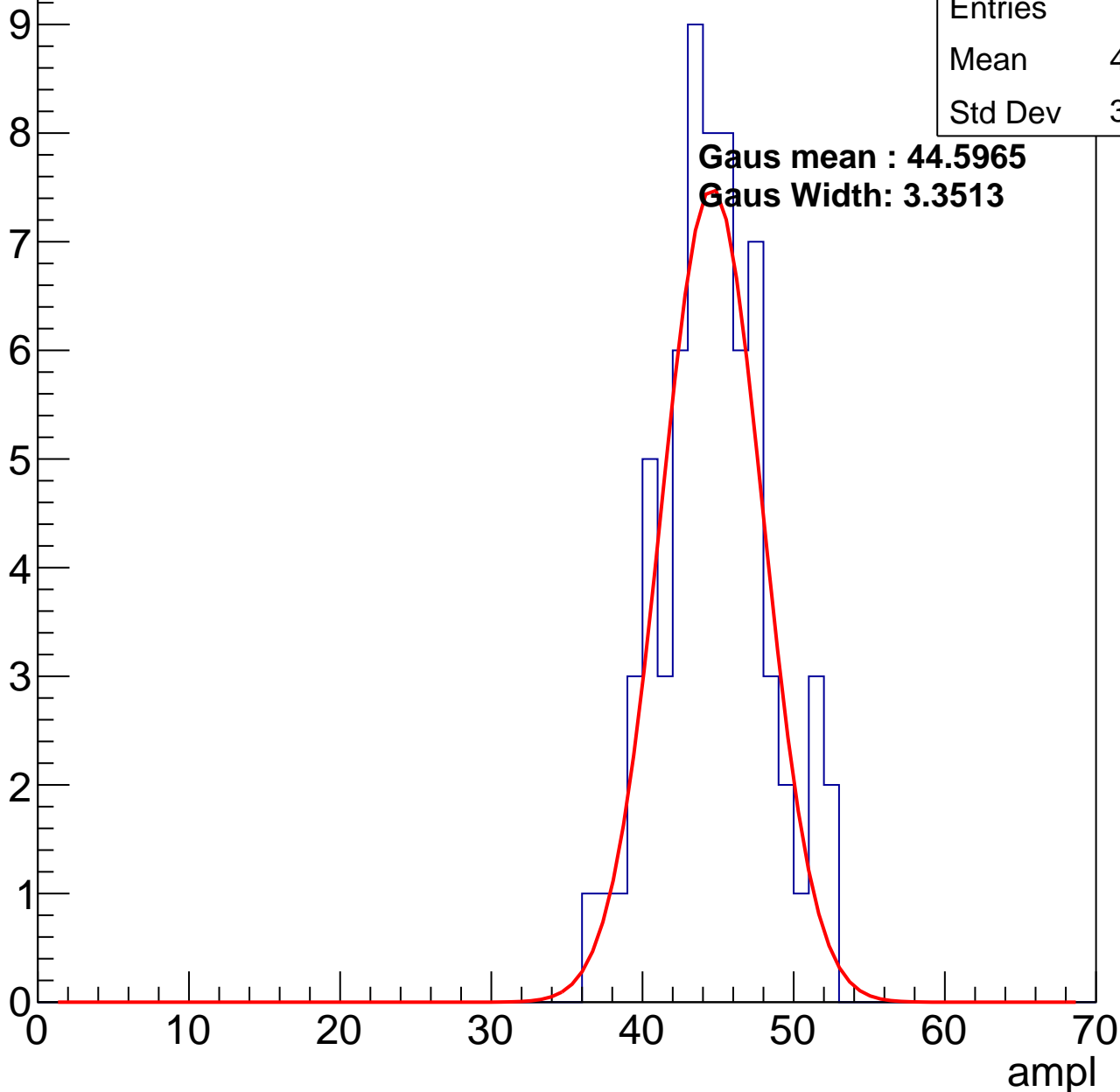
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.29
Std Dev	3.535

**Gaus mean : 44.5965**

**Gaus Width: 3.3513**



# B0L002S, U8-ch93, adc3

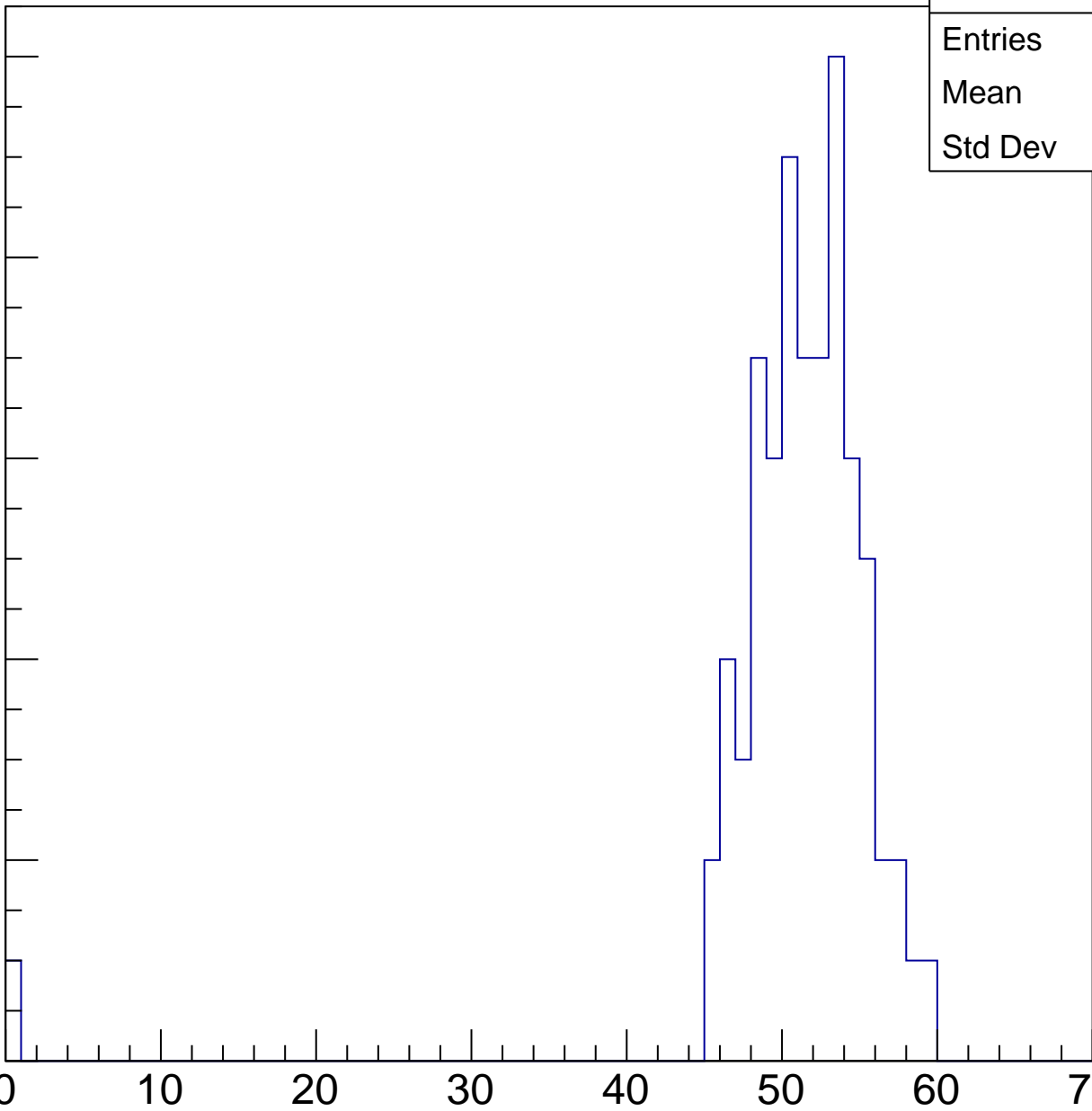
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	50.52
Std Dev	6.74

Entry

10  
8  
6  
4  
2  
0

ampl

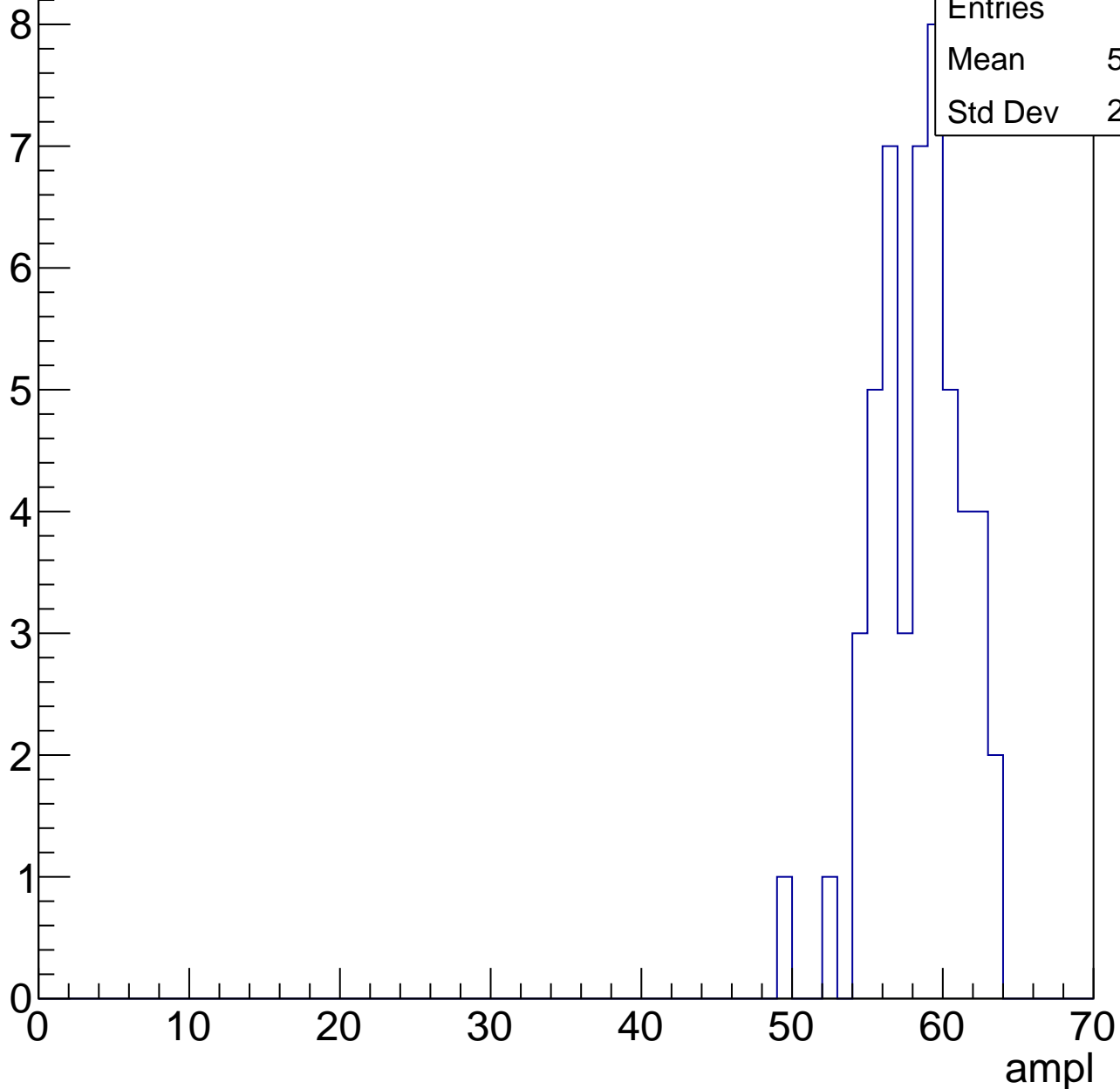


# B0L002S, U8-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	57.94
Std Dev	2.894

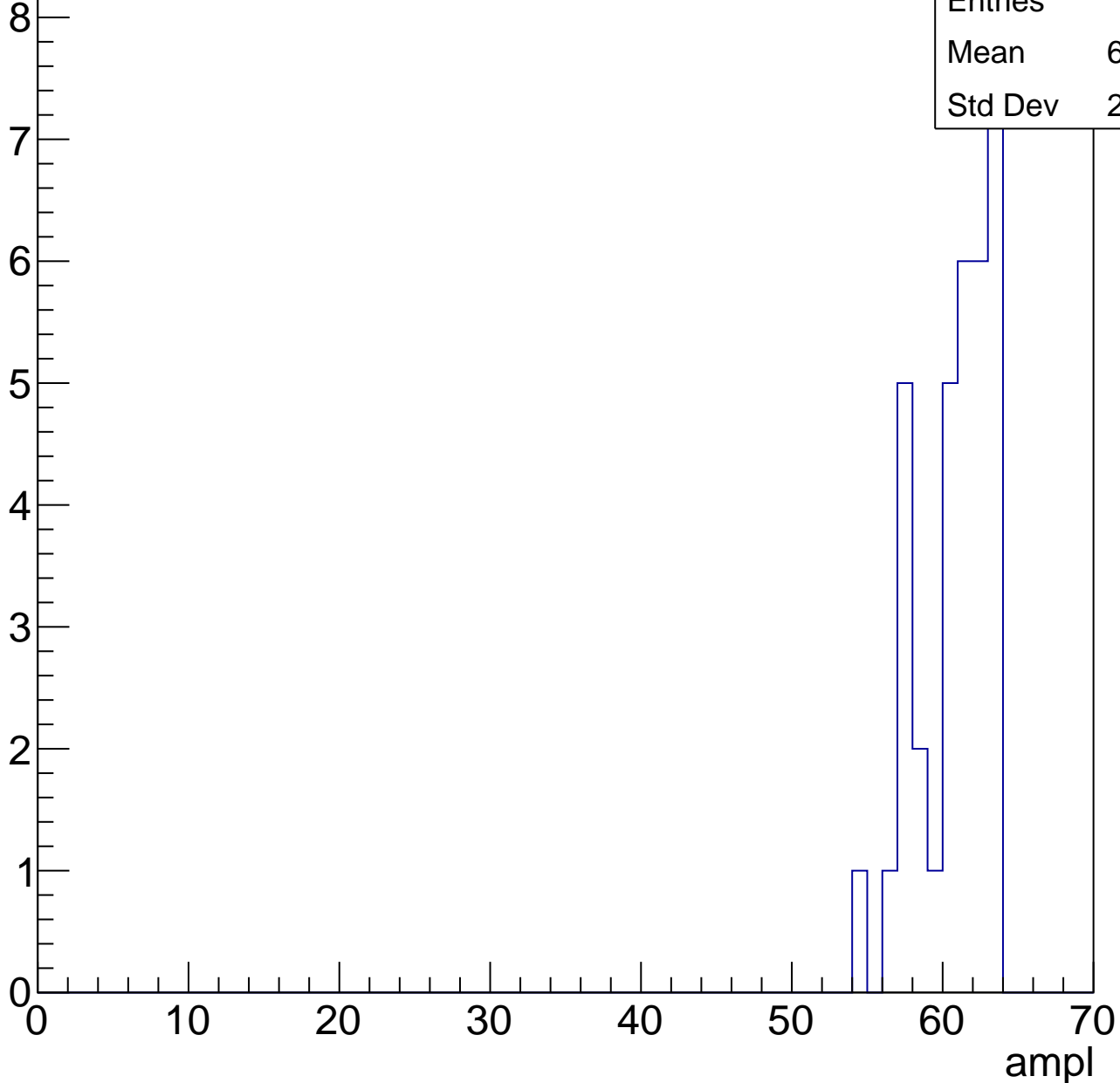


# B0L002S, U8-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.34
Std Dev	2.414



# B0L002S, U8-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch94, adc0

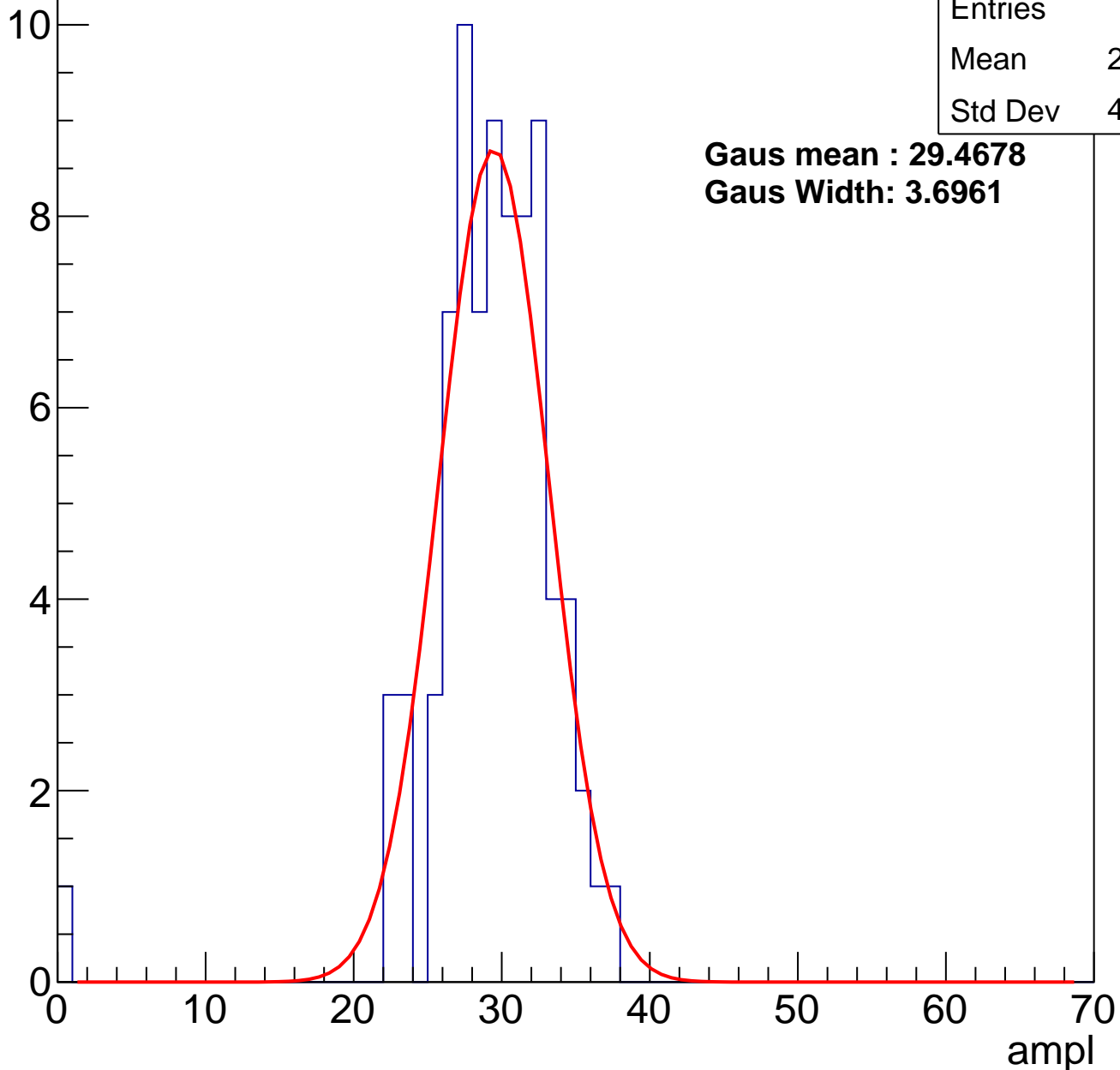
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	28.82
Std Dev	4.639

**Gaus mean : 29.4678**

**Gaus Width: 3.6961**

Entry



# B0L002S, U8-ch94, adc1

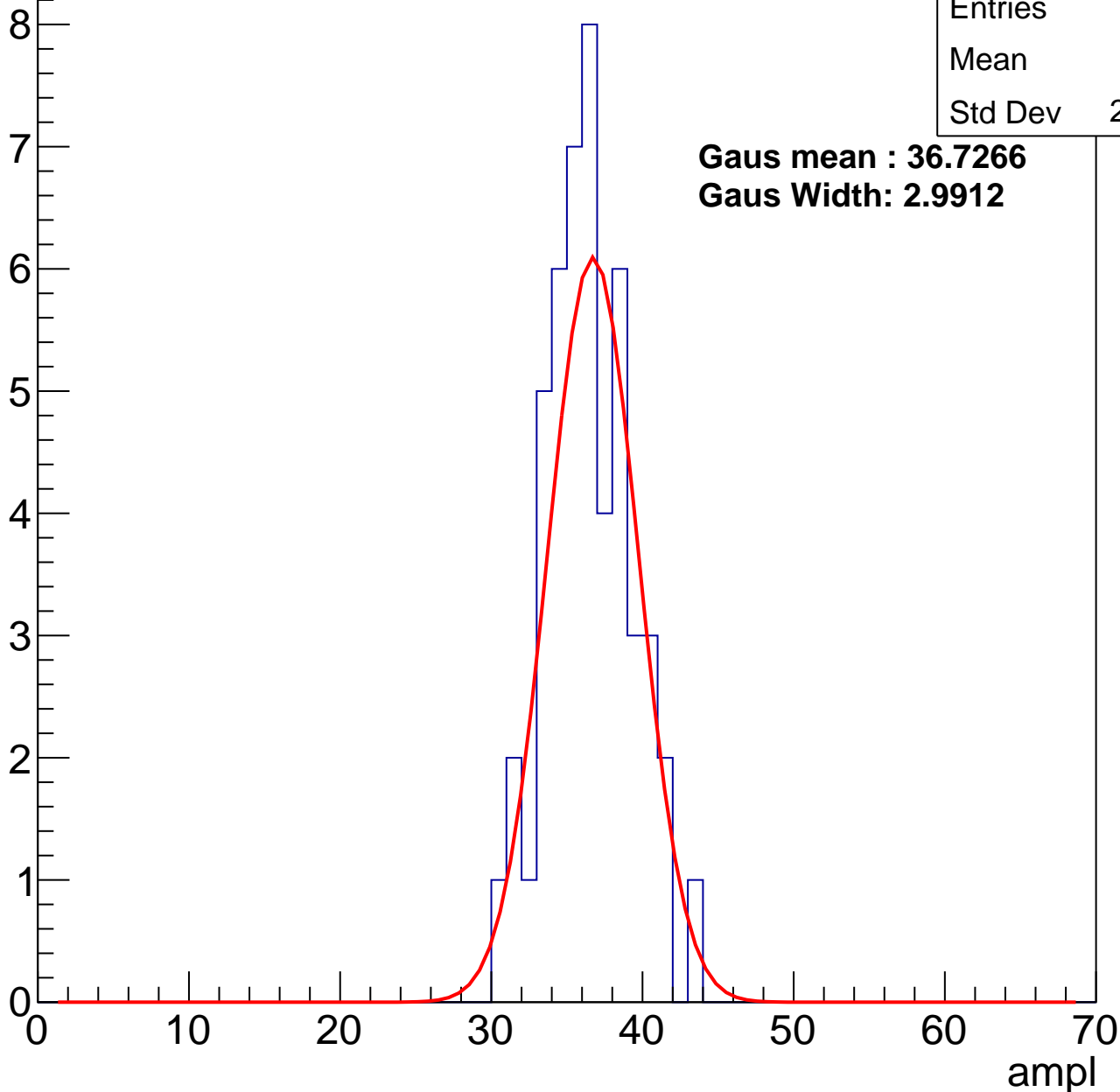
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	36
Std Dev	2.785

**Gaus mean : 36.7266**

**Gaus Width: 2.9912**



# B0L002S, U8-ch94, adc2

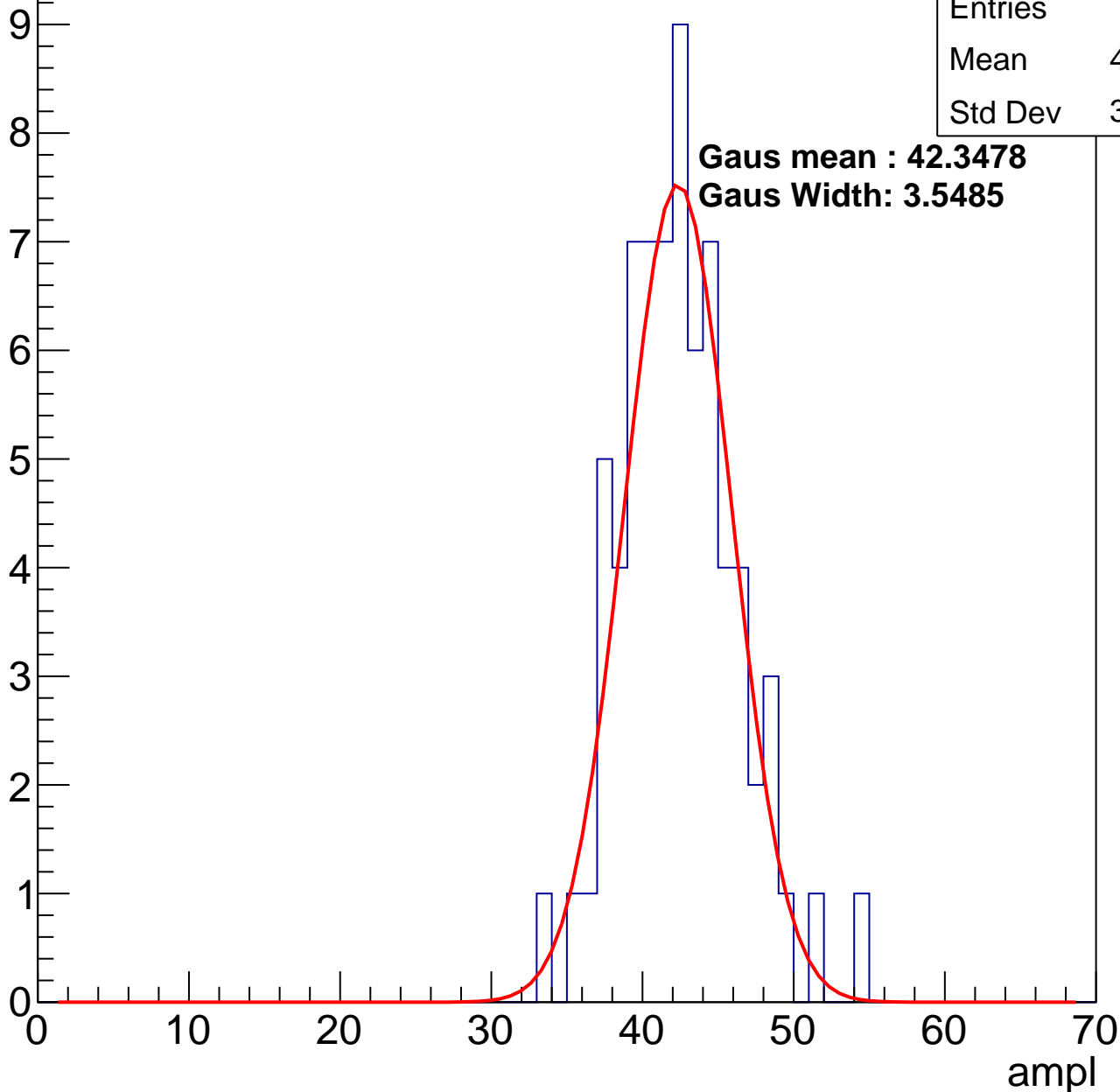
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	41.99
Std Dev	3.788

**Gaus mean : 42.3478**

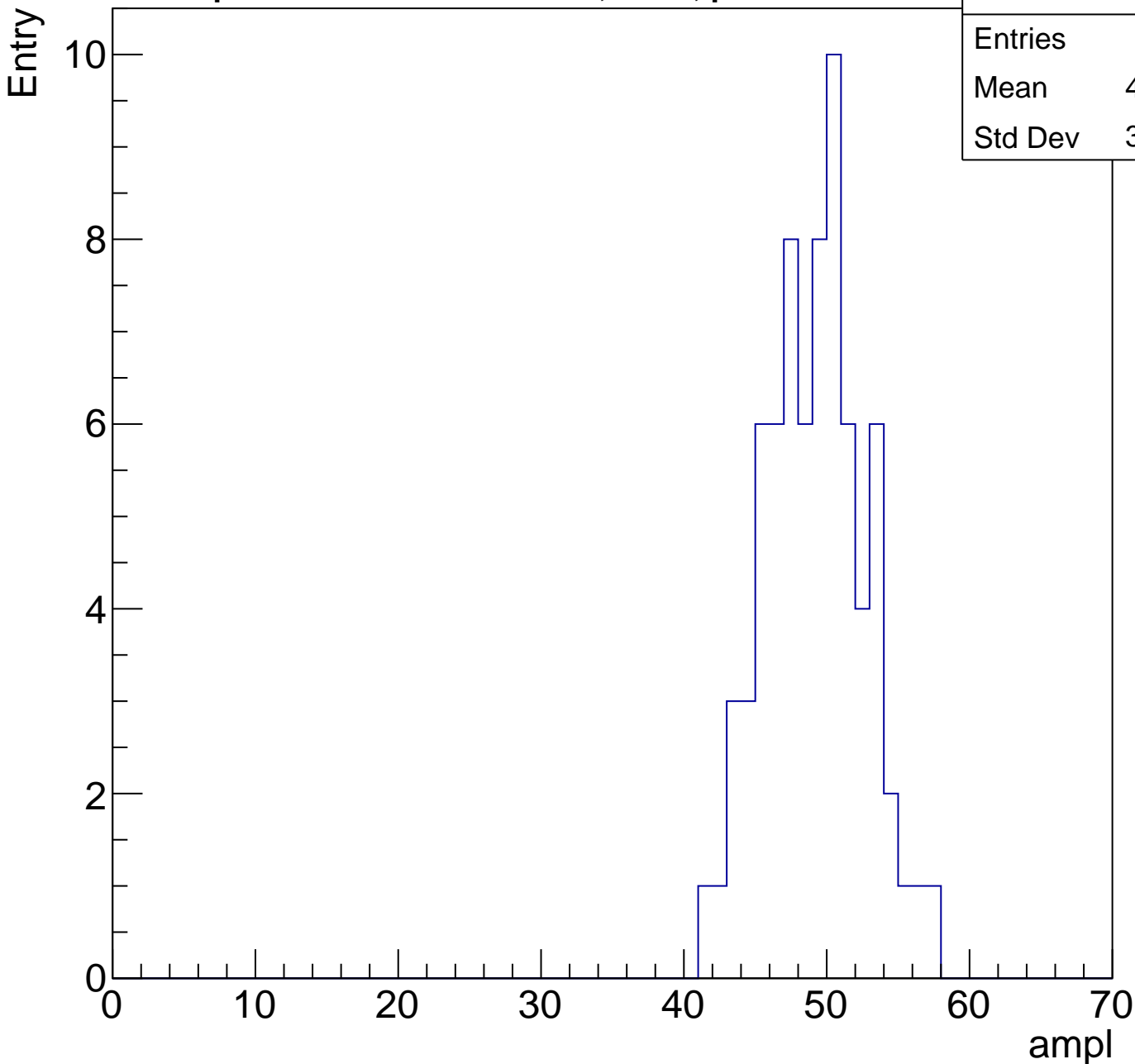
**Gaus Width: 3.5485**



# B0L002S, U8-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	48.68
Std Dev	3.396

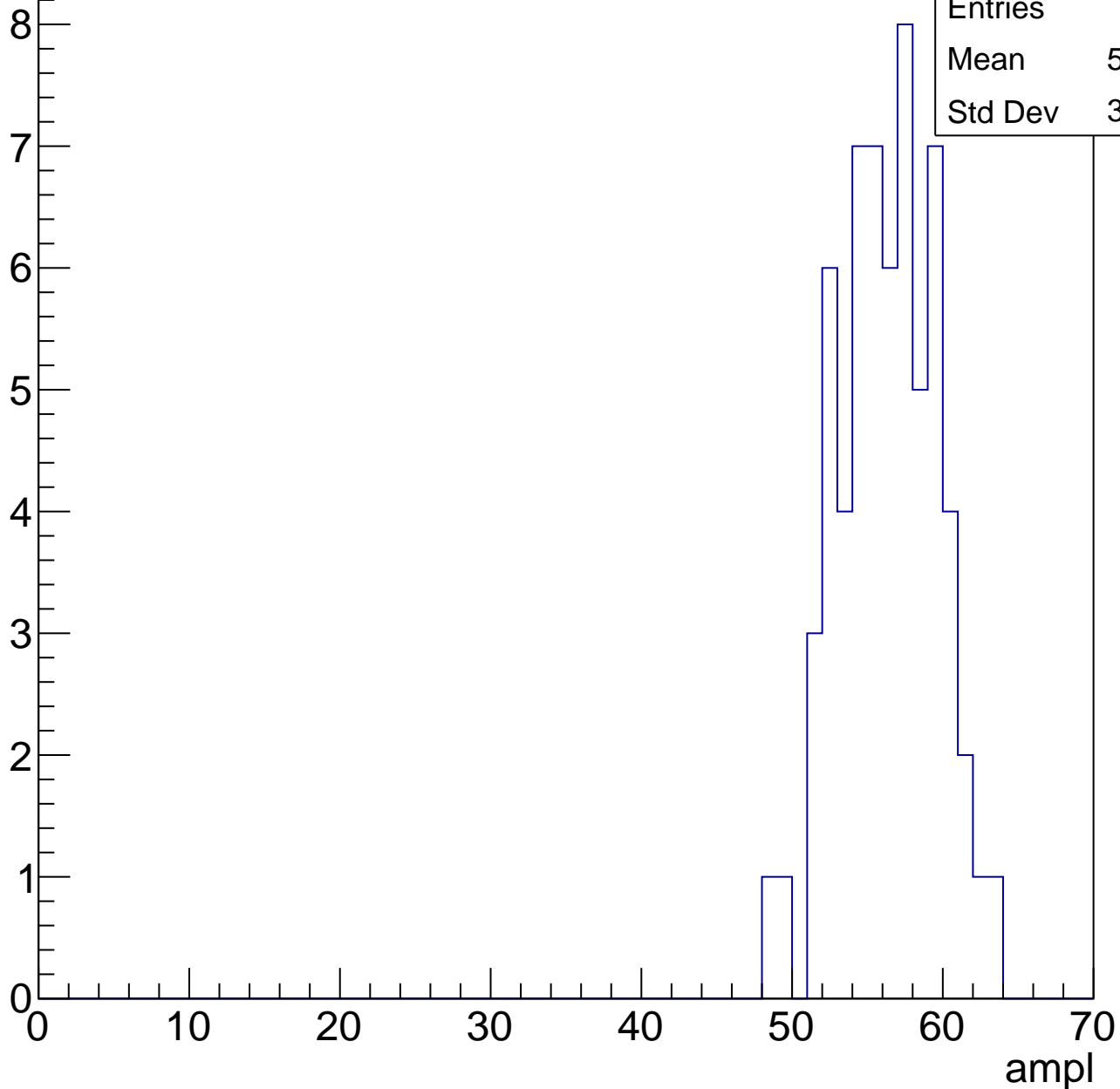


# B0L002S, U8-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	55.86
Std Dev	3.197

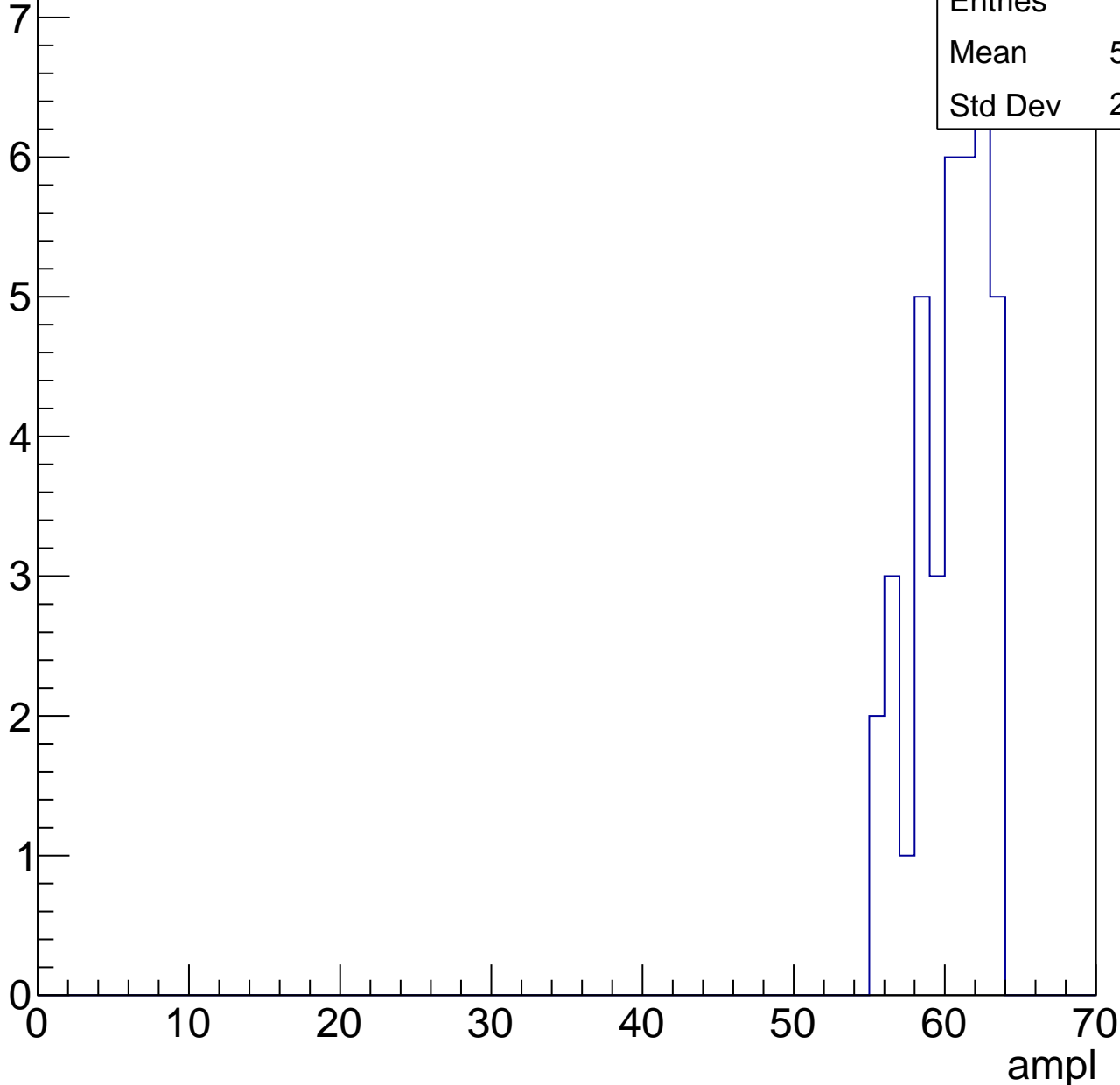


# B0L002S, U8-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

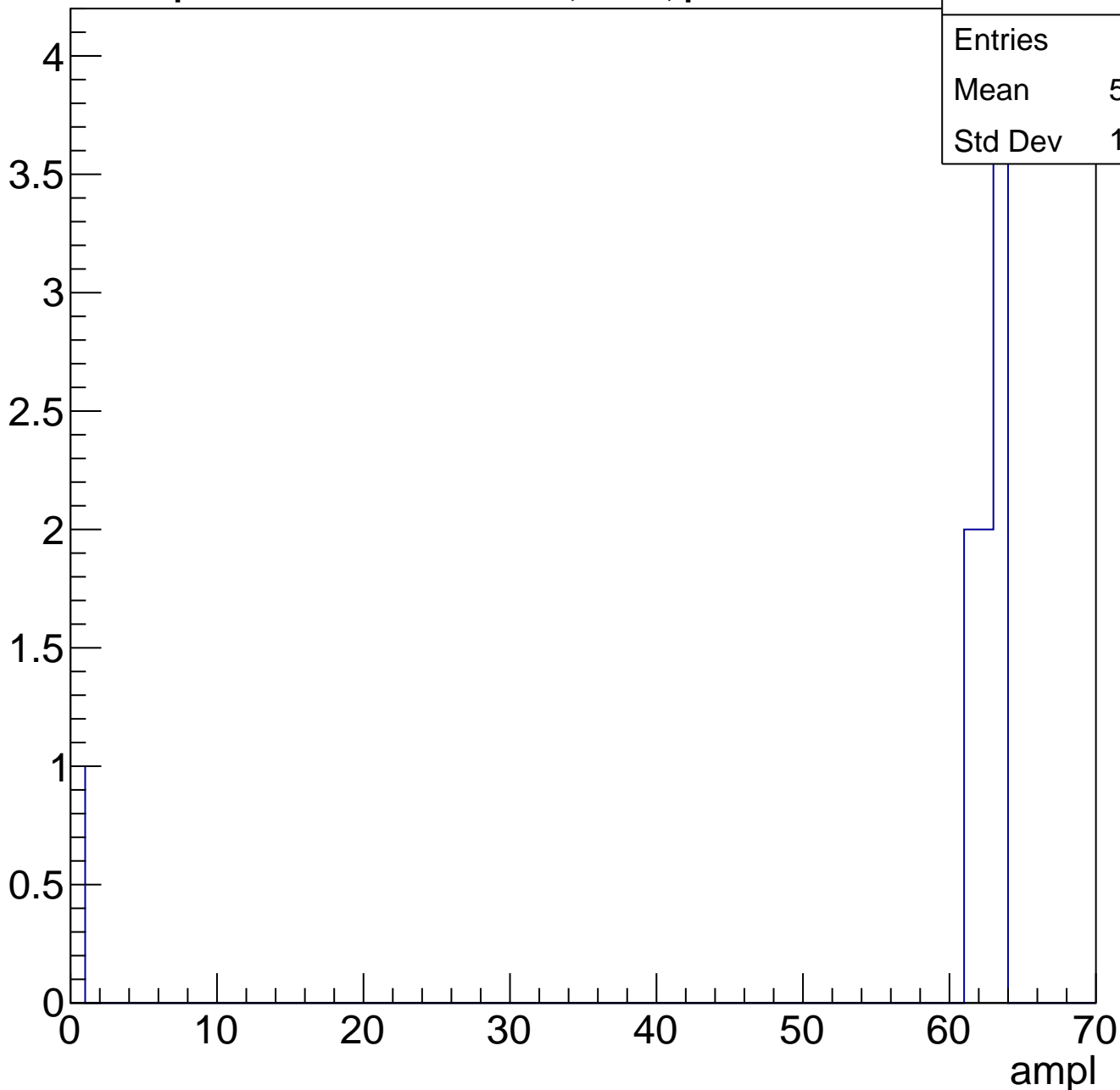
Entries	38
Mean	59.92
Std Dev	2.344



# B0L002S, U8-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch95, adc0

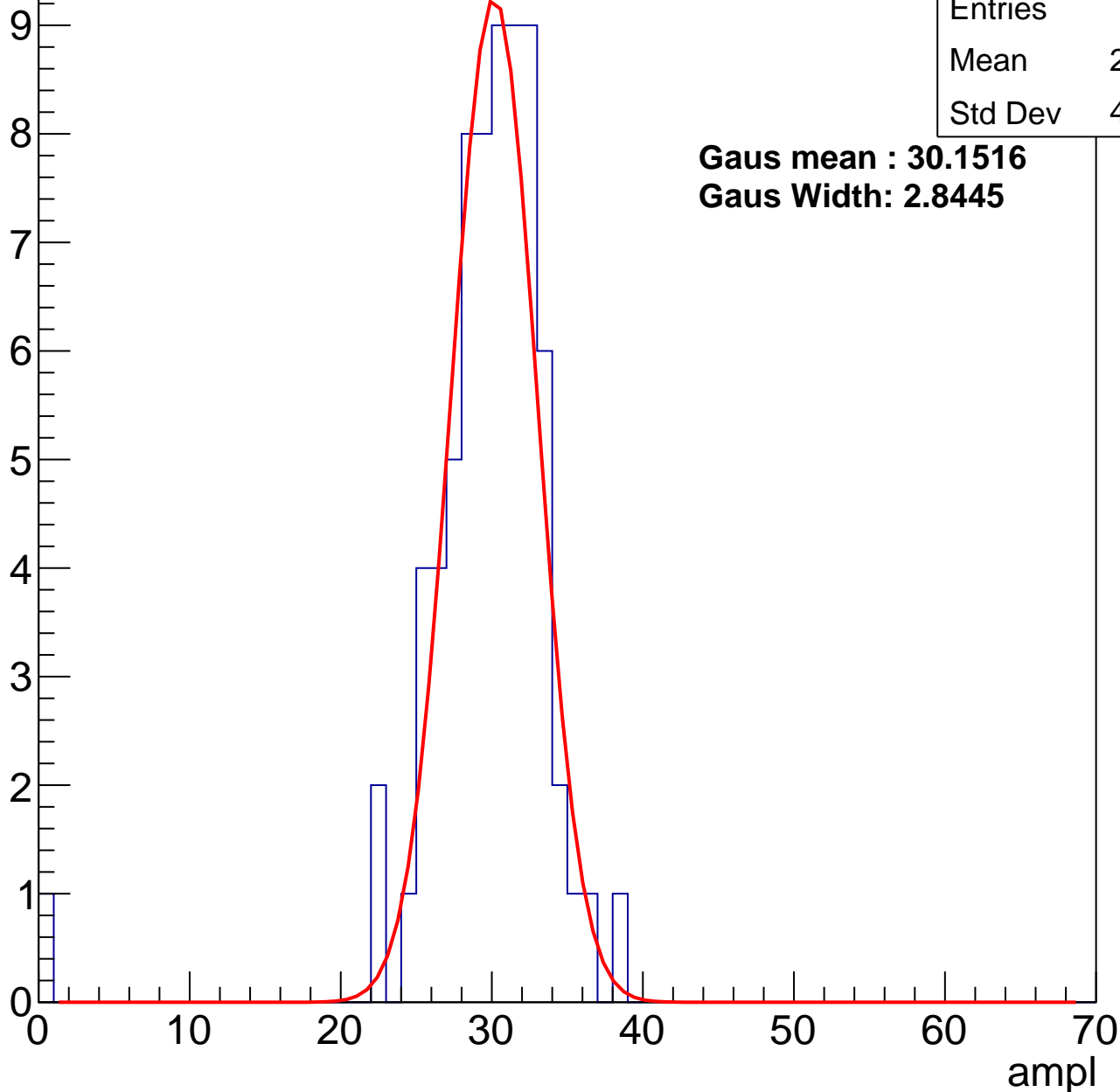
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.23
Std Dev	4.637

**Gaus mean : 30.1516**

**Gaus Width: 2.8445**



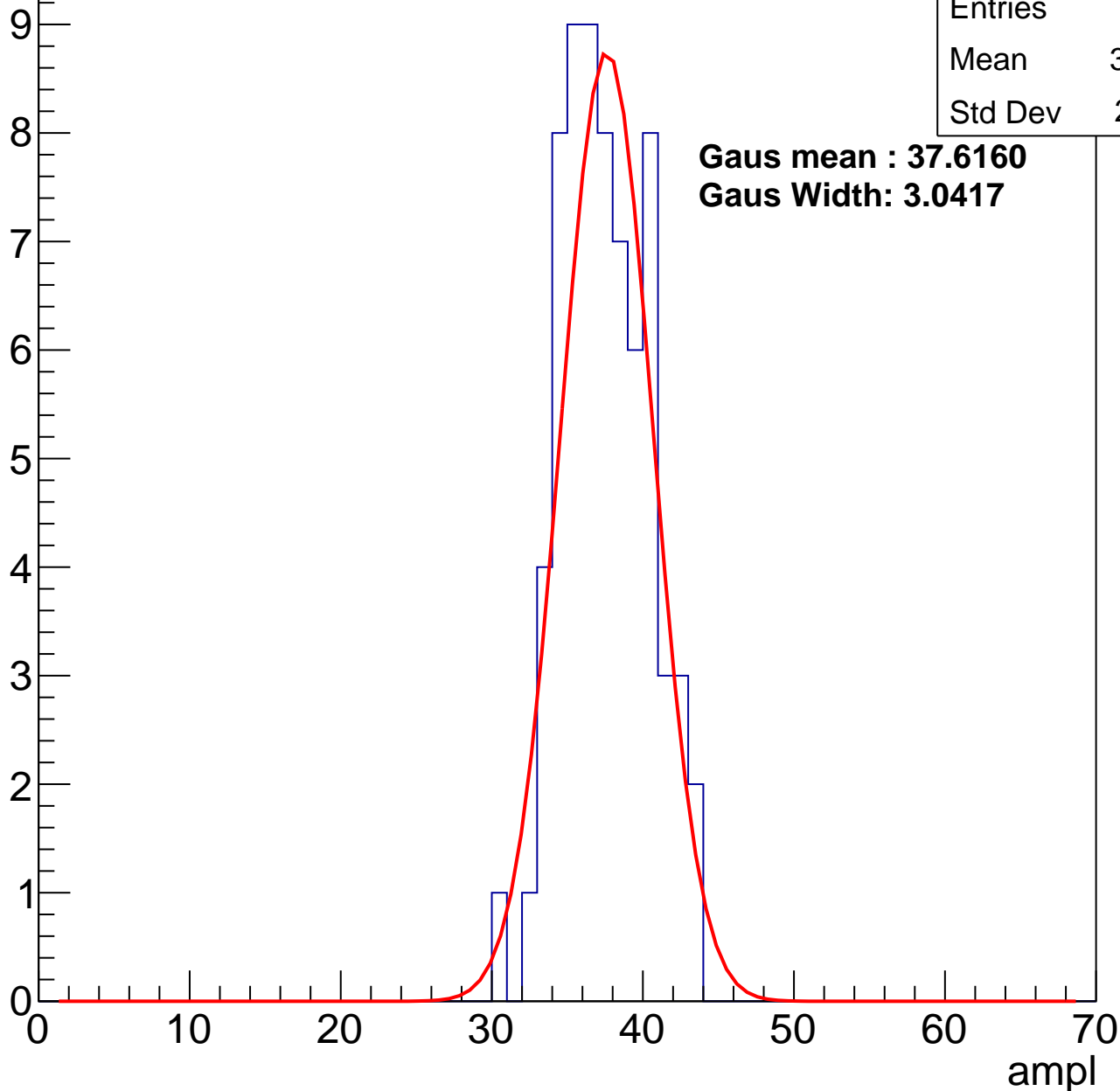
# B0L002S, U8-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

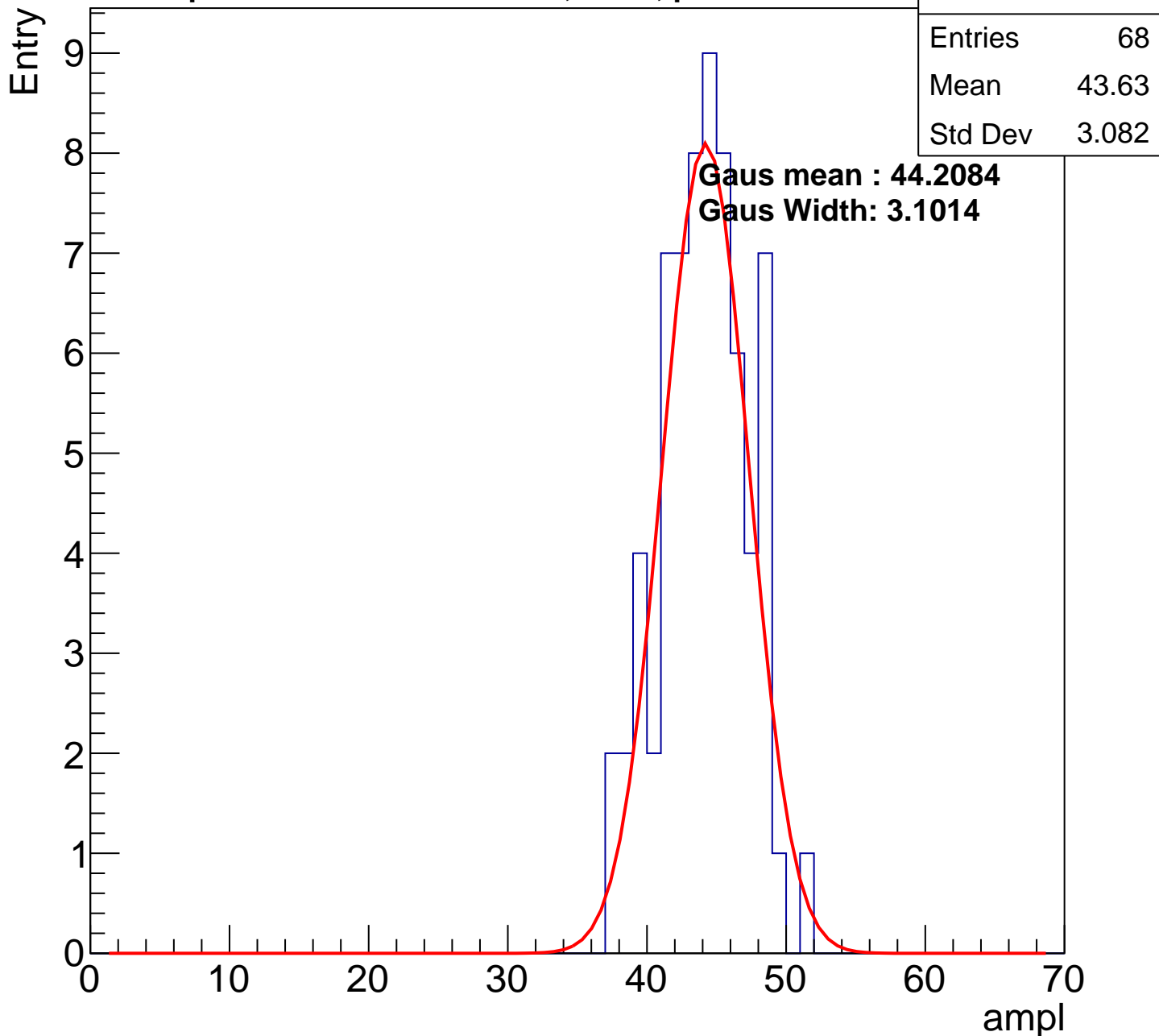
Entries	69
Mean	37.04
Std Dev	2.831

**Gaus mean : 37.6160**  
**Gaus Width: 3.0417**



# B0L002S, U8-ch95, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

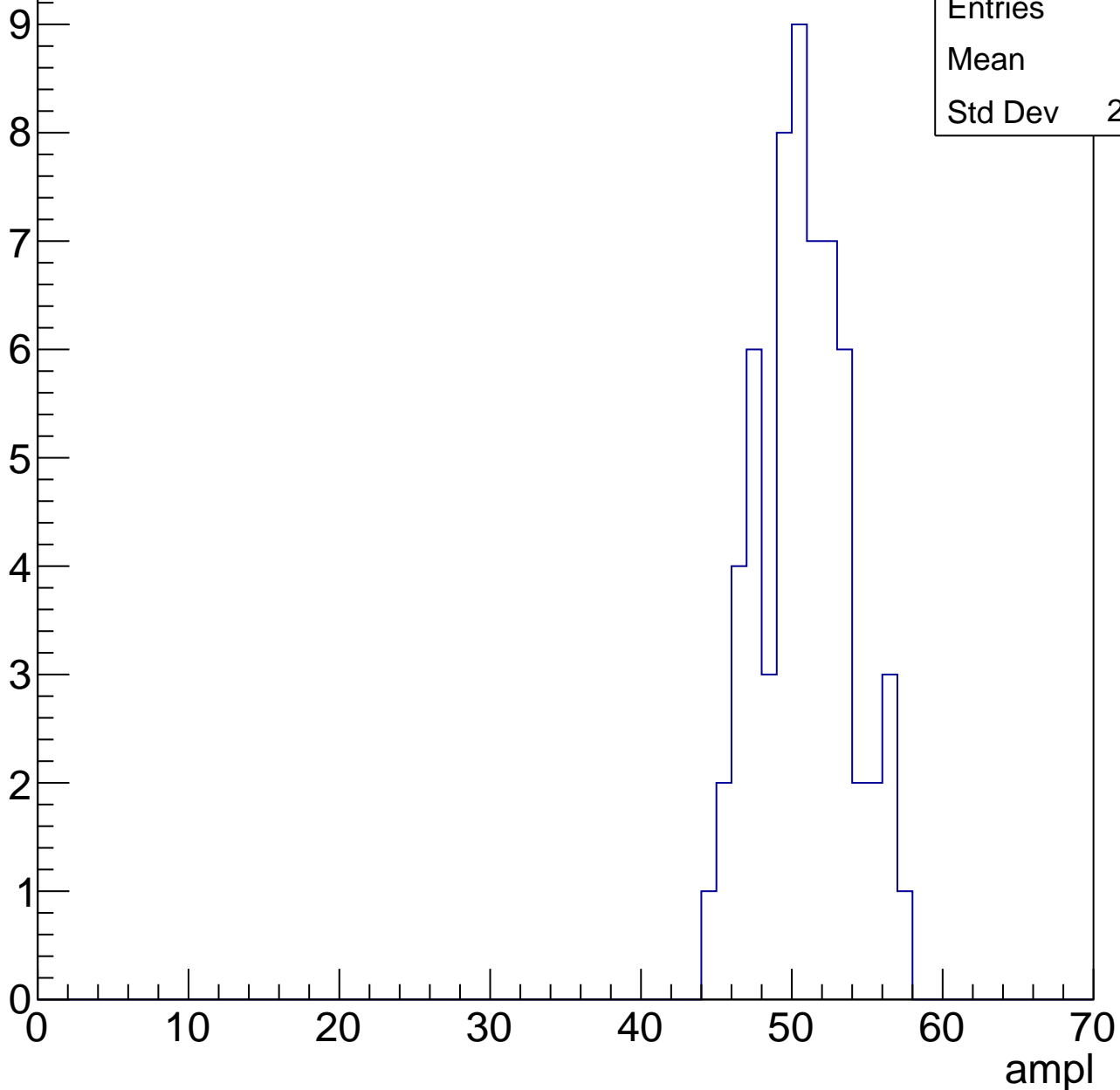


# B0L002S, U8-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.3
Std Dev	2.994

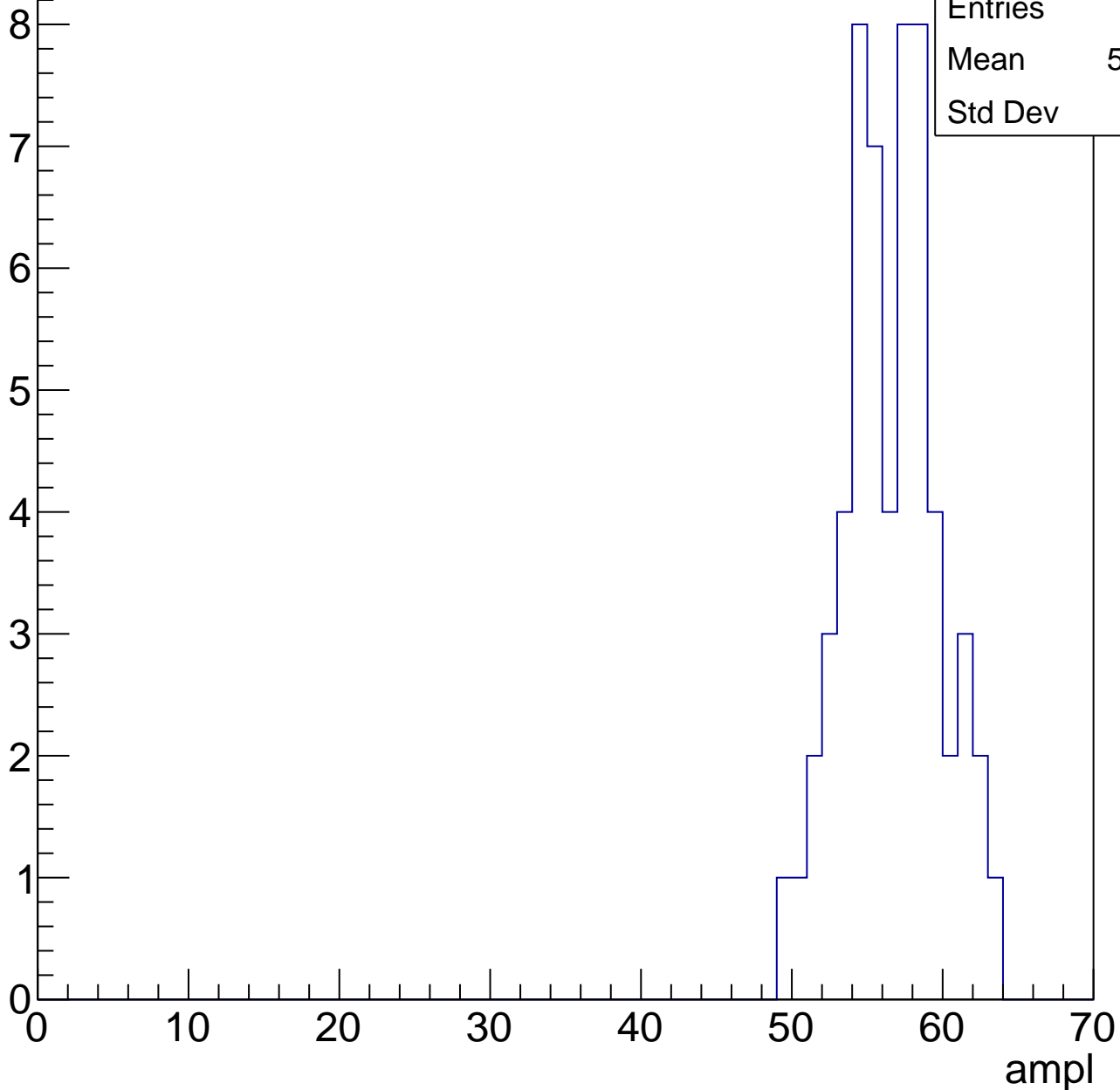


# B0L002S, U8-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

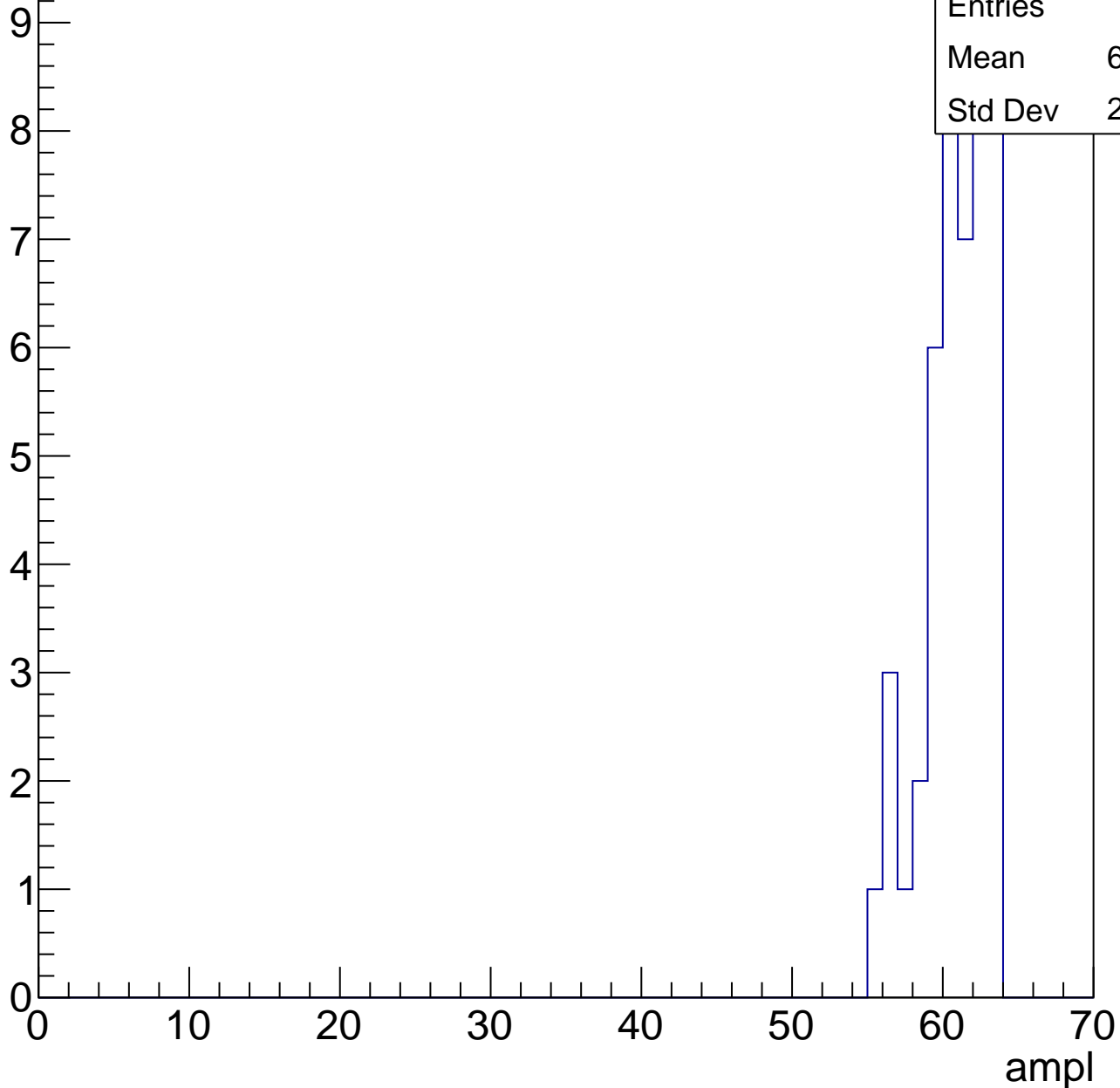
Entries	58
Mean	56.14
Std Dev	3.11



# B0L002S, U8-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0



# B0L002S, U8-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch96, adc0

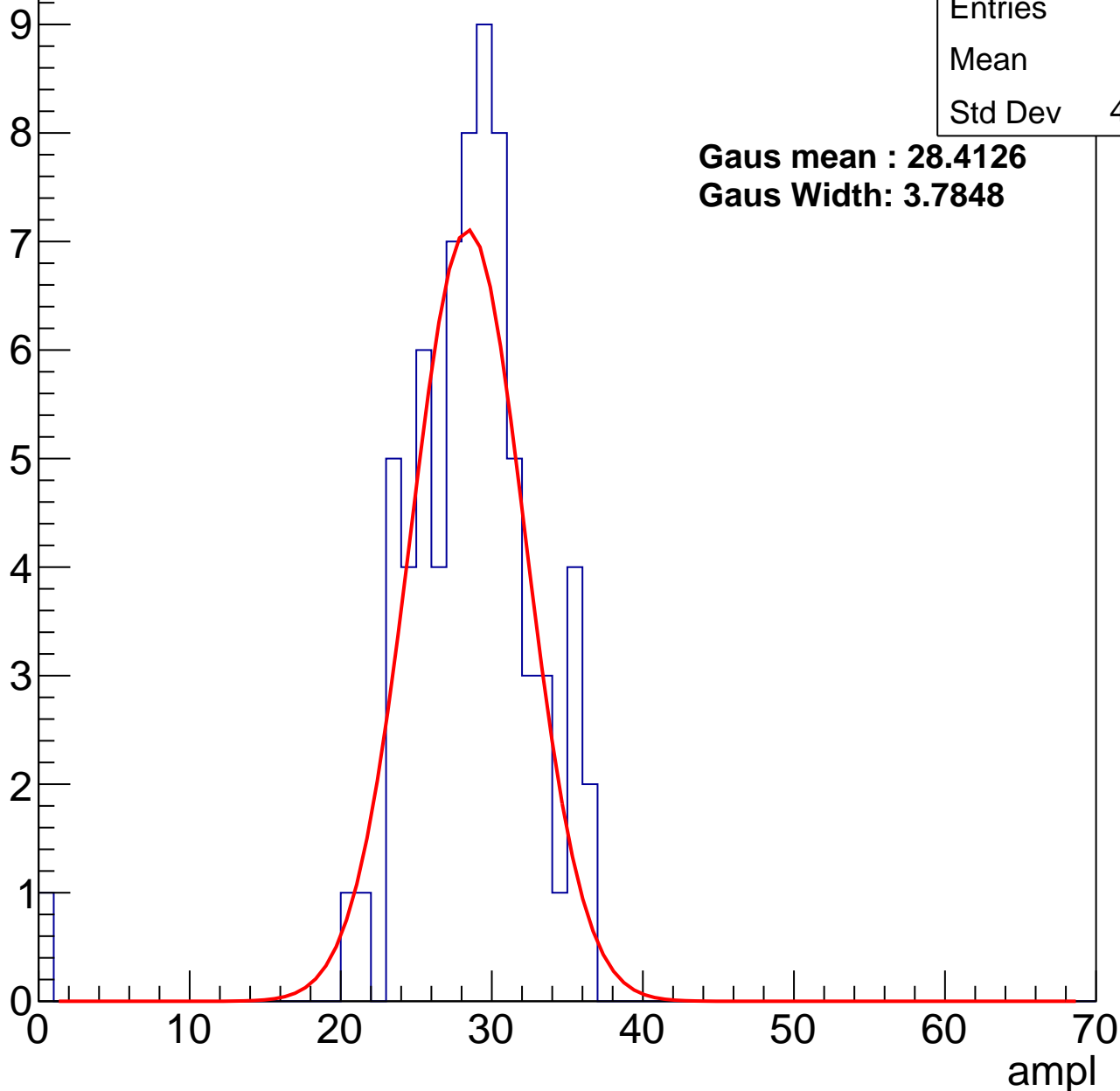
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	28
Std Dev	4.905

**Gaus mean : 28.4126**

**Gaus Width: 3.7848**



# B0L002S, U8-ch96, adc1

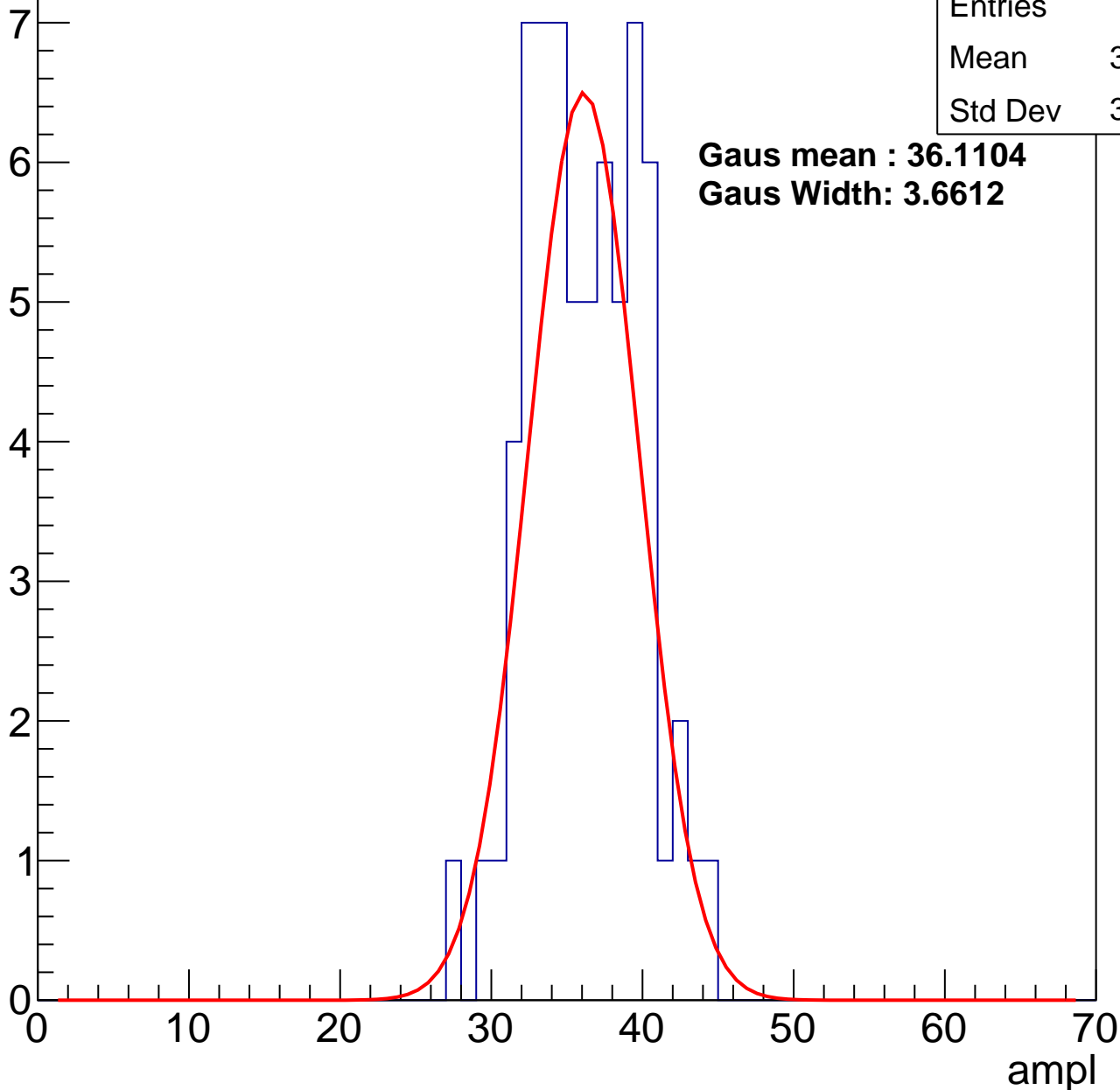
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.75
Std Dev	3.592

**Gaus mean : 36.1104**

**Gaus Width: 3.6612**



# B0L002S, U8-ch96, adc2

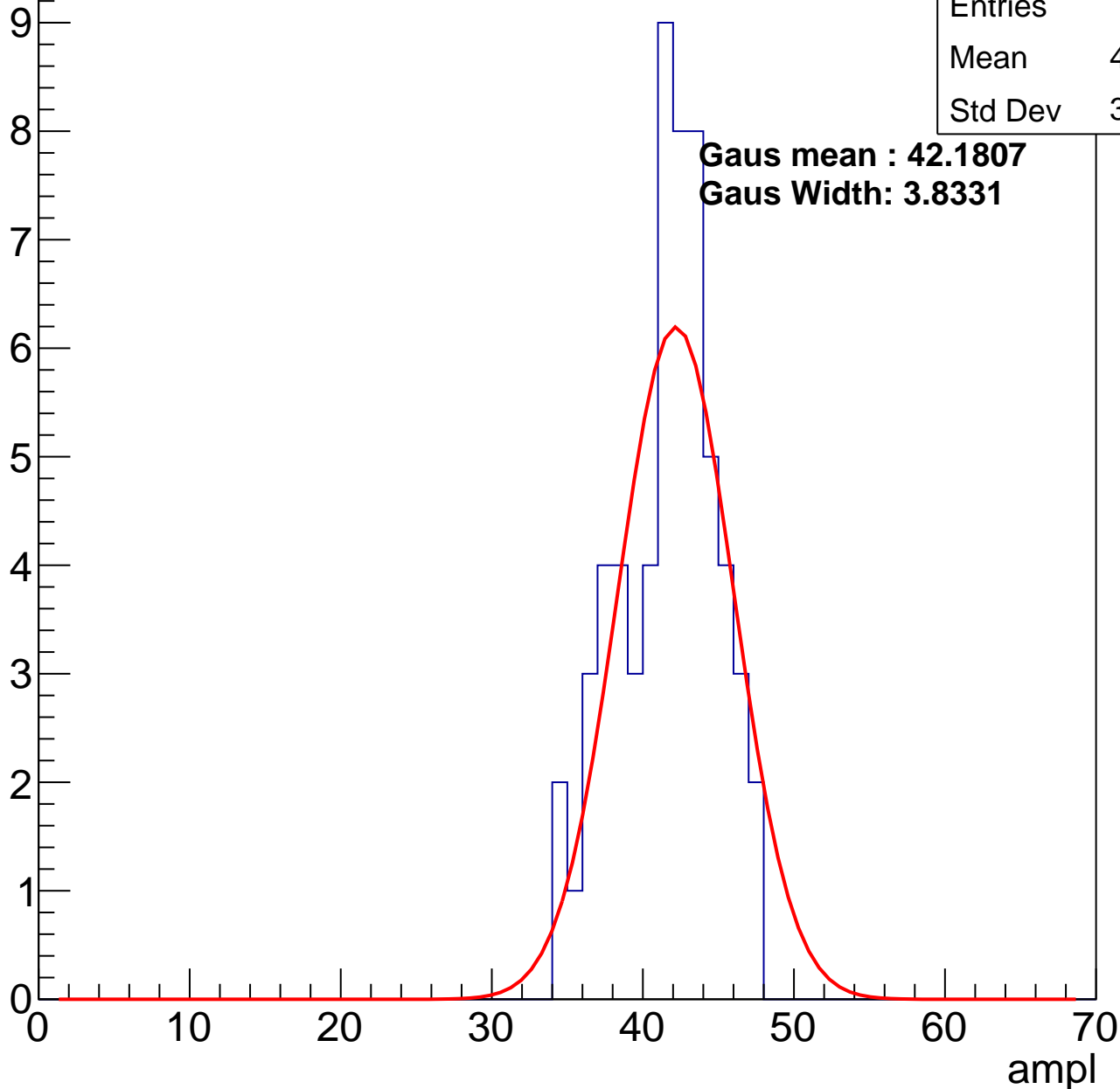
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	41.15
Std Dev	3.214

**Gaus mean : 42.1807**

**Gaus Width: 3.8331**

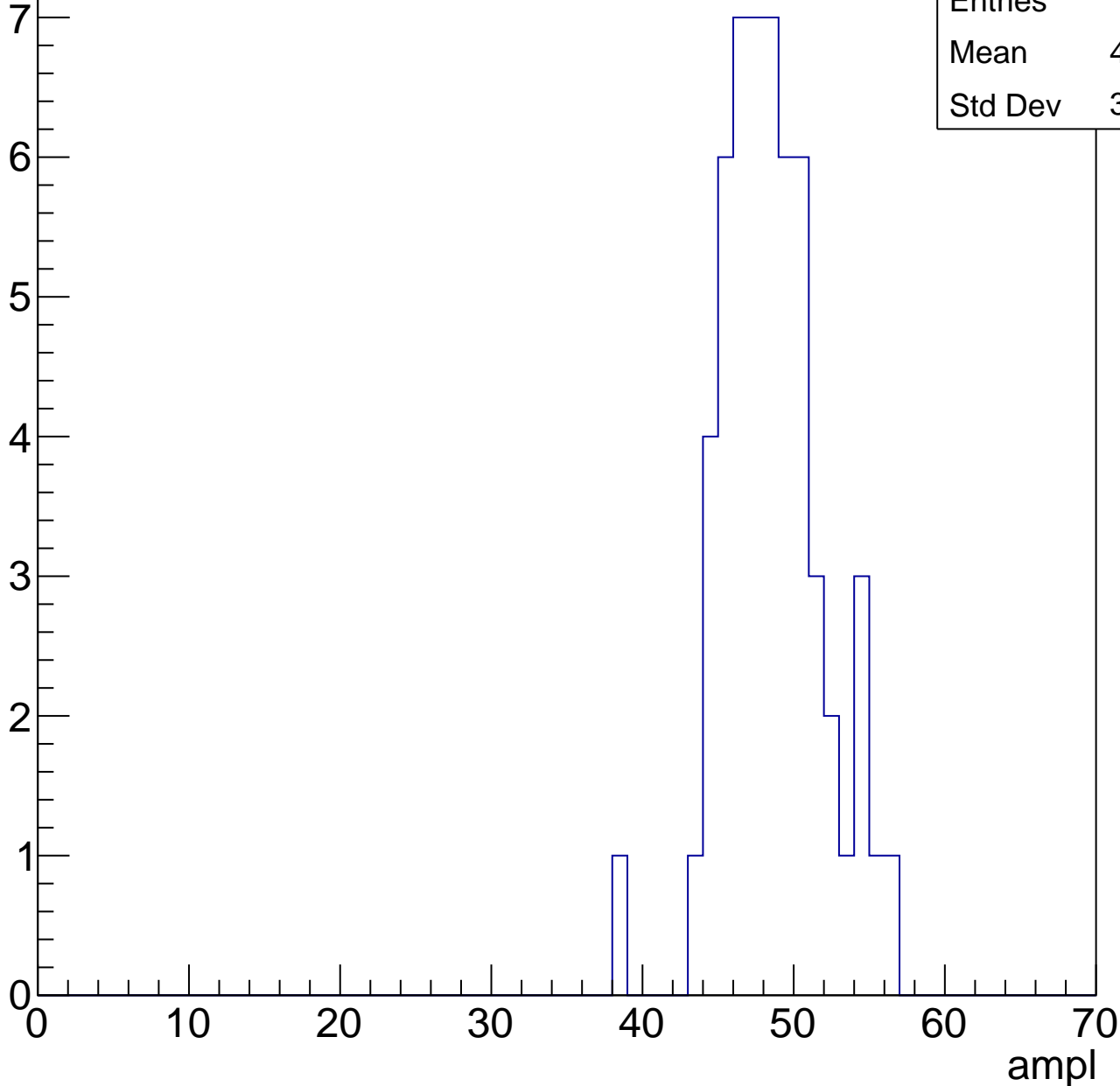


# B0L002S, U8-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	48.05
Std Dev	3.308

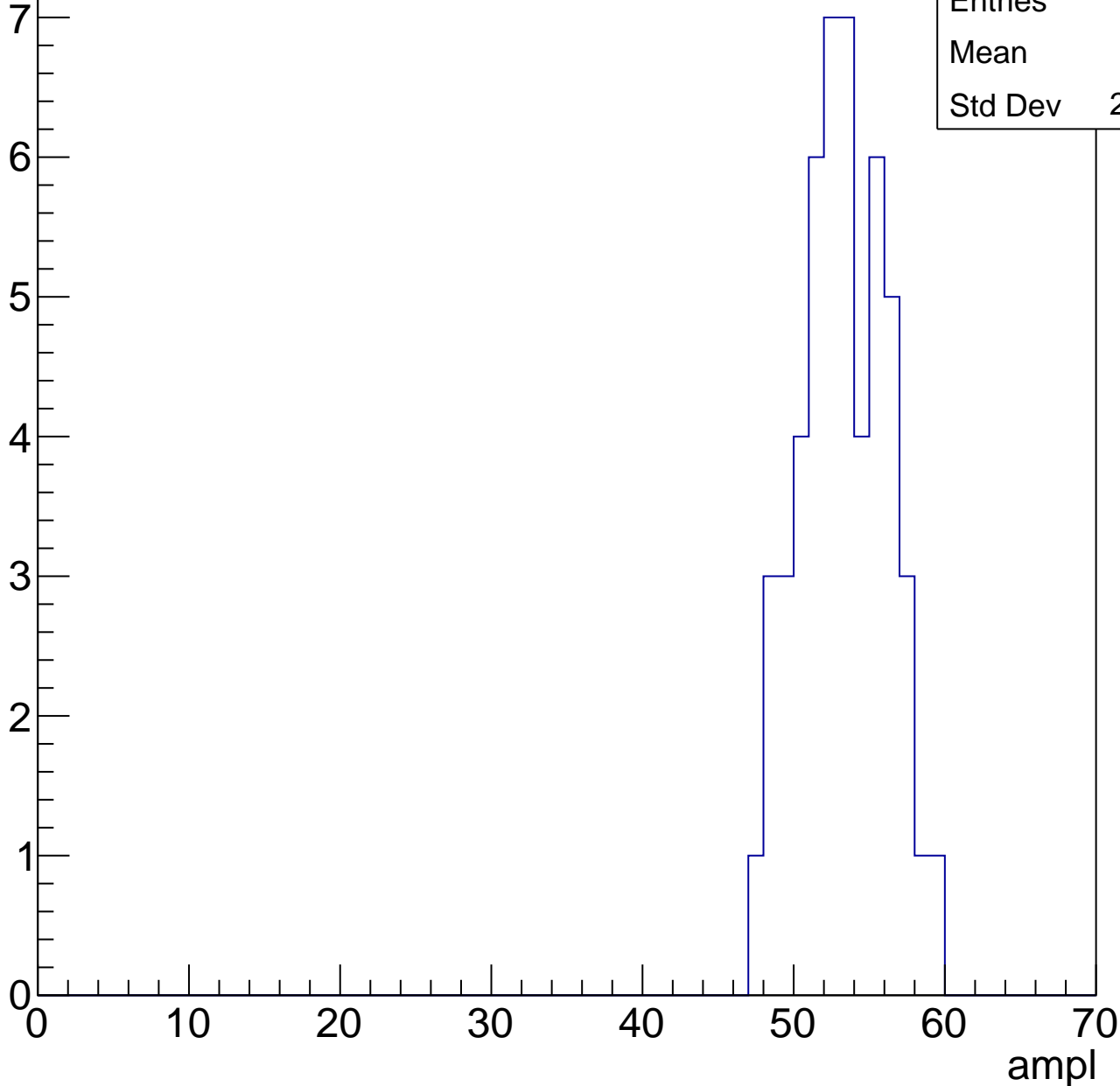


# B0L002S, U8-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	52.8
Std Dev	2.822

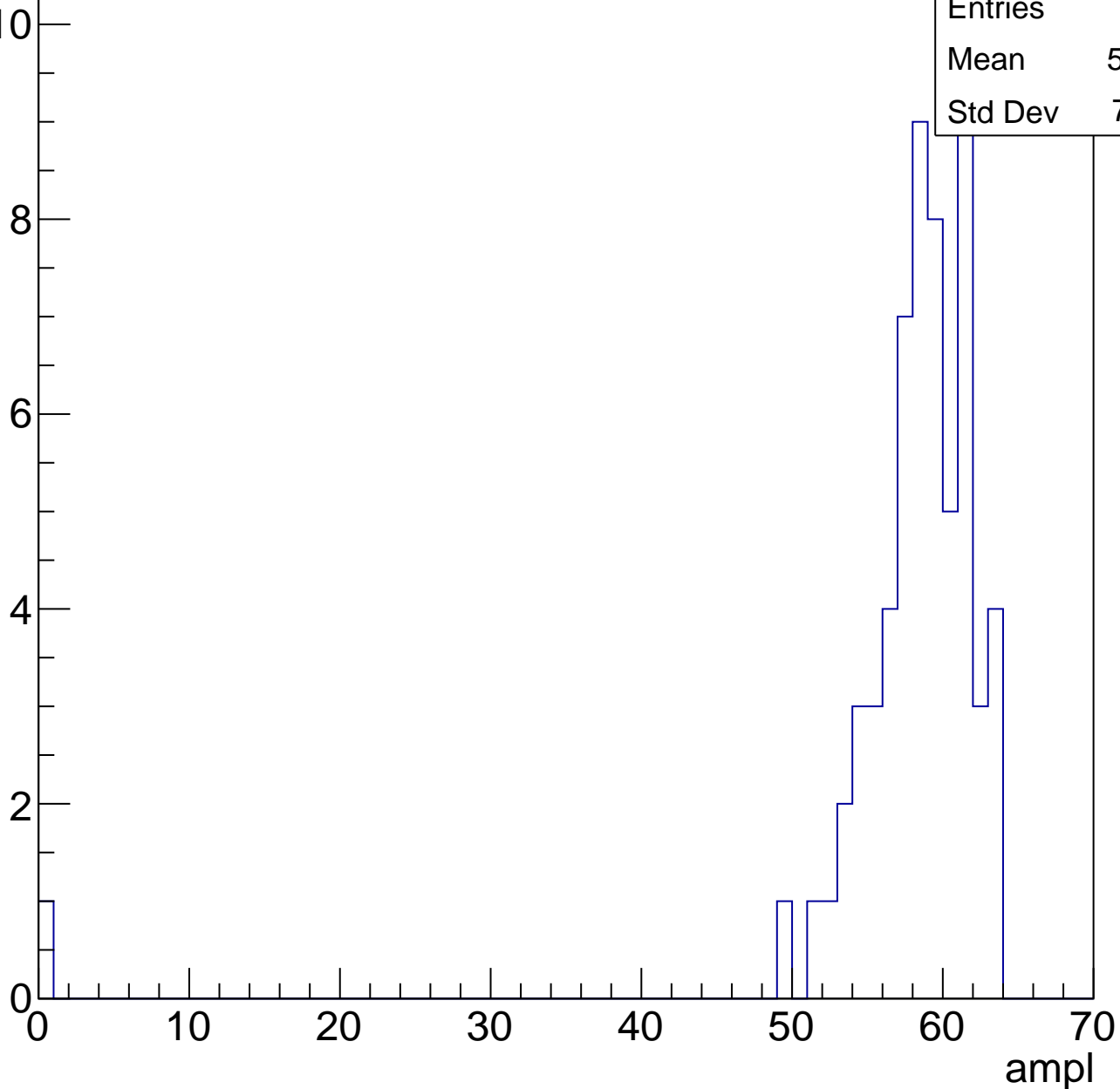


# B0L002S, U8-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	57.26
Std Dev	7.941

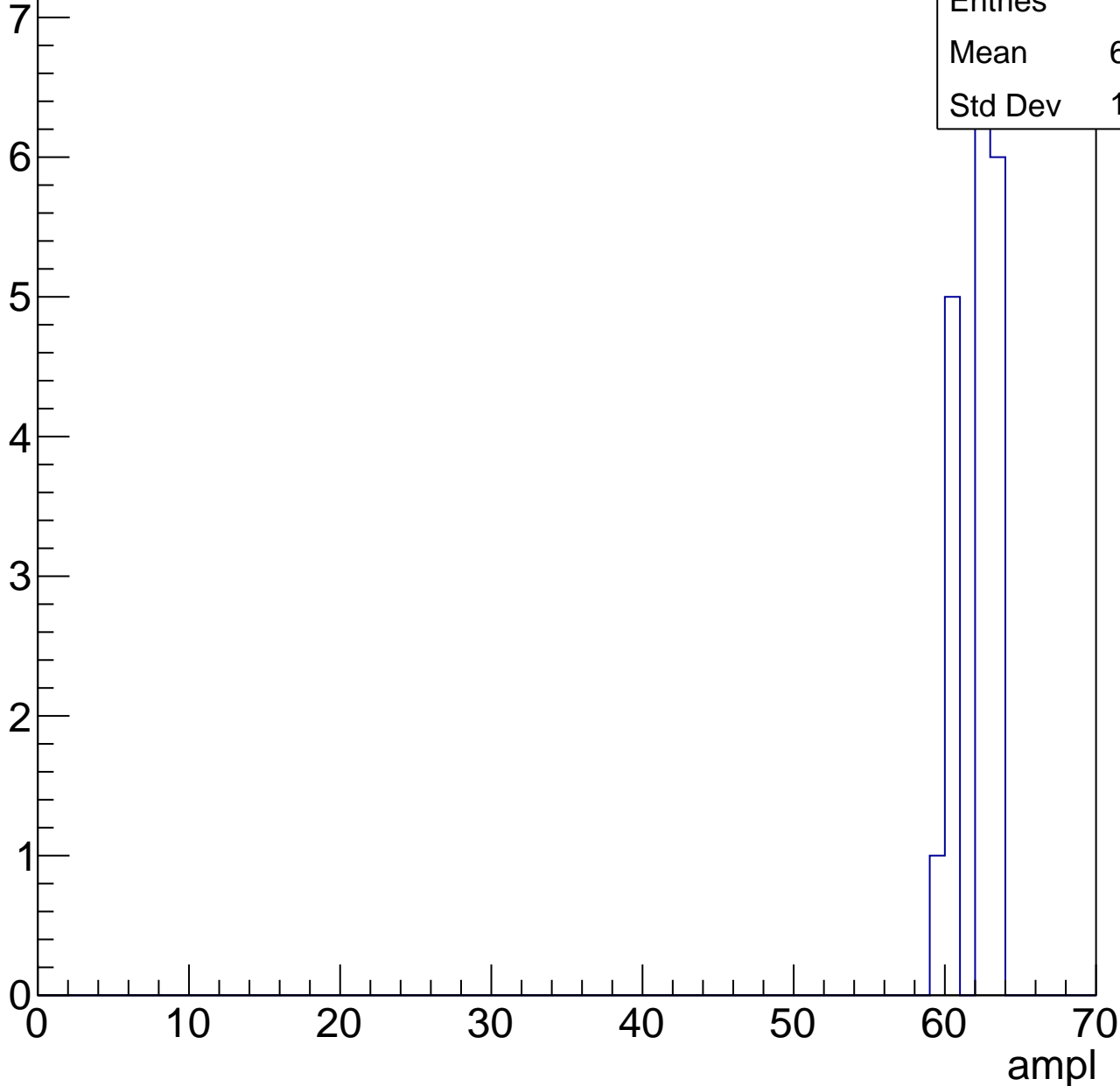


# B0L002S, U8-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	19
Mean	61.63
Std Dev	1.306

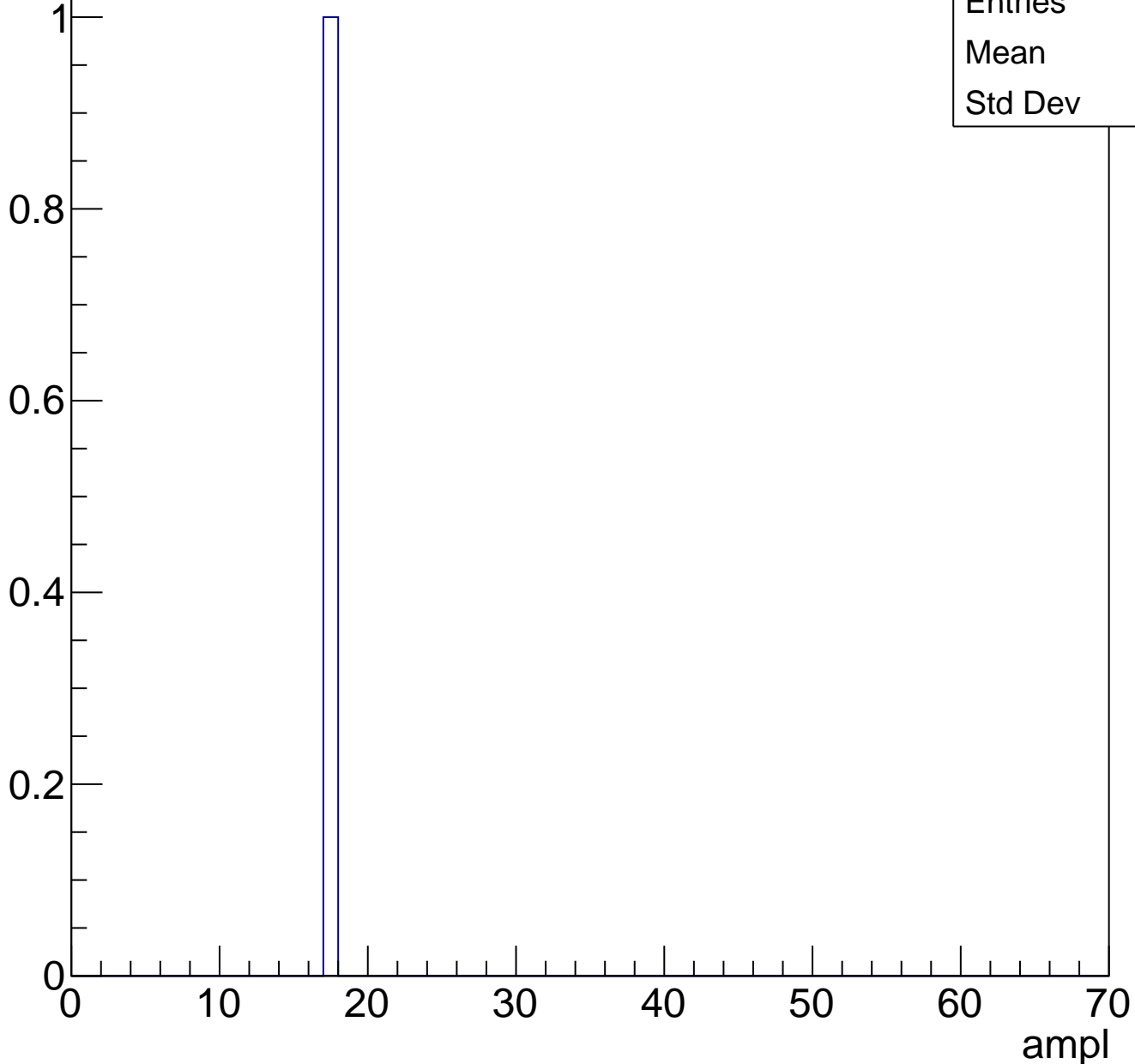




# B0L002S, U8-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	17
Std Dev	0

# B0L002S, U8-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	88
Mean	31.39
Std Dev	3.419

**Gaus mean : 31.6747**

**Gaus Width: 3.3333**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

# B0L002S, U8-ch97, adc1

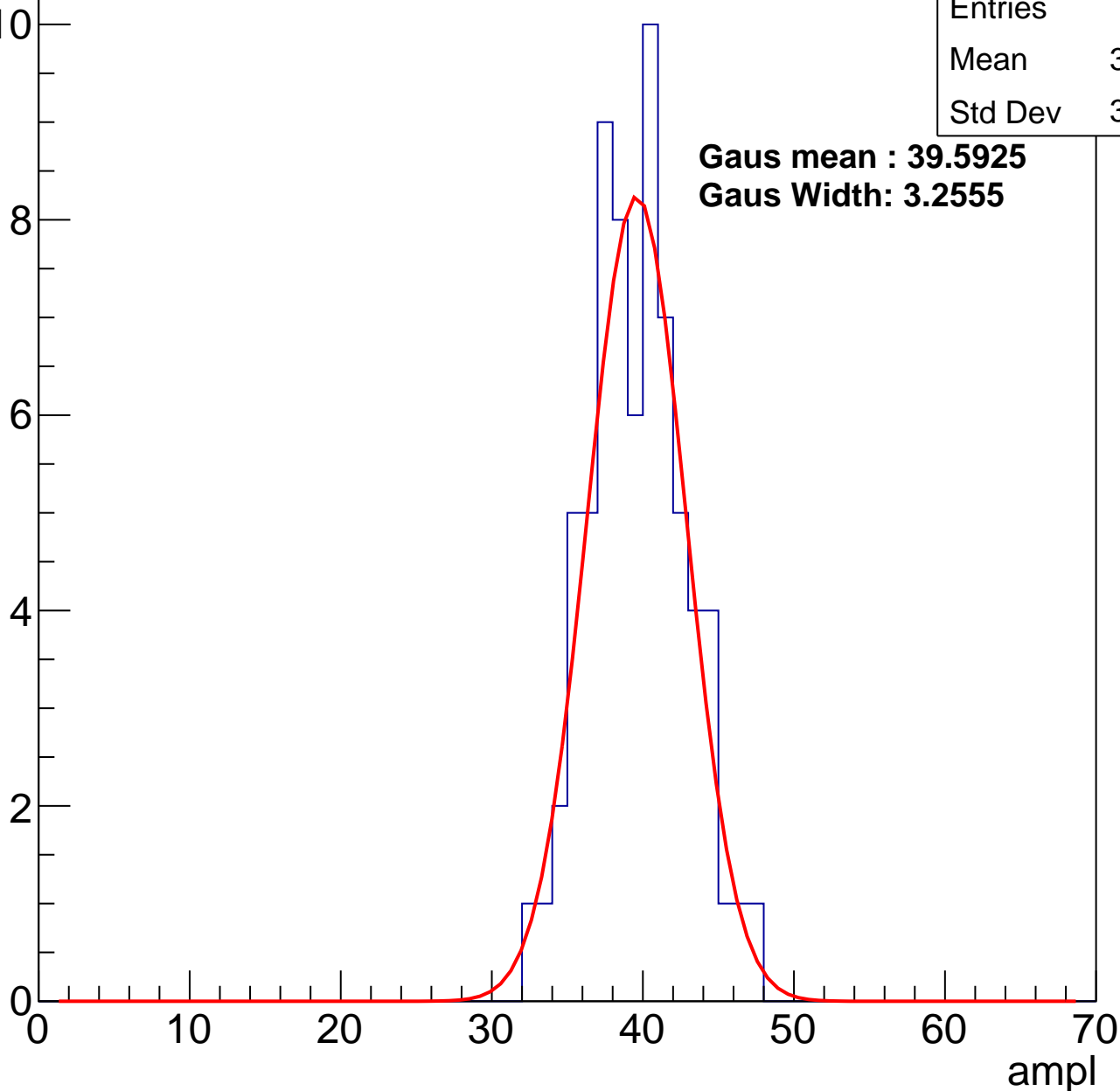
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	39.17
Std Dev	3.158

**Gaus mean : 39.5925**

**Gaus Width: 3.2555**



# B0L002S, U8-ch97, adc2

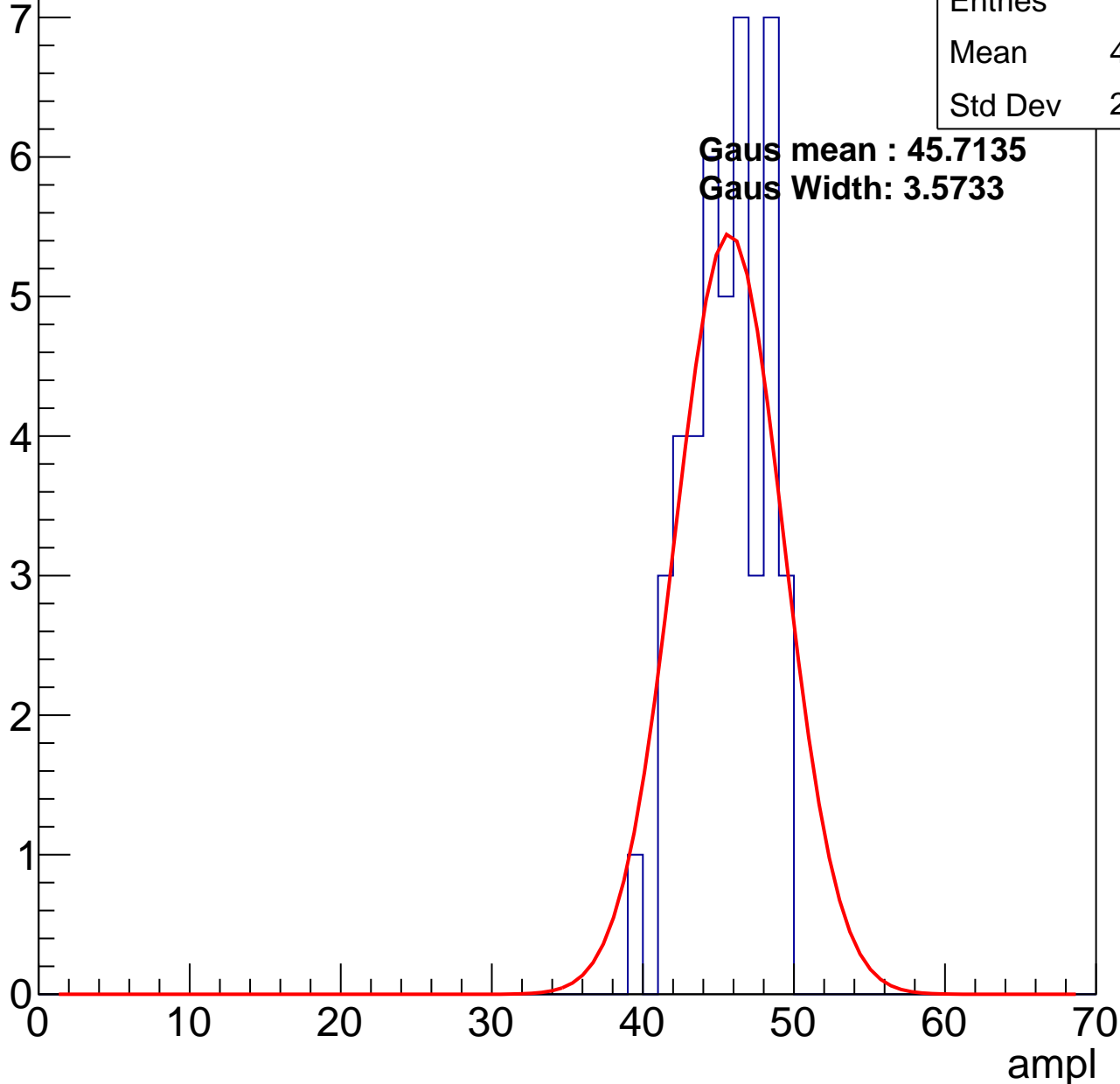
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	45.05
Std Dev	2.515

**Gaus mean : 45.7135**

**Gaus Width: 3.5733**

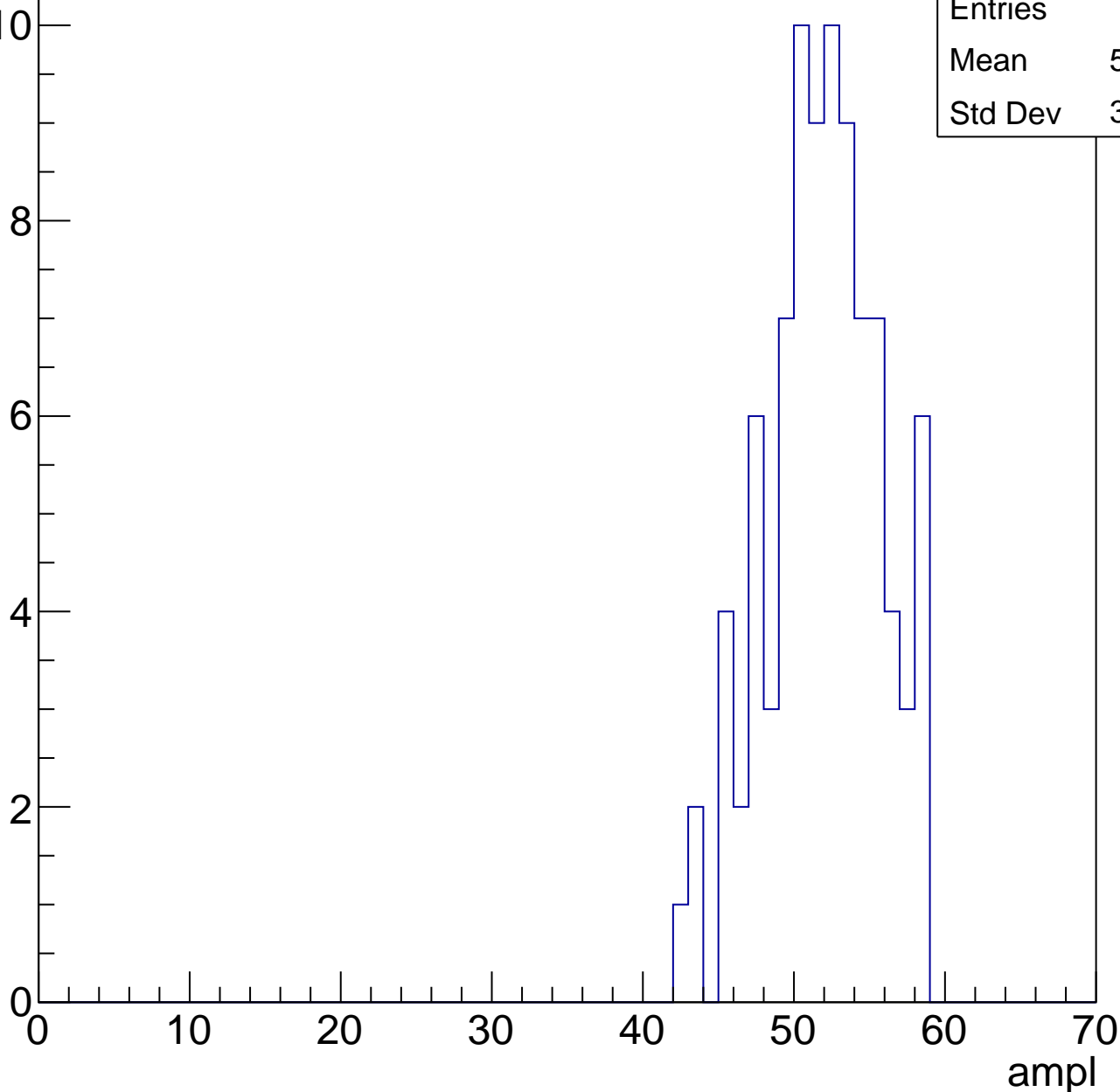


# B0L002S, U8-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	90
Mean	51.46
Std Dev	3.754

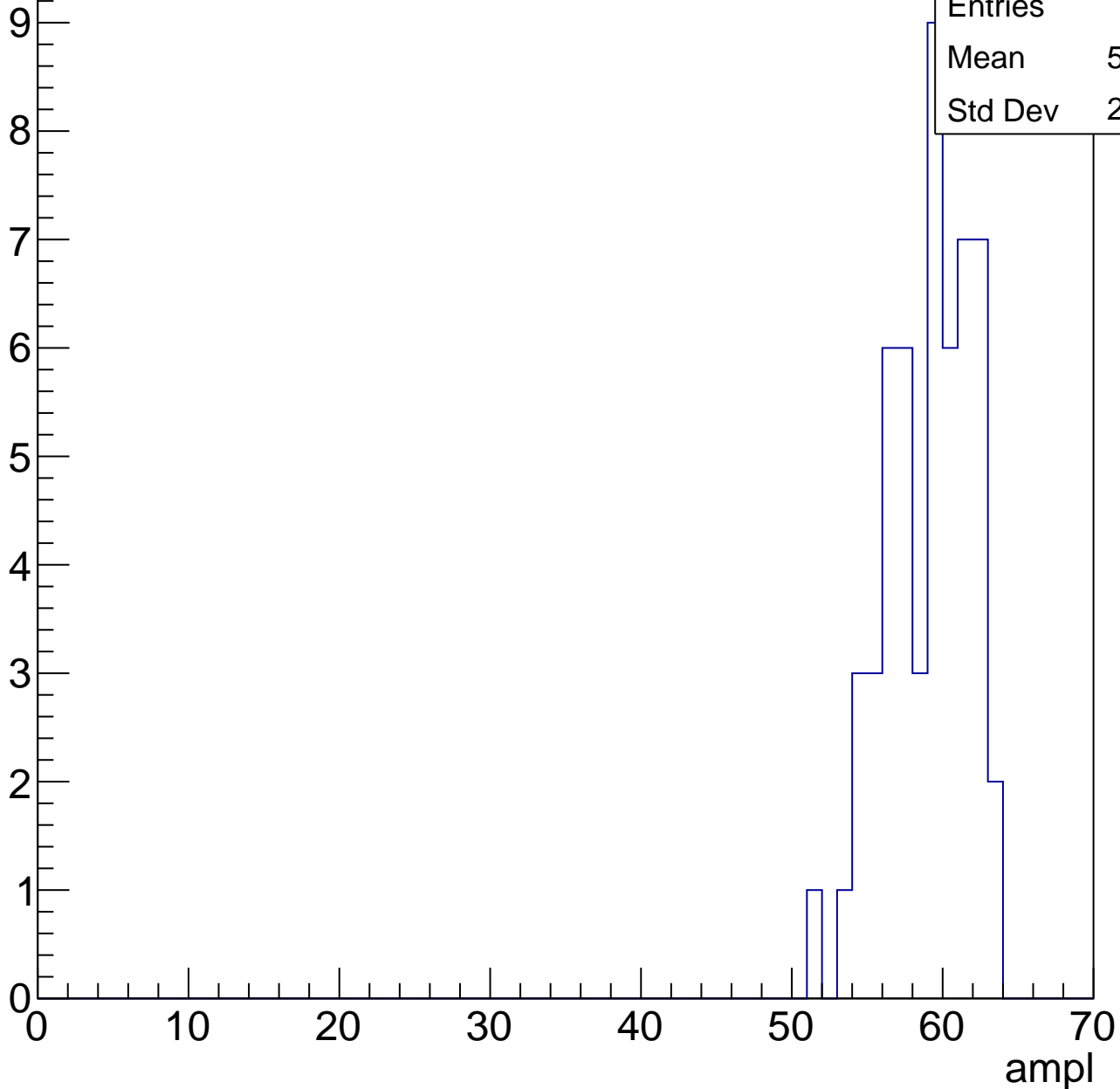


# B0L002S, U8-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.54
Std Dev	2.794

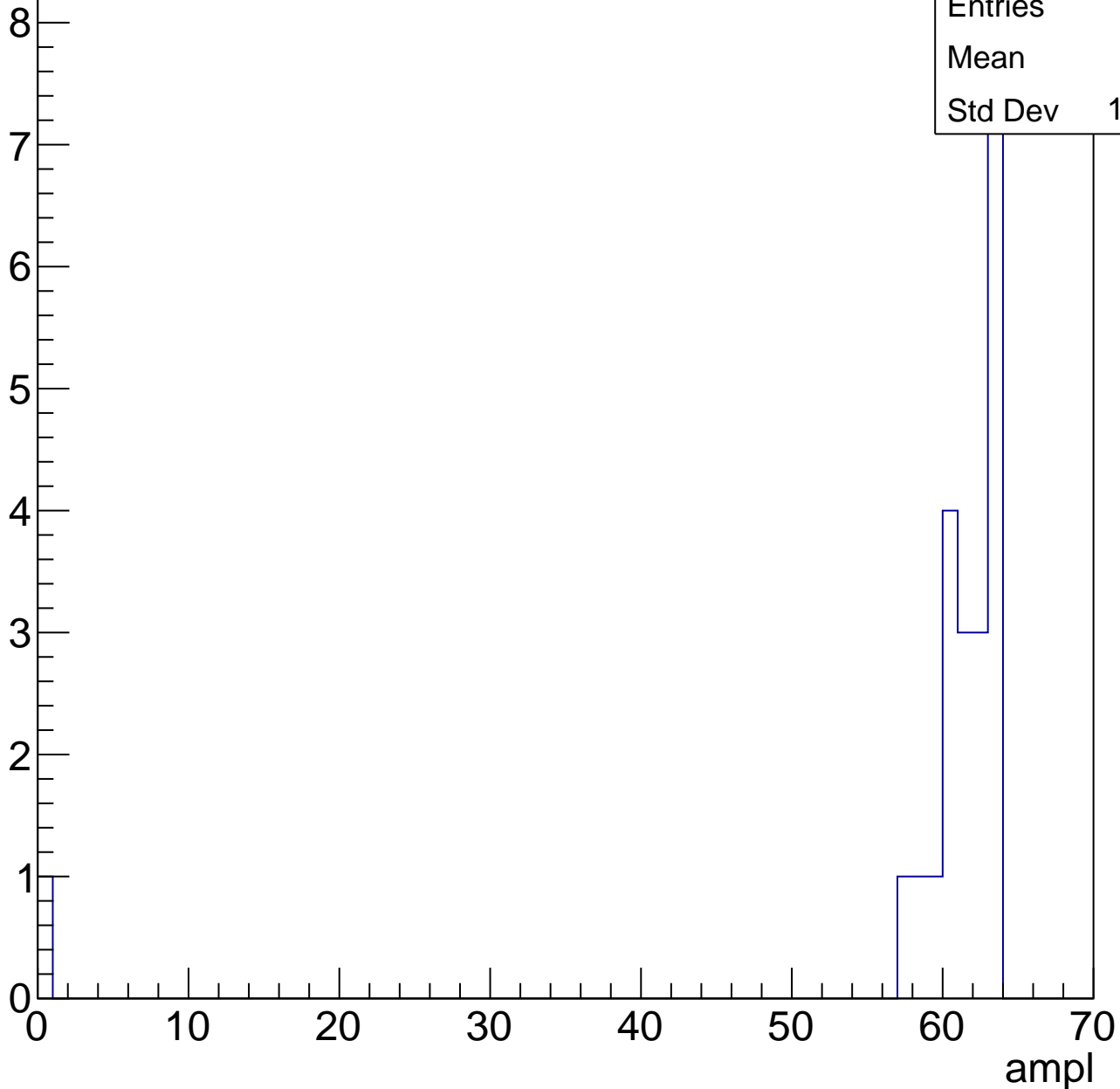


# B0L002S, U8-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.5
Std Dev	12.88



# B0L002S, U8-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch98, adc0

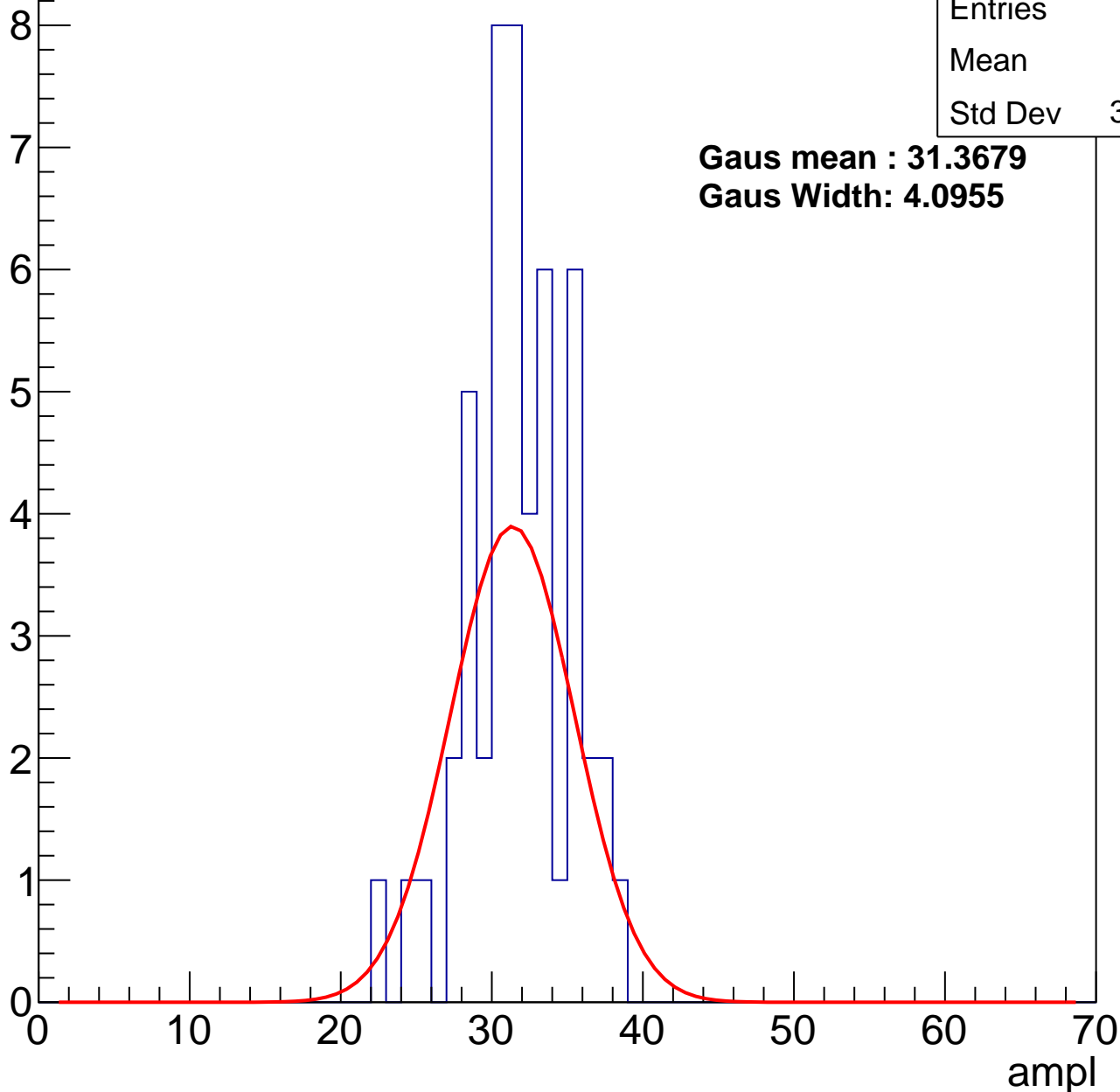
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	31.3
Std Dev	3.342

**Gaus mean : 31.3679**

**Gaus Width: 4.0955**



# B0L002S, U8-ch98, adc1

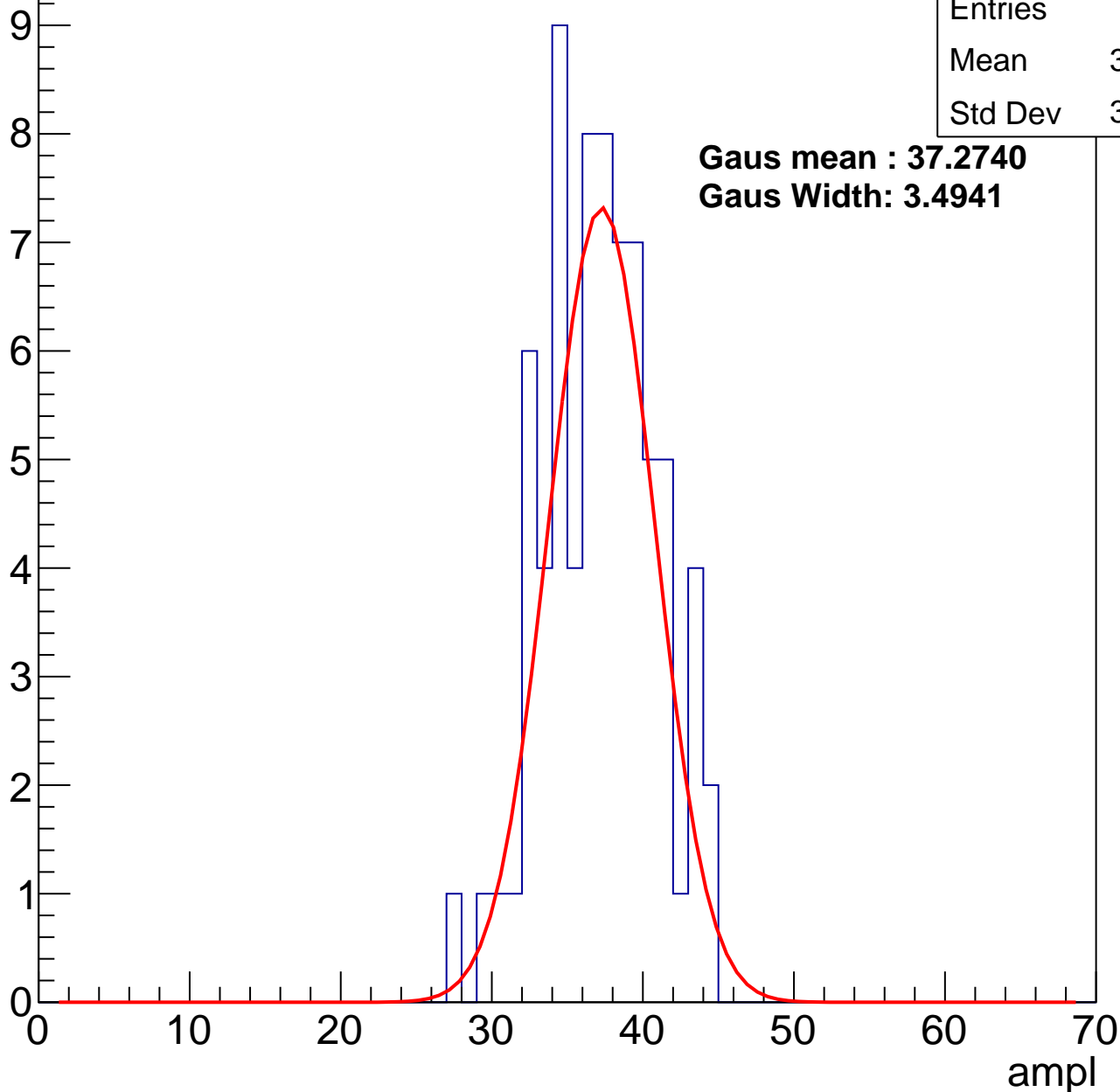
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.72
Std Dev	3.656

**Gaus mean : 37.2740**

**Gaus Width: 3.4941**



# B0L002S, U8-ch98, adc2

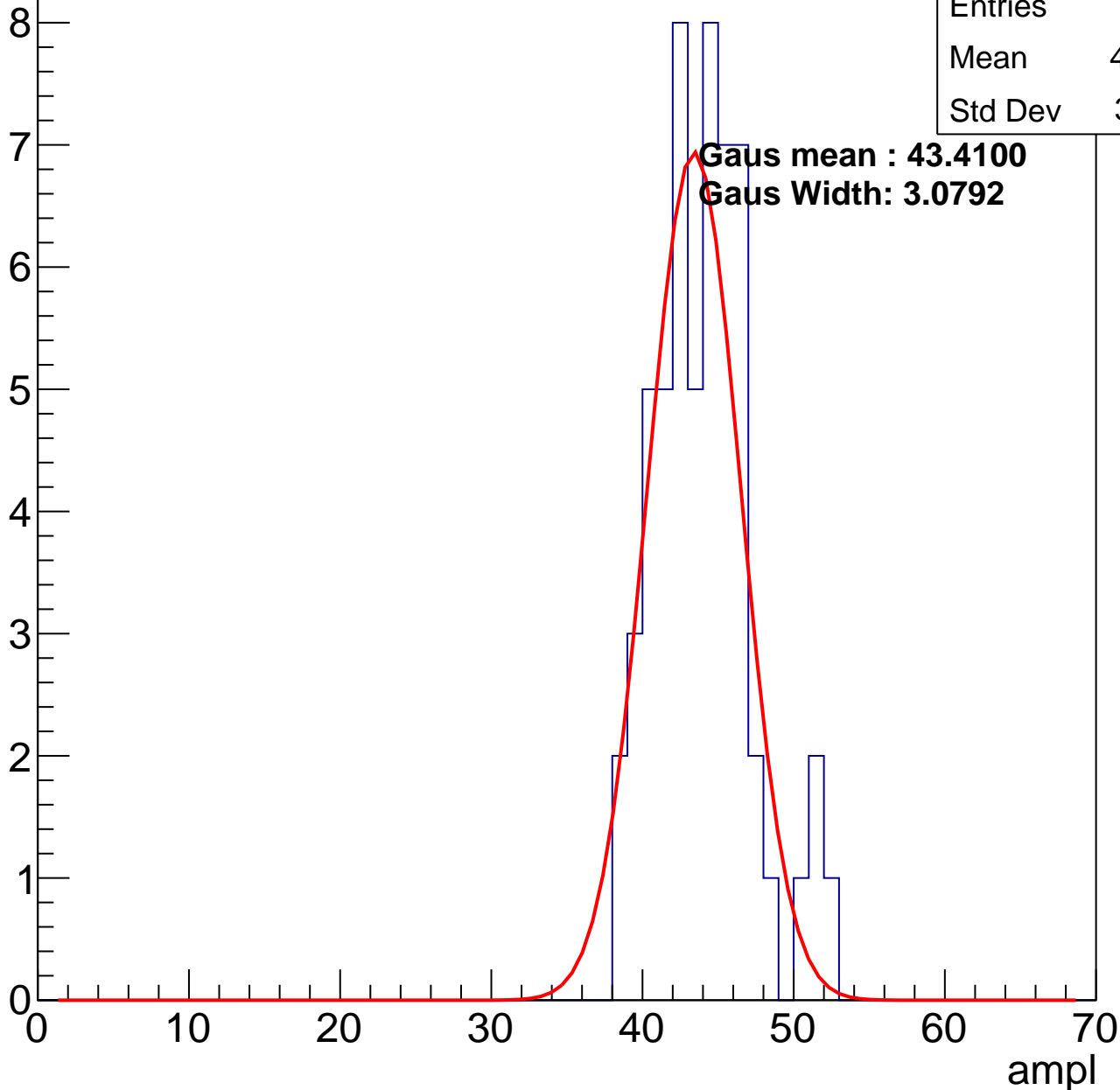
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.58
Std Dev	3.151

**Gaus mean : 43.4100**

**Gaus Width: 3.0792**



# B0L002S, U8-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

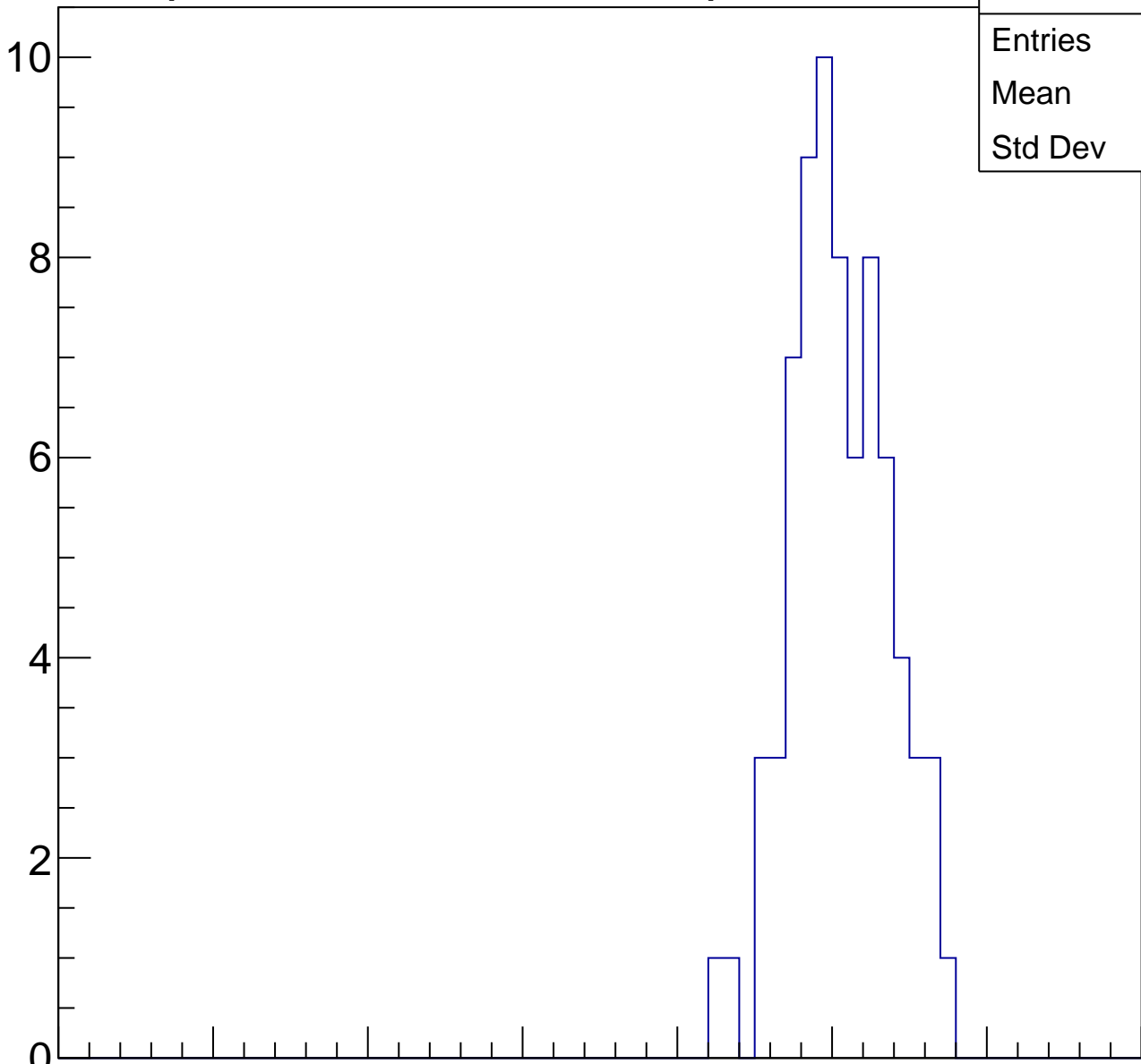
Entries	73
Mean	50.07
Std Dev	3.168

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

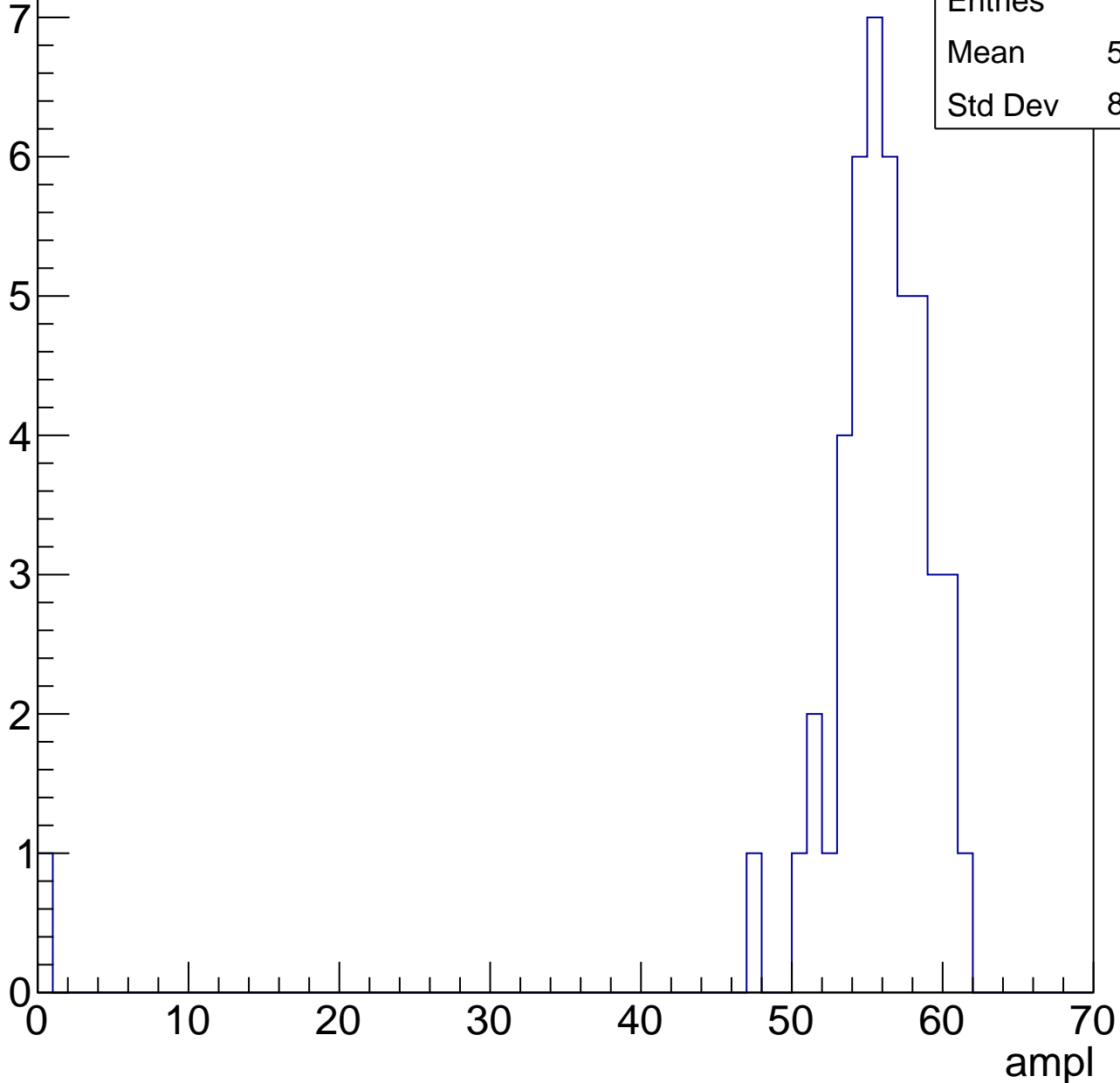


# B0L002S, U8-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	54.37
Std Dev	8.583

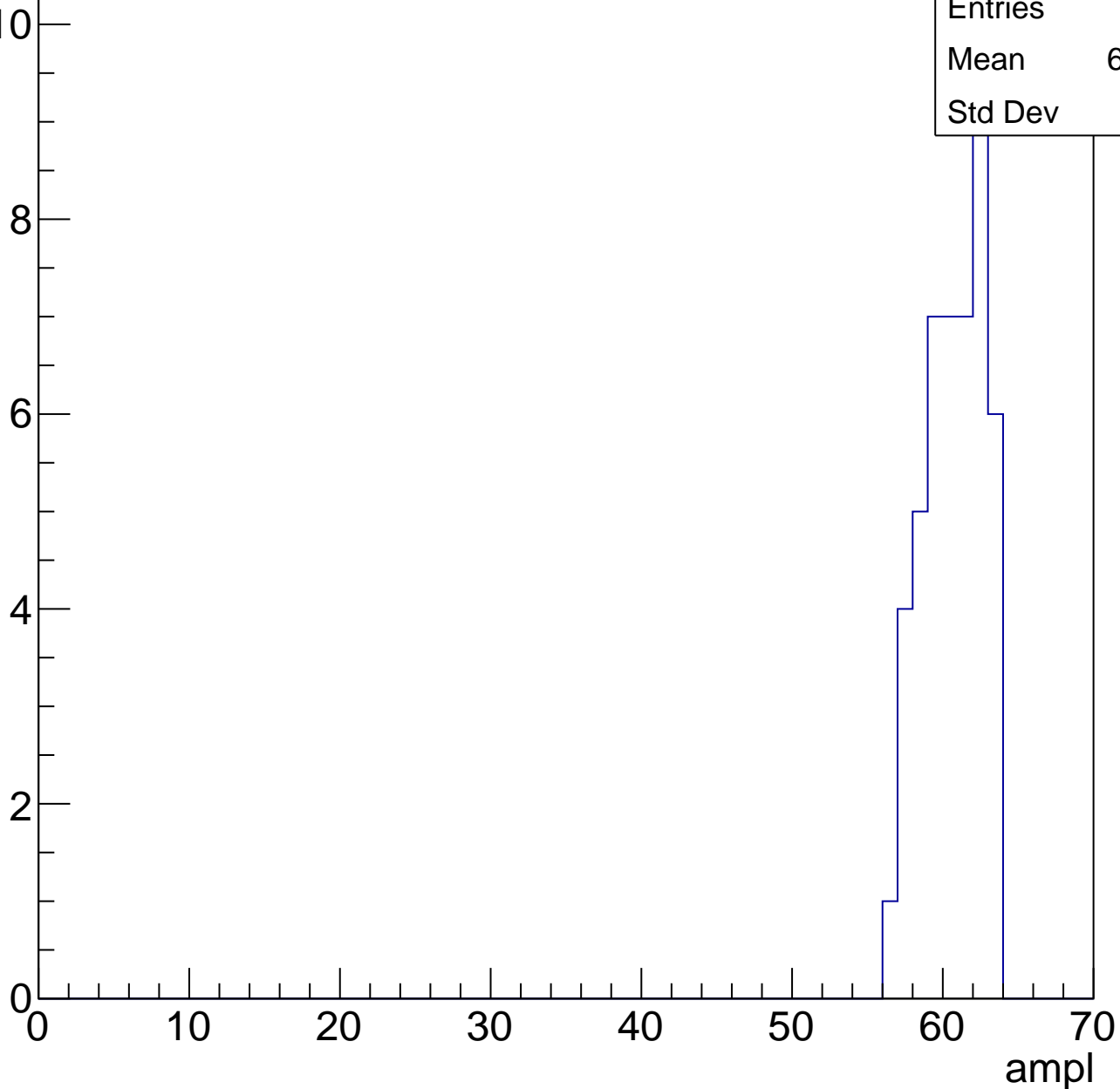


# B0L002S, U8-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

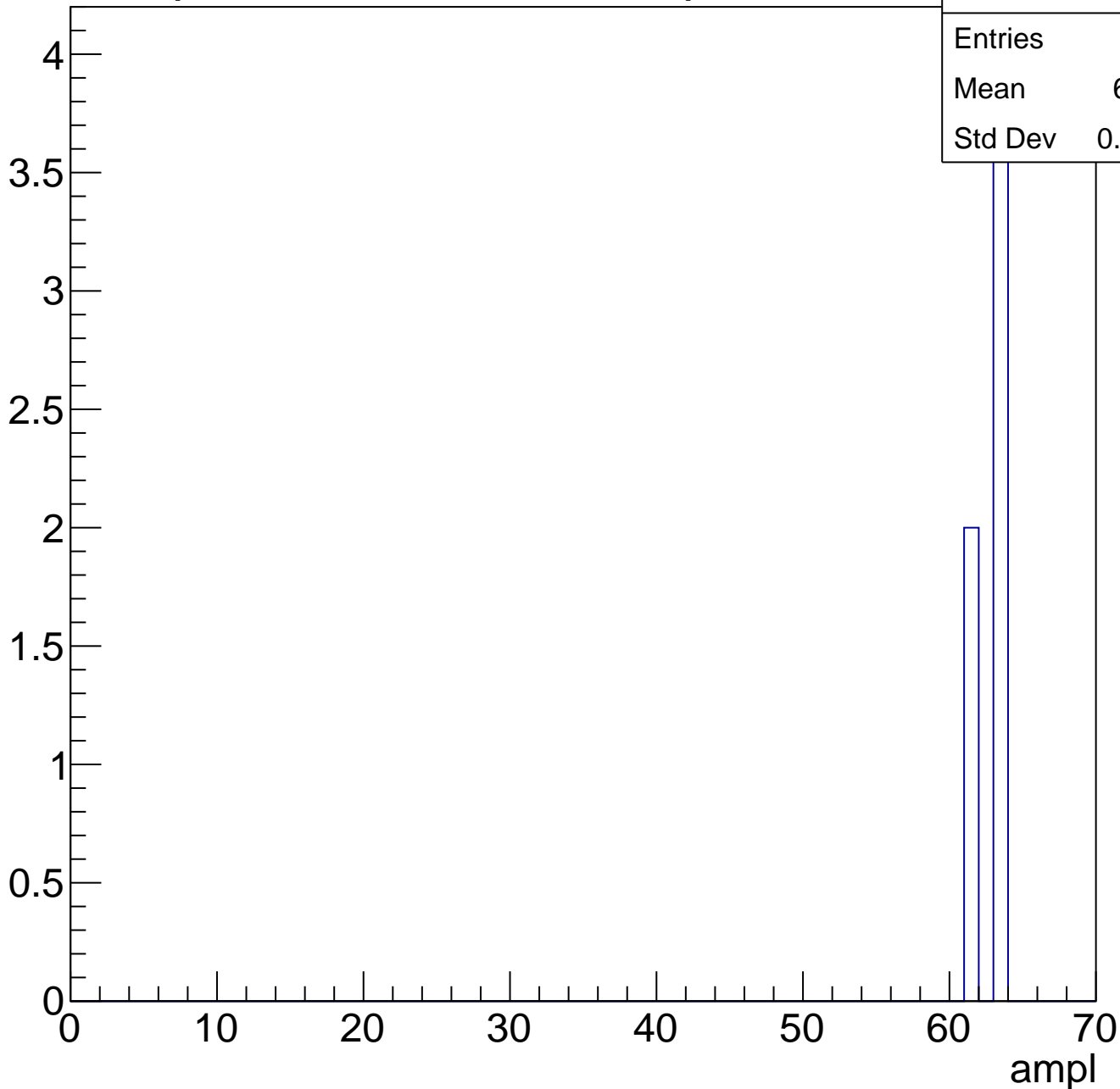
Entries	47
Mean	60.26
Std Dev	1.94



# B0L002S, U8-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	29.58
Std Dev	3.098

**Gaus mean : 30.0378**

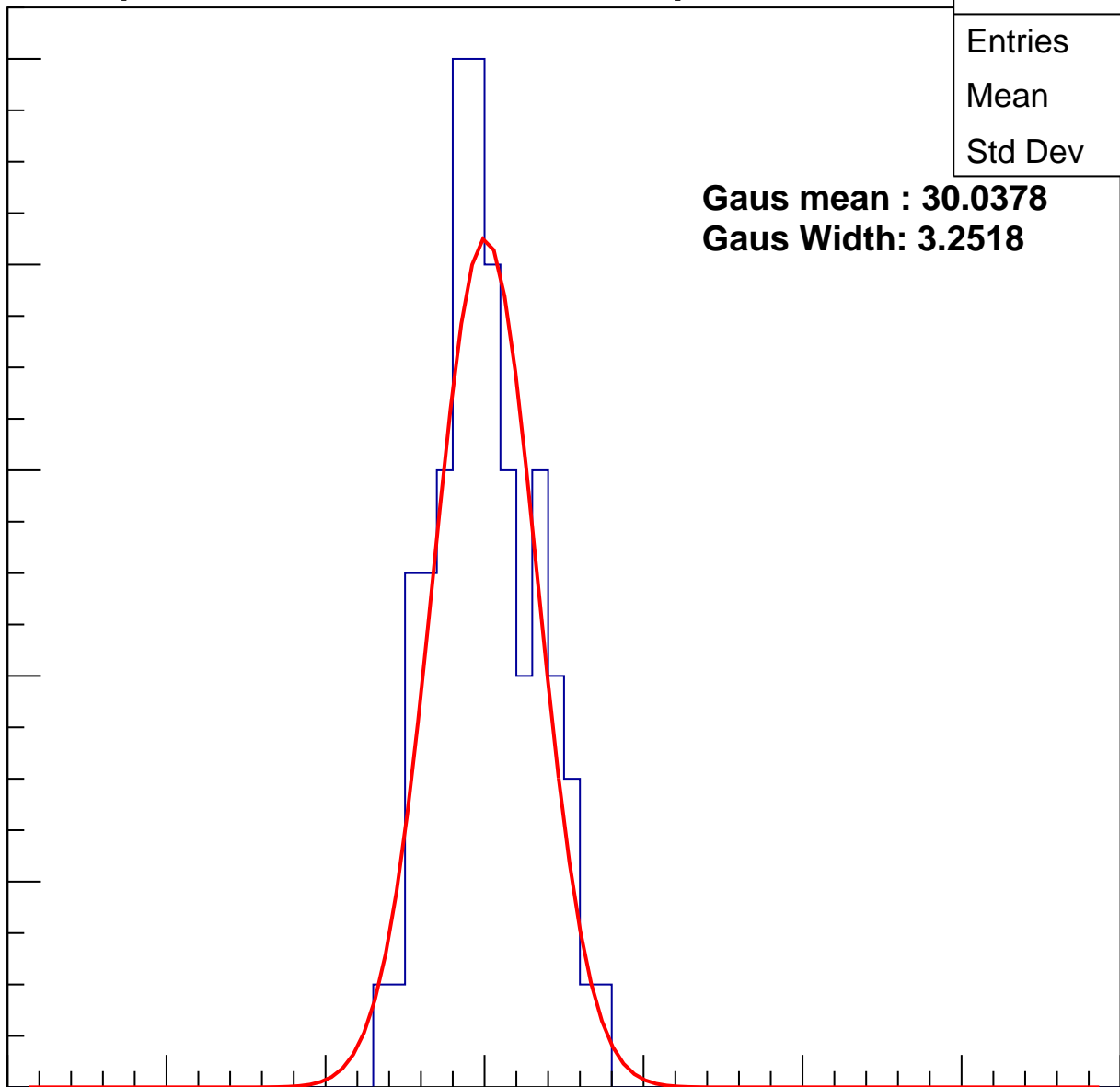
**Gaus Width: 3.2518**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch99, adc1

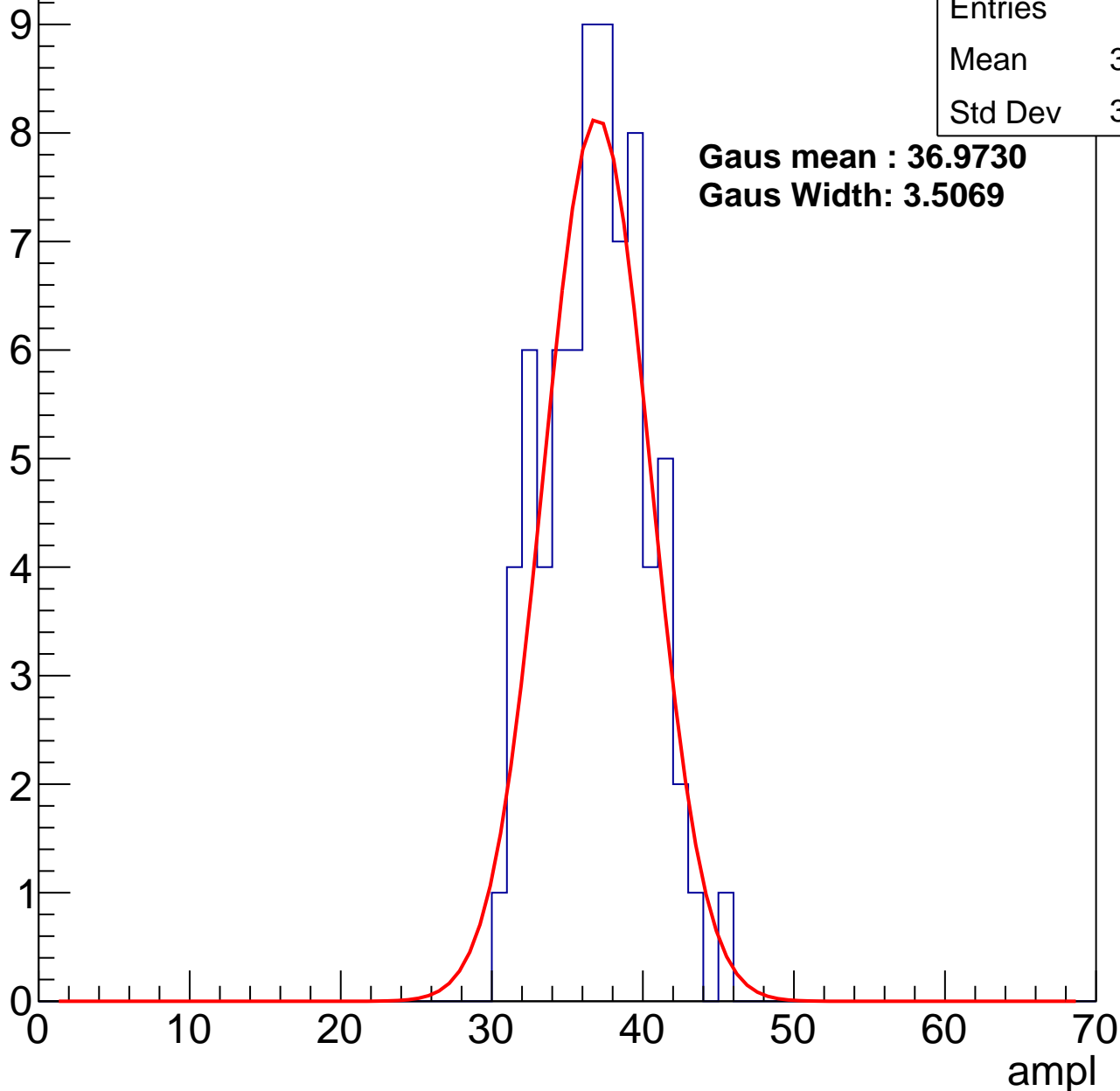
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.49
Std Dev	3.269

**Gaus mean : 36.9730**

**Gaus Width: 3.5069**



# B0L002S, U8-ch99, adc2

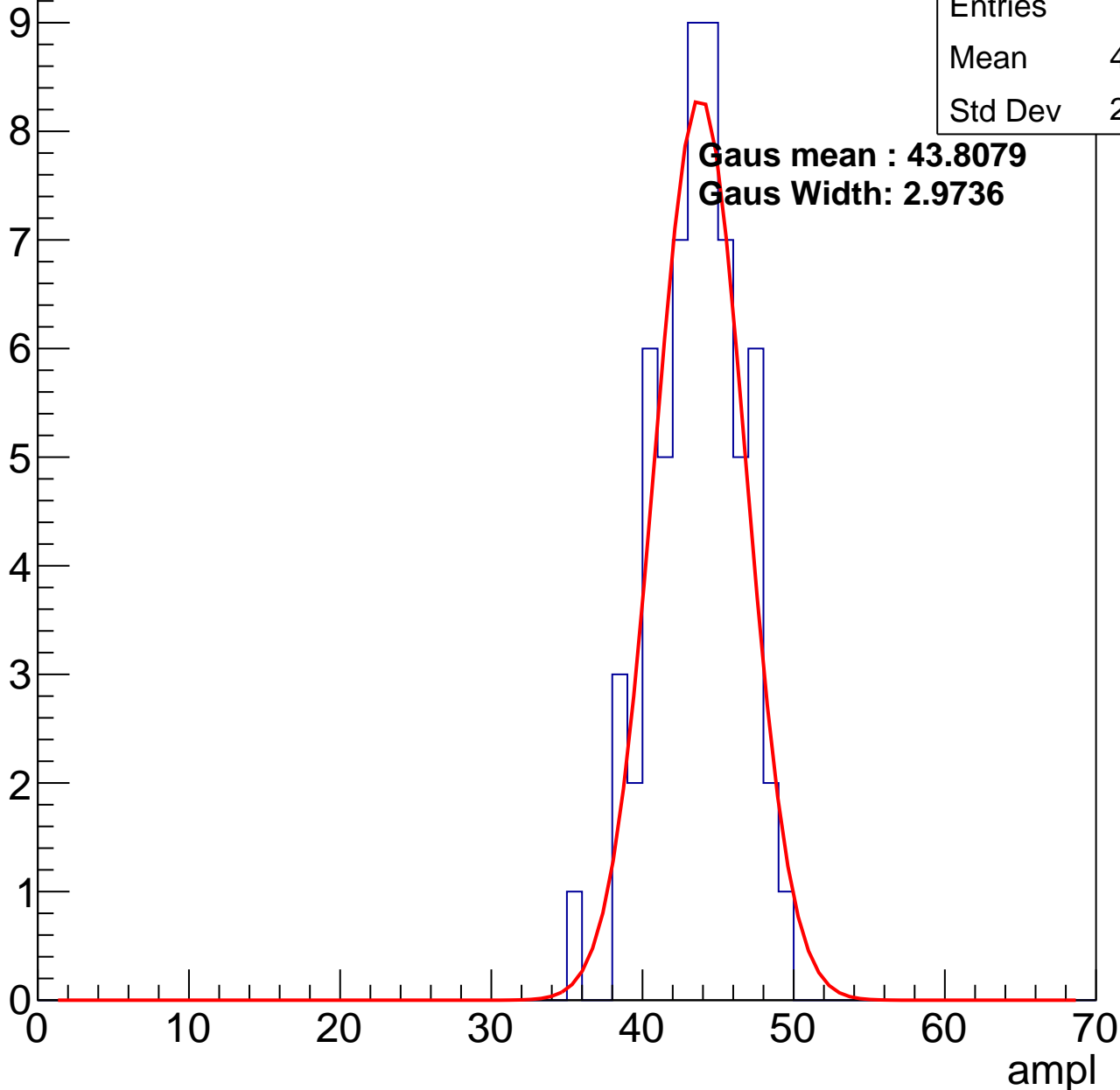
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.19
Std Dev	2.856

**Gaus mean : 43.8079**

**Gaus Width: 2.9736**

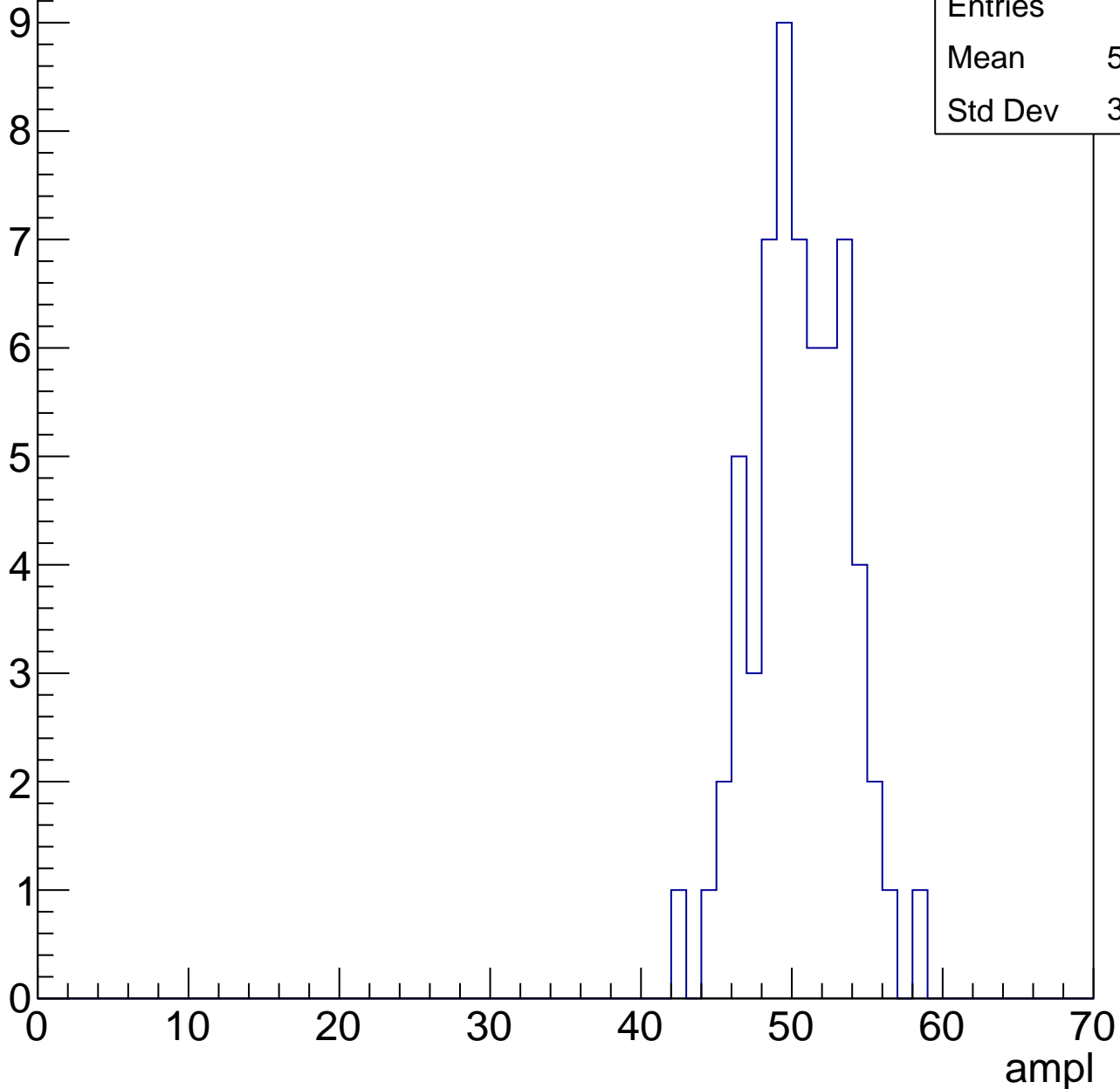


# B0L002S, U8-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

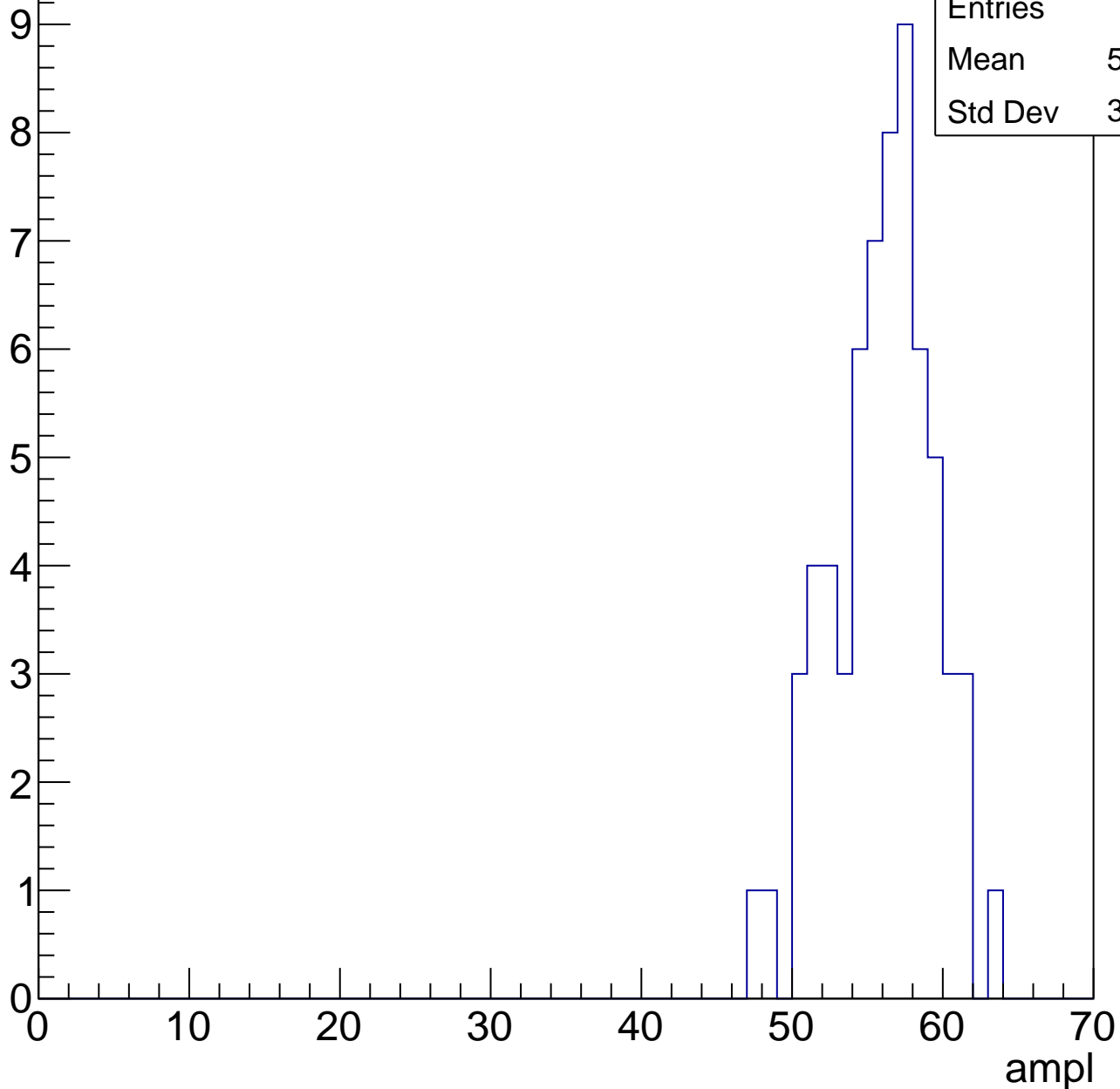
Entries	62
Mean	50.05
Std Dev	3.113



# B0L002S, U8-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

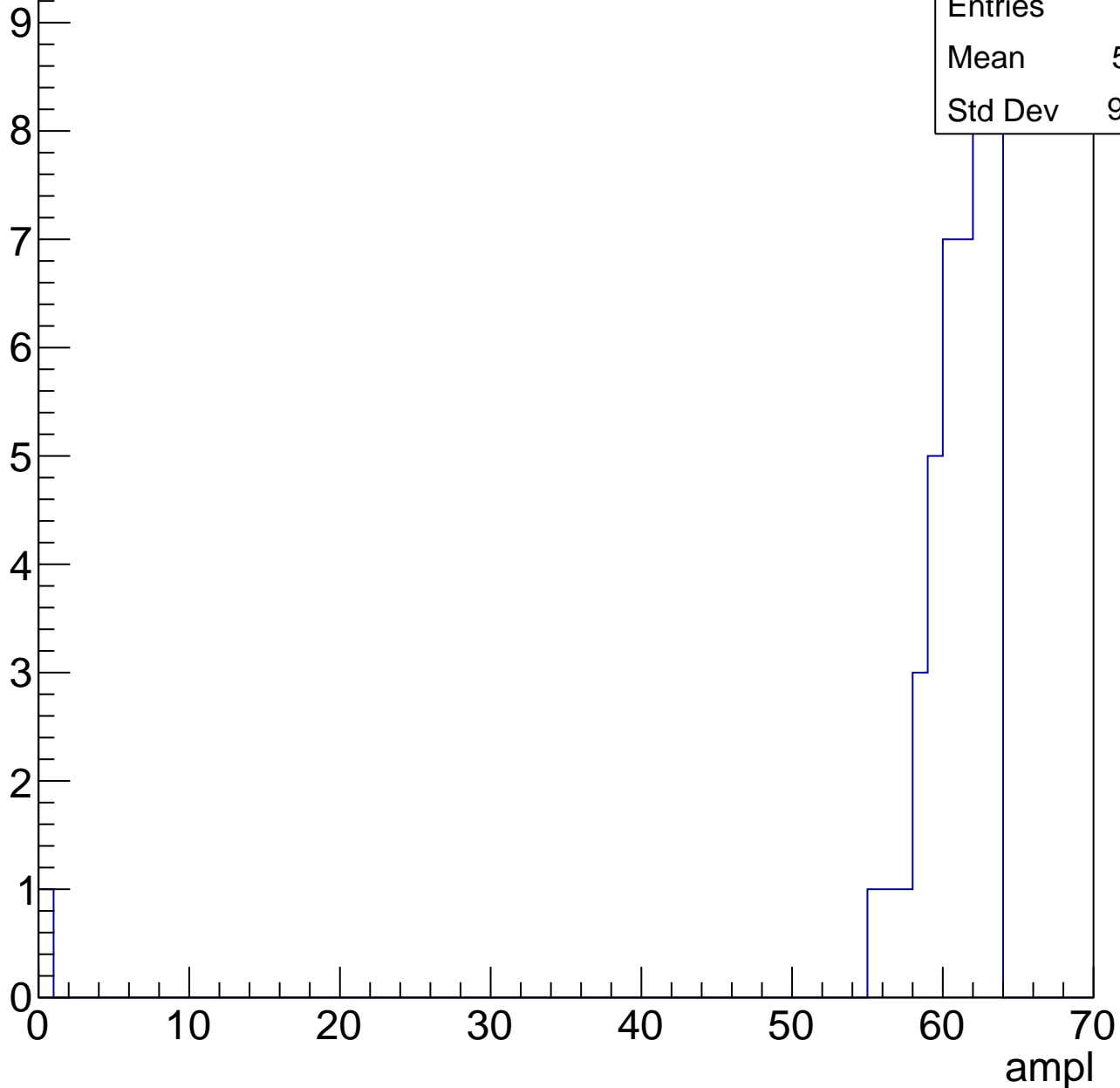


Entries	64
Mean	55.55
Std Dev	3.335

# B0L002S, U8-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L002S, U8-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	29.97
Std Dev	3.259

**Gaus mean : 30.2930**

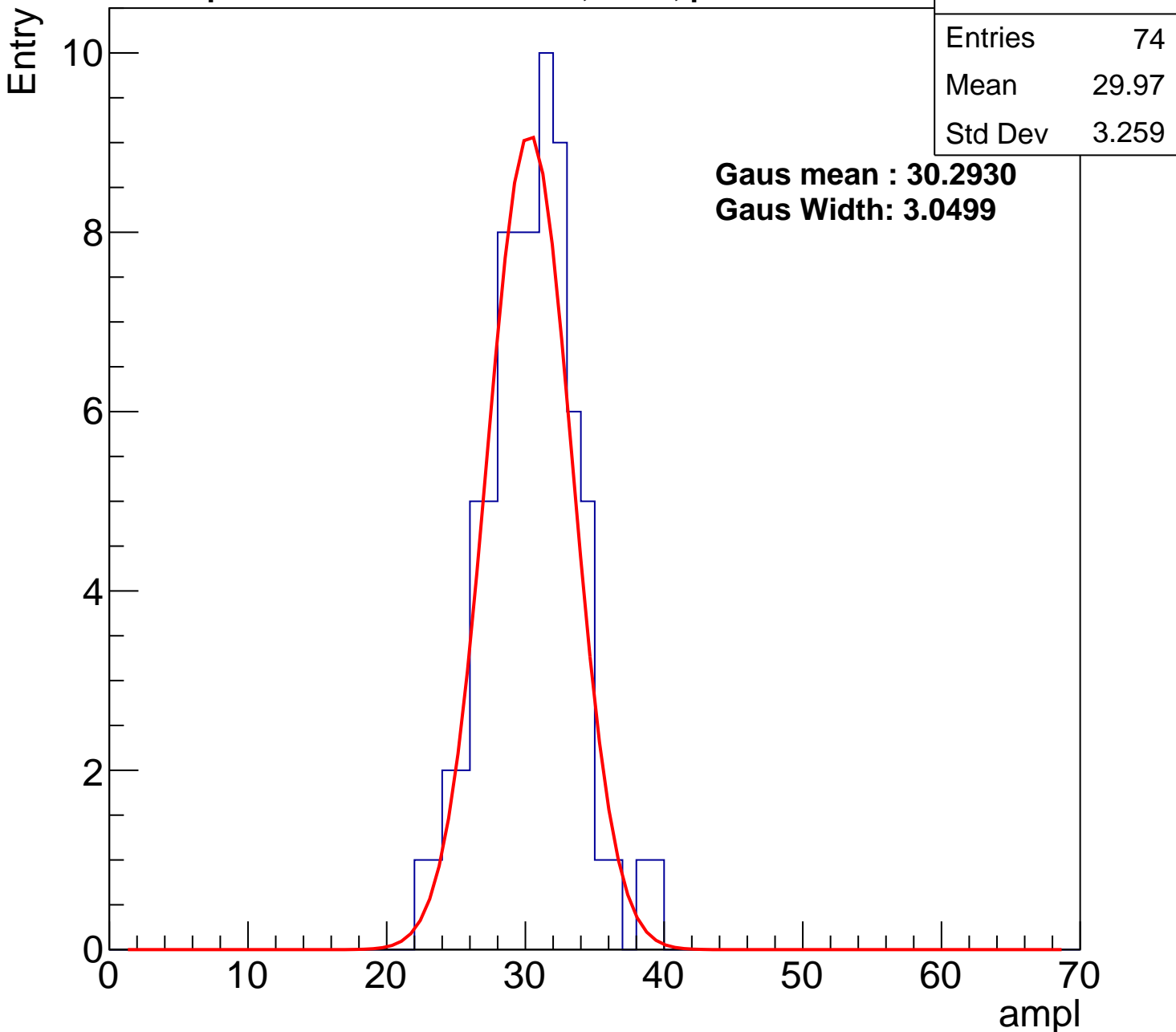
**Gaus Width: 3.0499**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch100, adc1

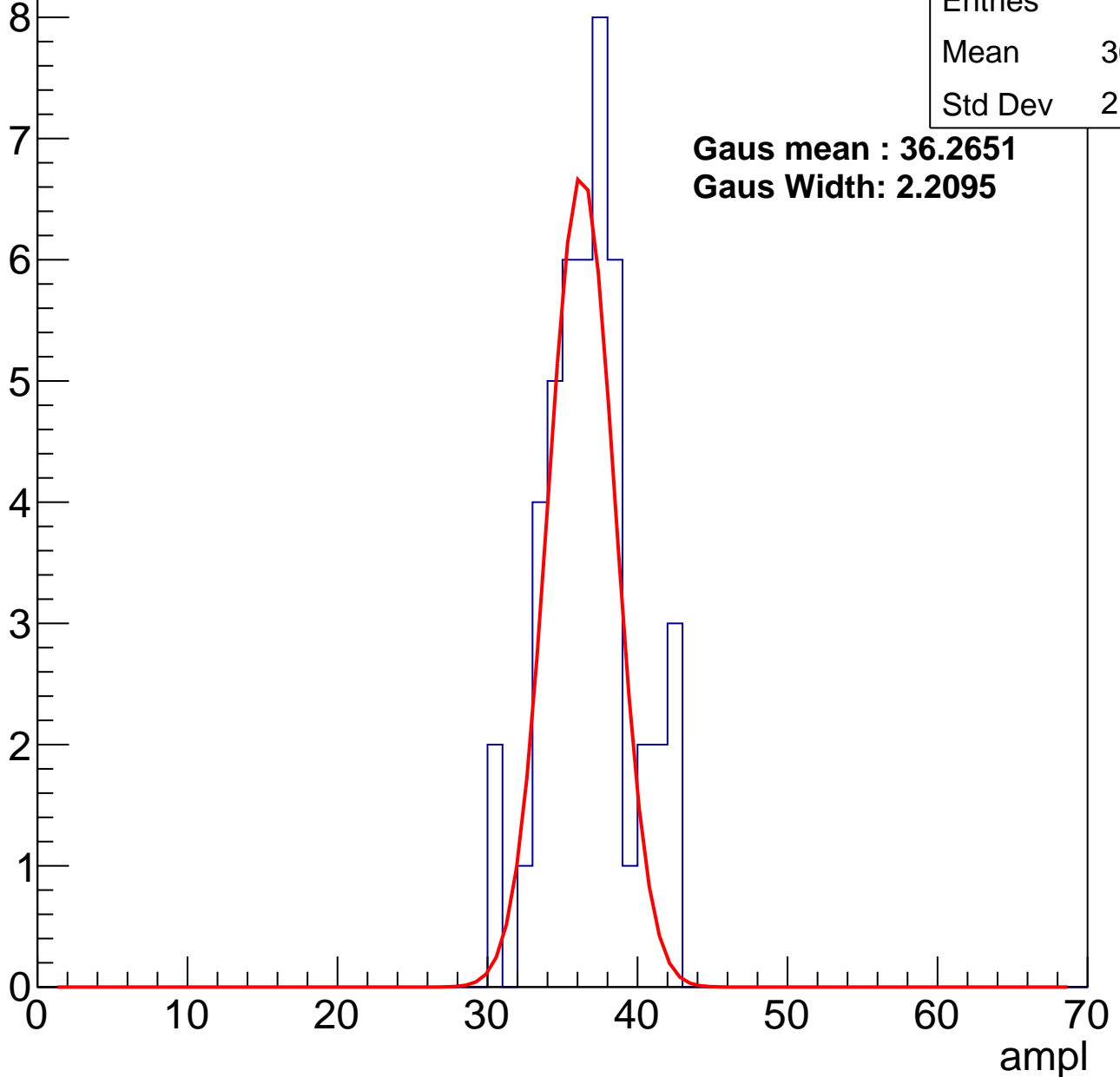
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	36.33
Std Dev	2.859

**Gaus mean : 36.2651**

**Gaus Width: 2.2095**



# B0L002S, U8-ch100, adc2

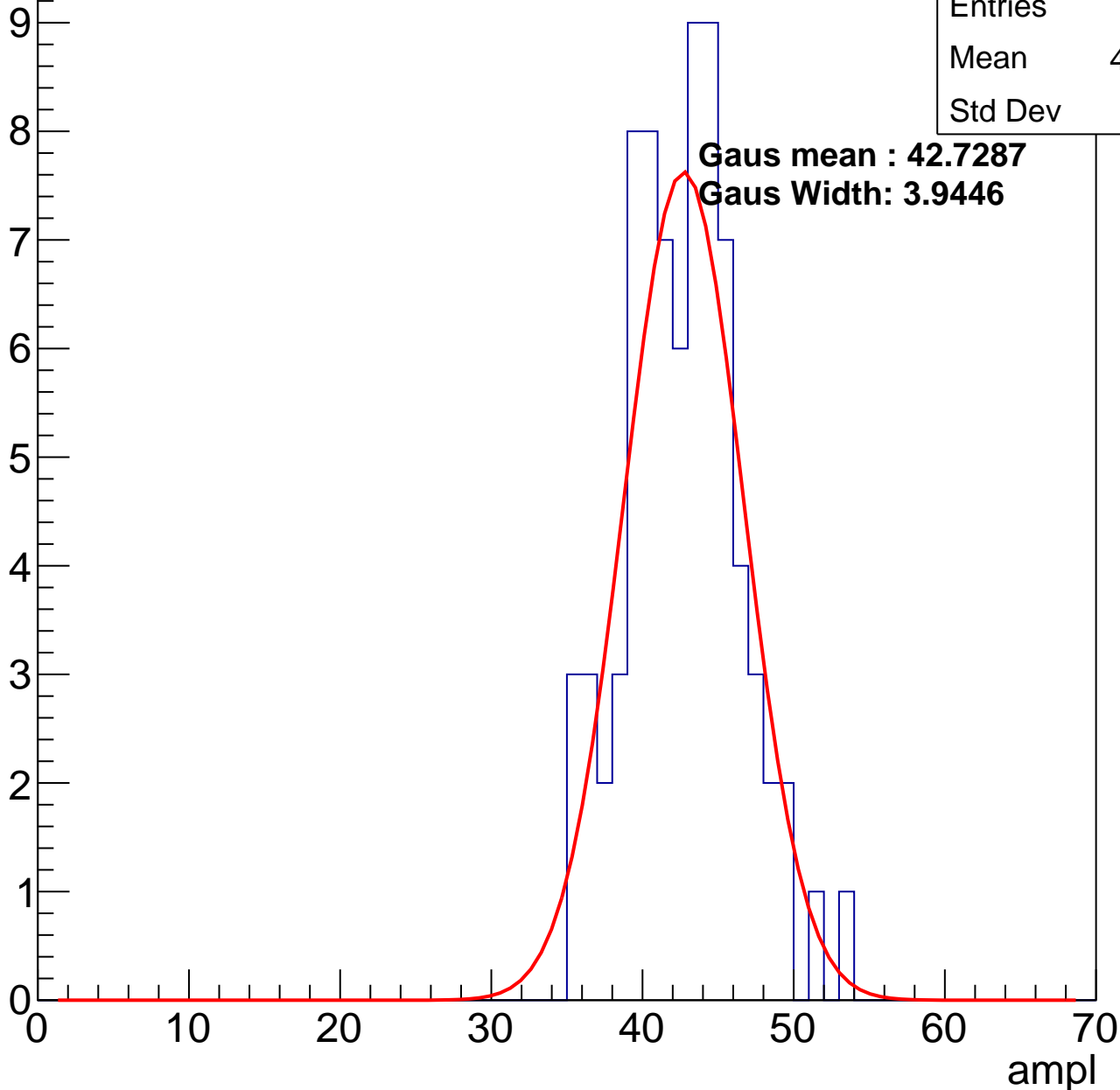
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	42.22
Std Dev	3.72

**Gaus mean : 42.7287**

**Gaus Width: 3.9446**

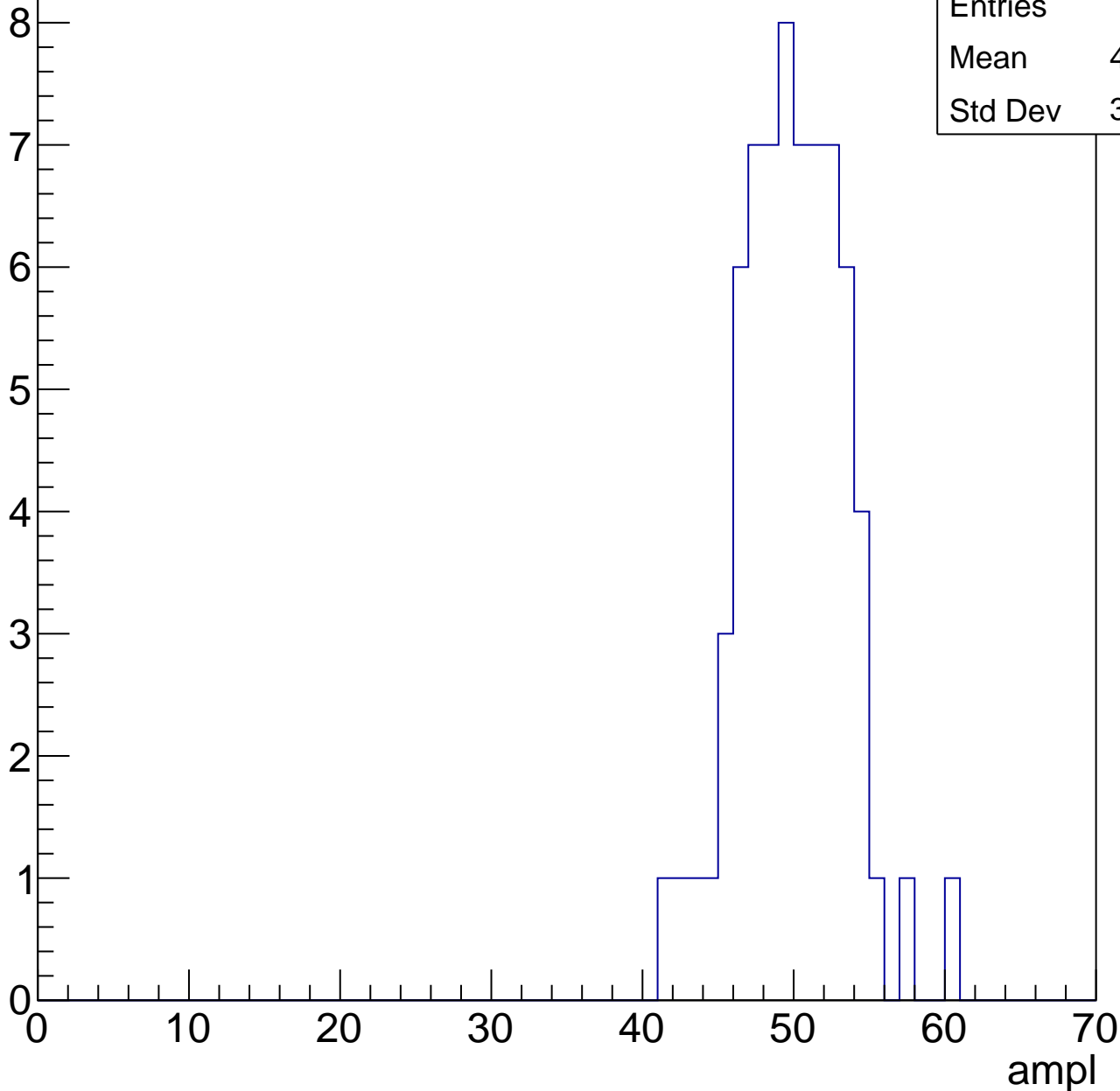


# B0L002S, U8-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.49
Std Dev	3.425

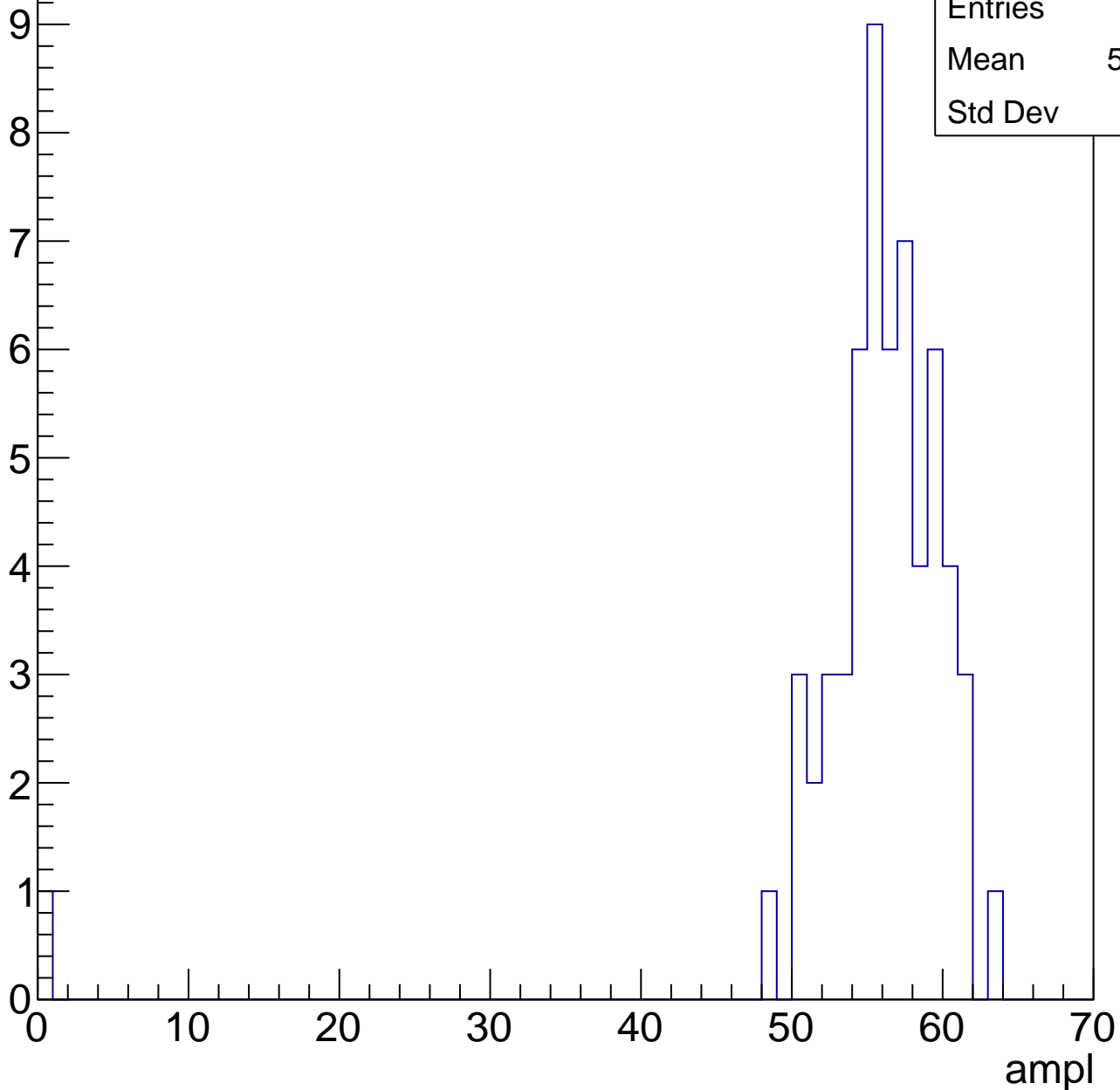


# B0L002S, U8-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	54.93
Std Dev	7.88

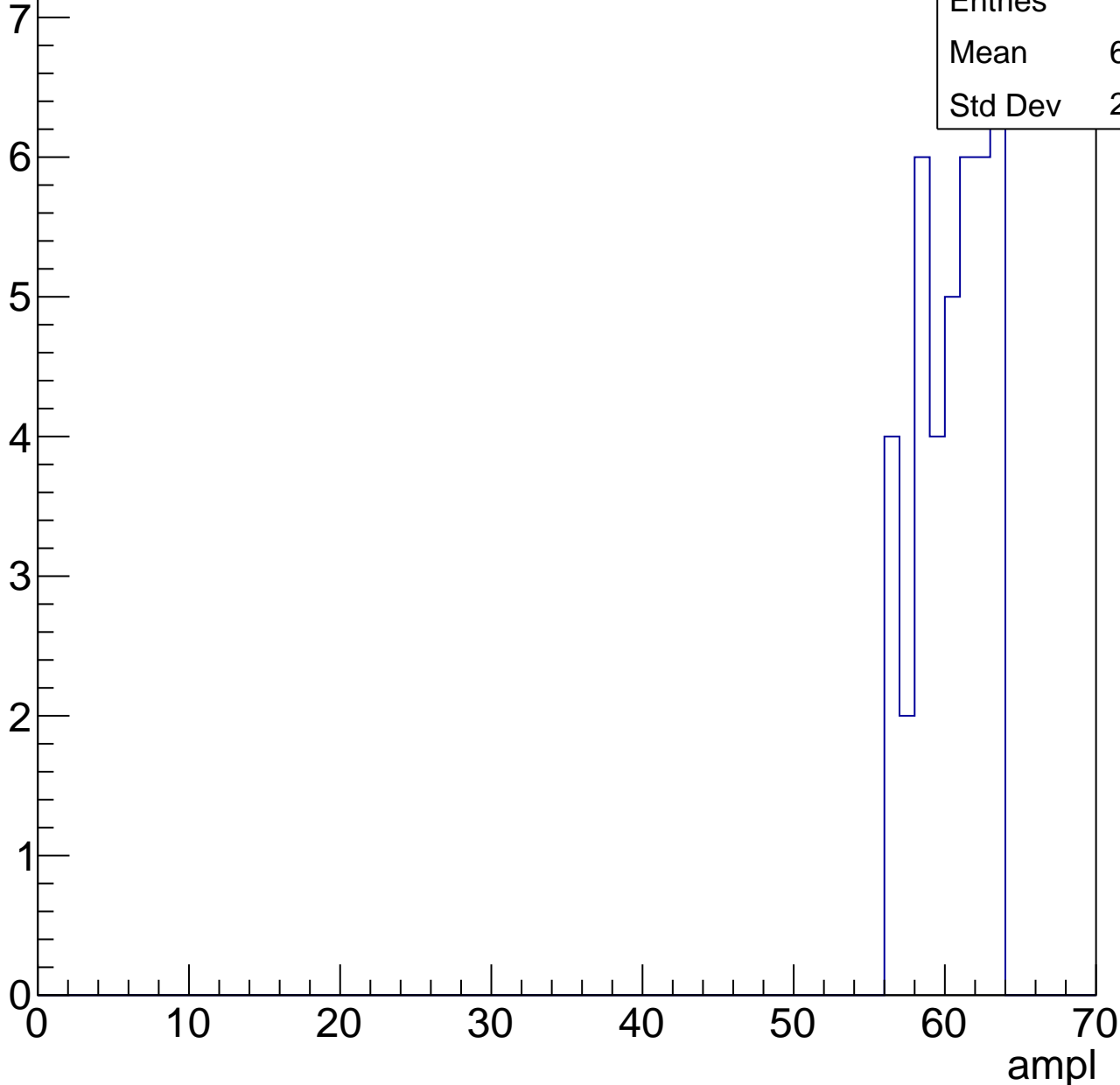


# B0L002S, U8-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

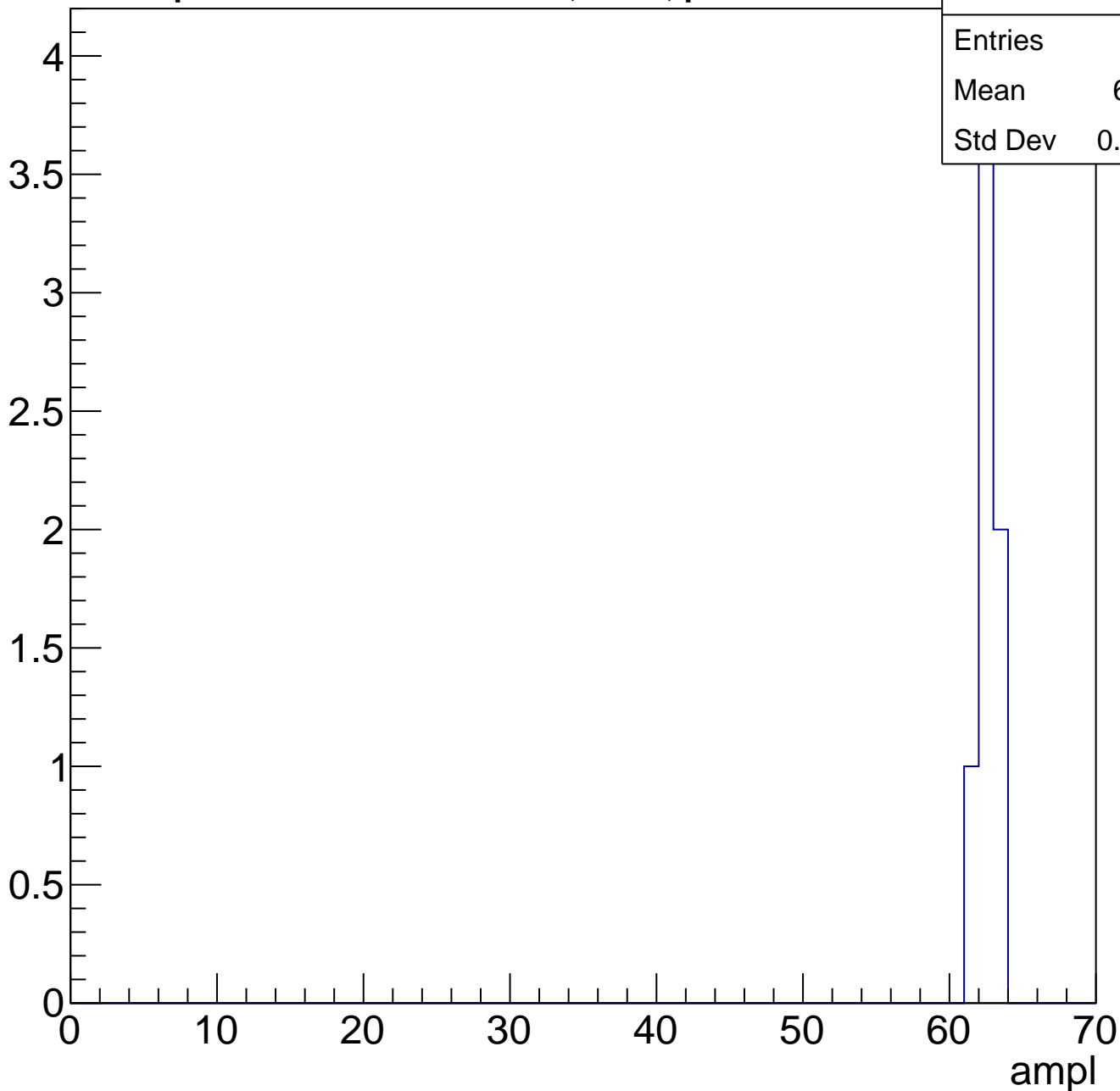
Entries	40
Mean	60.02
Std Dev	2.253



# B0L002S, U8-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch101, adc0

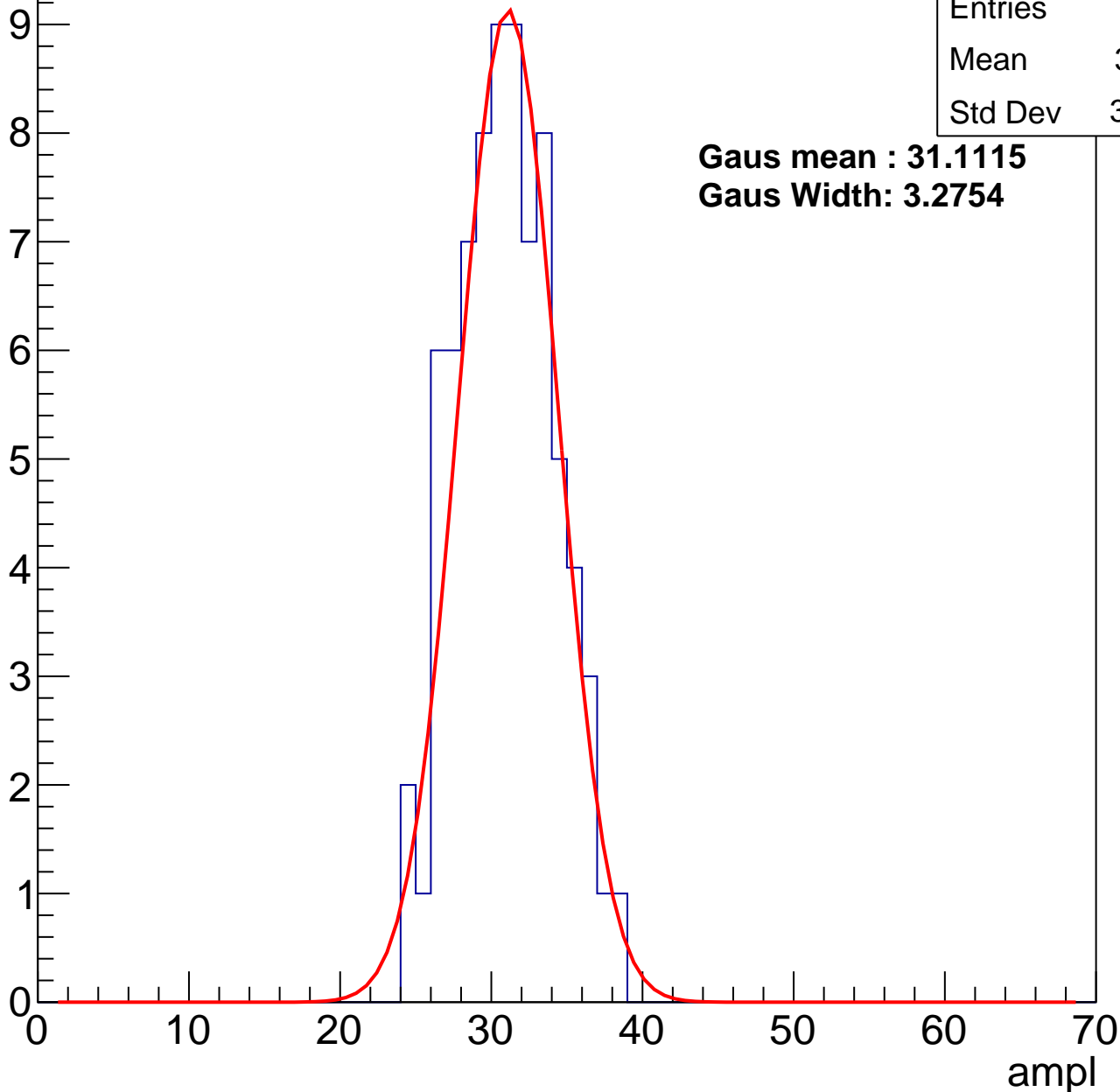
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.51
Std Dev	3.169

**Gaus mean : 31.1115**

**Gaus Width: 3.2754**



# B0L002S, U8-ch101, adc1

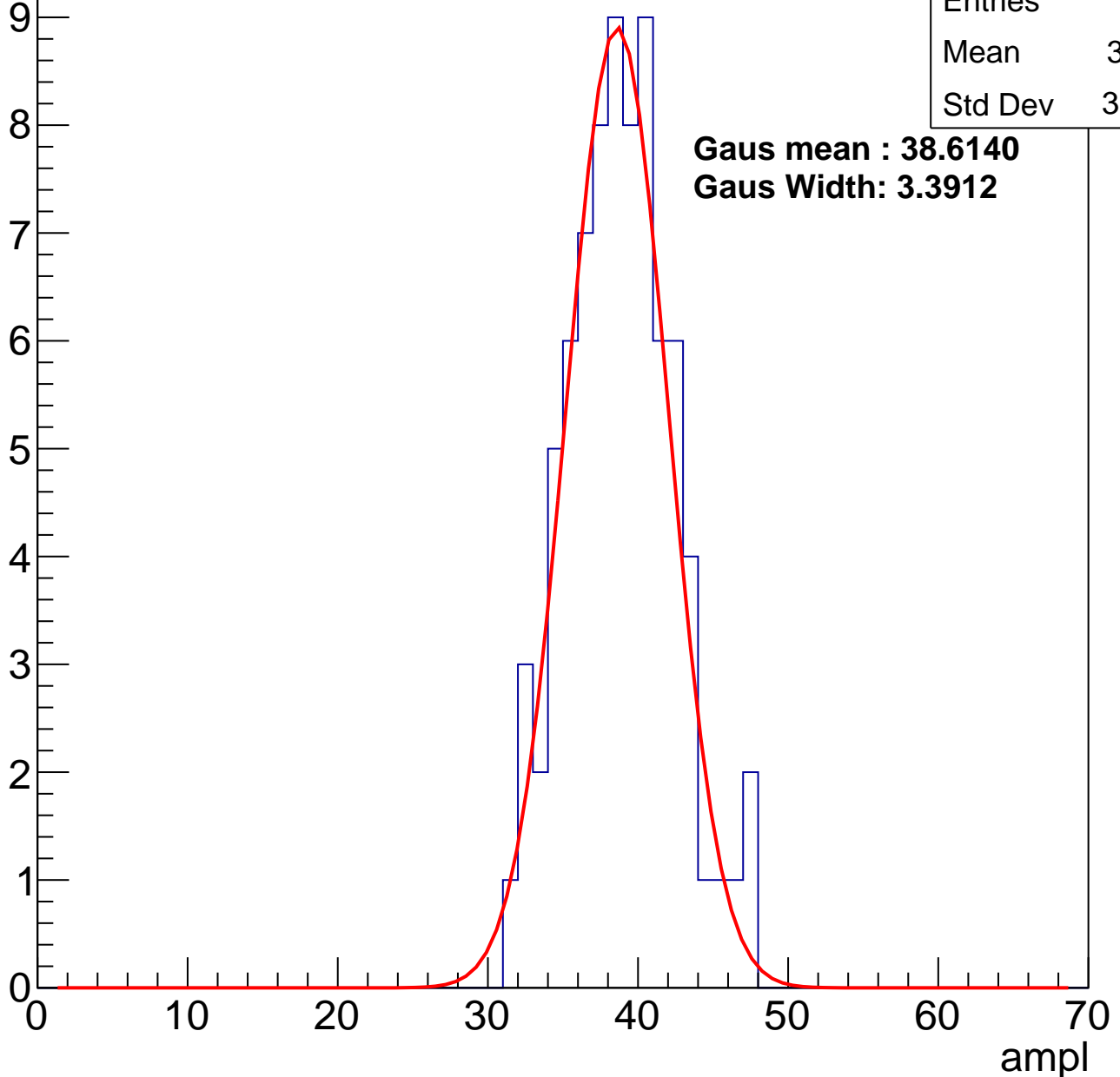
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.41
Std Dev	3.502

**Gaus mean : 38.6140**

**Gaus Width: 3.3912**



# B0L002S, U8-ch101, adc2

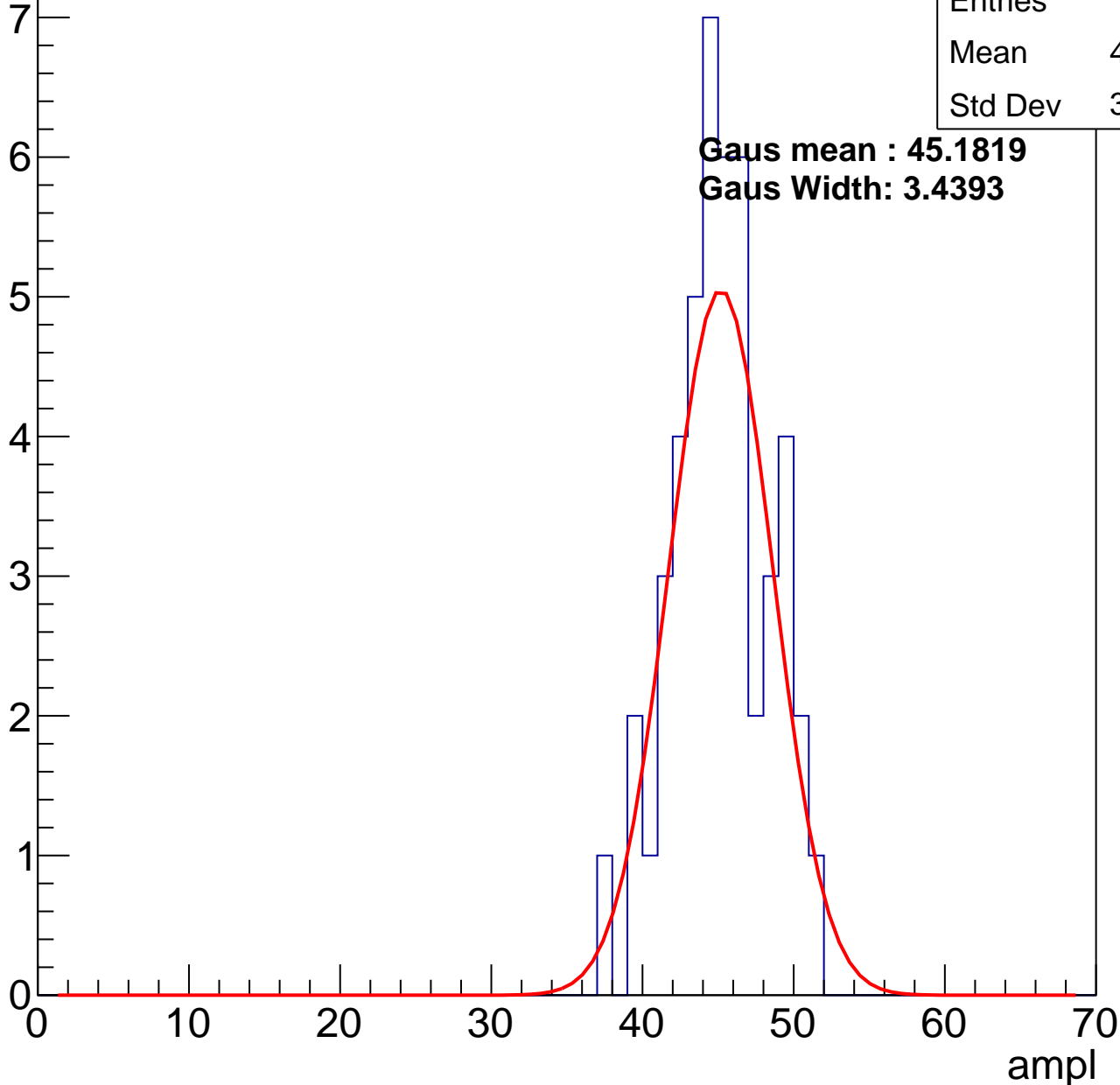
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.68
Std Dev	3.122

**Gaus mean : 45.1819**

**Gaus Width: 3.4393**

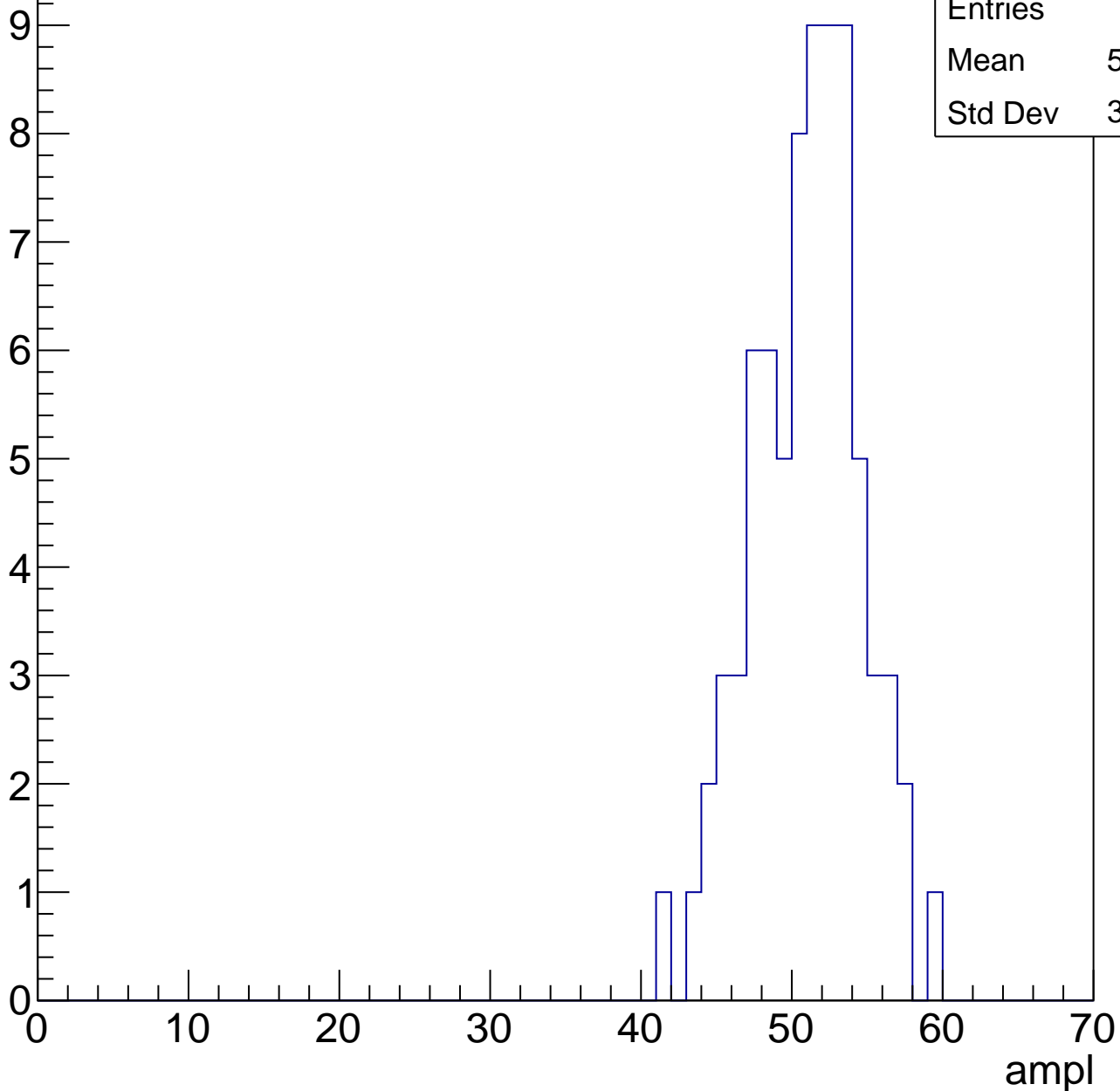


# B0L002S, U8-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	50.53
Std Dev	3.545

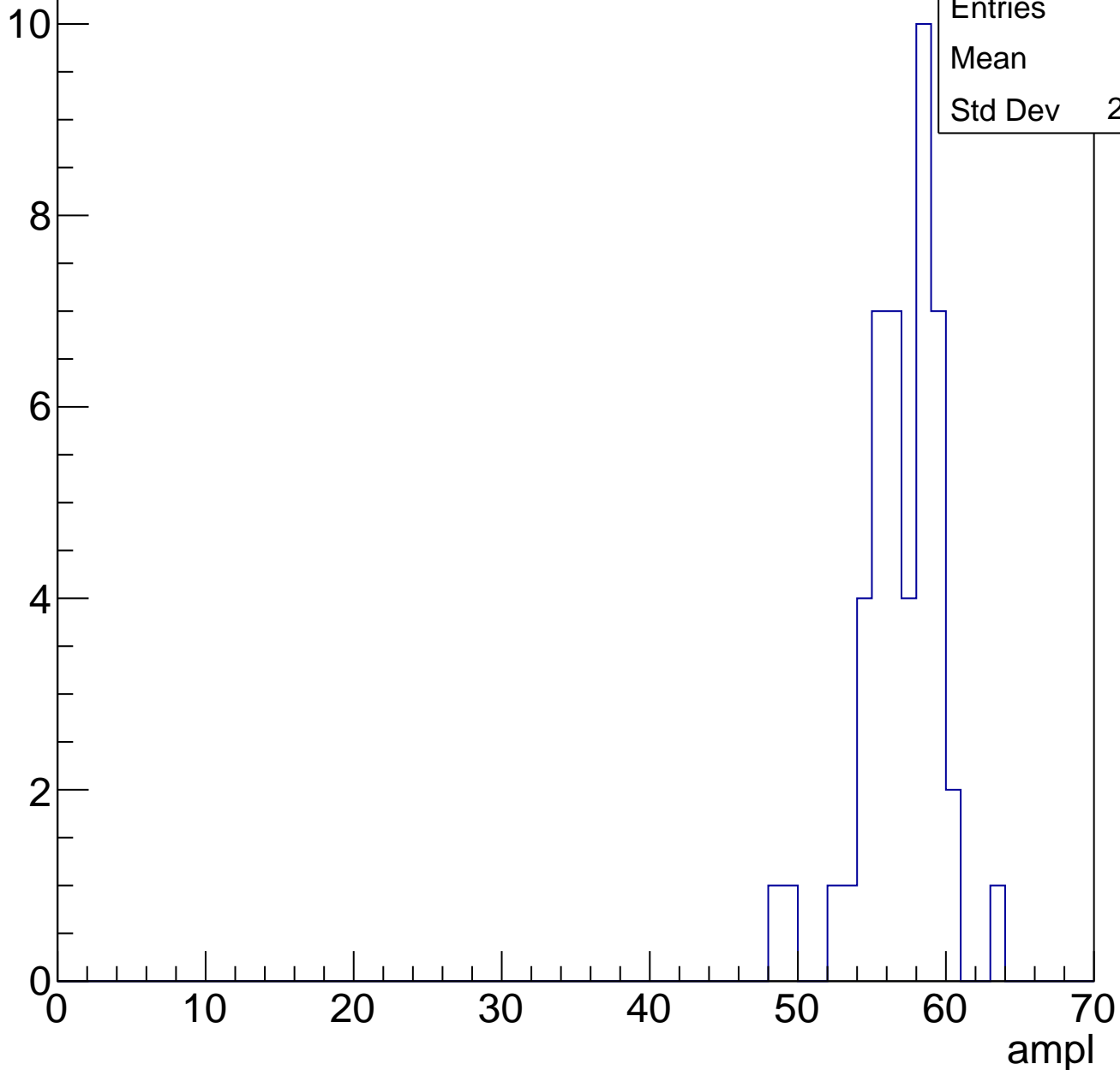


# B0L002S, U8-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	46
Mean	56.5
Std Dev	2.709

Entry



# B0L002S, U8-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	45
Mean	59.49
Std Dev	9.181

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

# B0L002S, U8-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch102, adc0

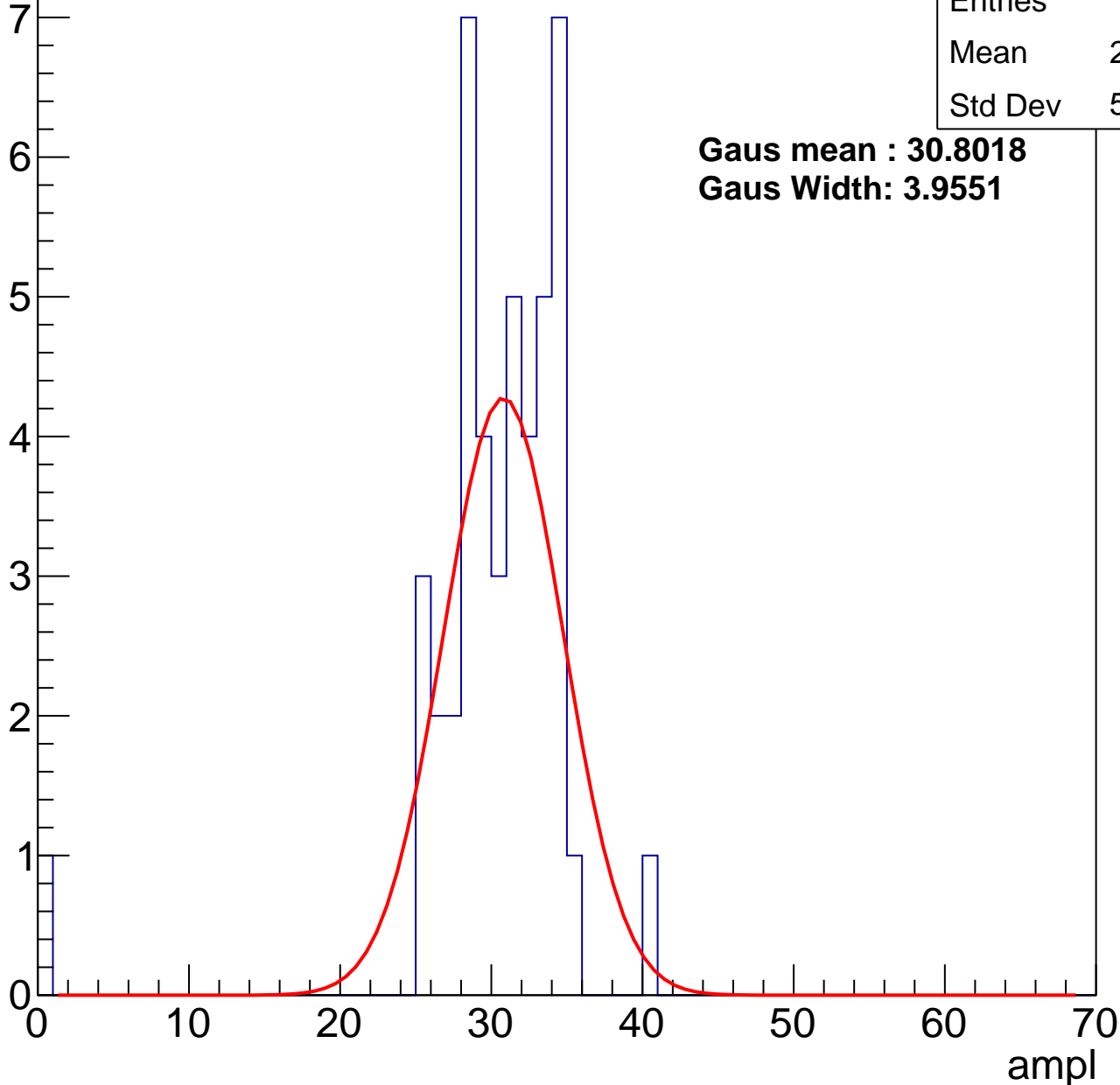
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	29.87
Std Dev	5.496

**Gaus mean : 30.8018**

**Gaus Width: 3.9551**



# B0L002S, U8-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.88
Std Dev	3.334

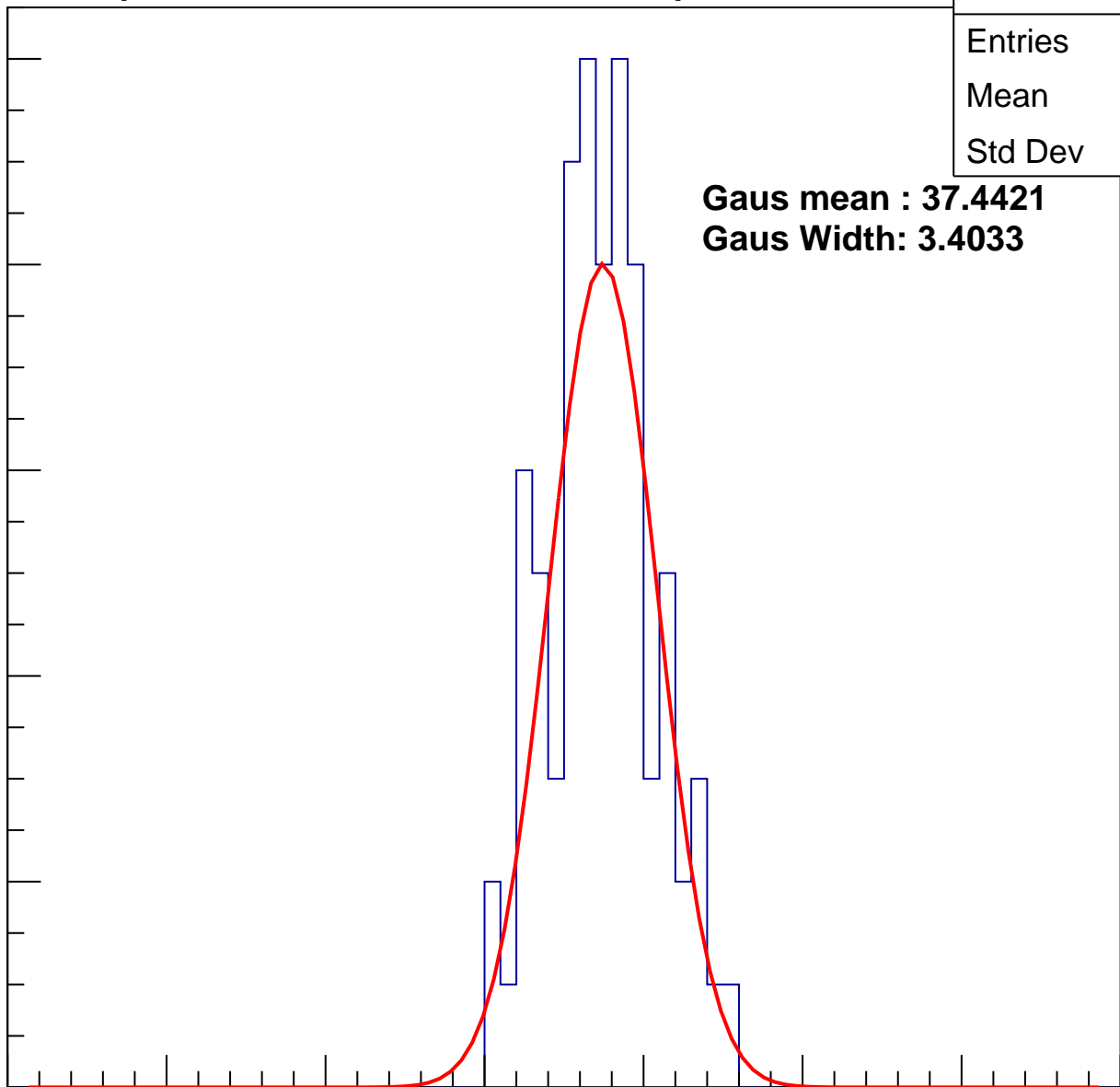
**Gaus mean : 37.4421**

**Gaus Width: 3.4033**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch102, adc2

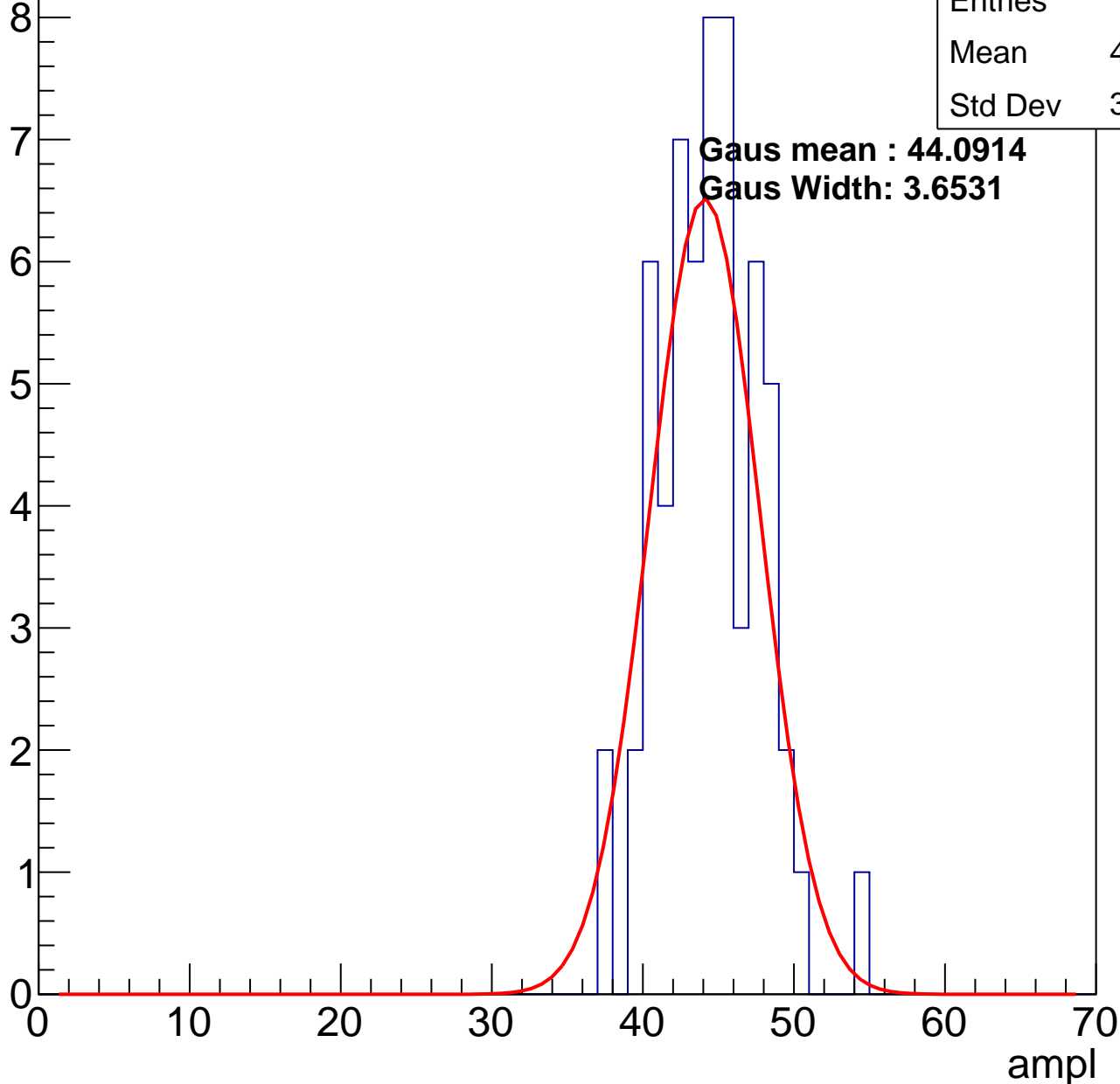
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.97
Std Dev	3.274

**Gaus mean : 44.0914**

**Gaus Width: 3.6531**

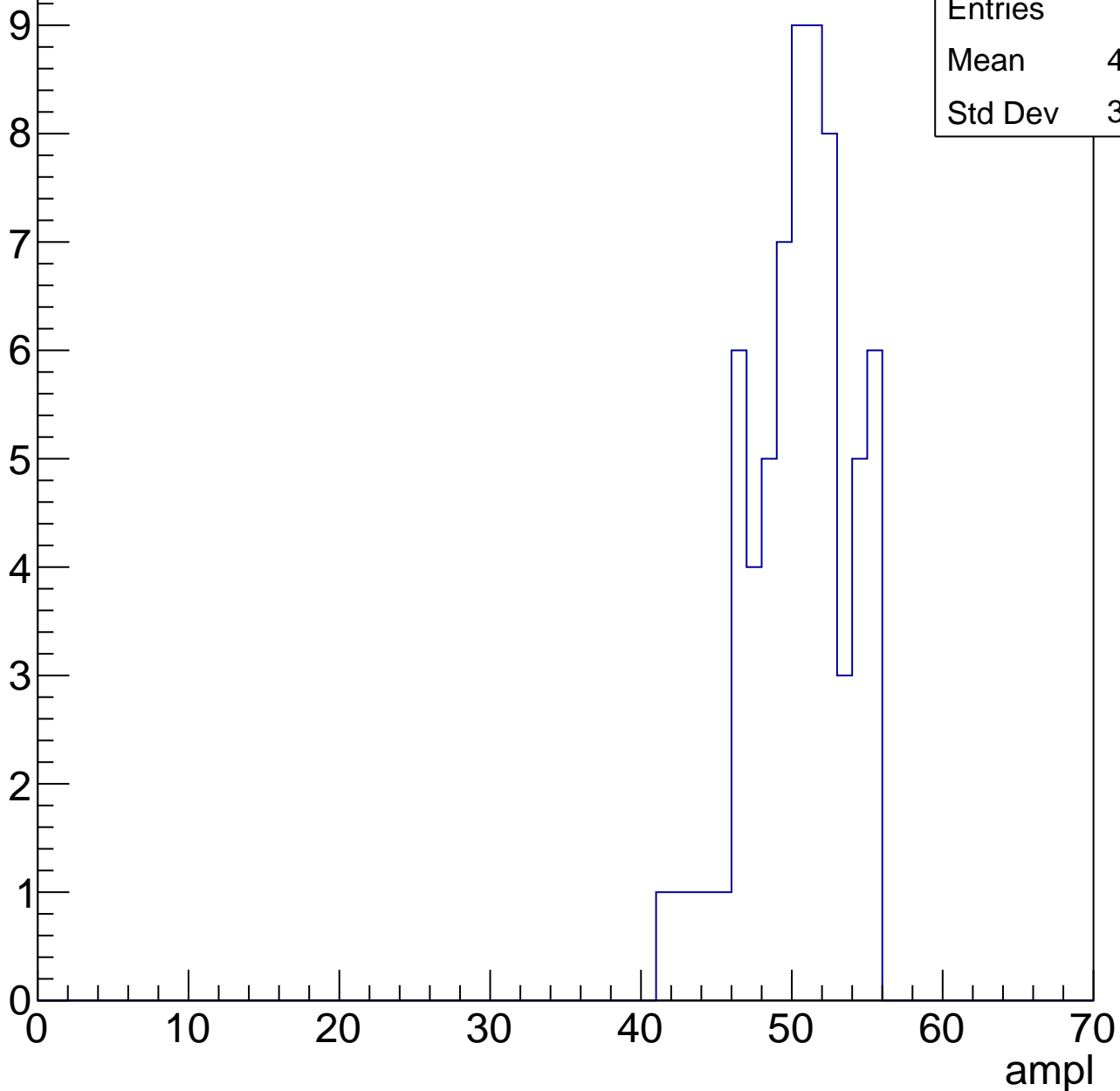


# B0L002S, U8-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	49.94
Std Dev	3.259

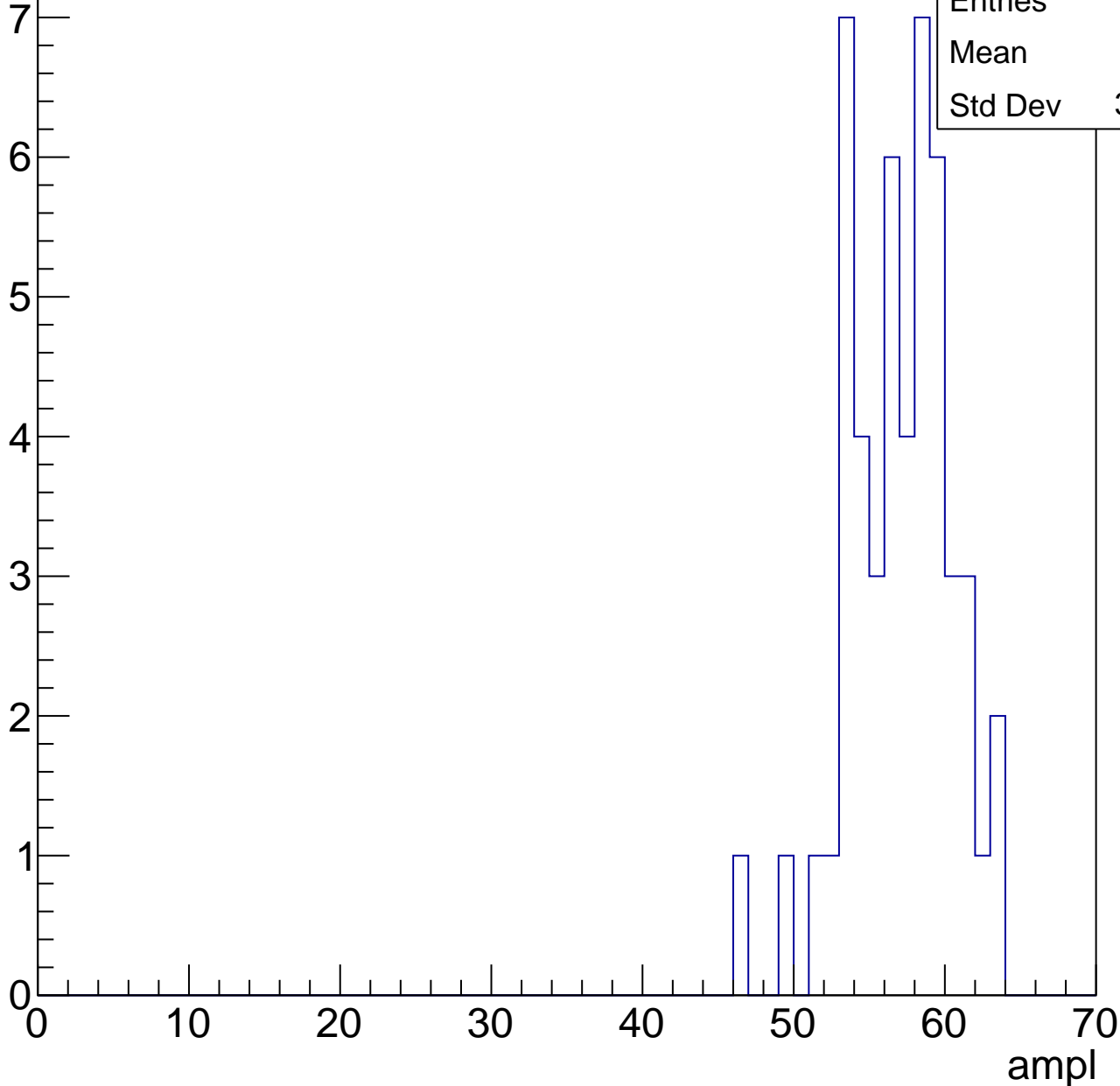


# B0L002S, U8-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

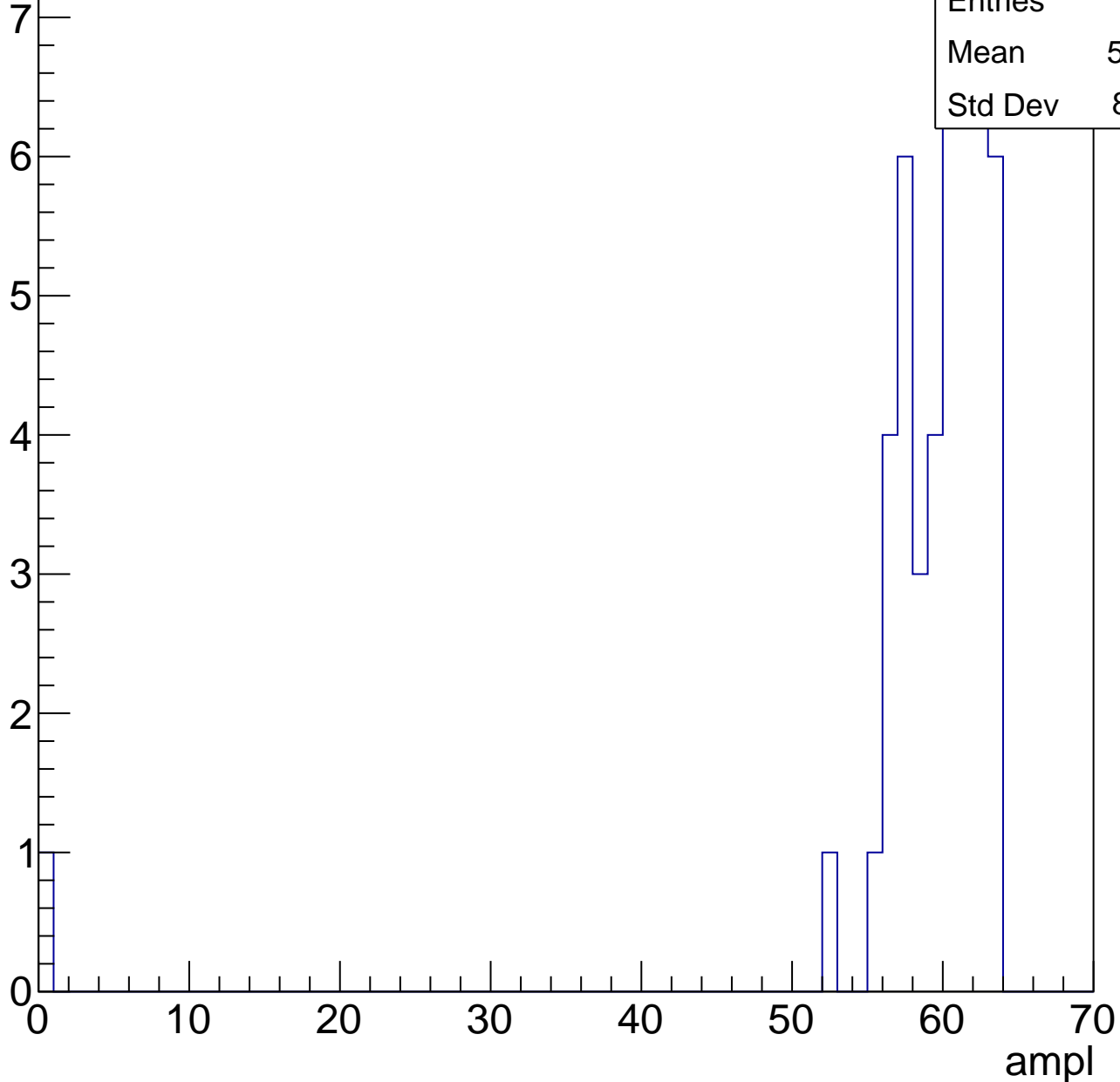
Entries	50
Mean	56.5
Std Dev	3.471



# B0L002S, U8-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U8-ch103, adc0

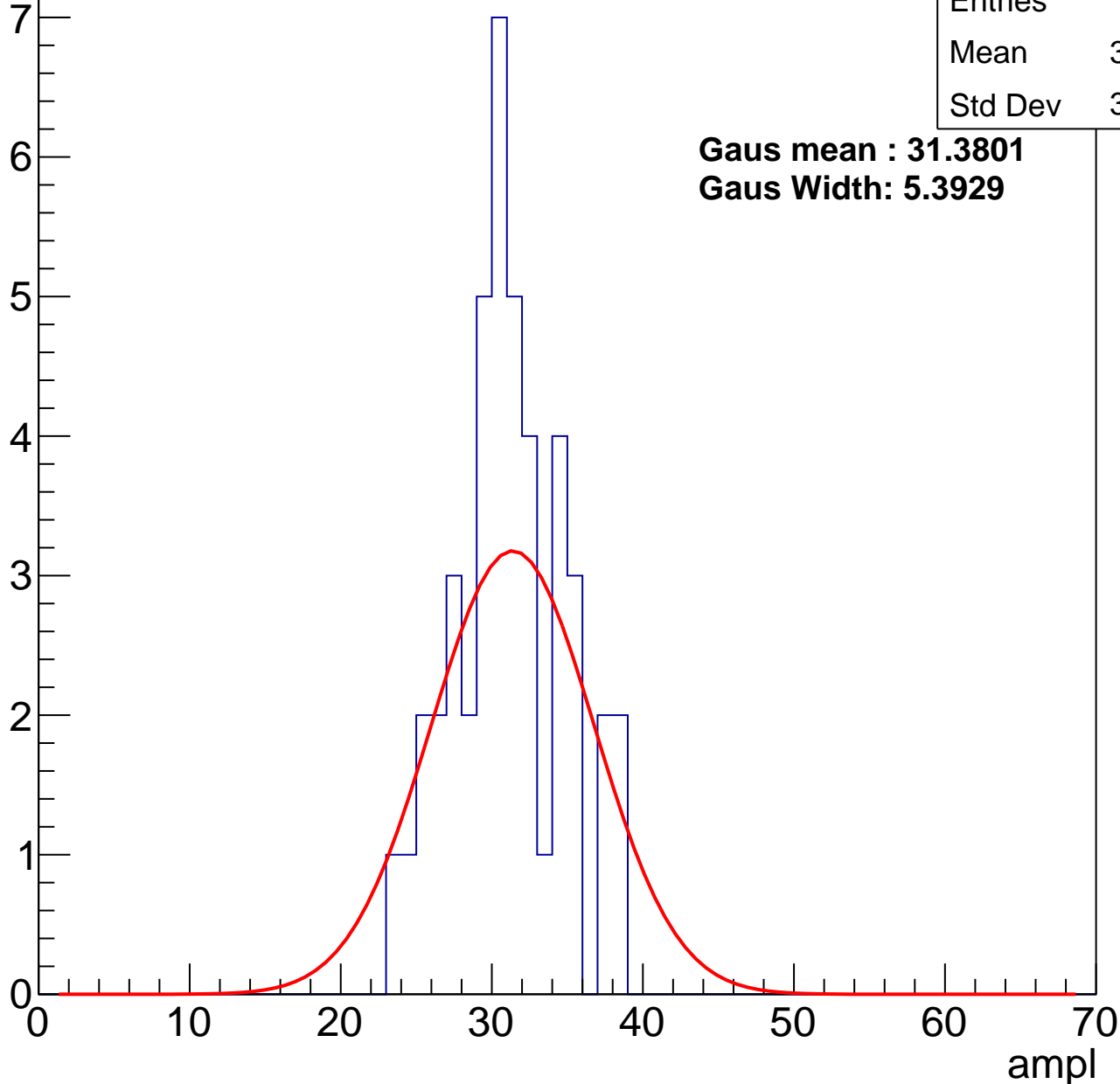
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	30.64
Std Dev	3.644

**Gaus mean : 31.3801**

**Gaus Width: 5.3929**



# B0L002S, U8-ch103, adc1

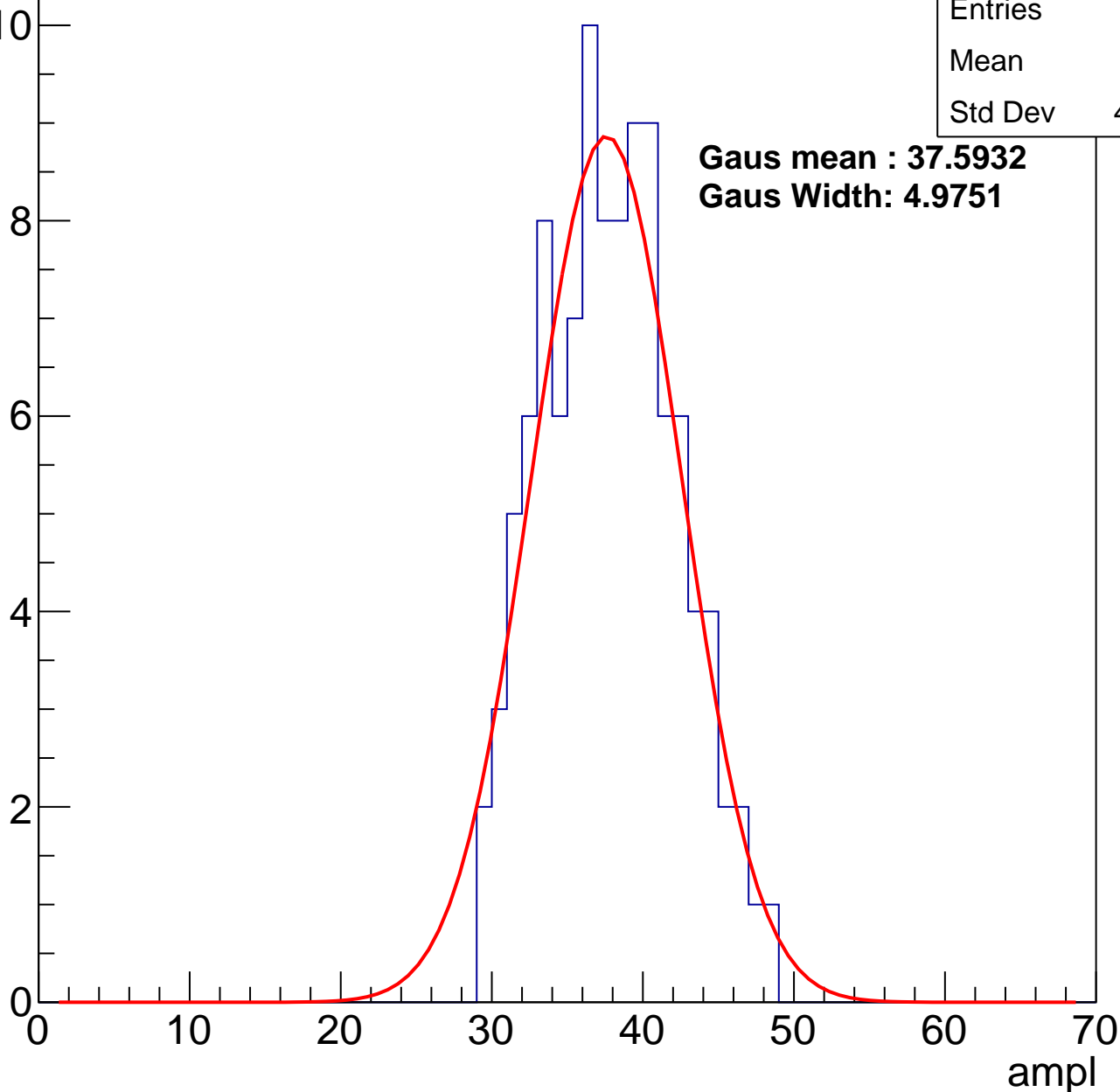
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	107
Mean	37.4
Std Dev	4.355

**Gaus mean : 37.5932**

**Gaus Width: 4.9751**



# B0L002S, U8-ch103, adc2

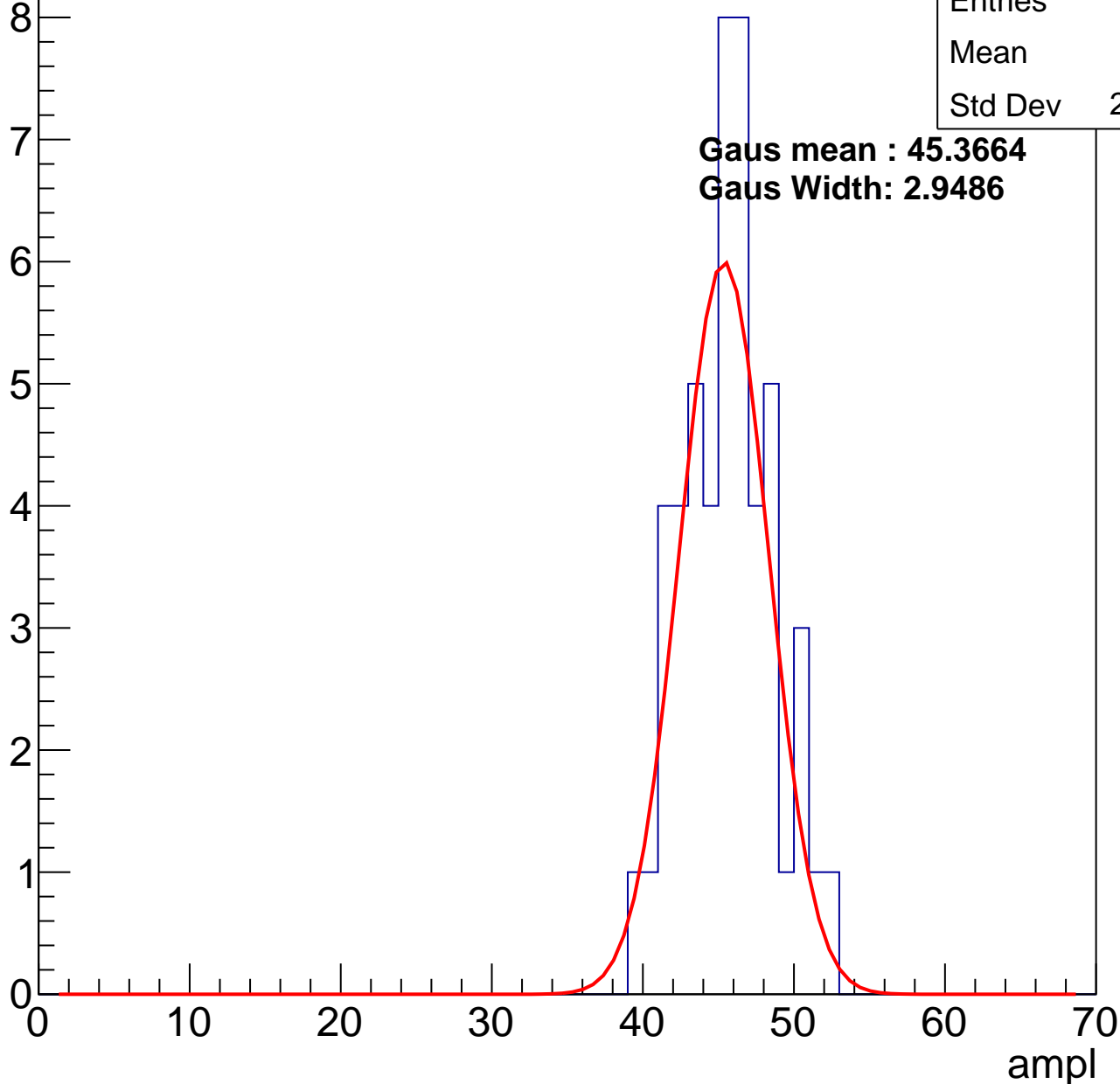
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	45.2
Std Dev	2.926

**Gaus mean : 45.3664**

**Gaus Width: 2.9486**

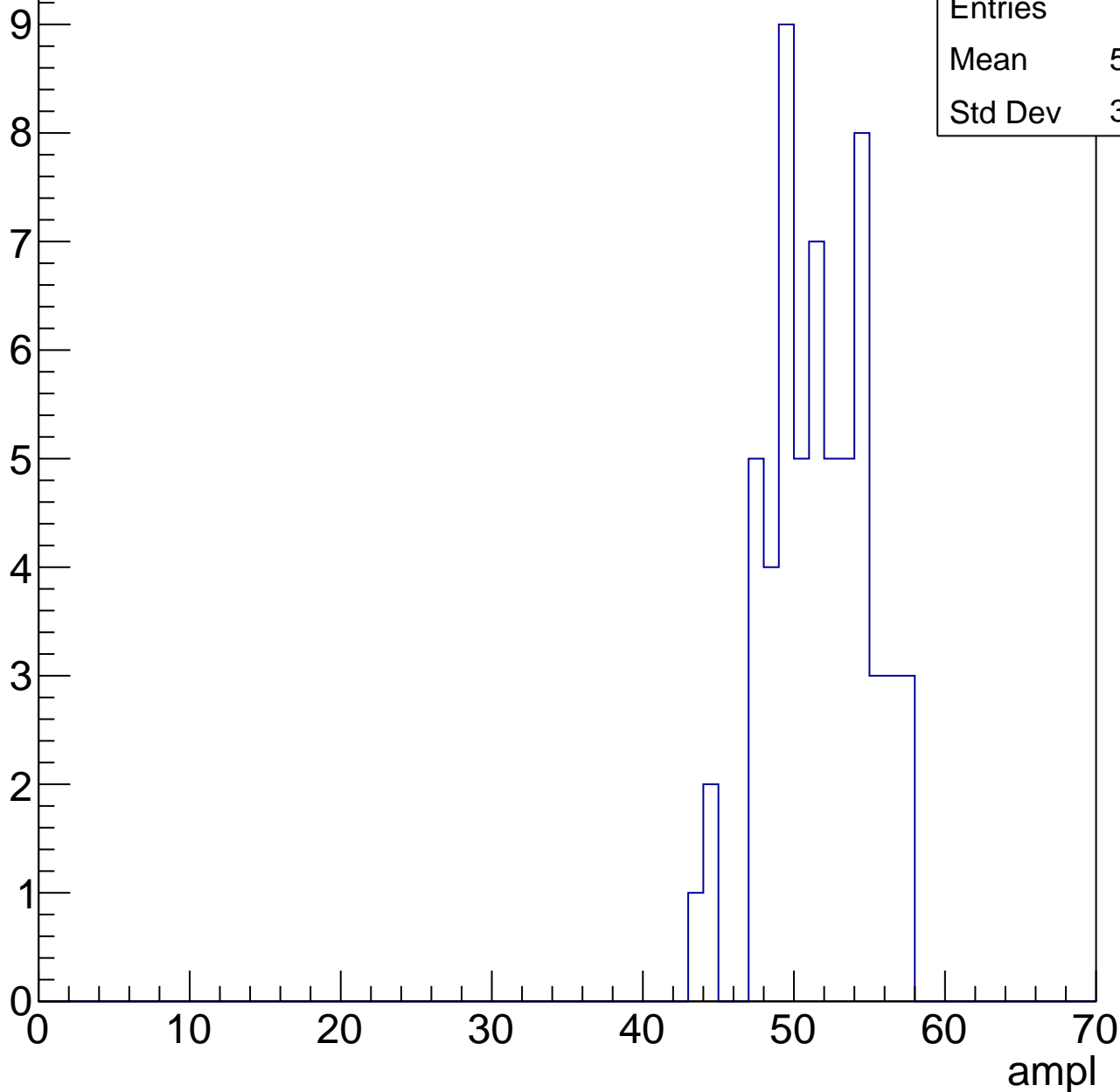


# B0L002S, U8-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51.12
Std Dev	3.282



# B0L002S, U8-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	56.3
Std Dev	7.357

Entry

10

8

6

4

2

0

0

10

20

30

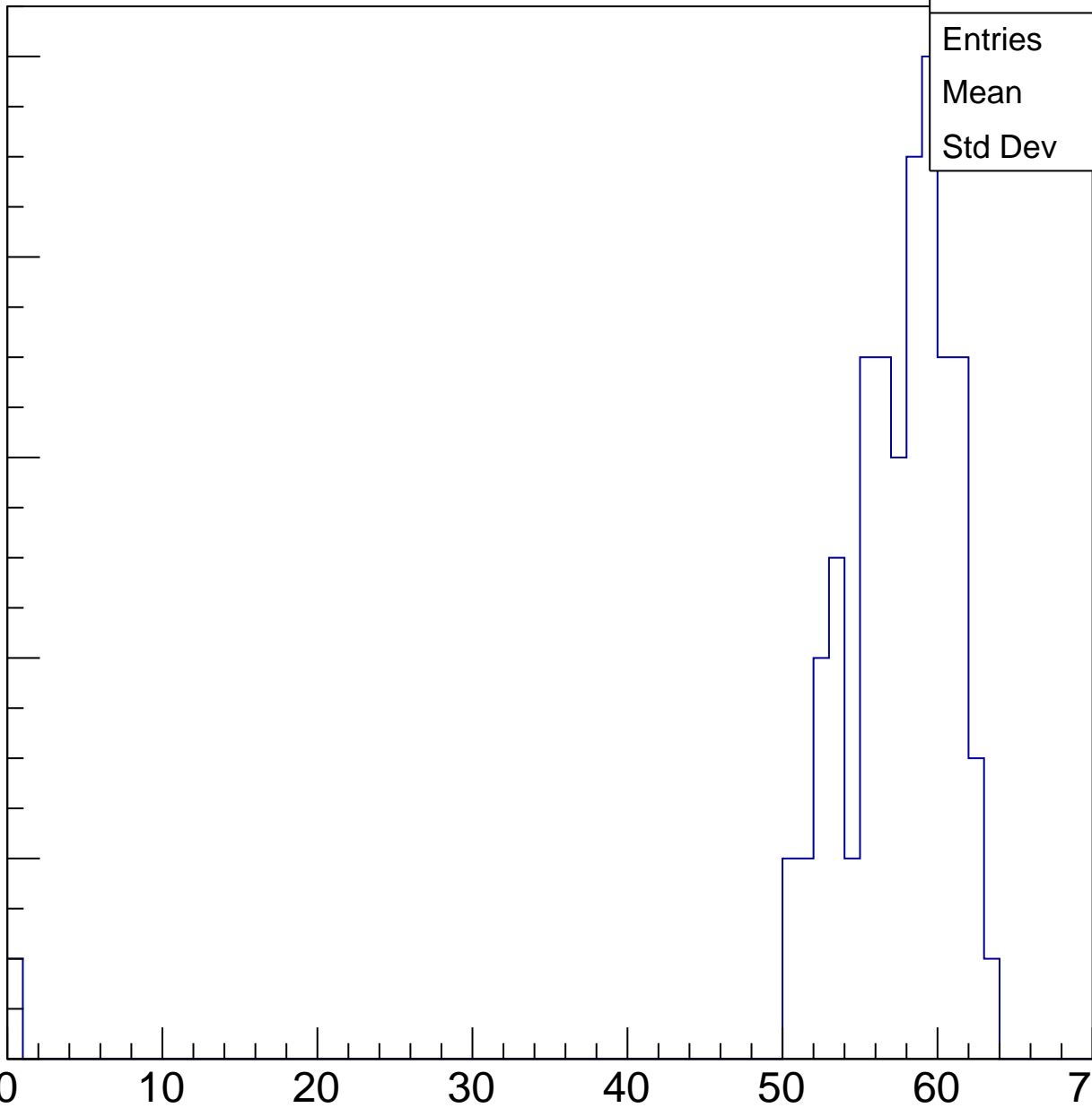
40

50

60

70

ampl

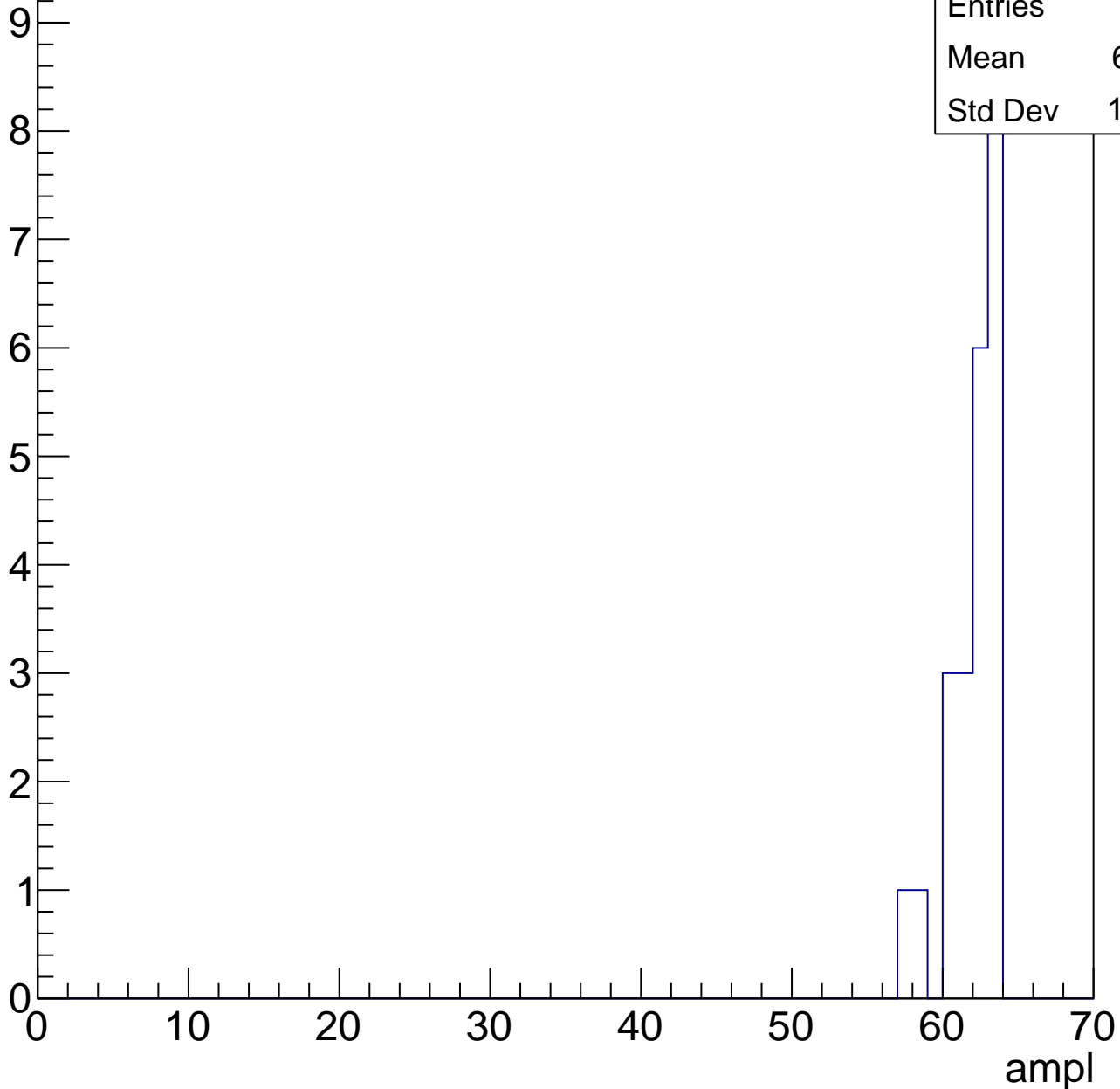


# B0L002S, U8-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	23
Mean	61.61
Std Dev	1.635



# B0L002S, U8-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch104, adc0

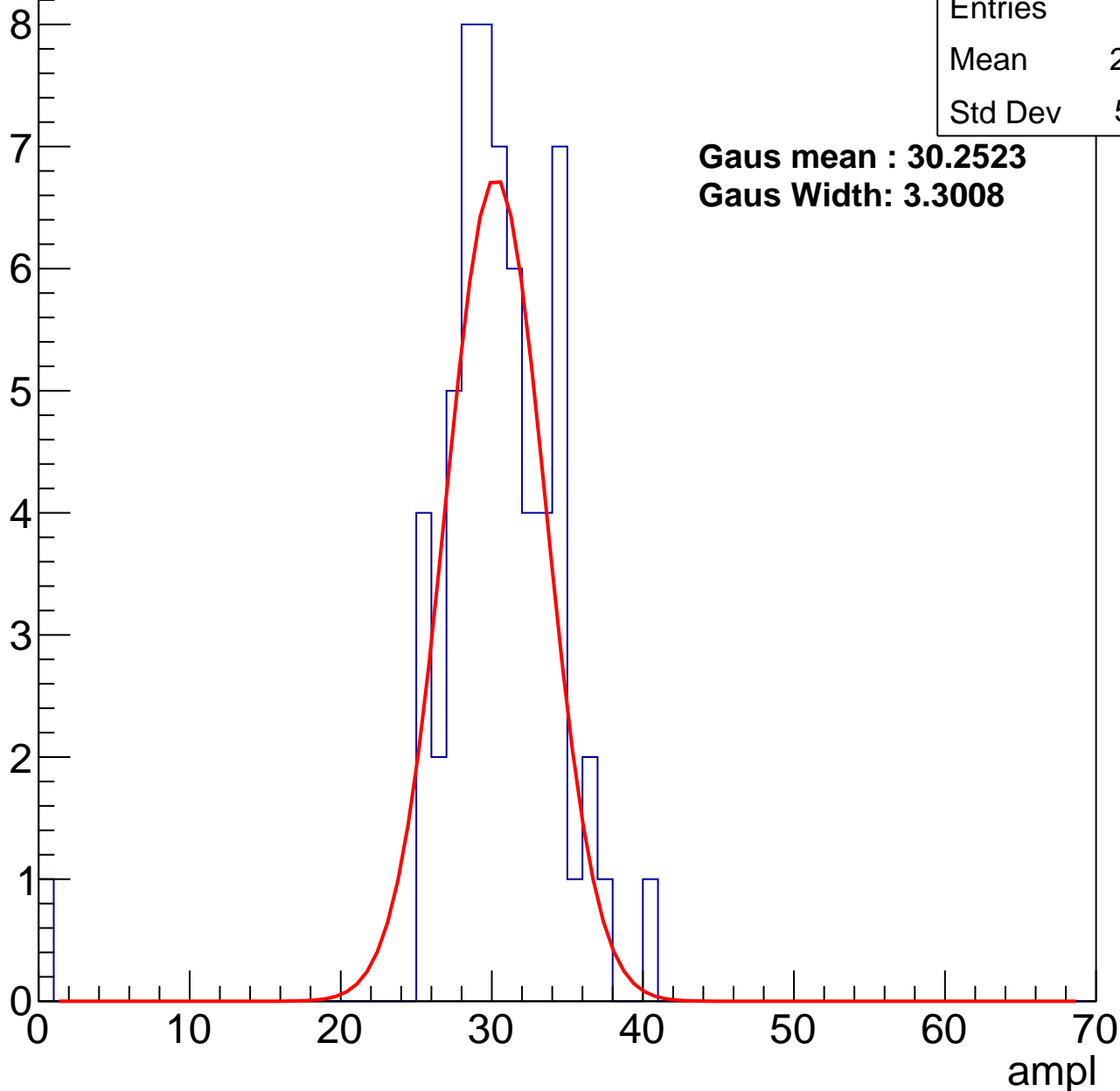
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.85
Std Dev	5.011

**Gaus mean : 30.2523**

**Gaus Width: 3.3008**



# B0L002S, U8-ch104, adc1

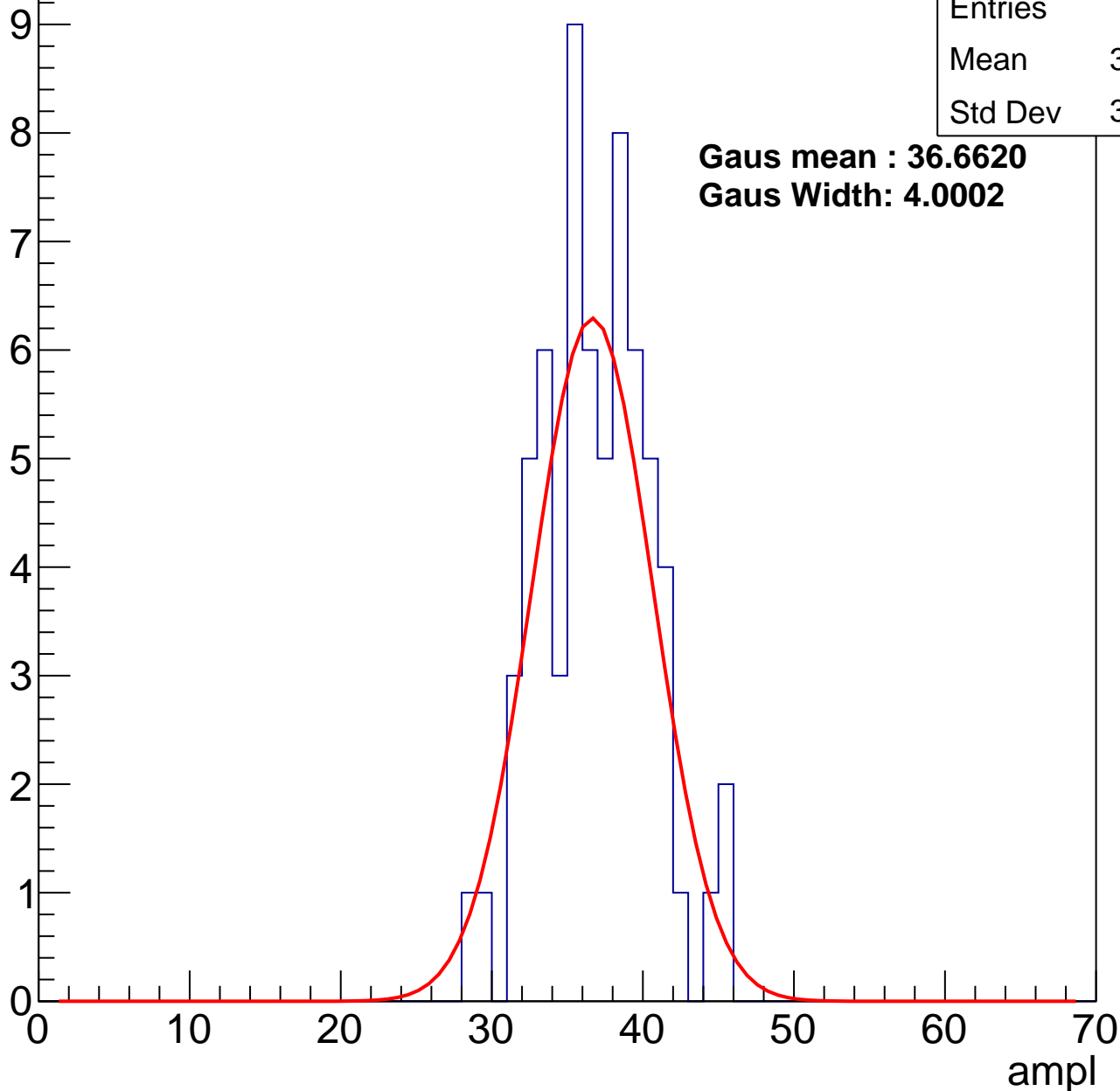
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.42
Std Dev	3.618

**Gaus mean : 36.6620**

**Gaus Width: 4.0002**



# B0L002S, U8-ch104, adc2

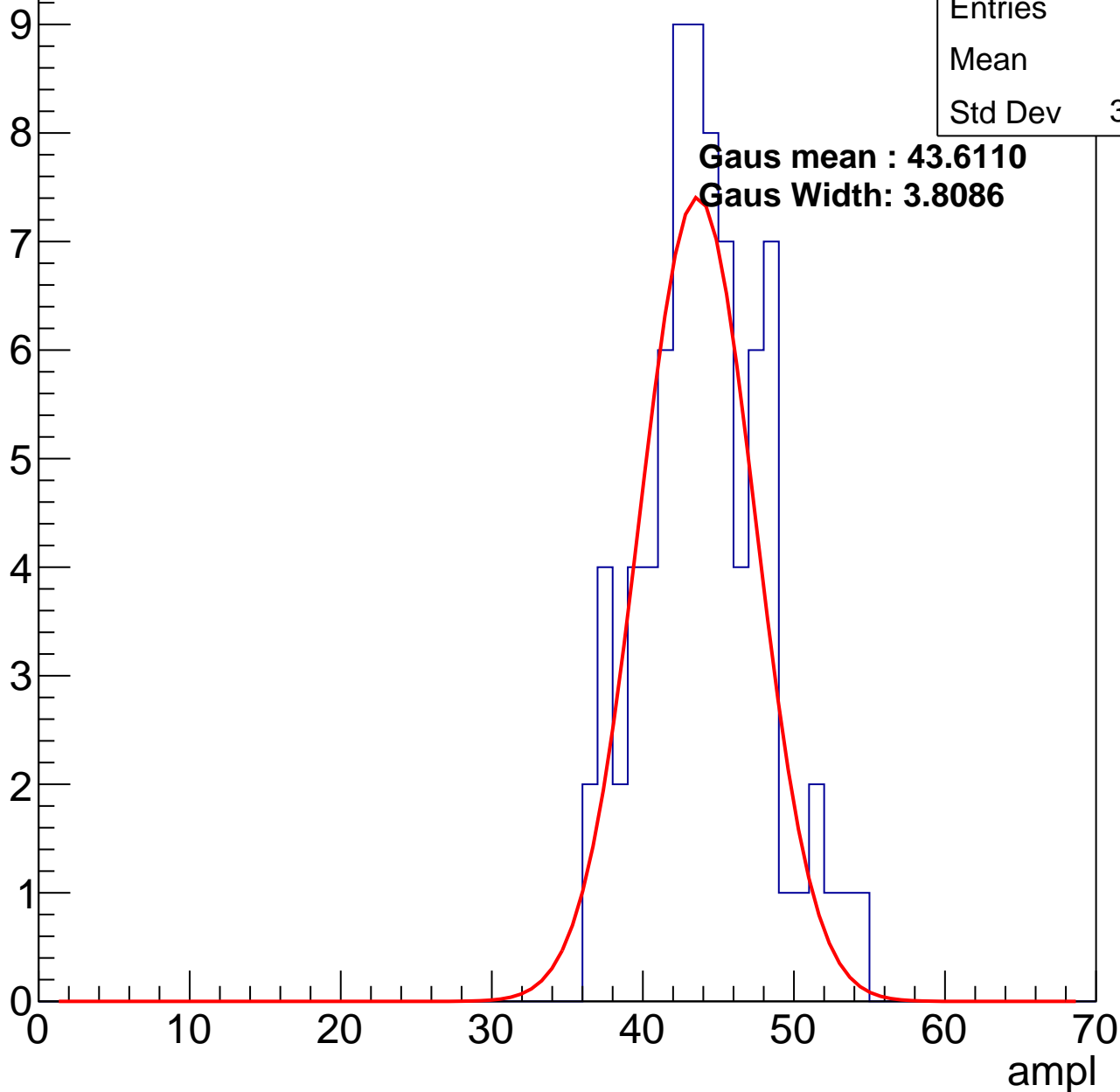
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	43.7
Std Dev	3.969

**Gaus mean : 43.6110**

**Gaus Width: 3.8086**

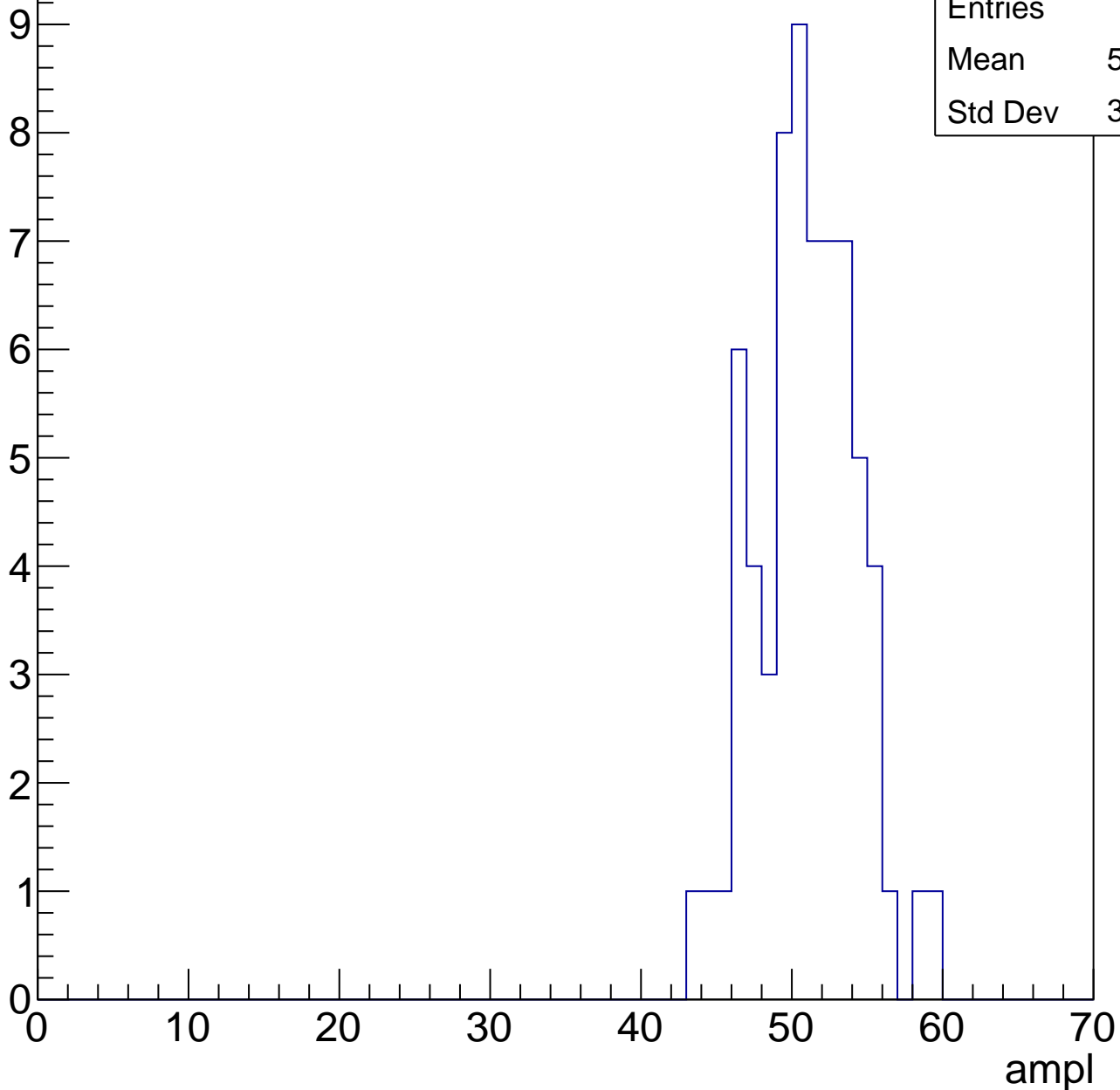


# B0L002S, U8-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.56
Std Dev	3.257

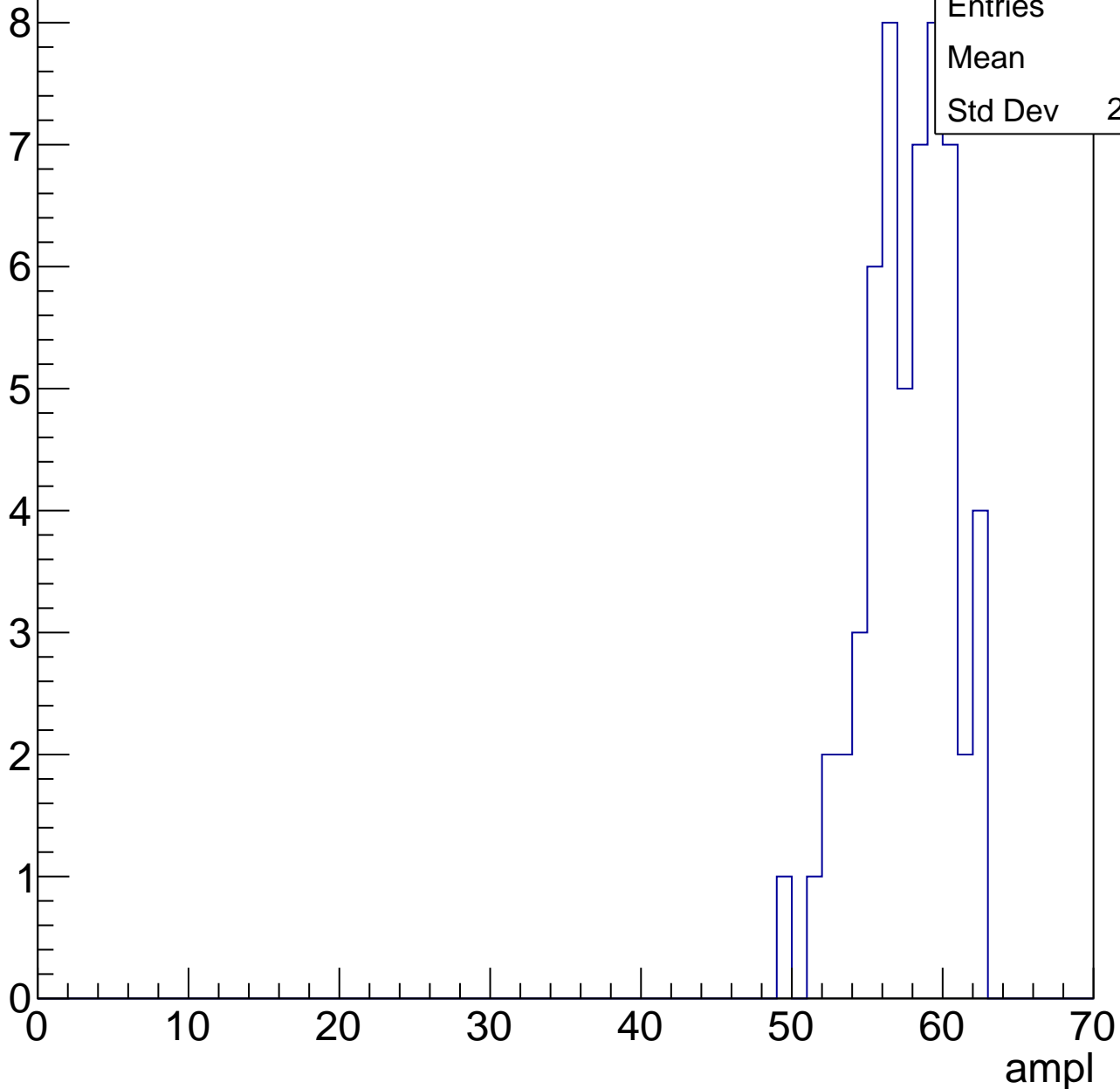


# B0L002S, U8-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.2
Std Dev	2.906

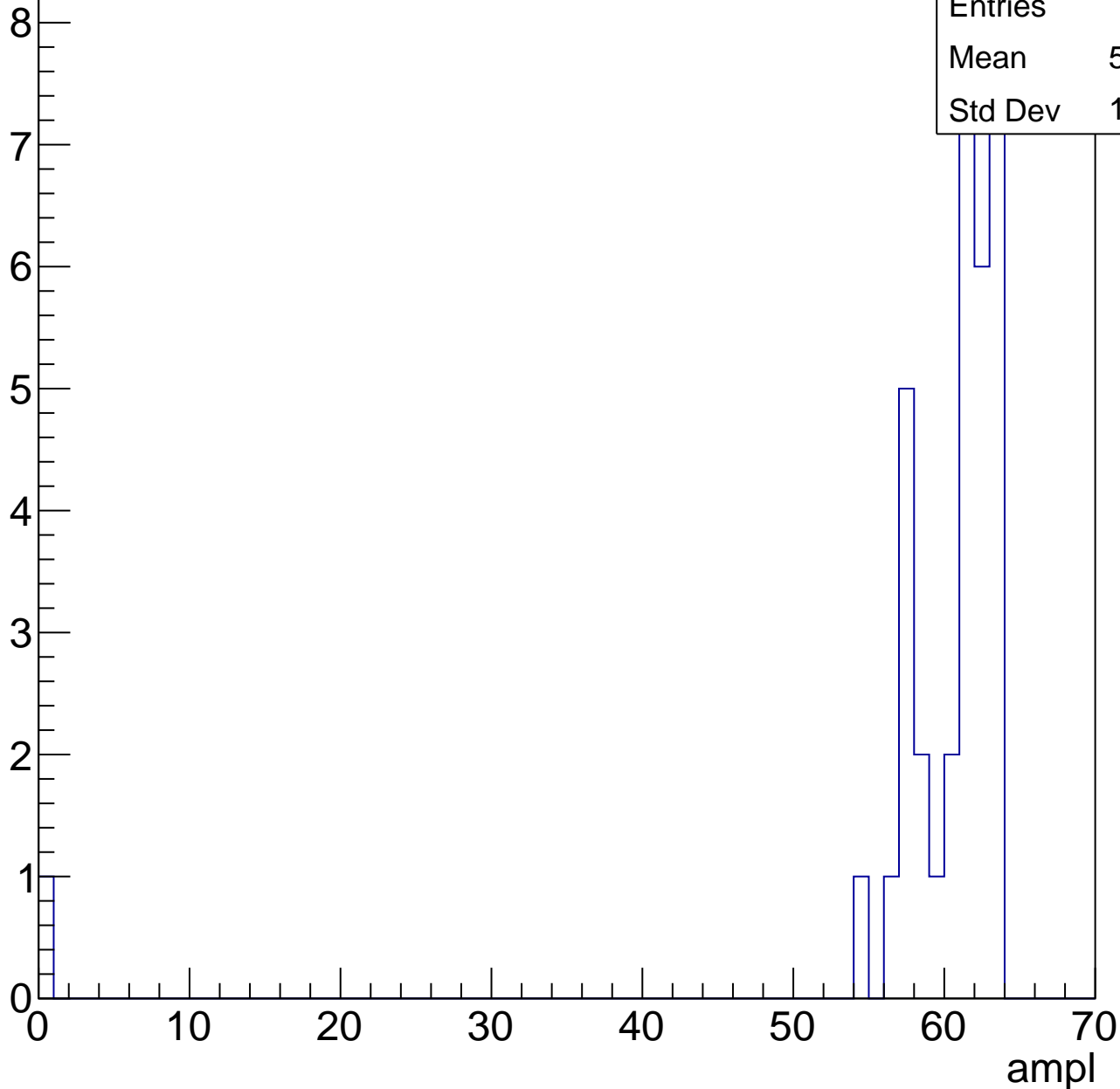


# B0L002S, U8-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.69
Std Dev	10.35



# B0L002S, U8-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl

# B0L002S, U8-ch105, adc0

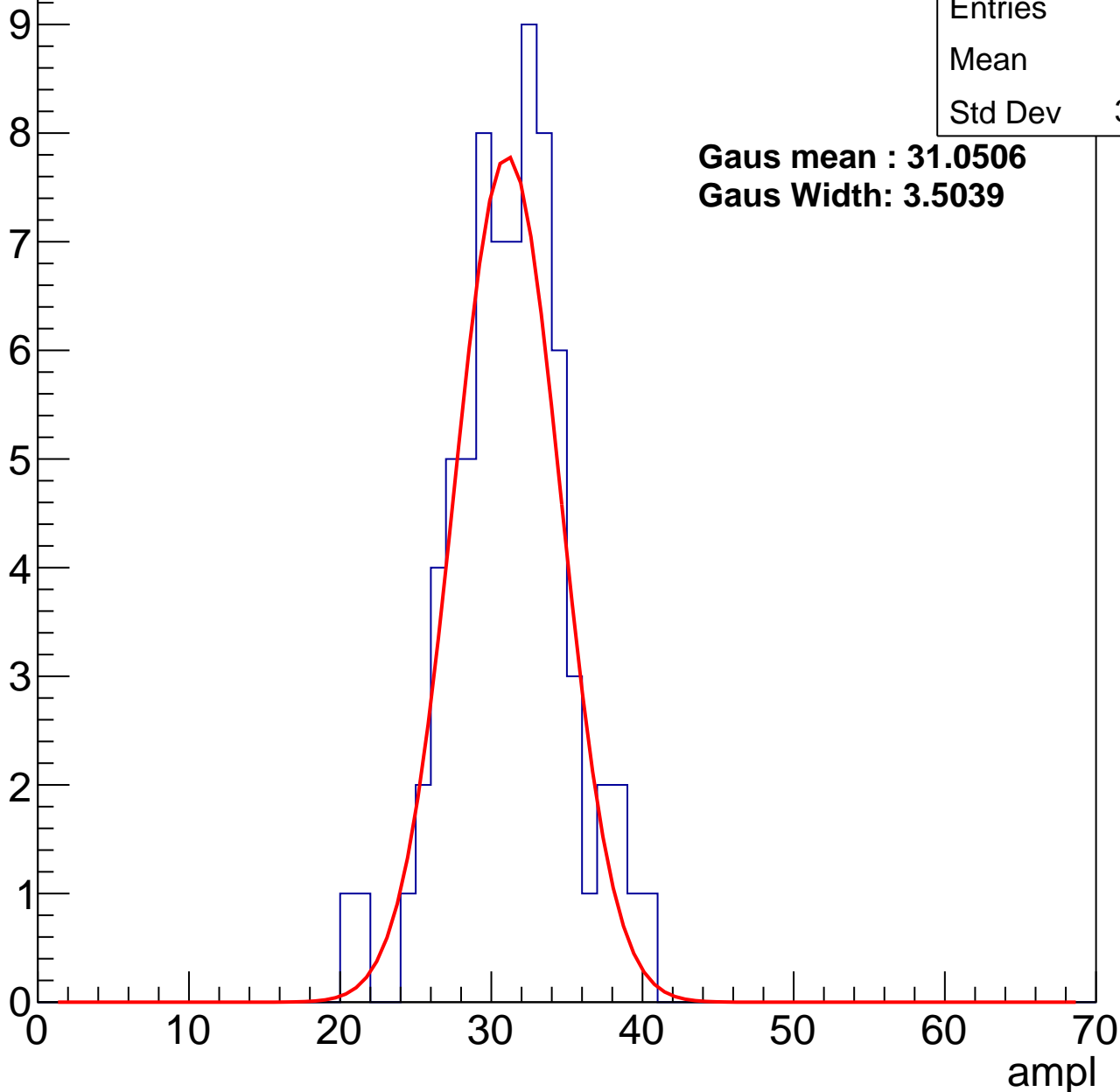
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.8
Std Dev	3.831

**Gaus mean : 31.0506**

**Gaus Width: 3.5039**



# B0L002S, U8-ch105, adc1

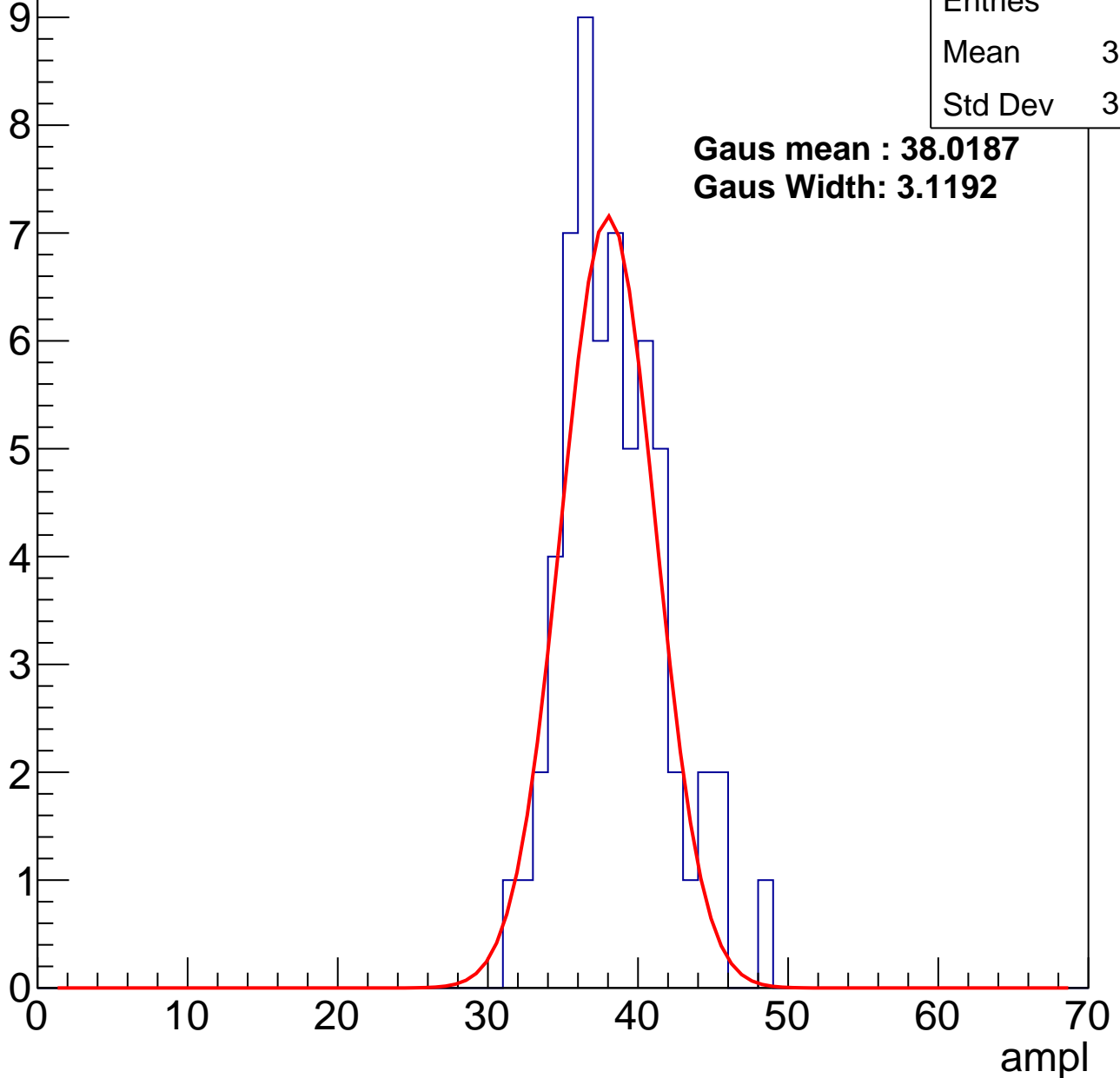
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.95
Std Dev	3.399

**Gaus mean : 38.0187**

**Gaus Width: 3.1192**



# B0L002S, U8-ch105, adc2

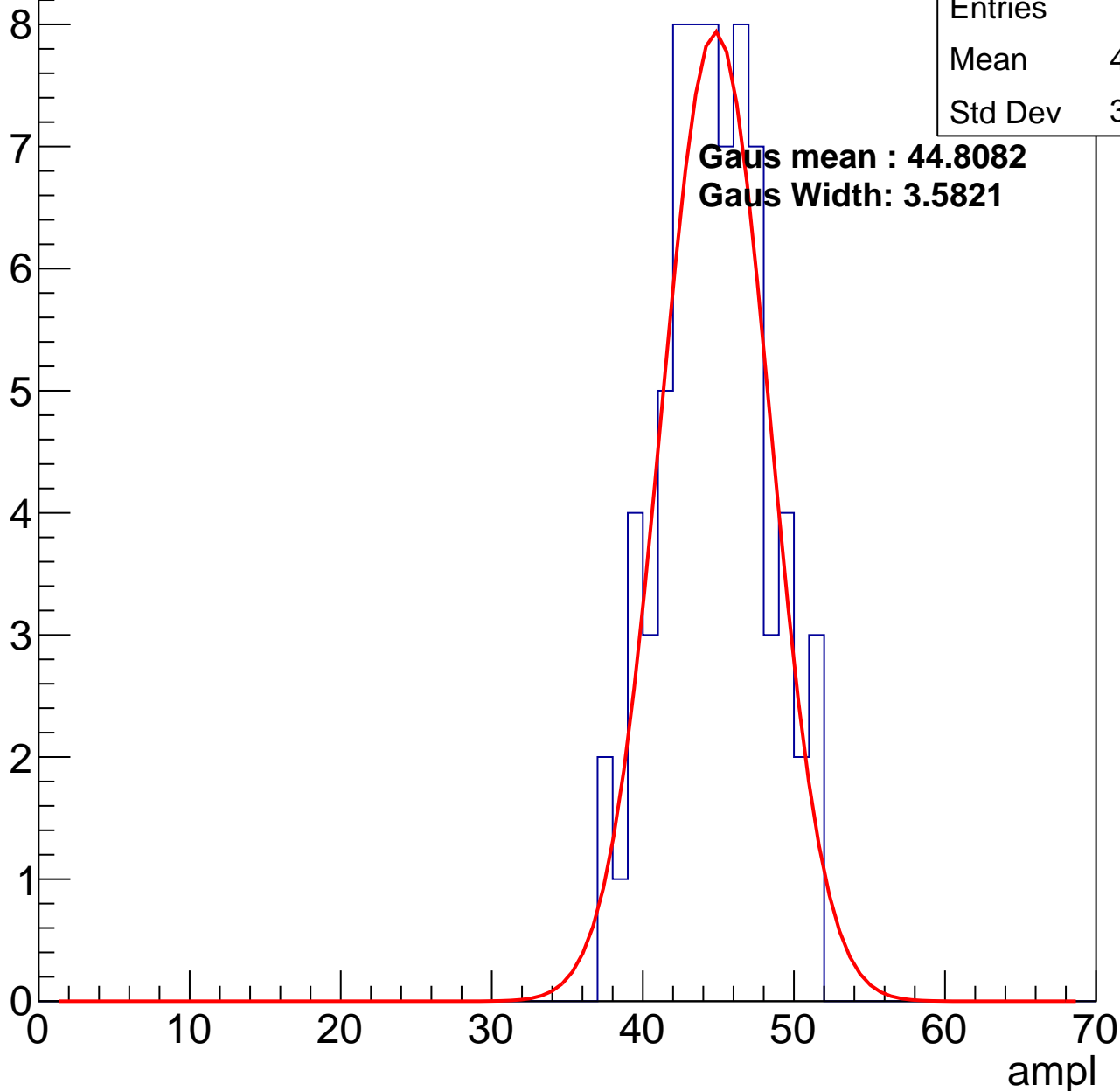
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.25
Std Dev	3.375

**Gaus mean : 44.8082**

**Gaus Width: 3.5821**

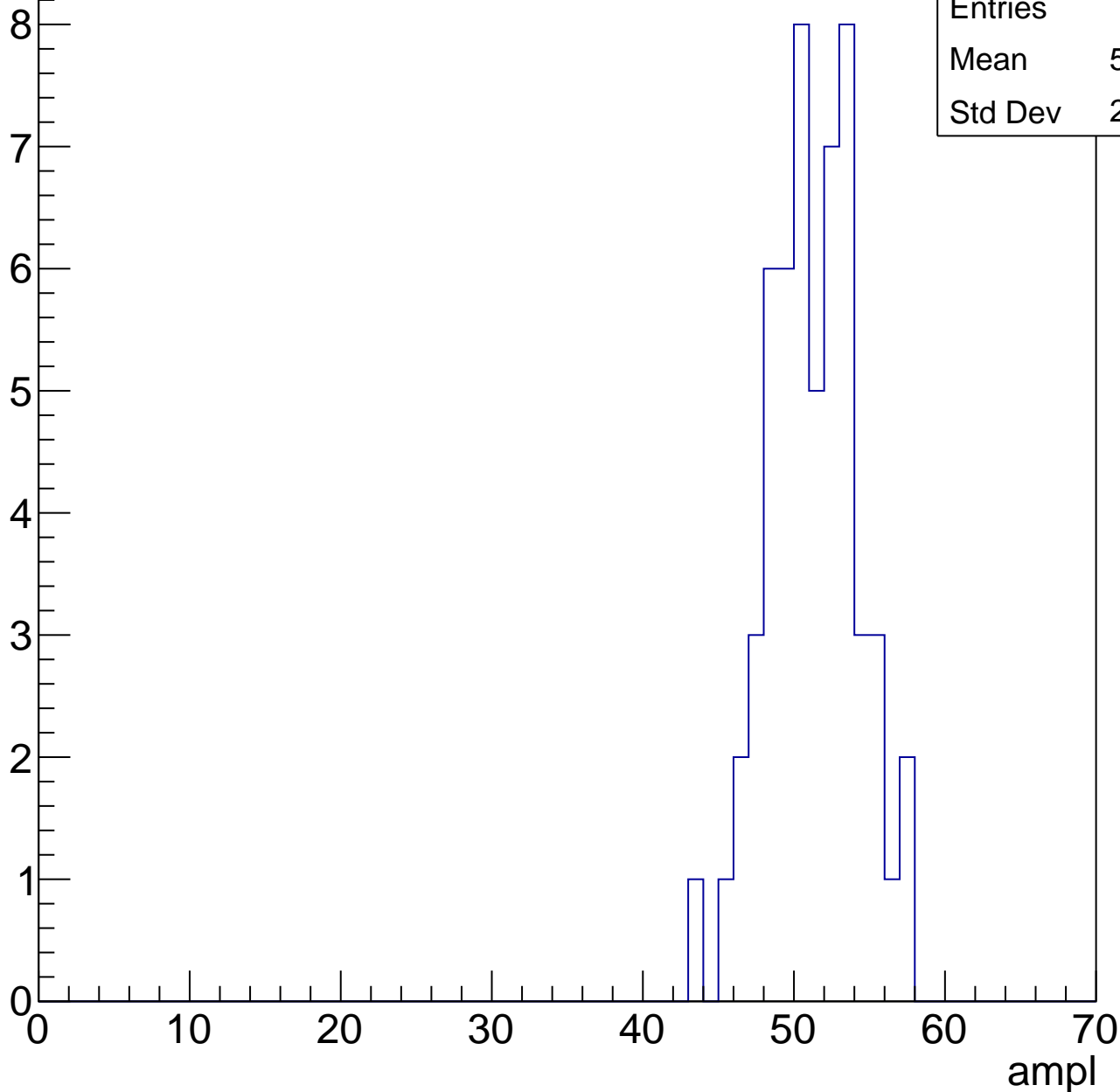


# B0L002S, U8-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

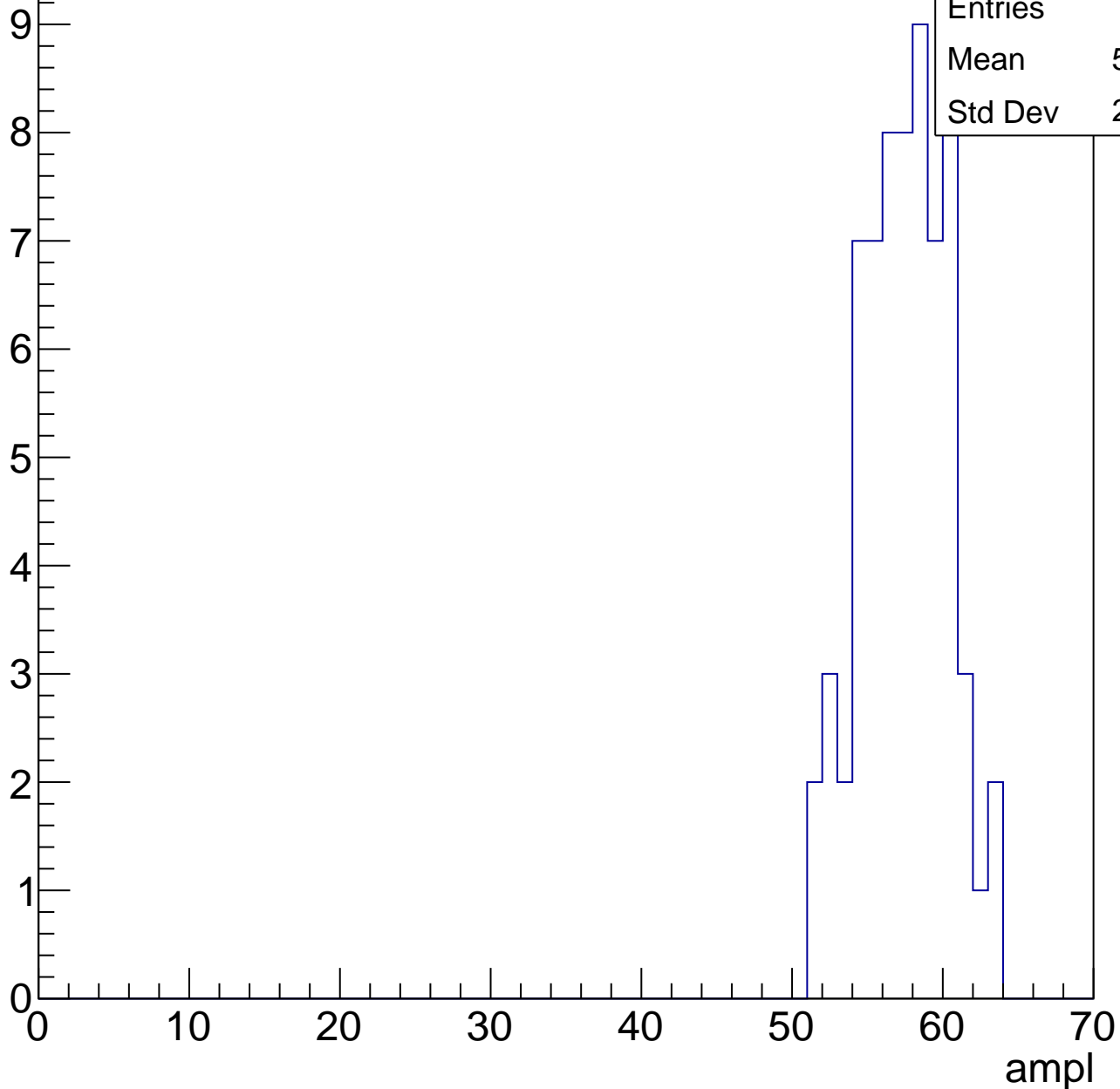
Entries	56
Mean	50.77
Std Dev	2.964



# B0L002S, U8-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

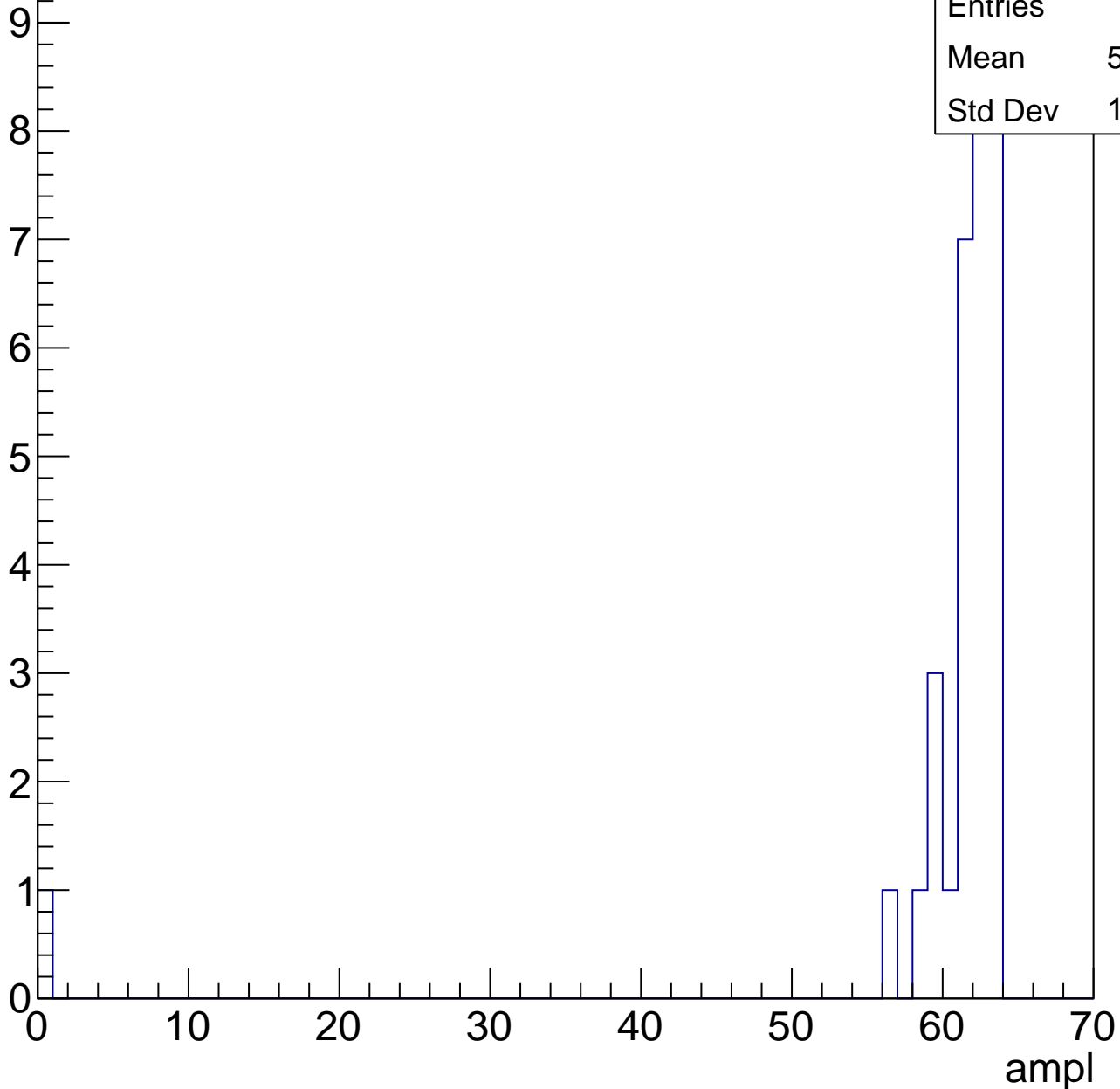


# B0L002S, U8-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.35
Std Dev	10.96



# B0L002S, U8-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch106, adc0

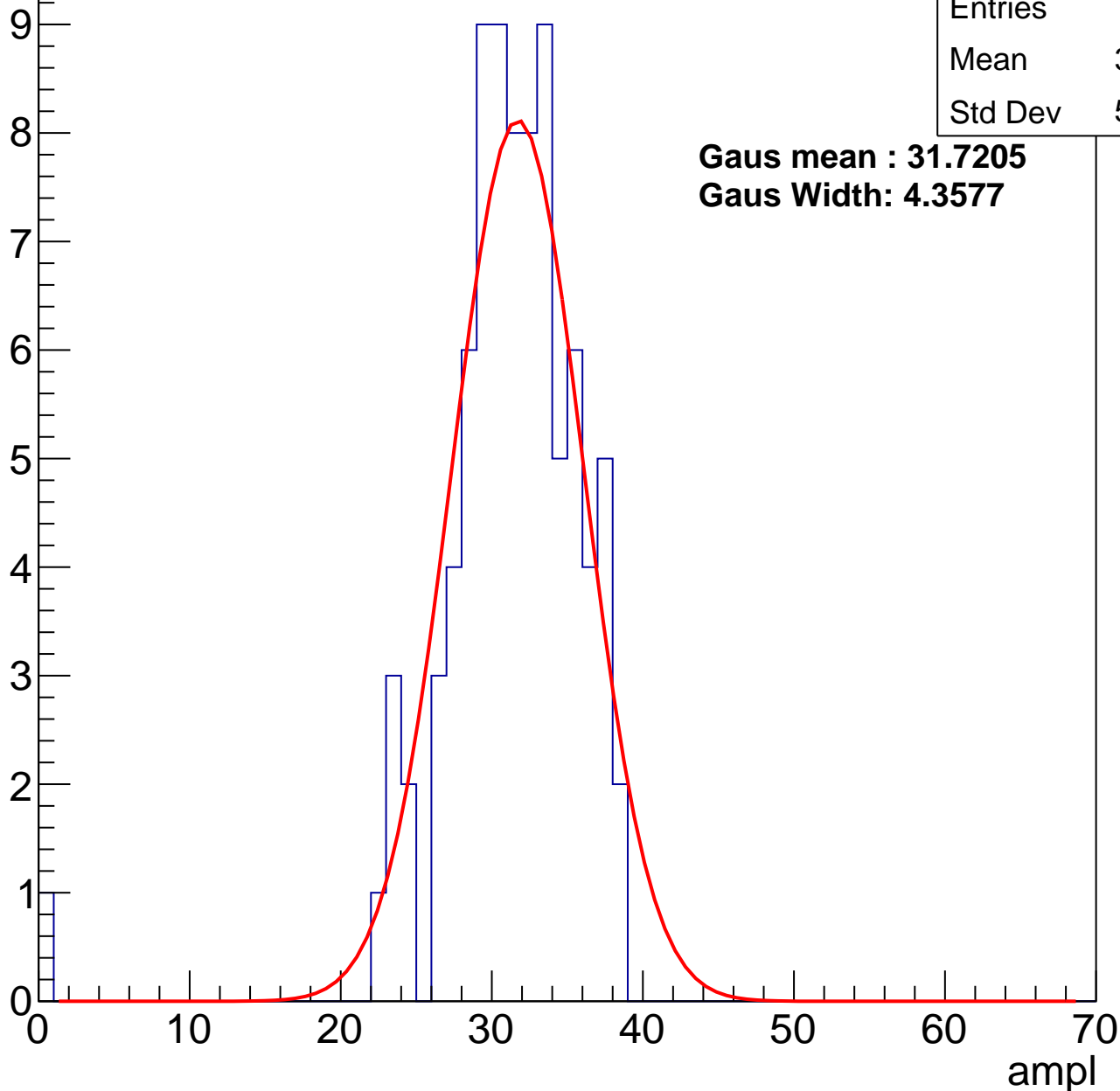
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	85
Mean	30.71
Std Dev	5.001

**Gaus mean : 31.7205**

**Gaus Width: 4.3577**



# B0L002S, U8-ch106, adc1

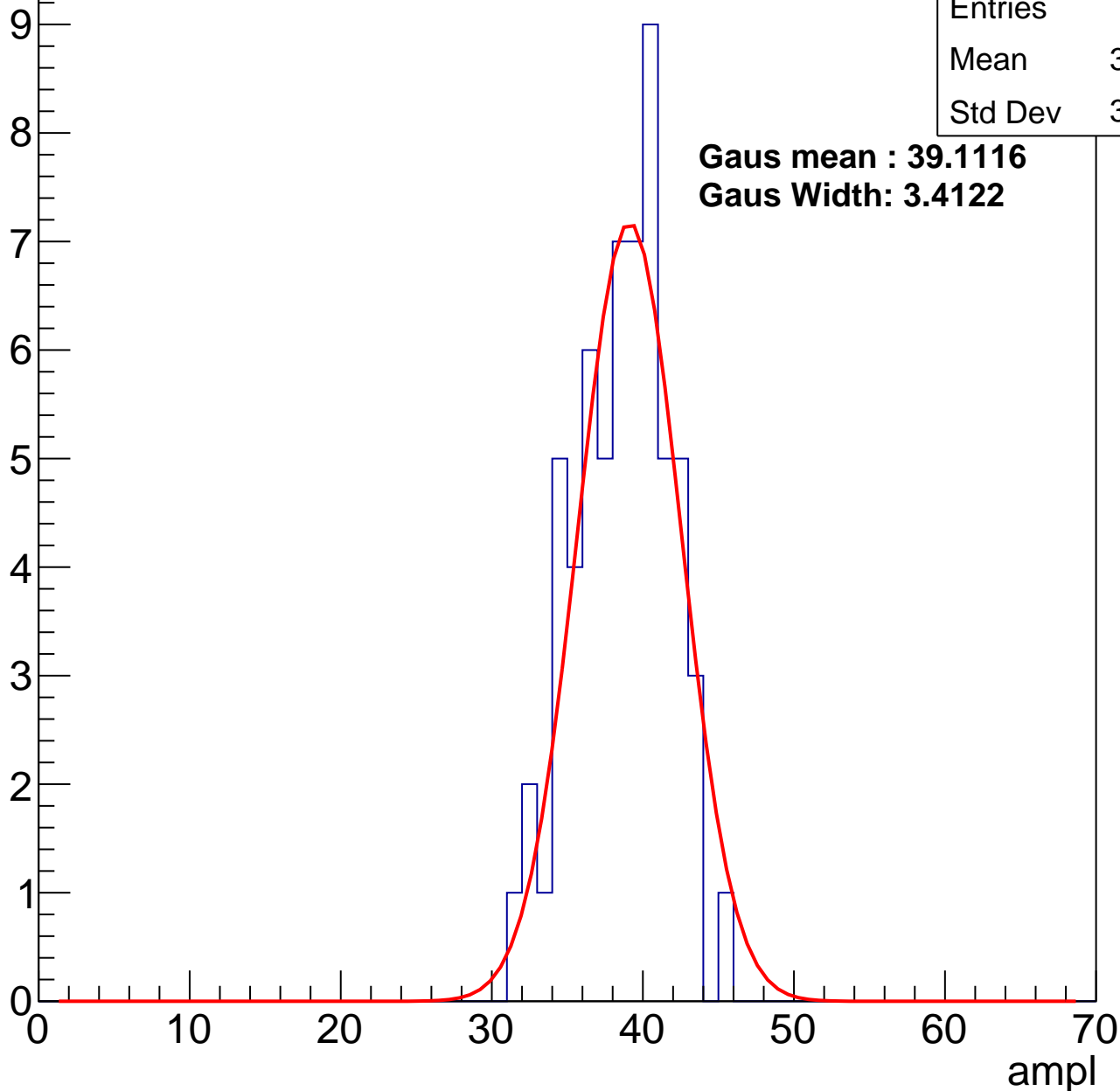
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.15
Std Dev	3.088

**Gaus mean : 39.1116**

**Gaus Width: 3.4122**



# B0L002S, U8-ch106, adc2

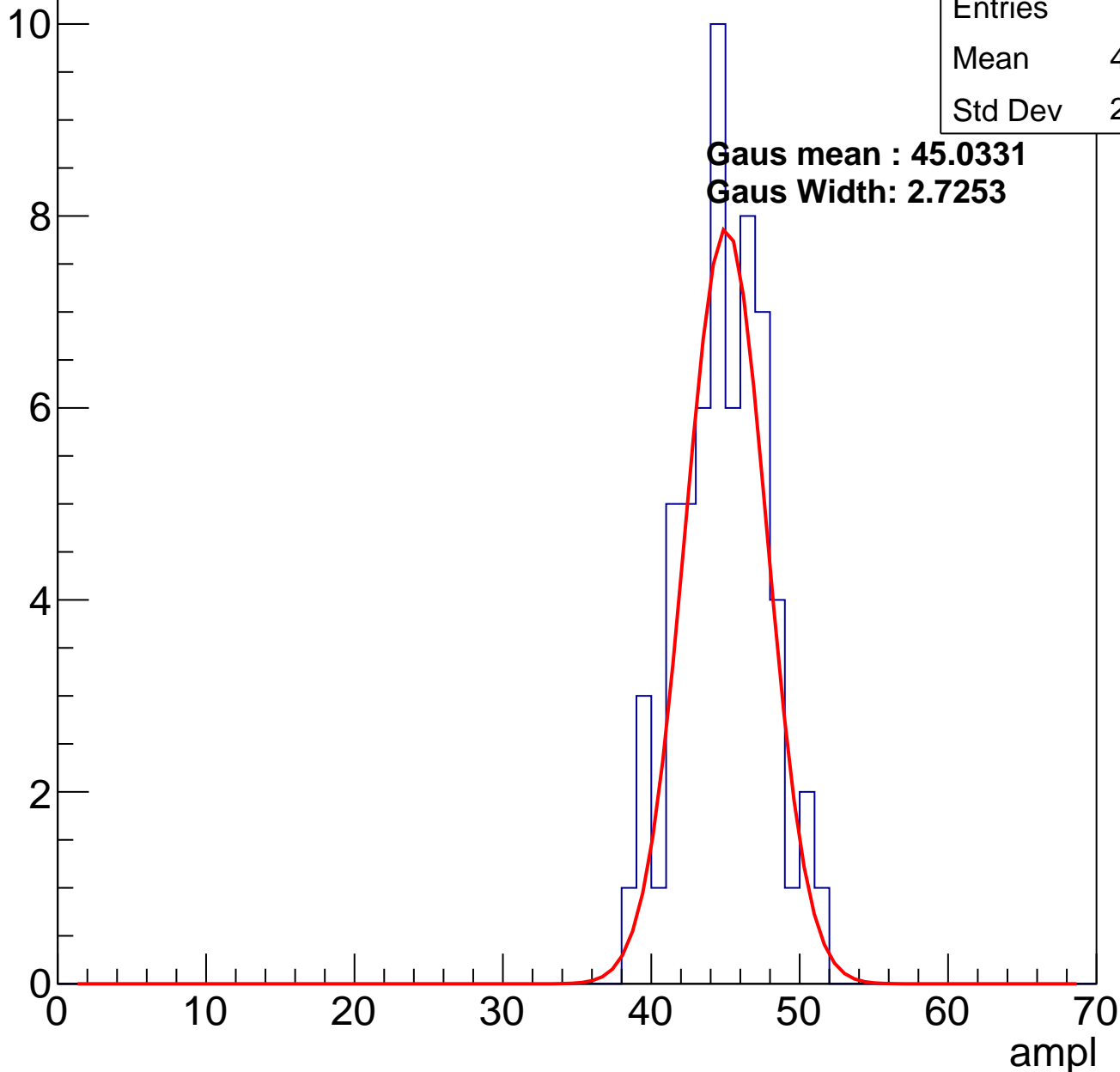
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	60
Mean	44.45
Std Dev	2.878

**Gaus mean : 45.0331**

**Gaus Width: 2.7253**

Entry

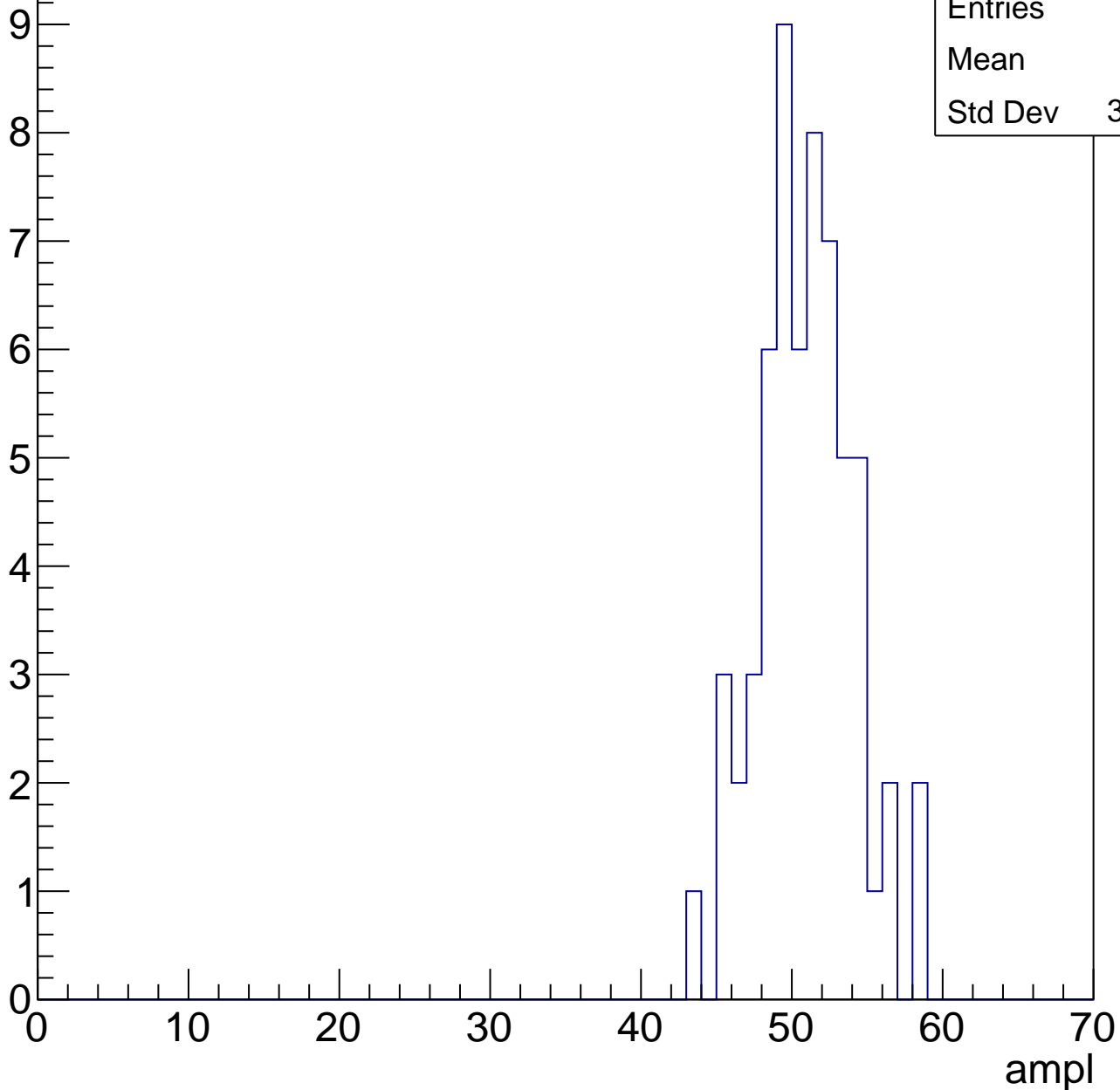


# B0L002S, U8-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

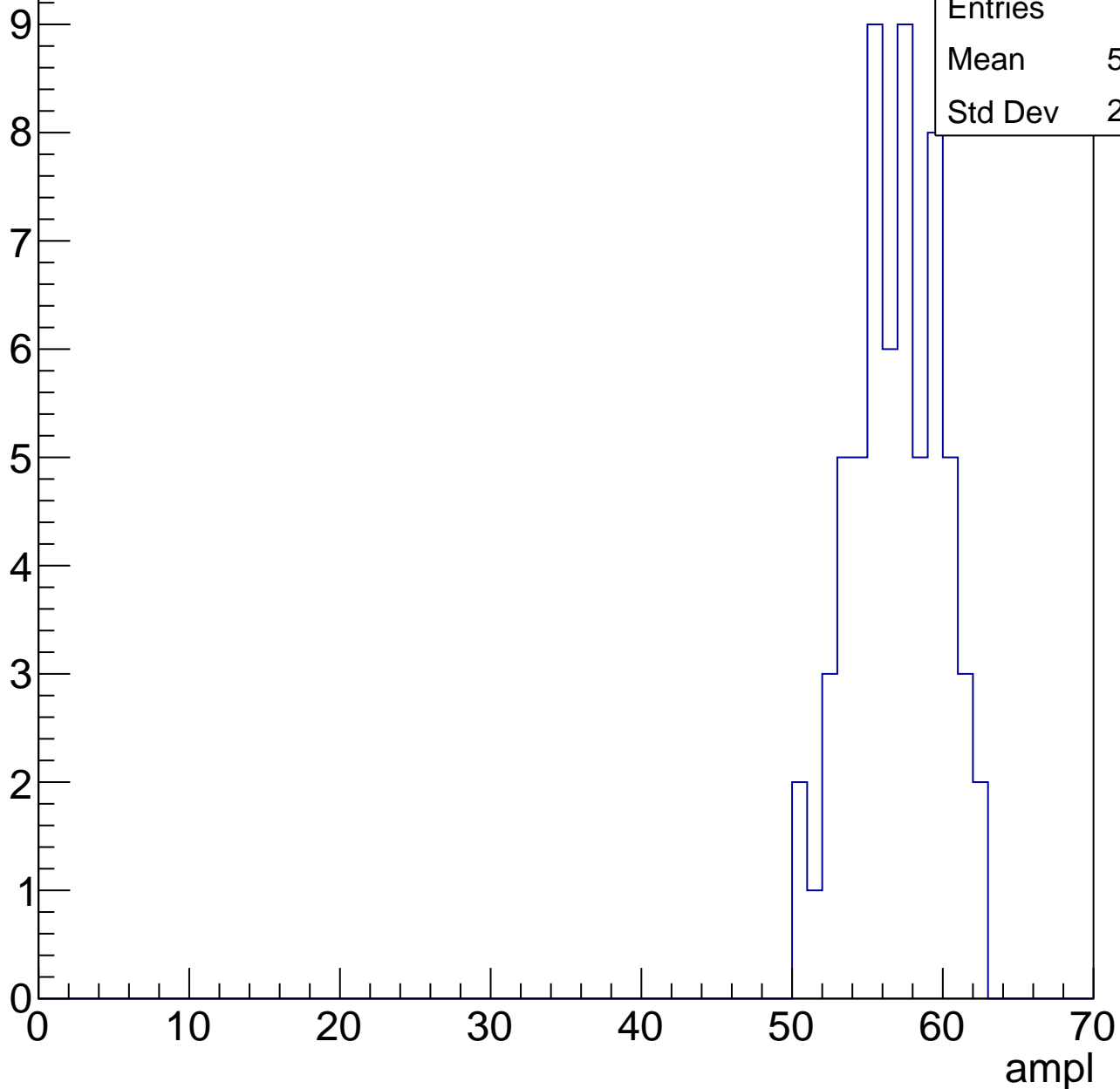
Entries	60
Mean	50.5
Std Dev	3.128



# B0L002S, U8-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



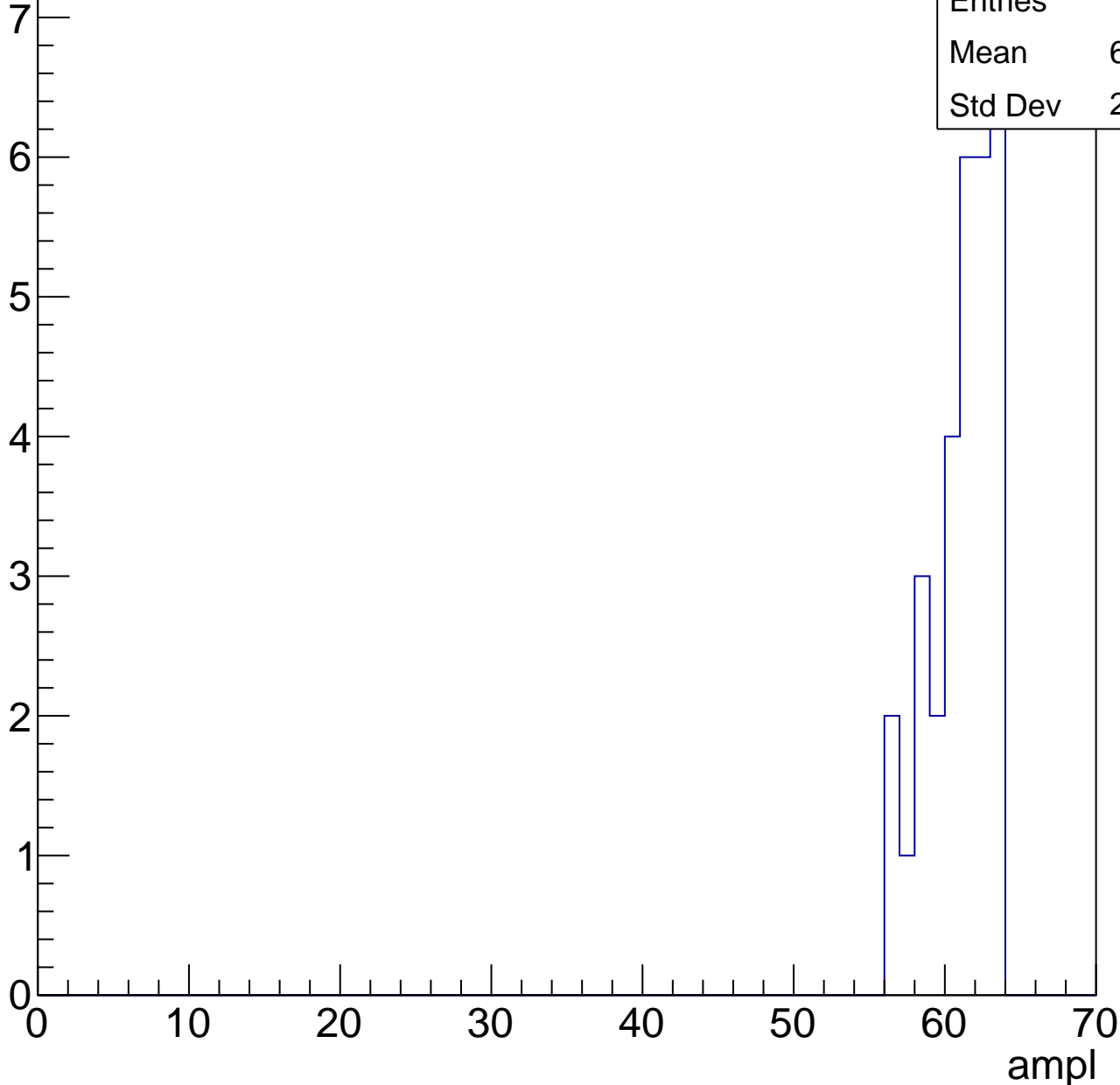
Entries	63
Mean	56.43
Std Dev	2.915

# B0L002S, U8-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

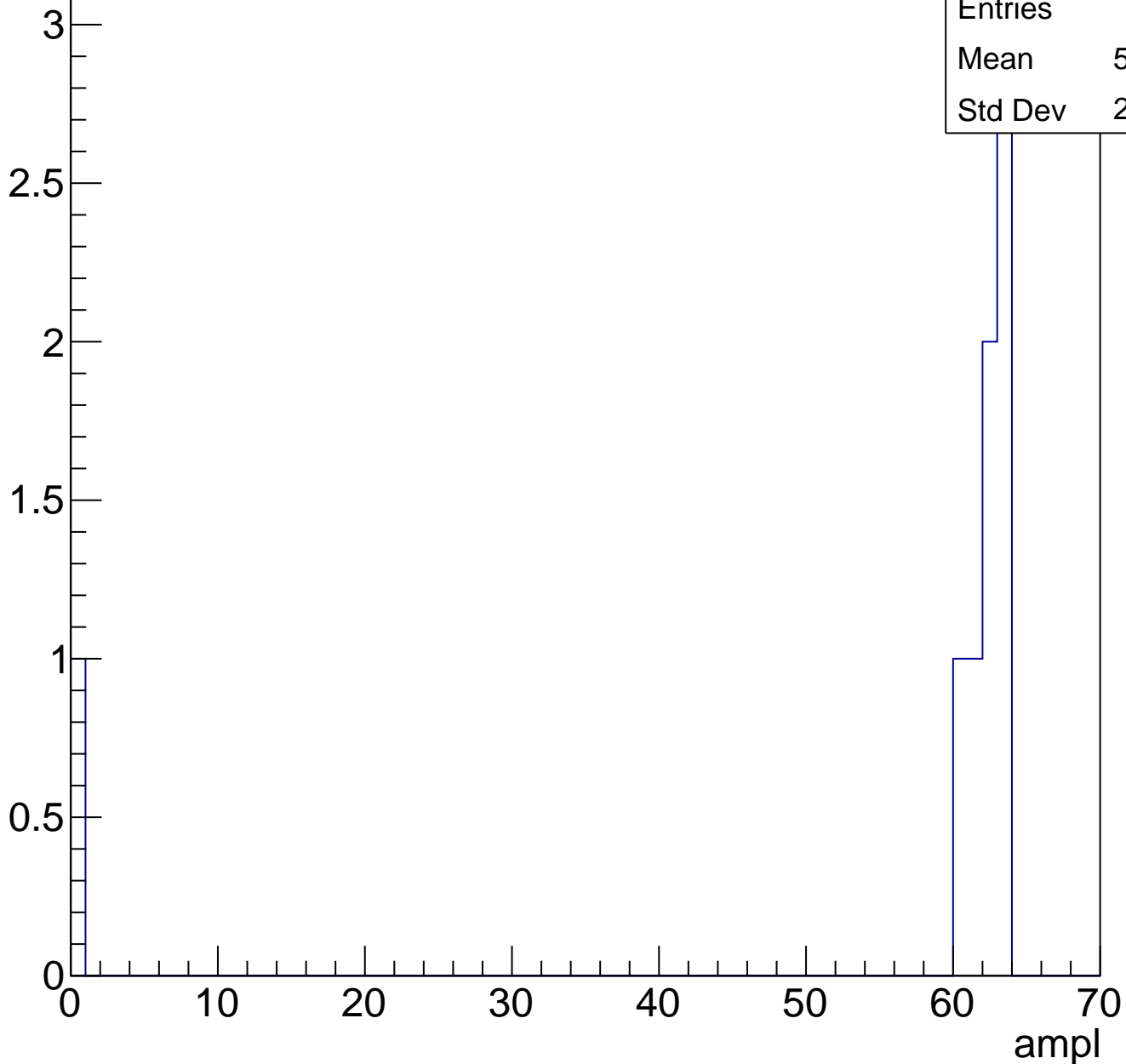
Entries	31
Mean	60.65
Std Dev	2.088



# B0L002S, U8-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch107, adc0

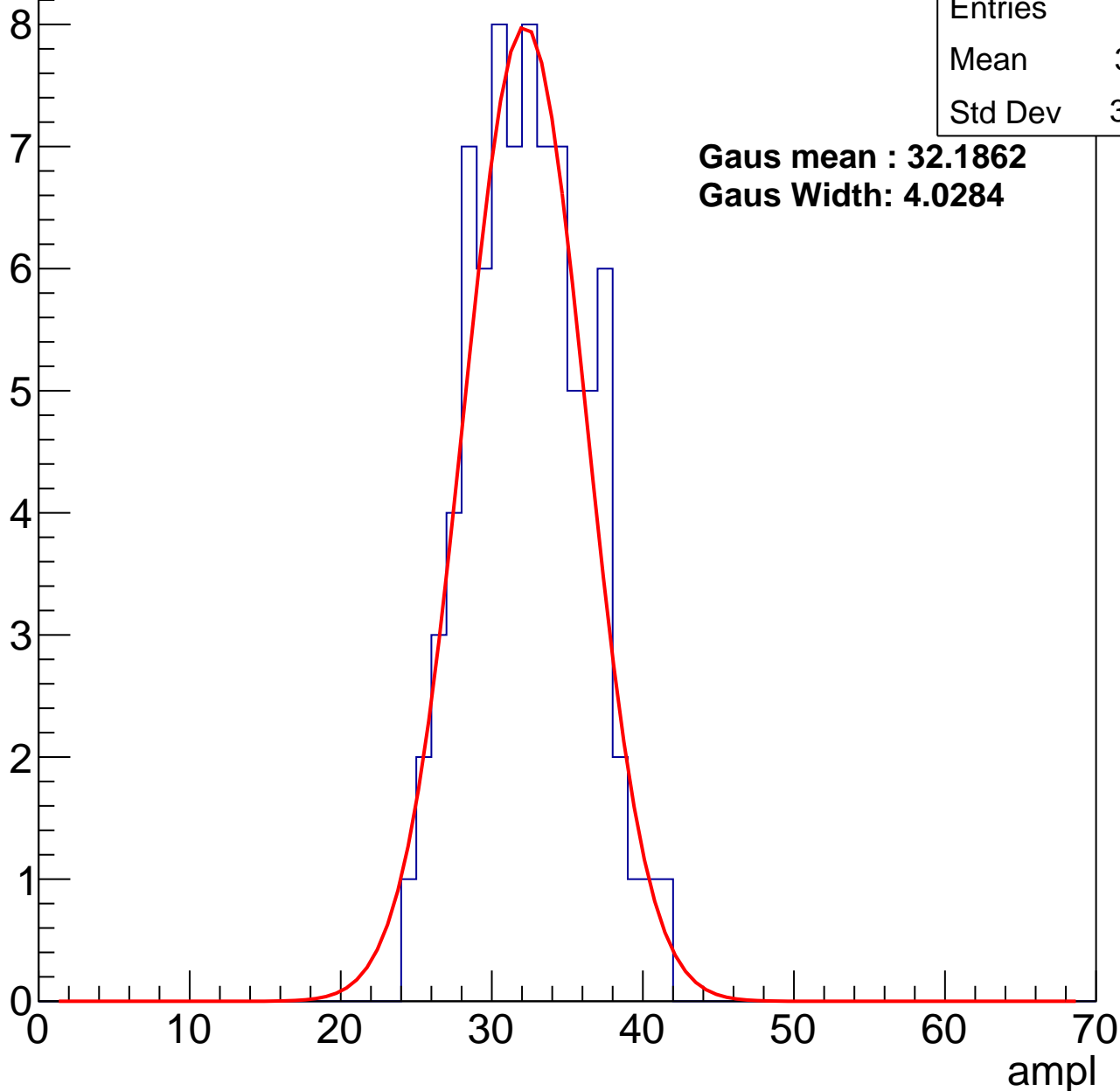
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.91
Std Dev	3.769

**Gaus mean : 32.1862**

**Gaus Width: 4.0284**



# B0L002S, U8-ch107, adc1

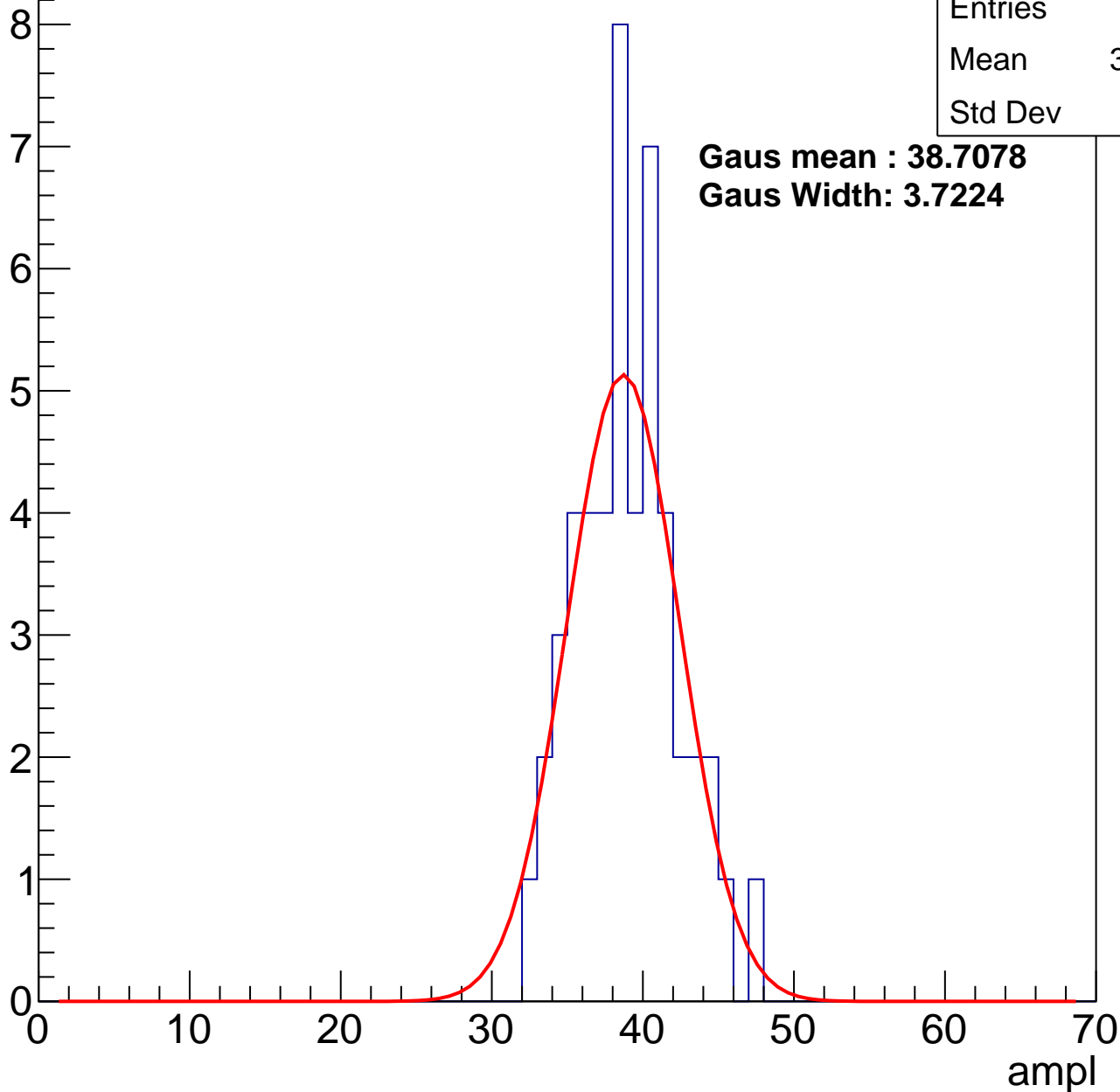
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	38.49
Std Dev	3.29

**Gaus mean : 38.7078**

**Gaus Width: 3.7224**



# B0L002S, U8-ch107, adc2

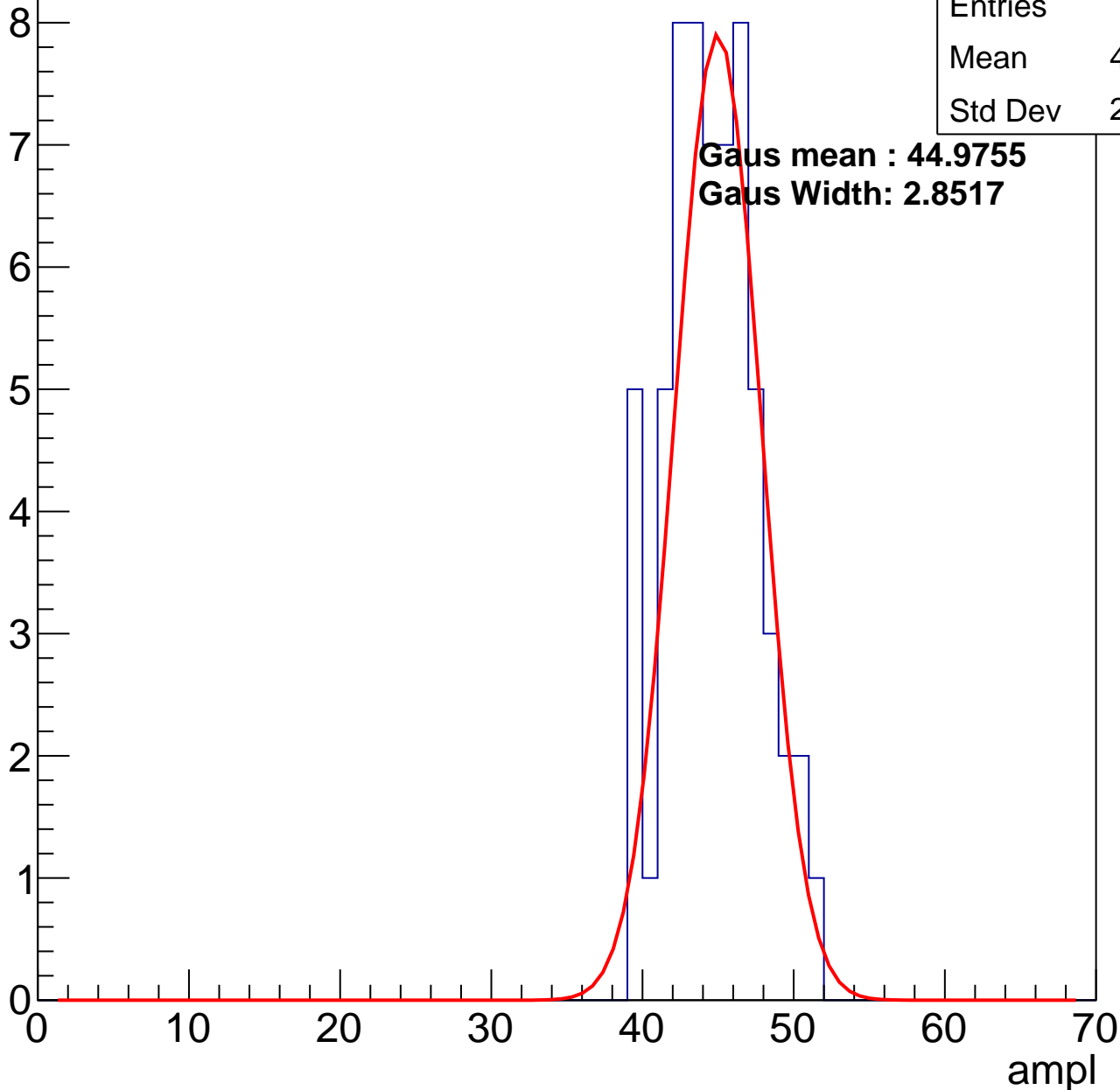
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.18
Std Dev	2.916

**Gaus mean : 44.9755**

**Gaus Width: 2.8517**

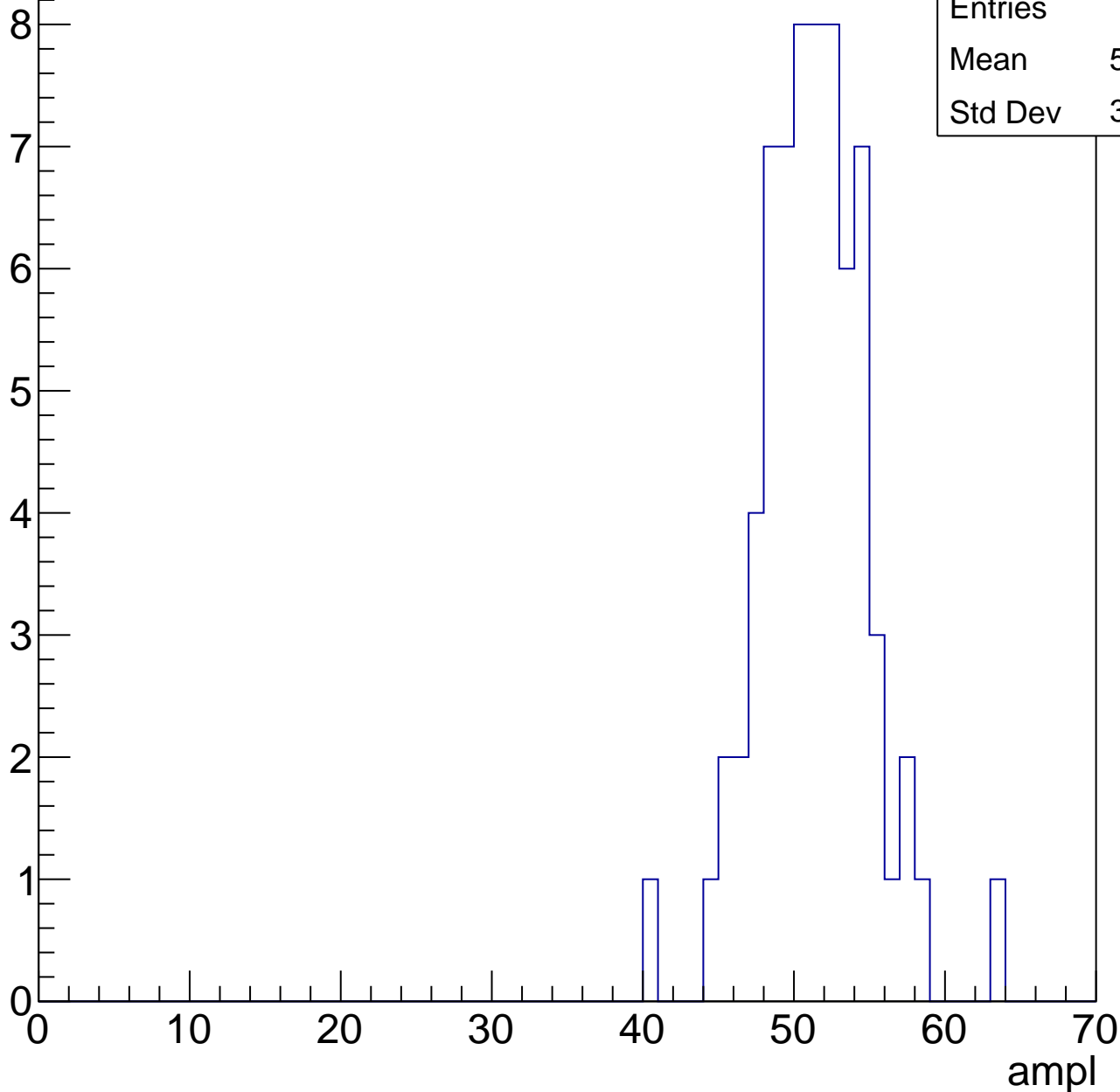


# B0L002S, U8-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.86
Std Dev	3.584

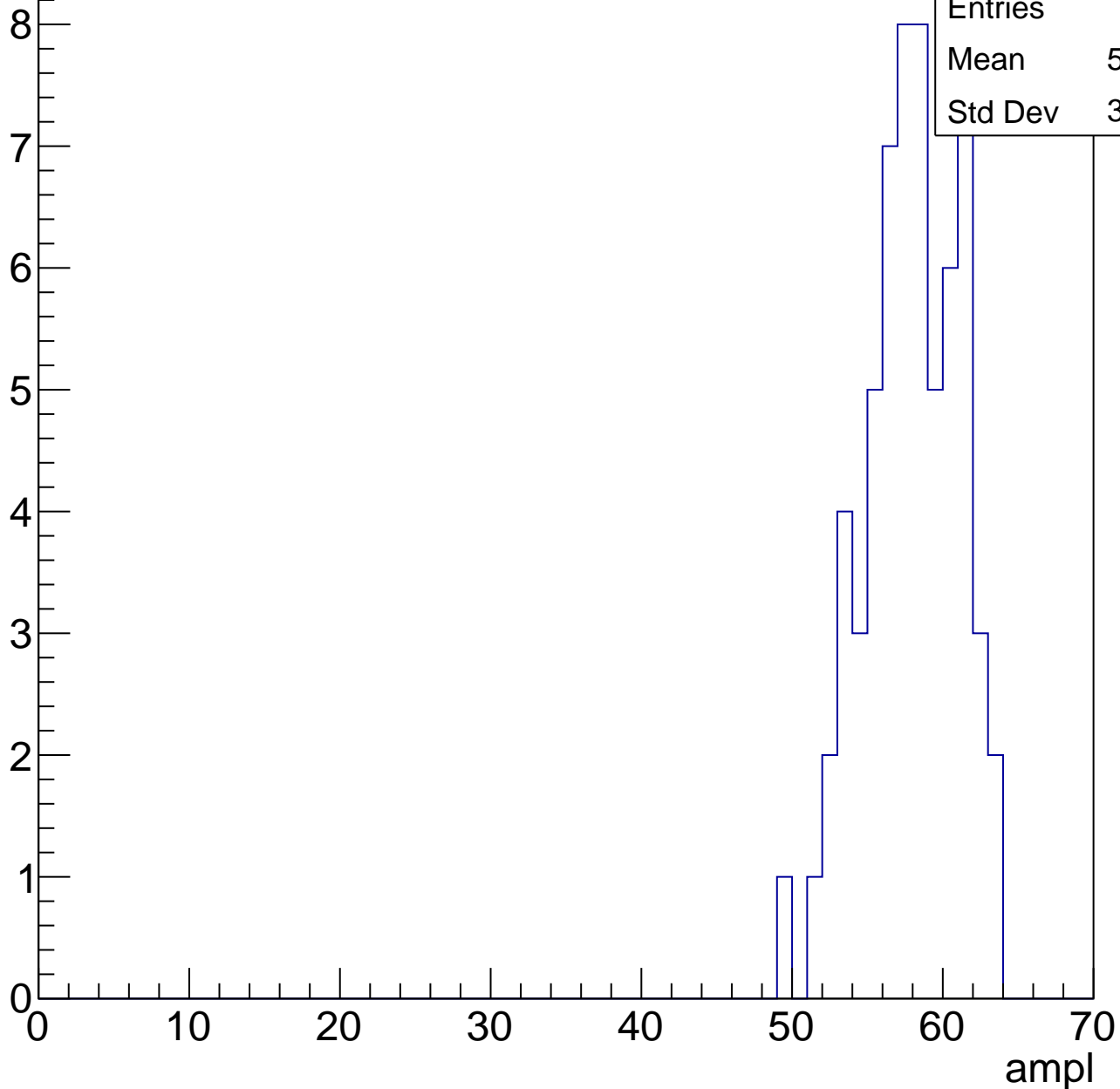


# B0L002S, U8-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	57.46
Std Dev	3.116

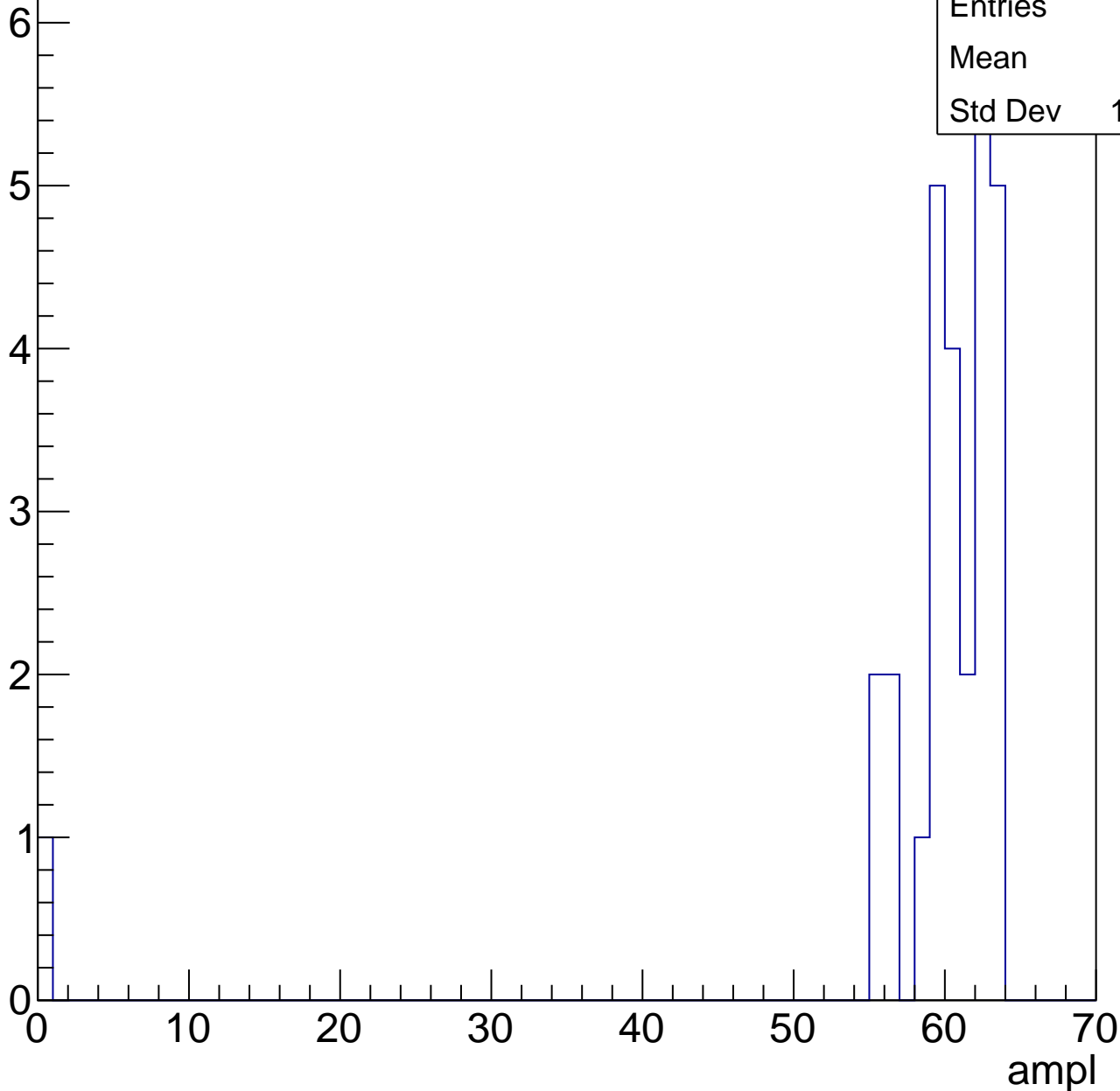


# B0L002S, U8-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58
Std Dev	11.42



# B0L002S, U8-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch108, adc0

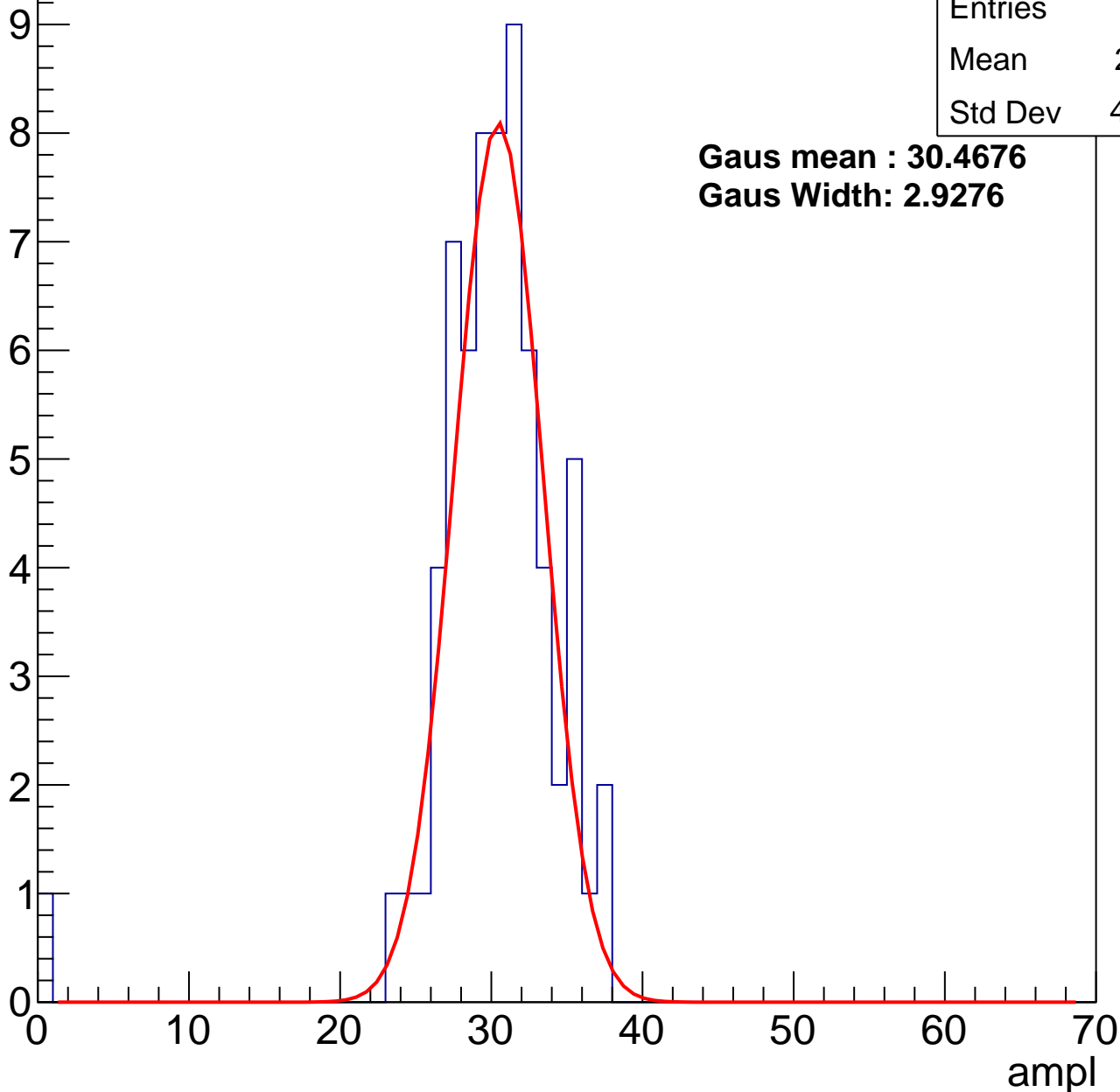
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.71
Std Dev	4.805

**Gaus mean : 30.4676**

**Gaus Width: 2.9276**



# B0L002S, U8-ch108, adc1

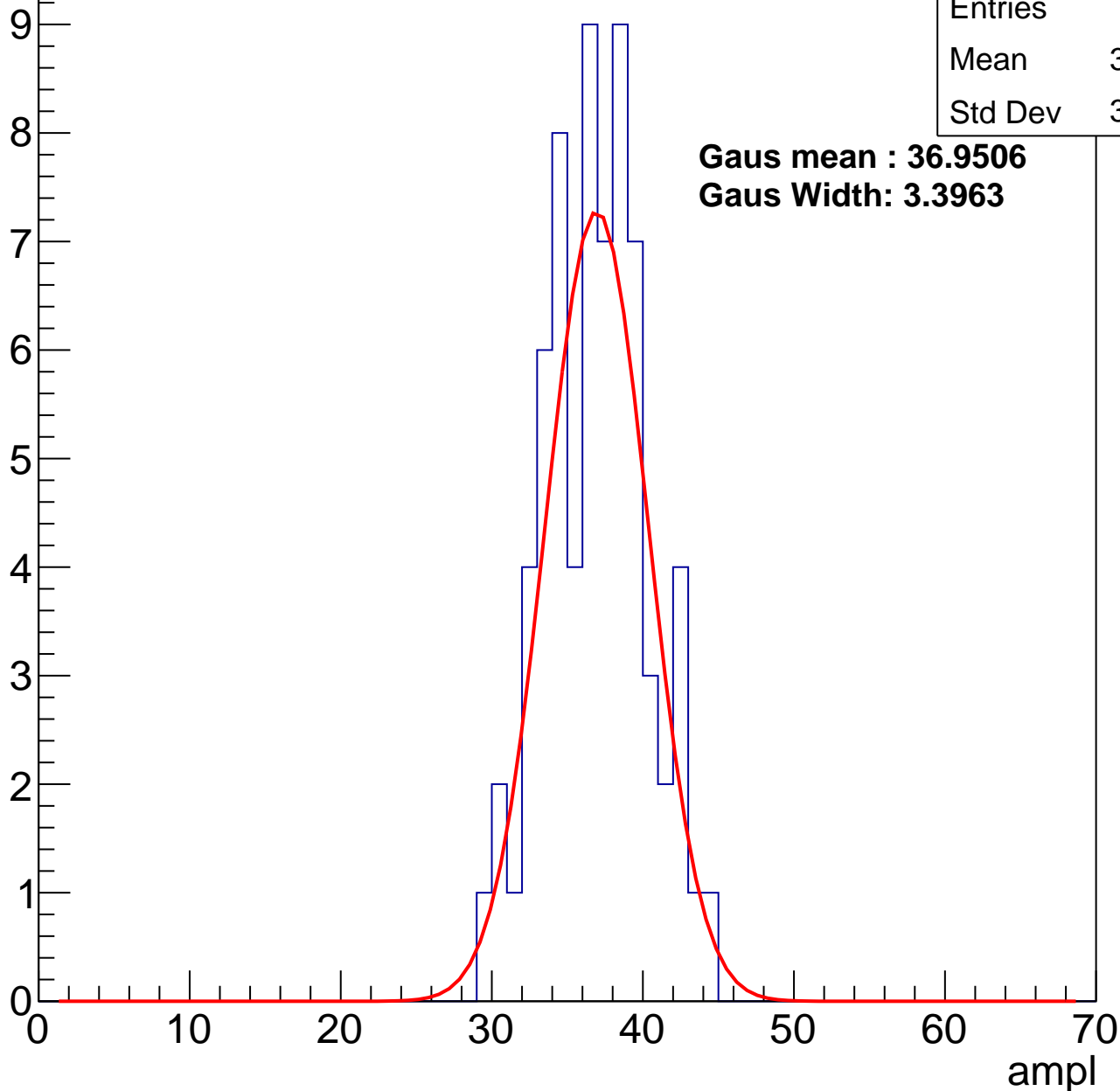
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.42
Std Dev	3.294

**Gaus mean : 36.9506**

**Gaus Width: 3.3963**



# B0L002S, U8-ch108, adc2

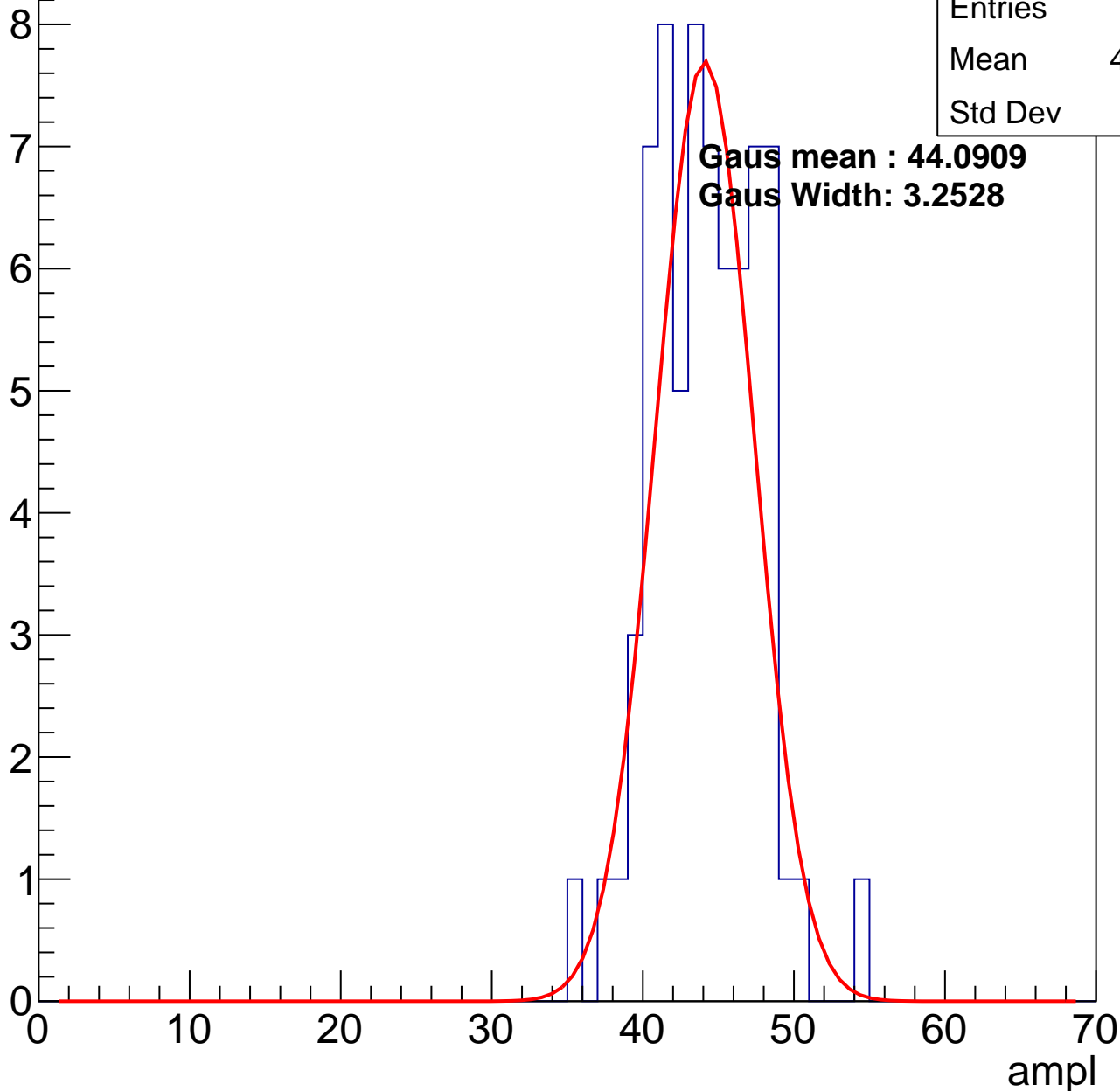
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.73
Std Dev	3.41

**Gaus mean : 44.0909**

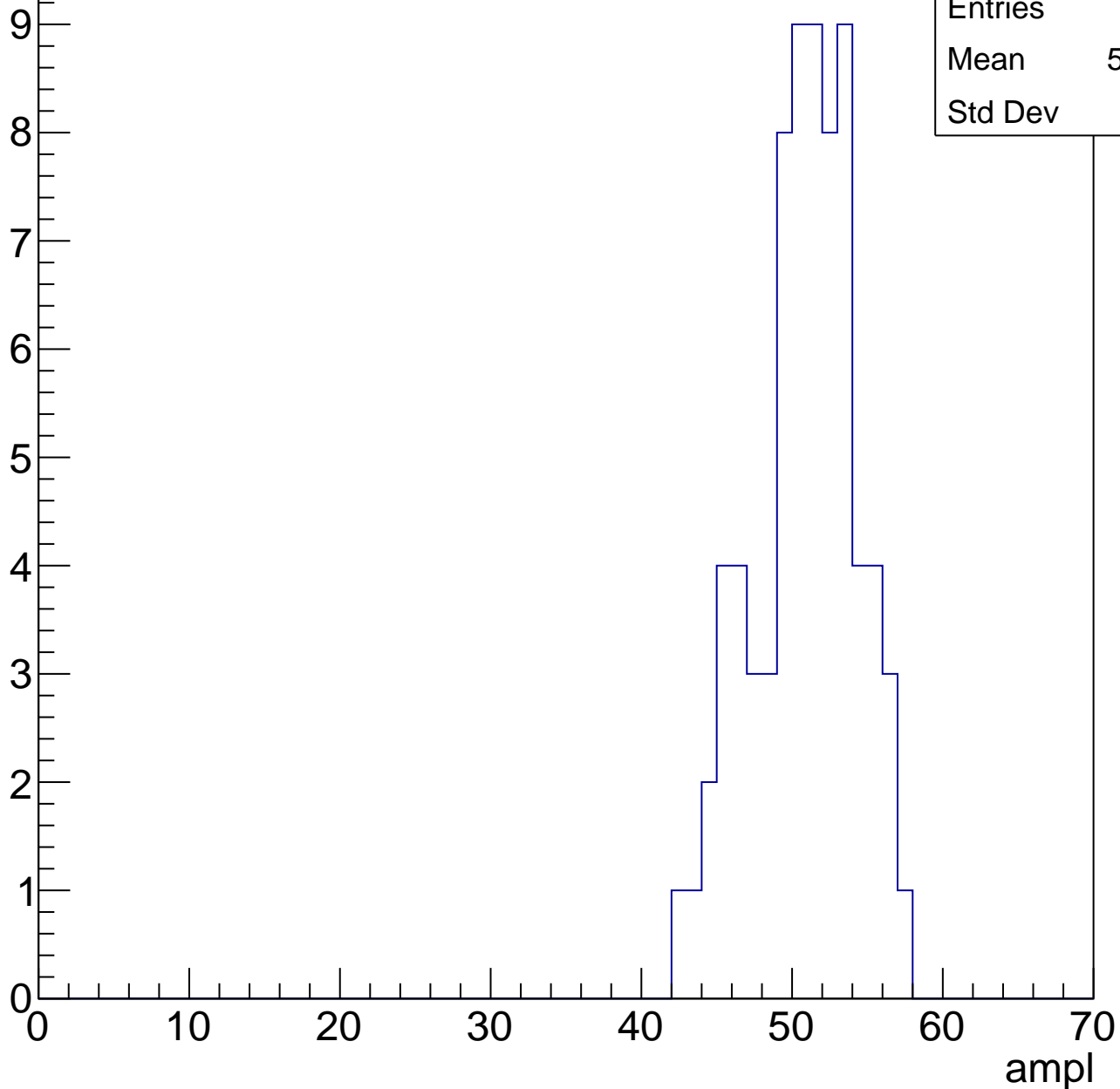
**Gaus Width: 3.2528**



# B0L002S, U8-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



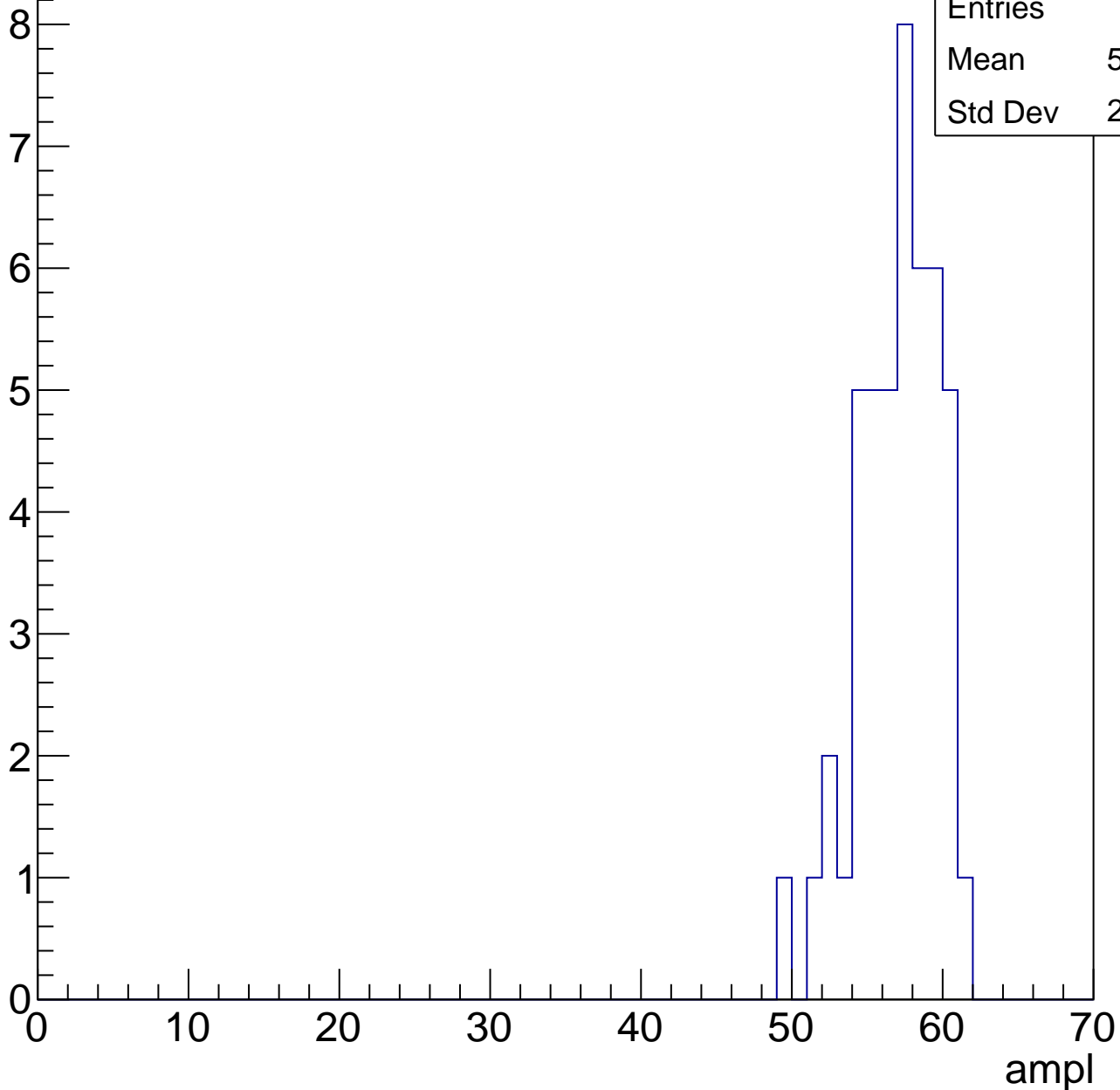
Entries	73
Mean	50.37
Std Dev	3.37

# B0L002S, U8-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	56.54
Std Dev	2.627



# B0L002S, U8-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	44
Mean	59.18
Std Dev	9.264

Entry

10

8

6

4

2

0

0

10

20

30

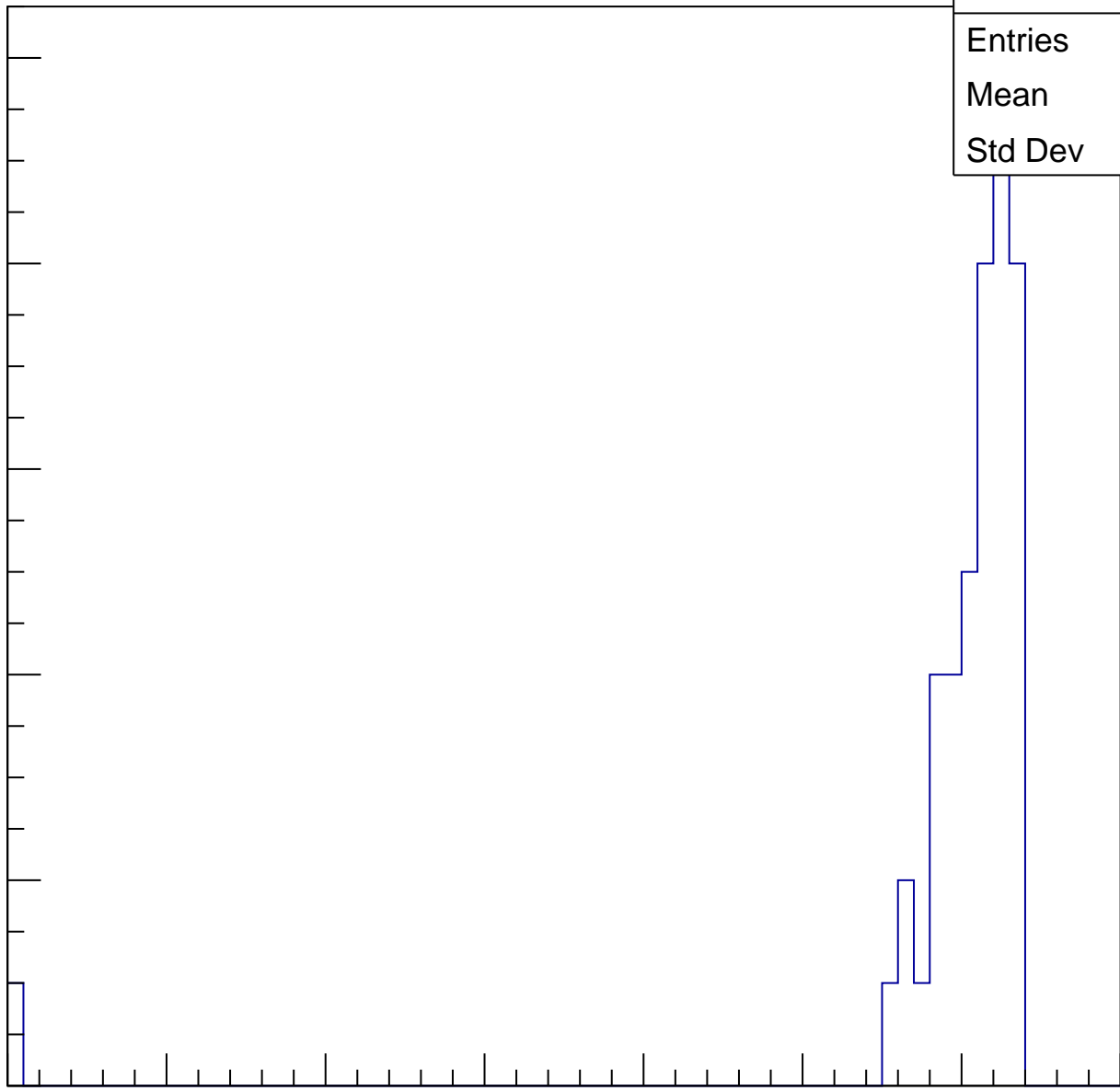
40

50

60

70

ampl



# B0L002S, U8-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch109, adc0

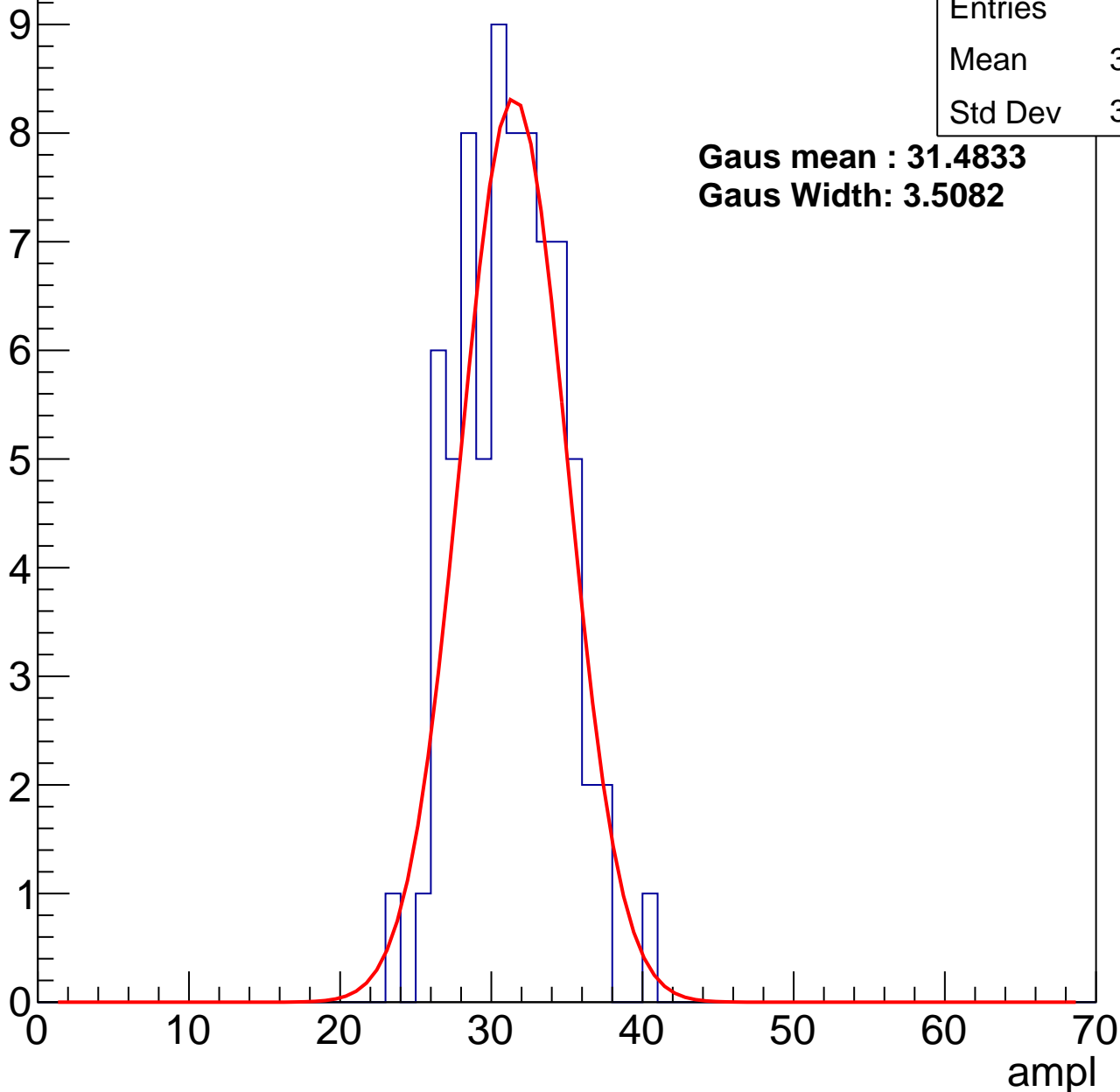
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.83
Std Dev	3.288

**Gaus mean : 31.4833**

**Gaus Width: 3.5082**



# B0L002S, U8-ch109, adc1

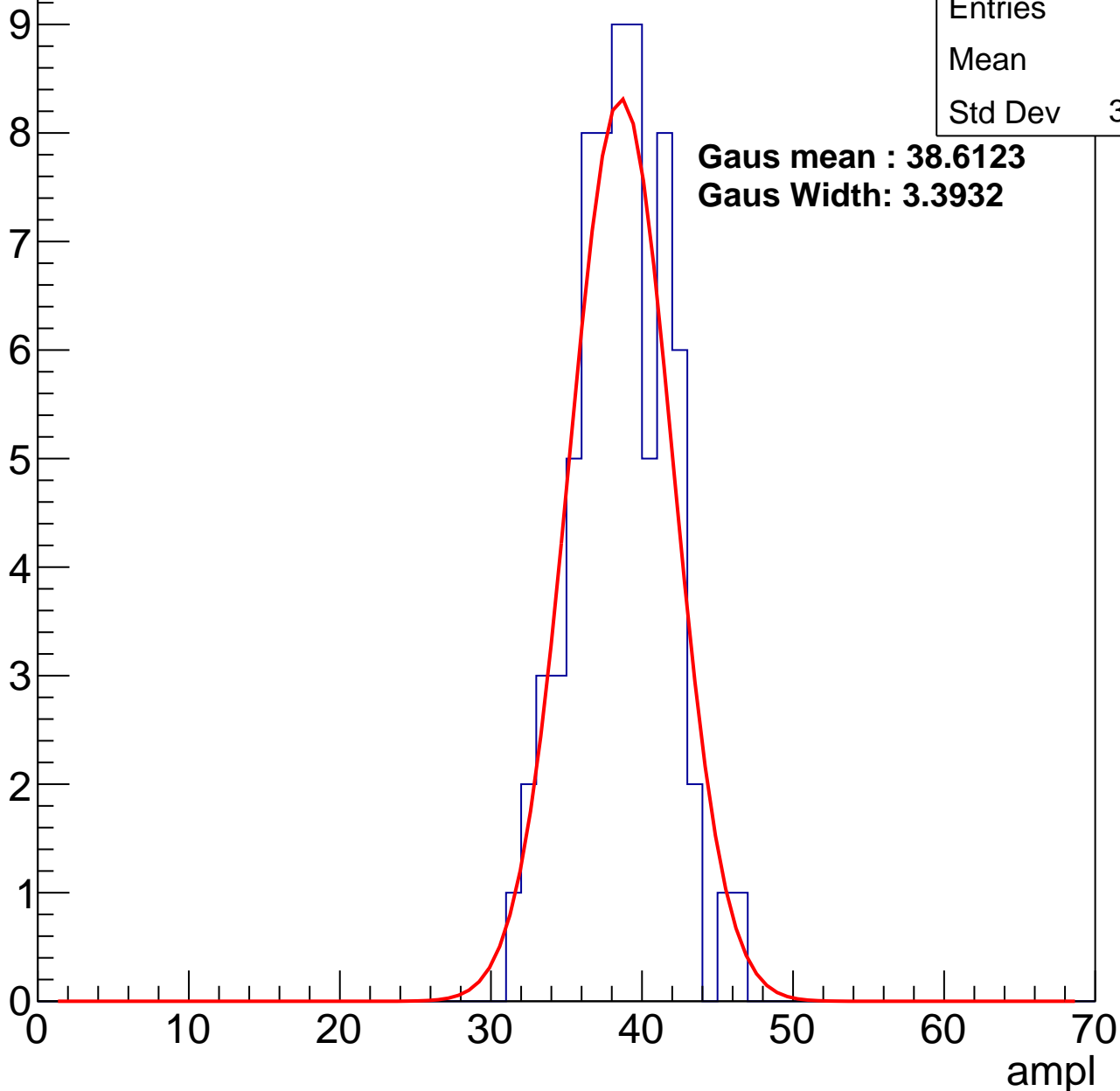
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.1
Std Dev	3.114

**Gaus mean : 38.6123**

**Gaus Width: 3.3932**



# B0L002S, U8-ch109, adc2

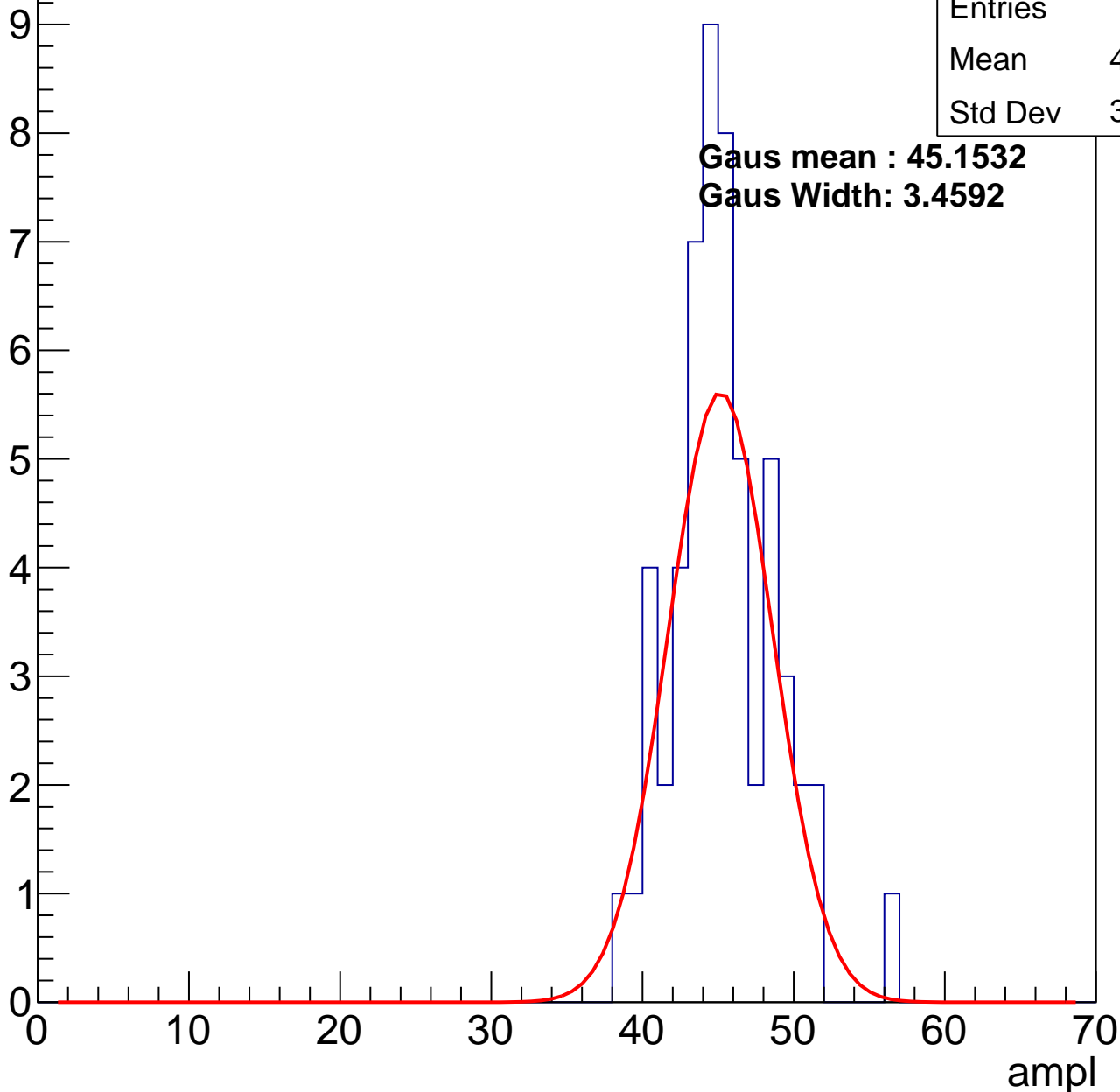
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.88
Std Dev	3.376

**Gaus mean : 45.1532**

**Gaus Width: 3.4592**

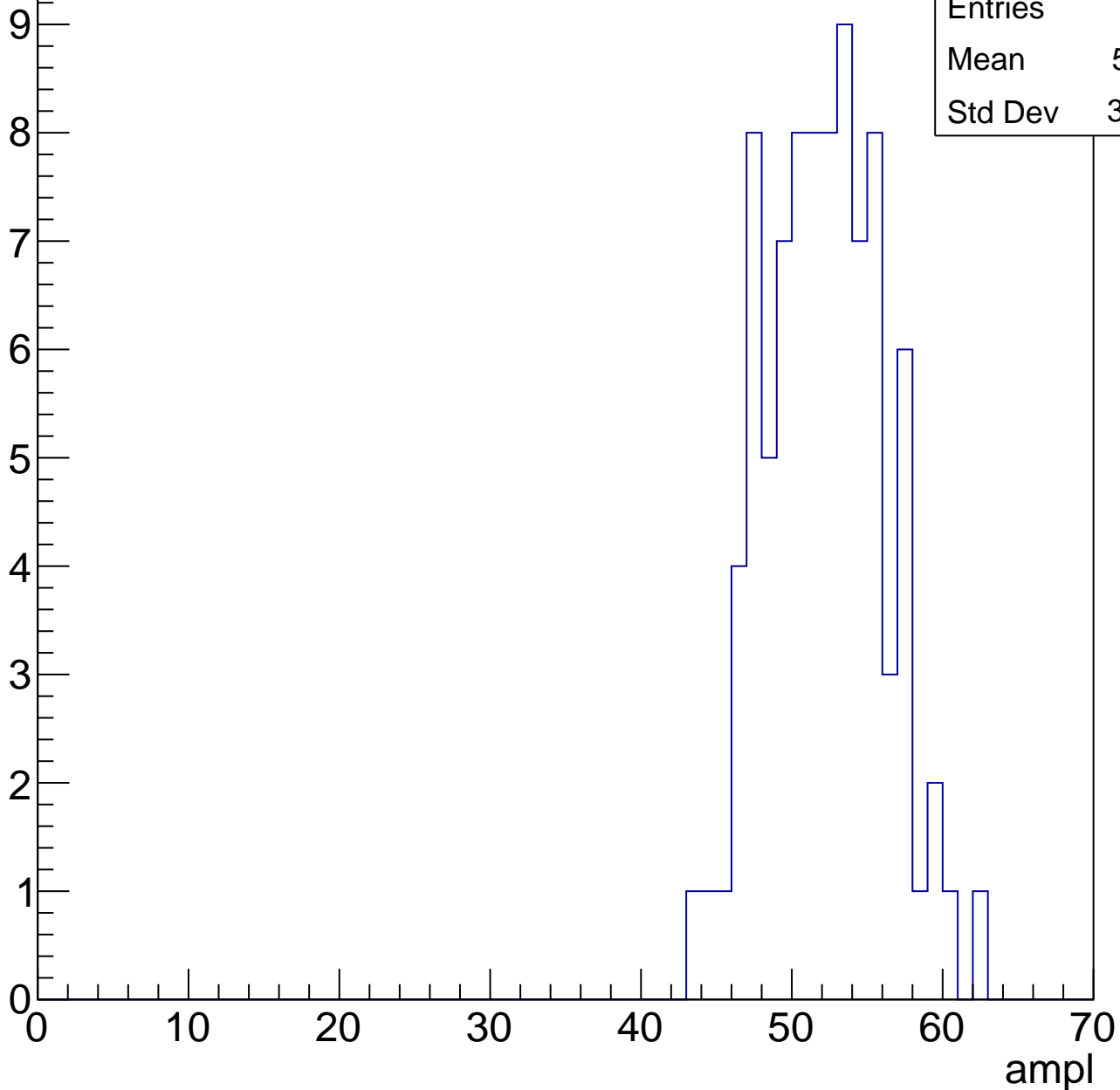


# B0L002S, U8-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

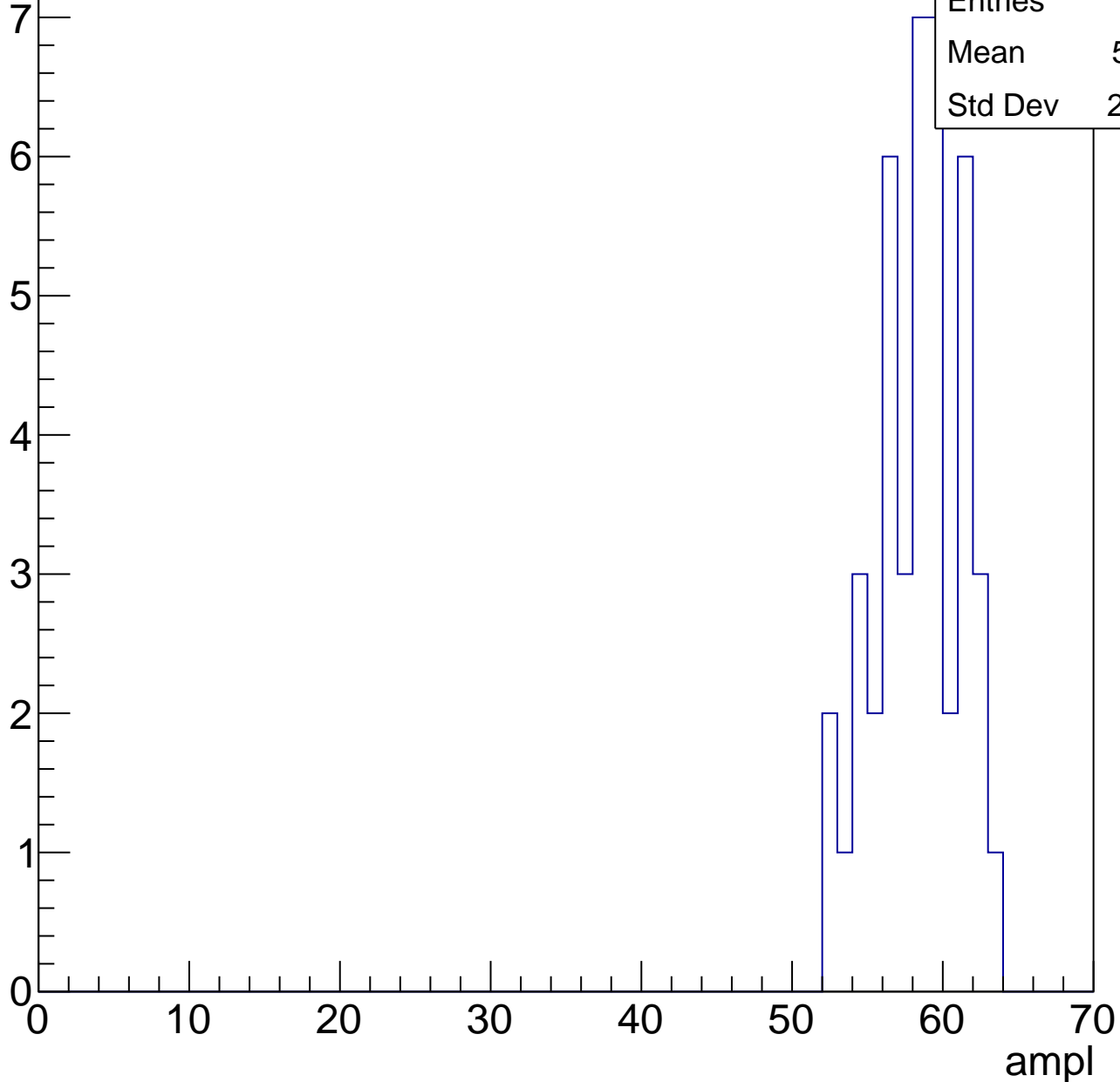
Entries	89
Mean	51.71
Std Dev	3.849



# B0L002S, U8-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

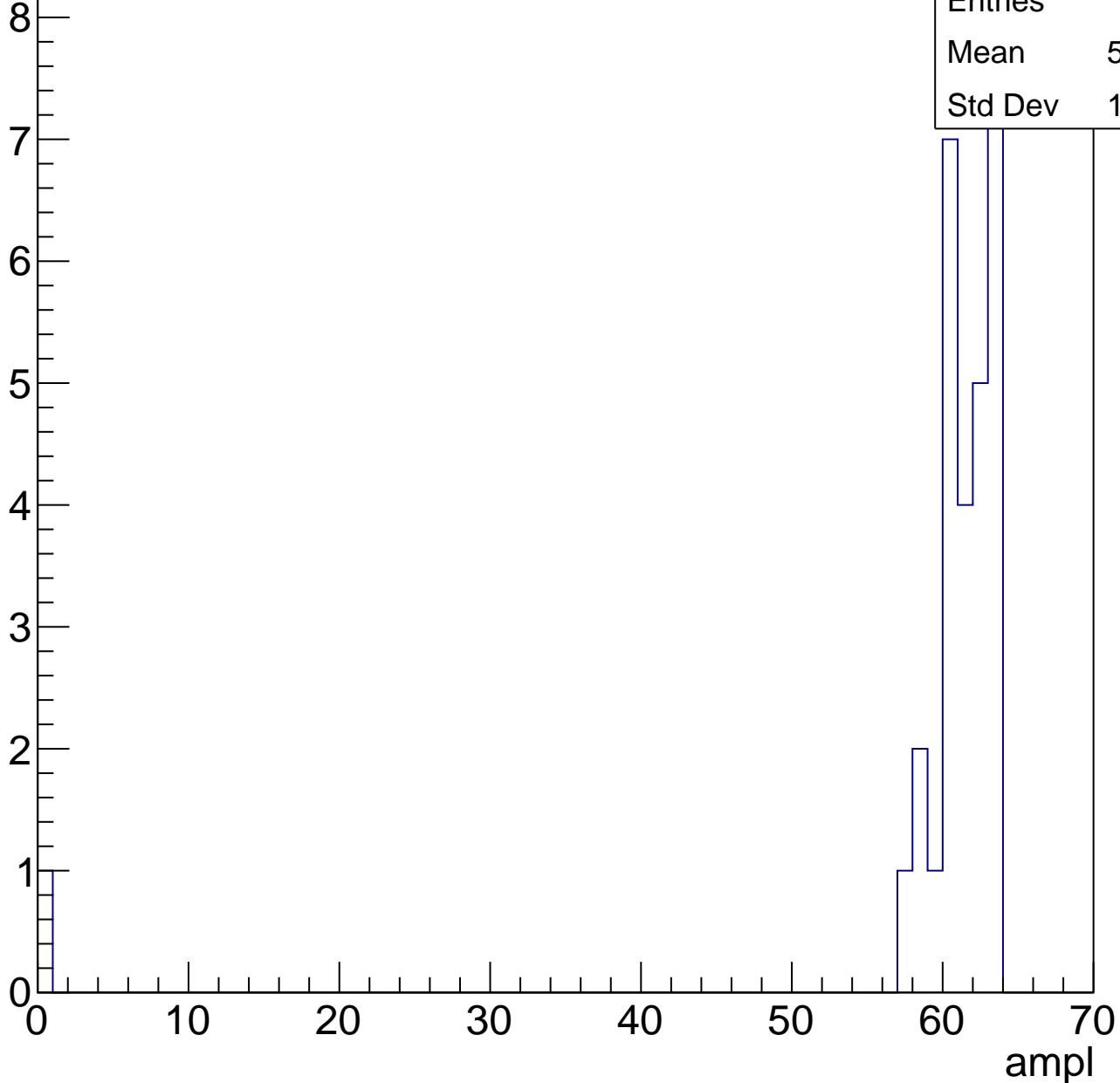


# B0L002S, U8-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.97
Std Dev	11.27



# B0L002S, U8-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	62
Mean	30.35
Std Dev	3.351

**Gaus mean : 30.7573**

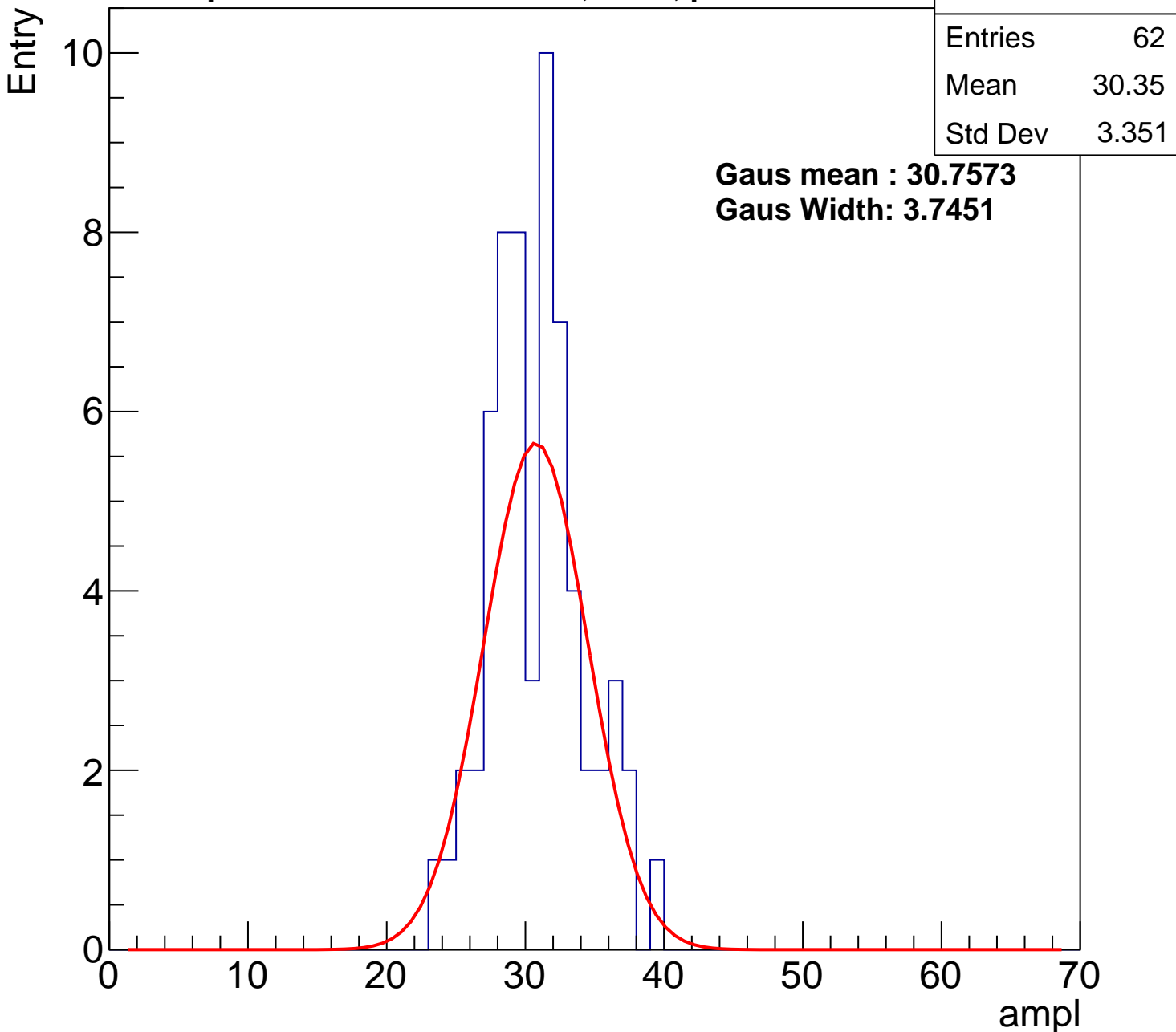
**Gaus Width: 3.7451**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch110, adc1

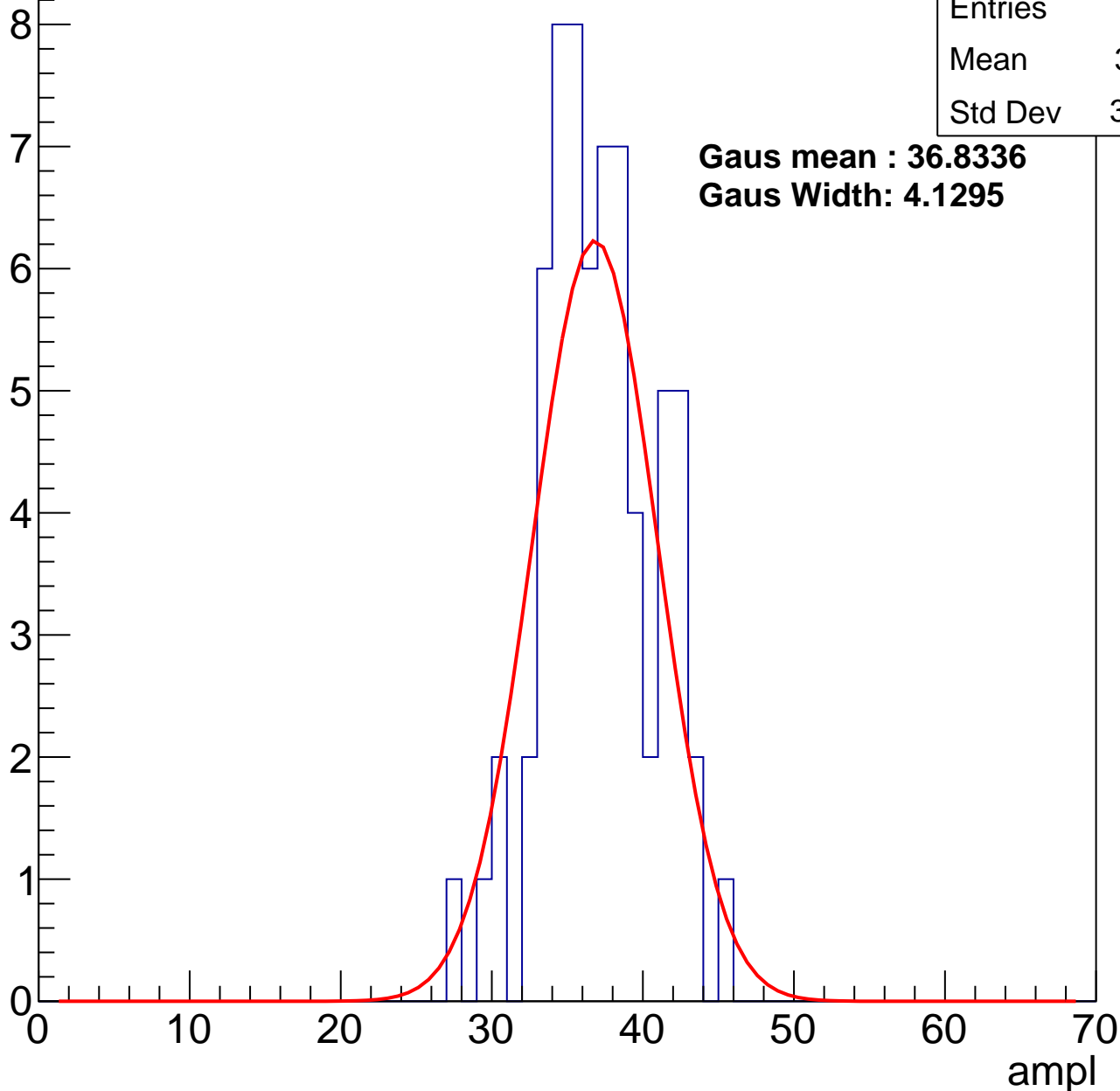
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.61
Std Dev	3.644

**Gaus mean : 36.8336**

**Gaus Width: 4.1295**



# B0L002S, U8-ch110, adc2

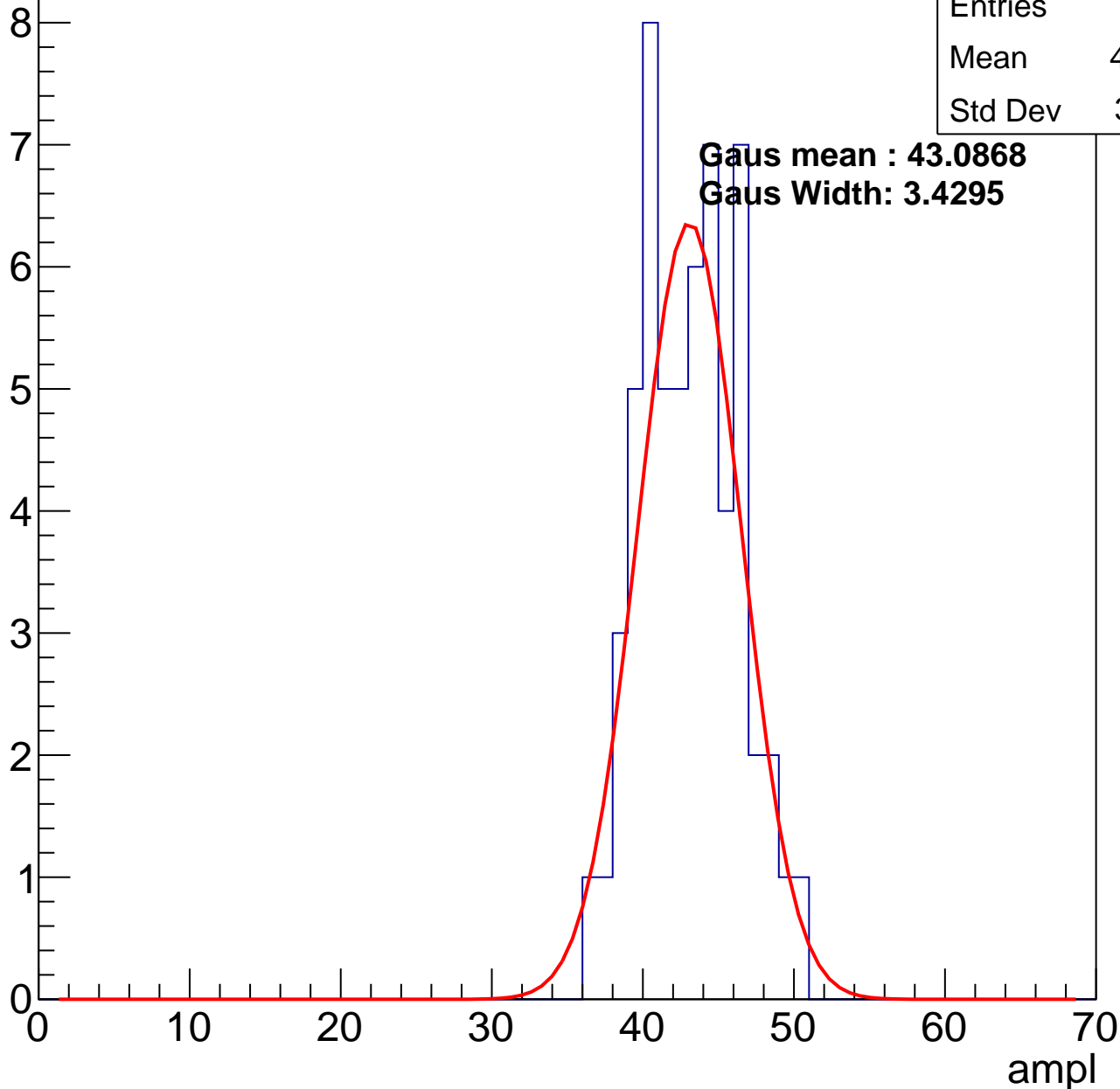
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.66
Std Dev	3.171

**Gaus mean : 43.0868**

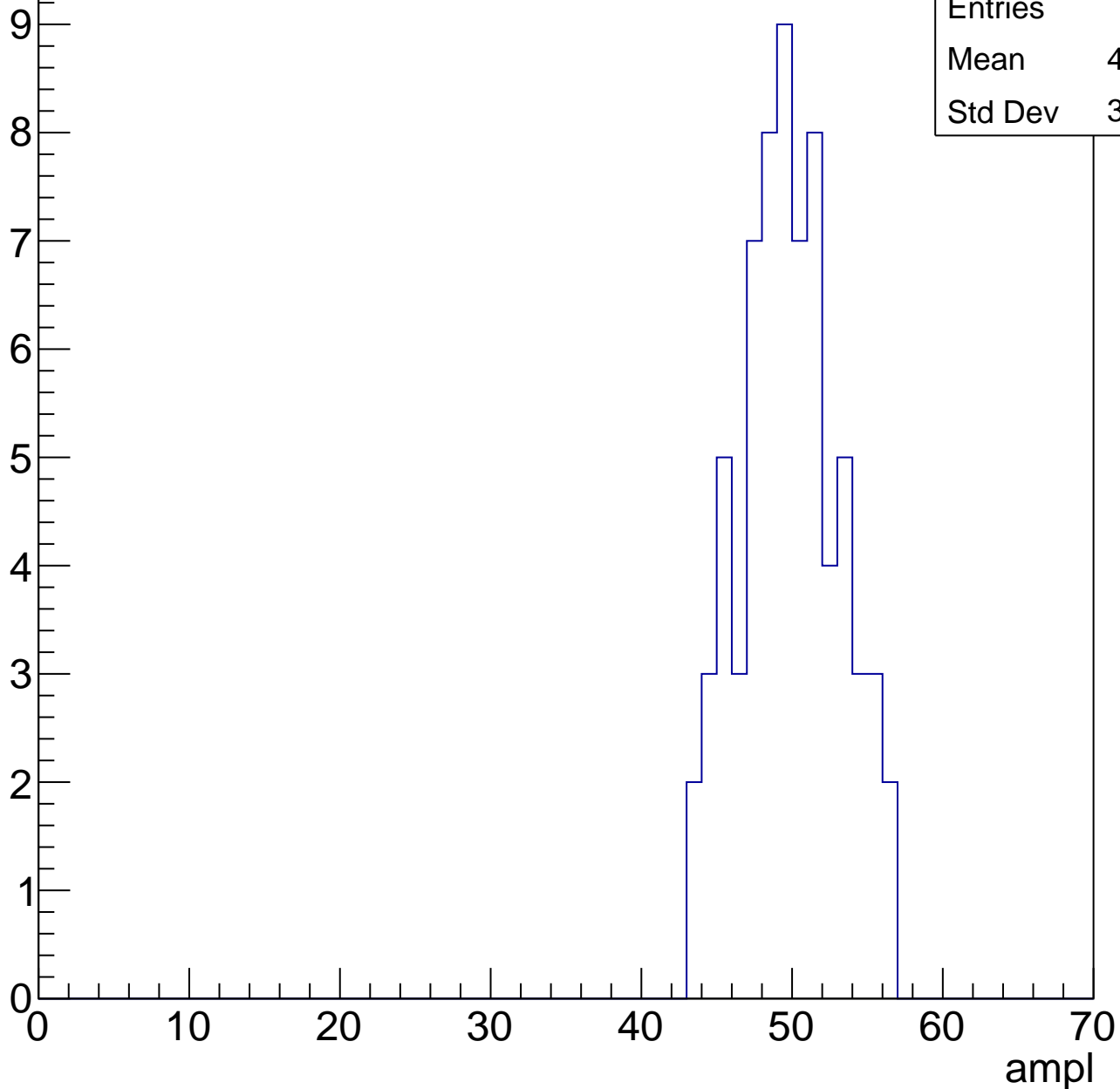
**Gaus Width: 3.4295**



# B0L002S, U8-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



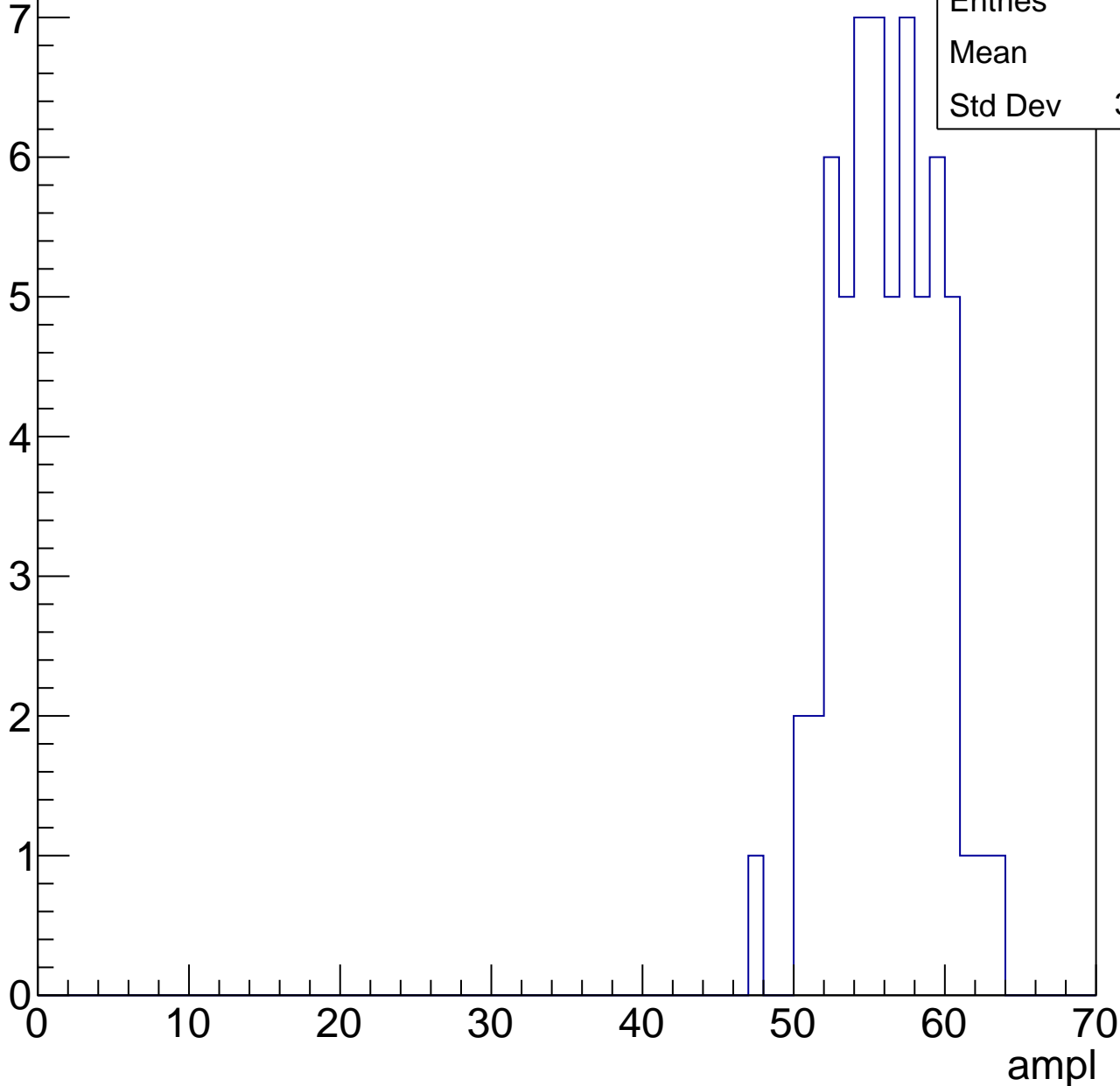
Entries	69
Mean	49.35
Std Dev	3.225

# B0L002S, U8-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.7
Std Dev	3.251

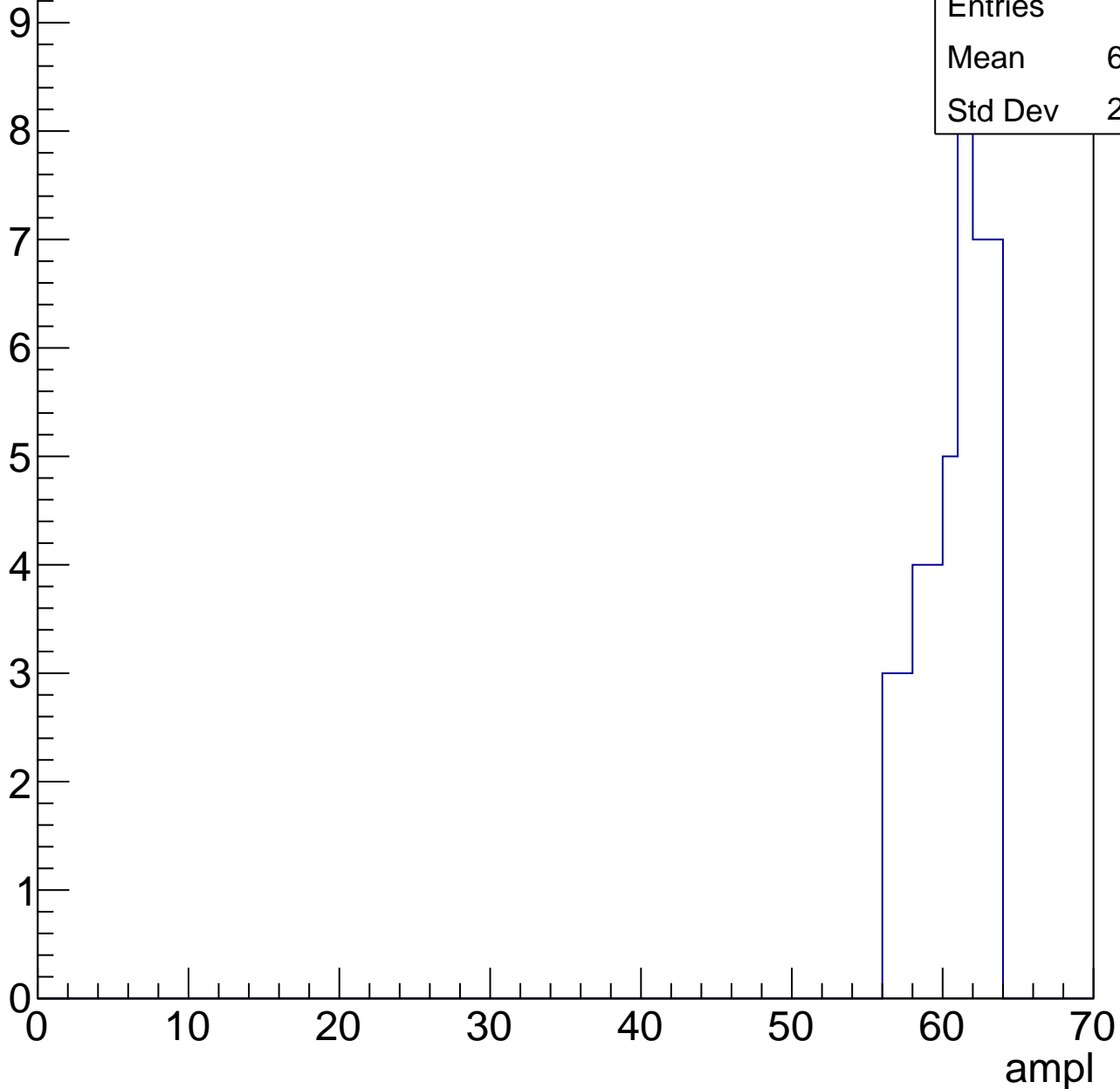


# B0L002S, U8-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

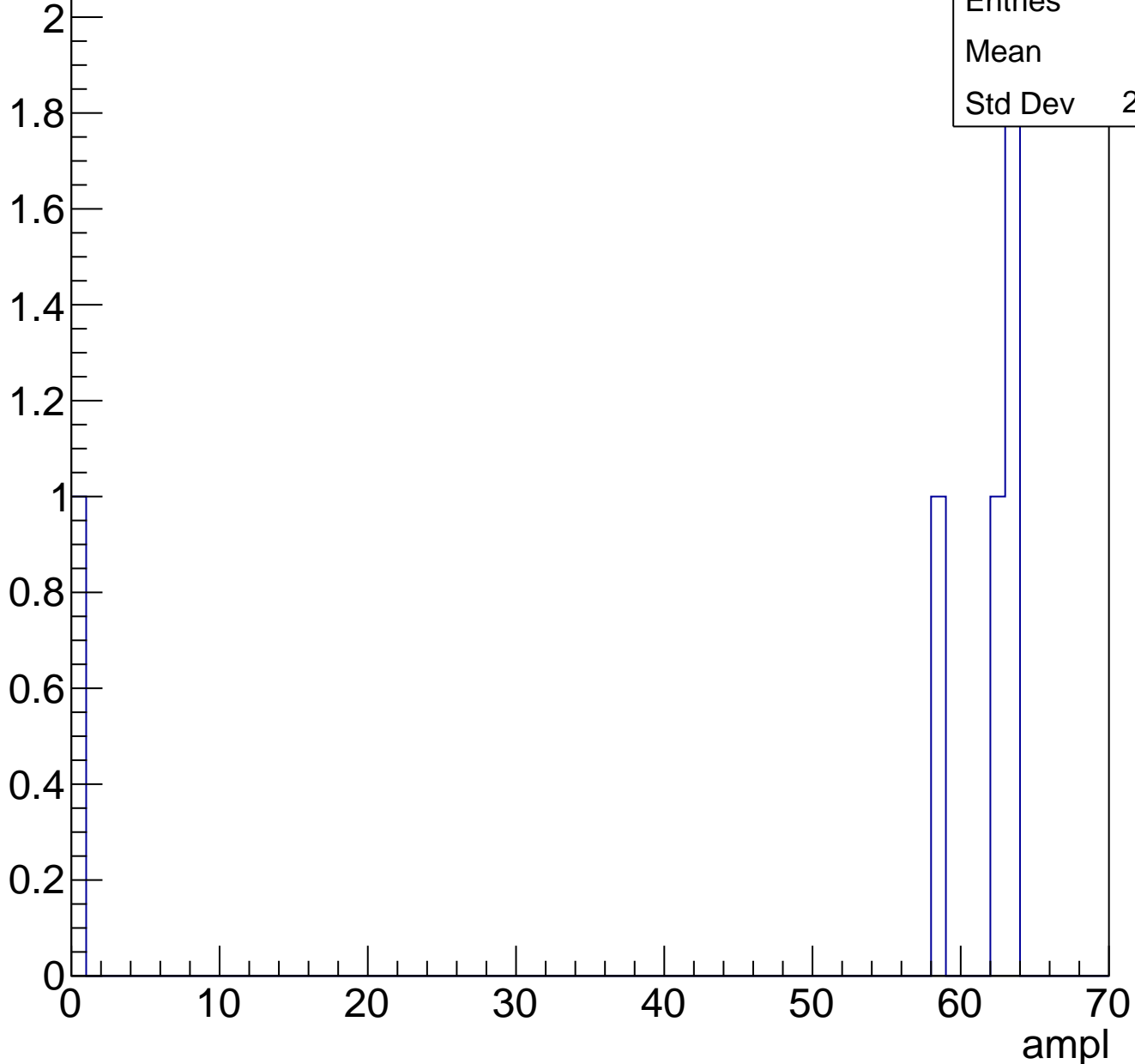
Entries	42
Mean	60.26
Std Dev	2.139



# B0L002S, U8-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

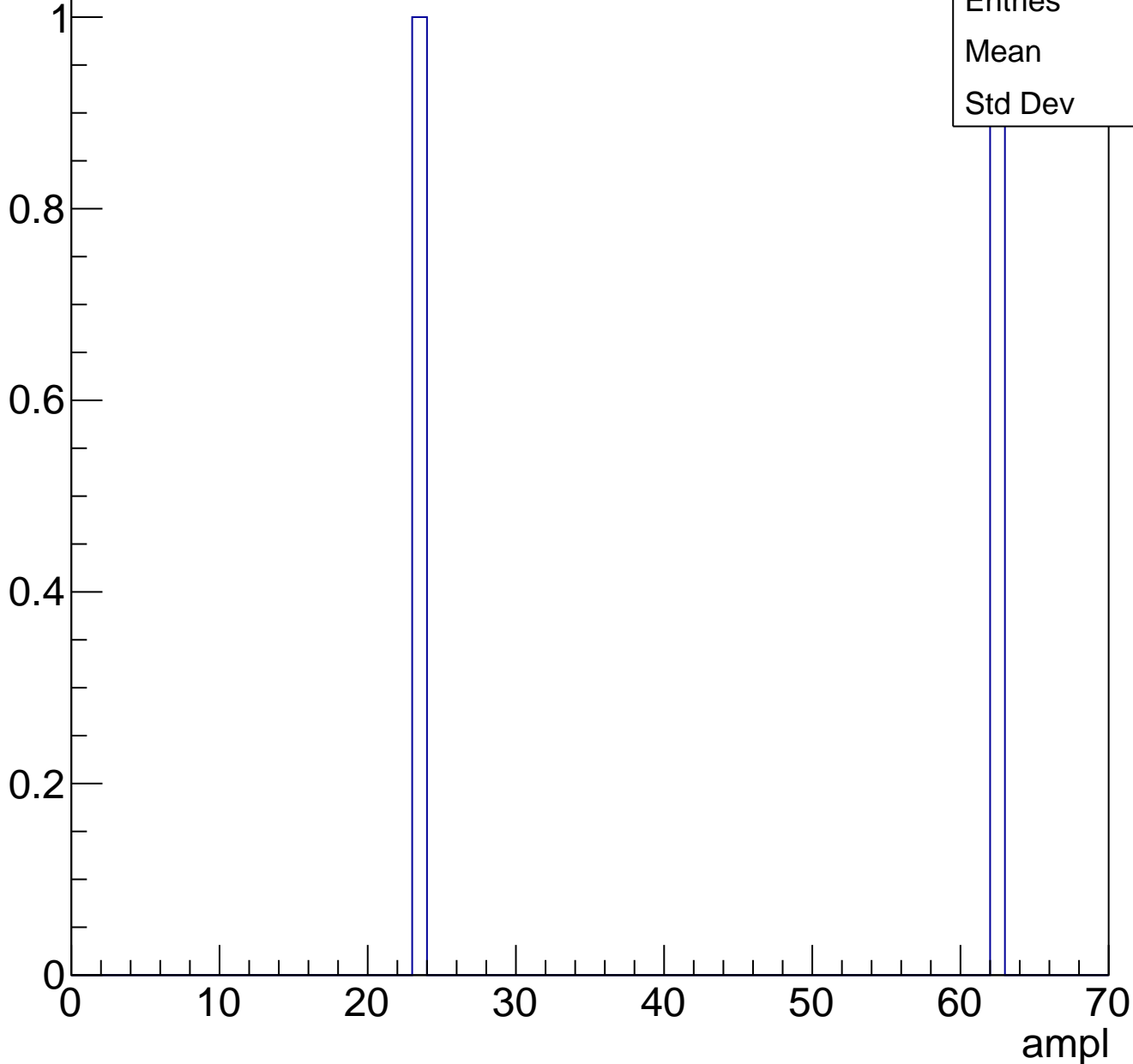




# B0L002S, U8-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch111, adc0

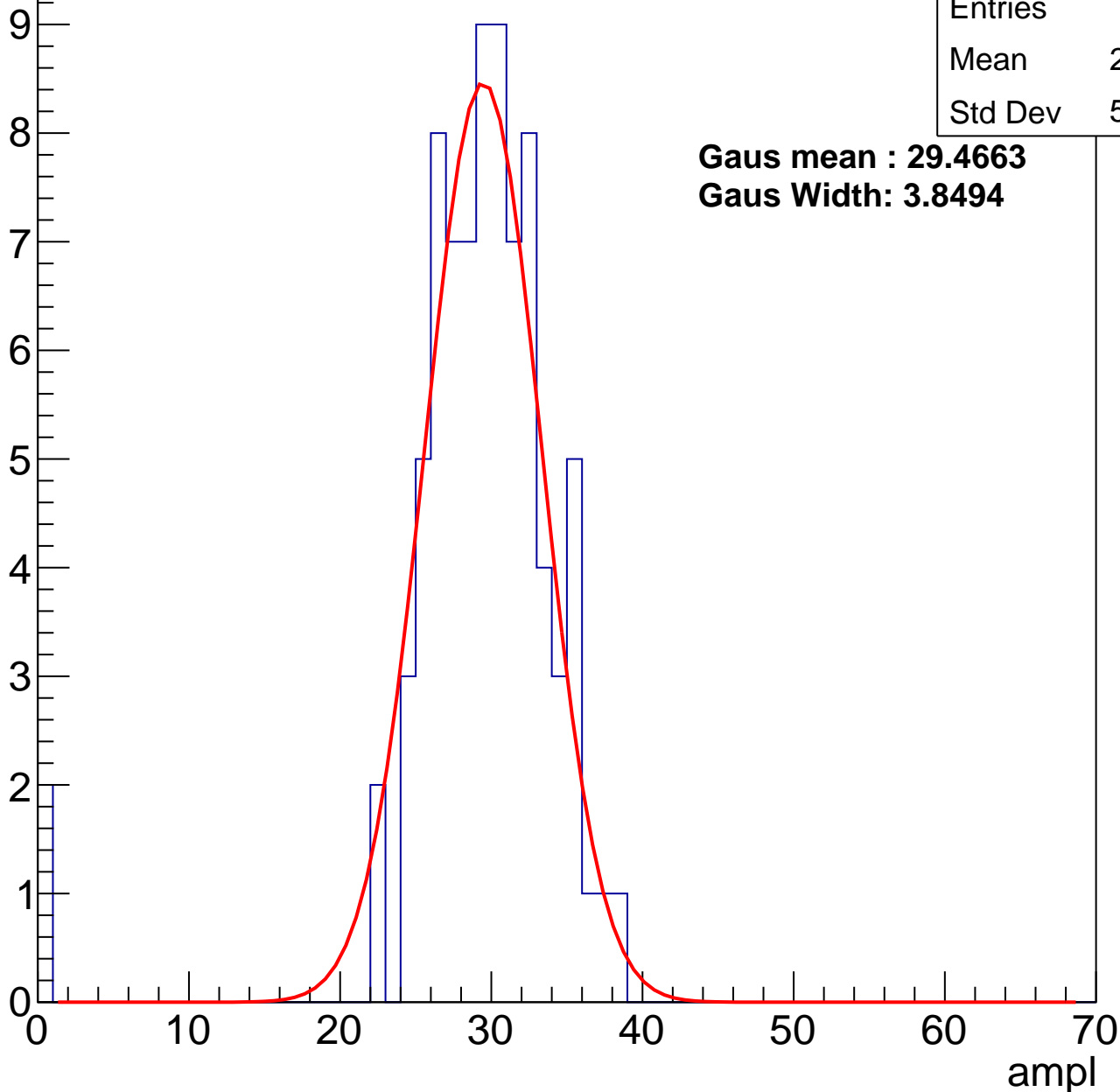
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	82
Mean	28.76
Std Dev	5.695

**Gaus mean : 29.4663**

**Gaus Width: 3.8494**



# B0L002S, U8-ch111, adc1

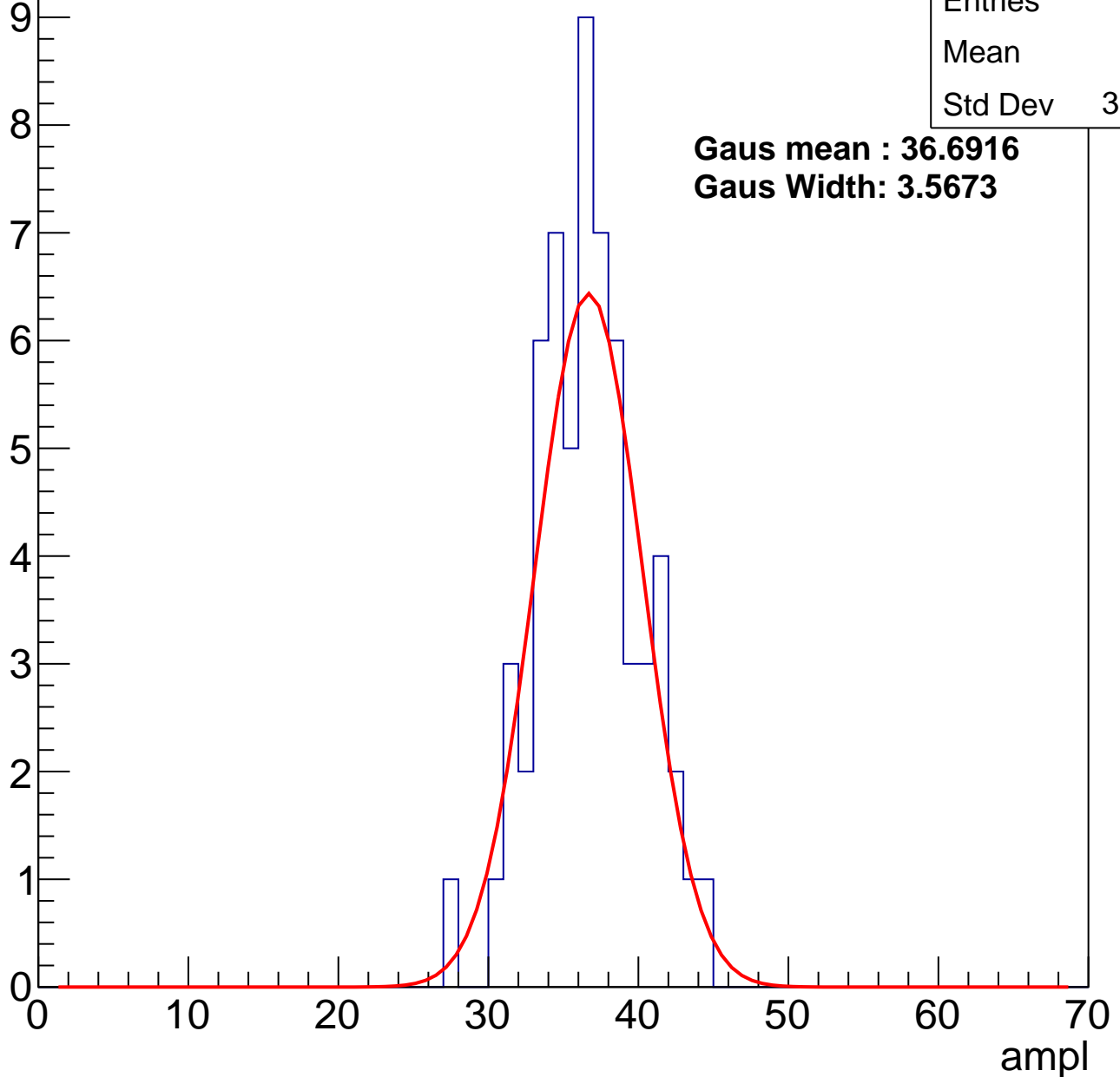
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.2
Std Dev	3.387

**Gaus mean : 36.6916**

**Gaus Width: 3.5673**



# B0L002S, U8-ch111, adc2

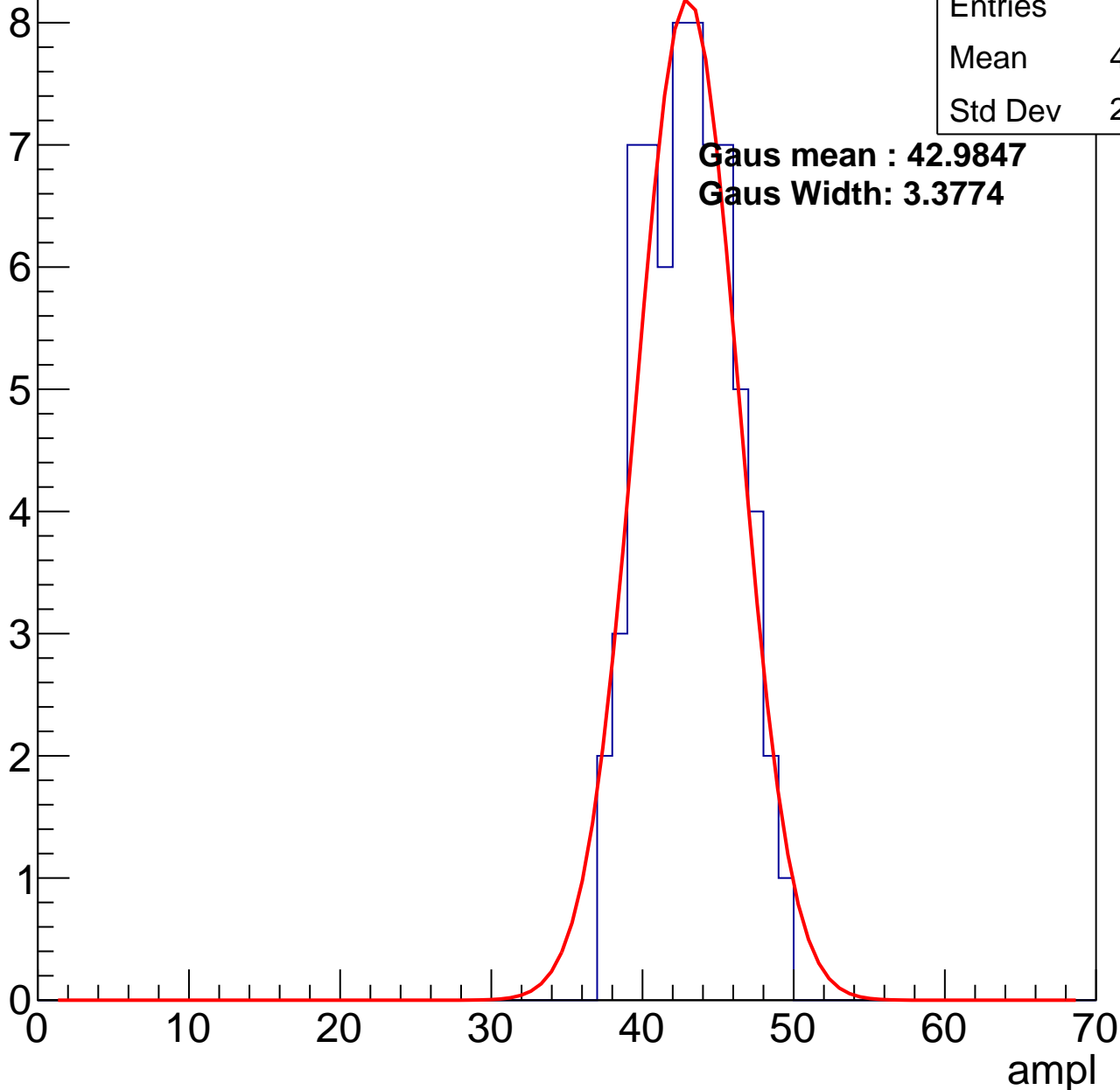
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.58
Std Dev	2.923

**Gaus mean : 42.9847**

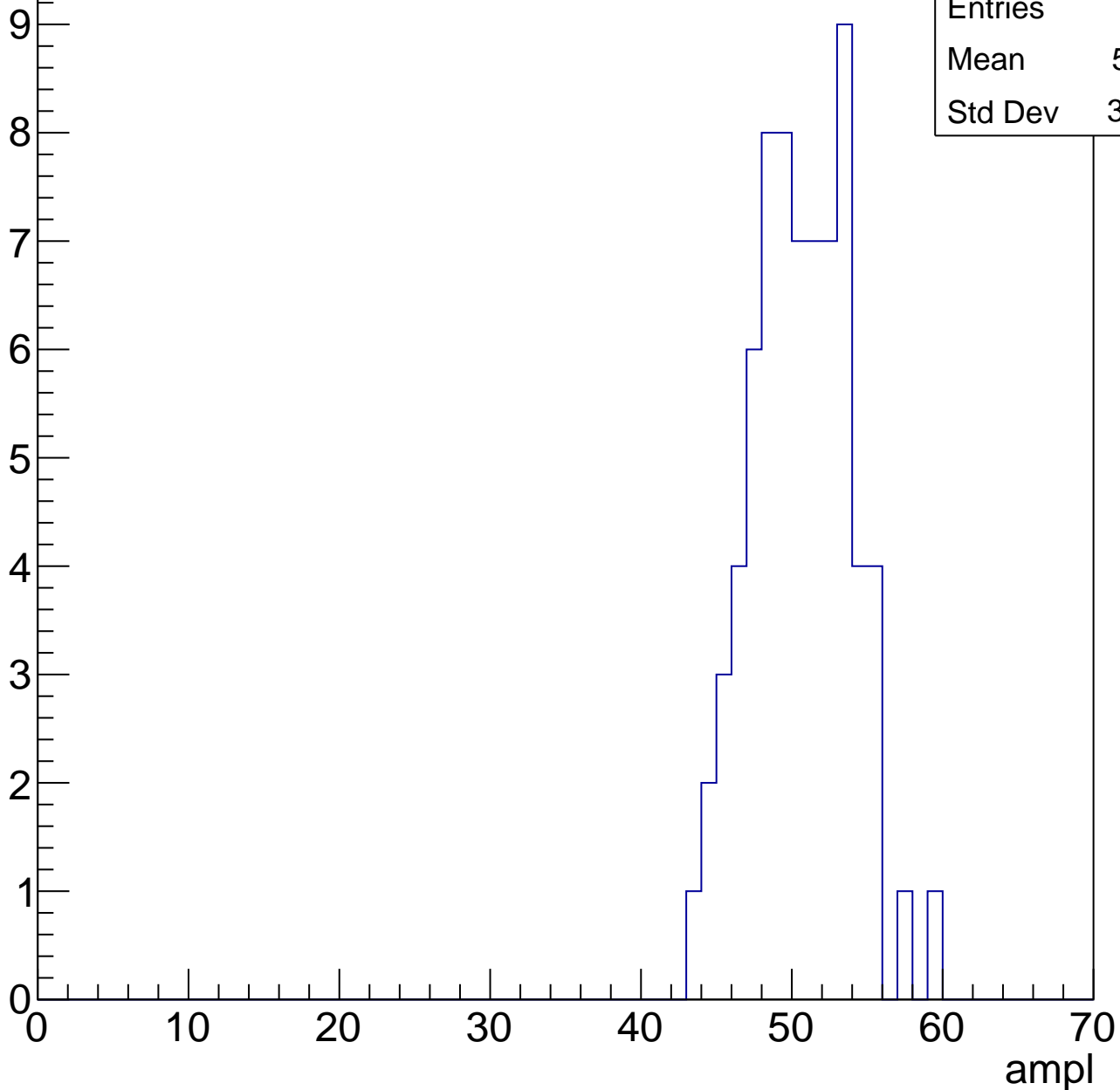
**Gaus Width: 3.3774**



# B0L002S, U8-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



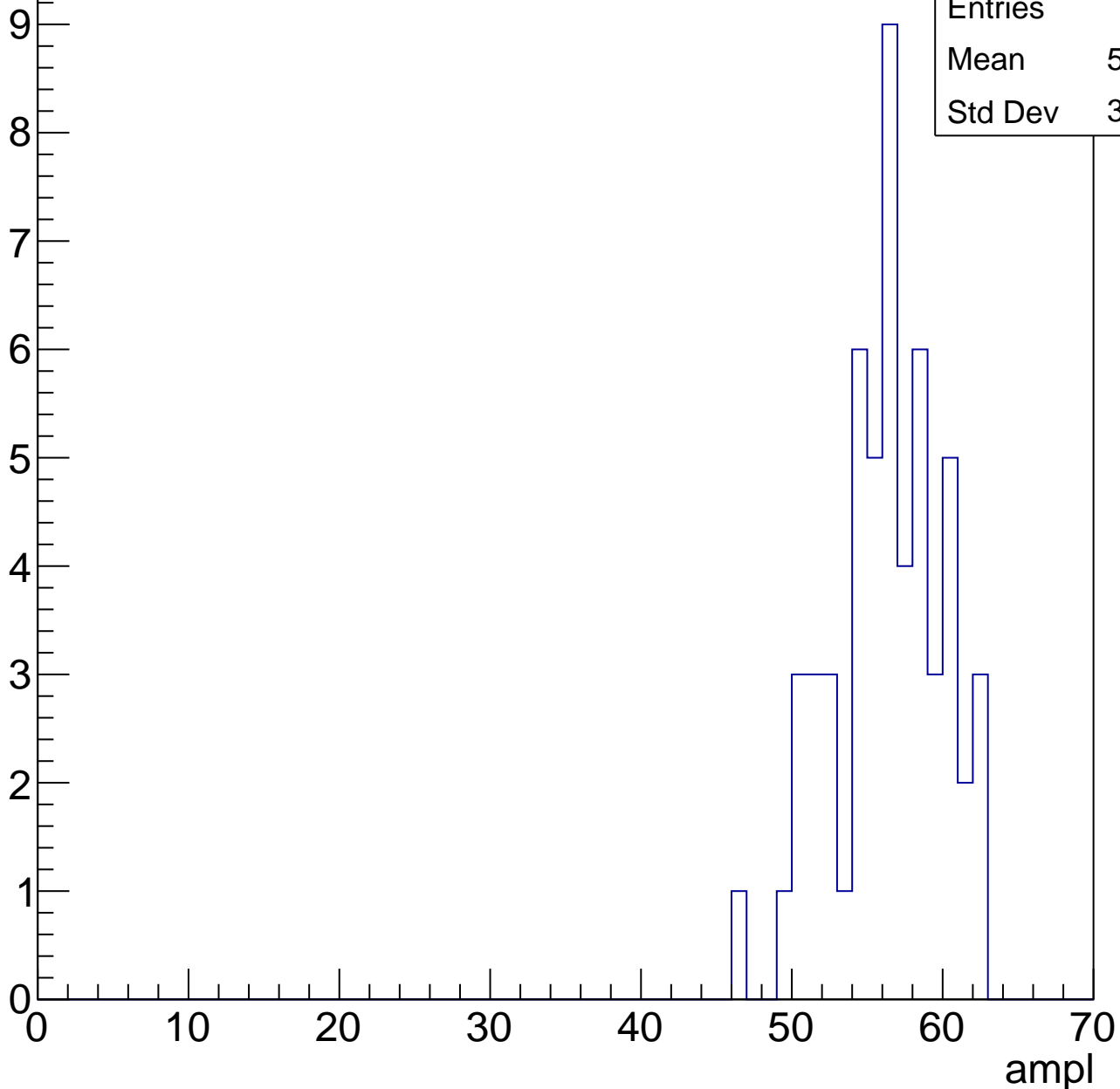
Entries	72
Mean	50.11
Std Dev	3.256

# B0L002S, U8-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	55.84
Std Dev	3.592

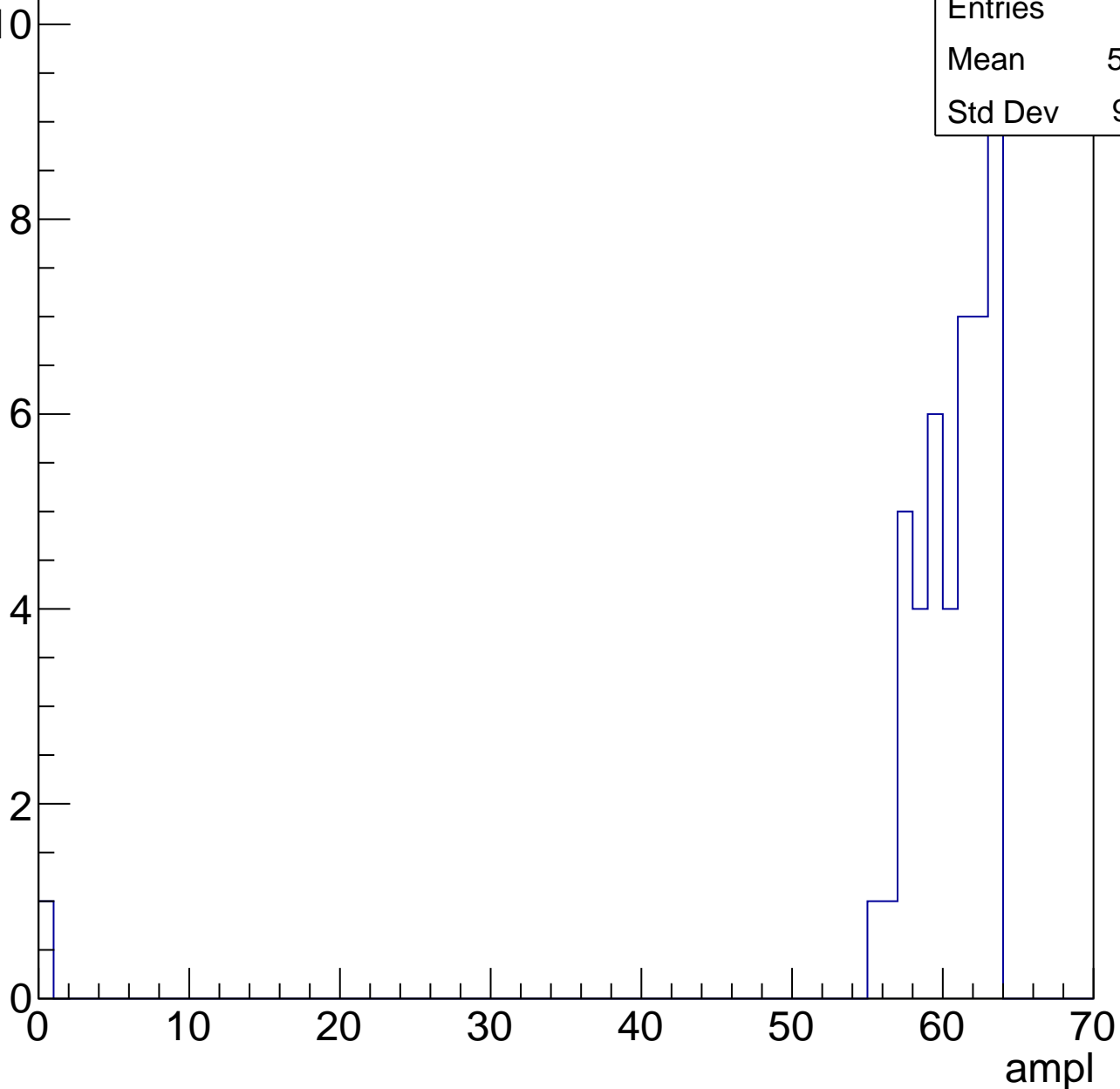


# B0L002S, U8-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

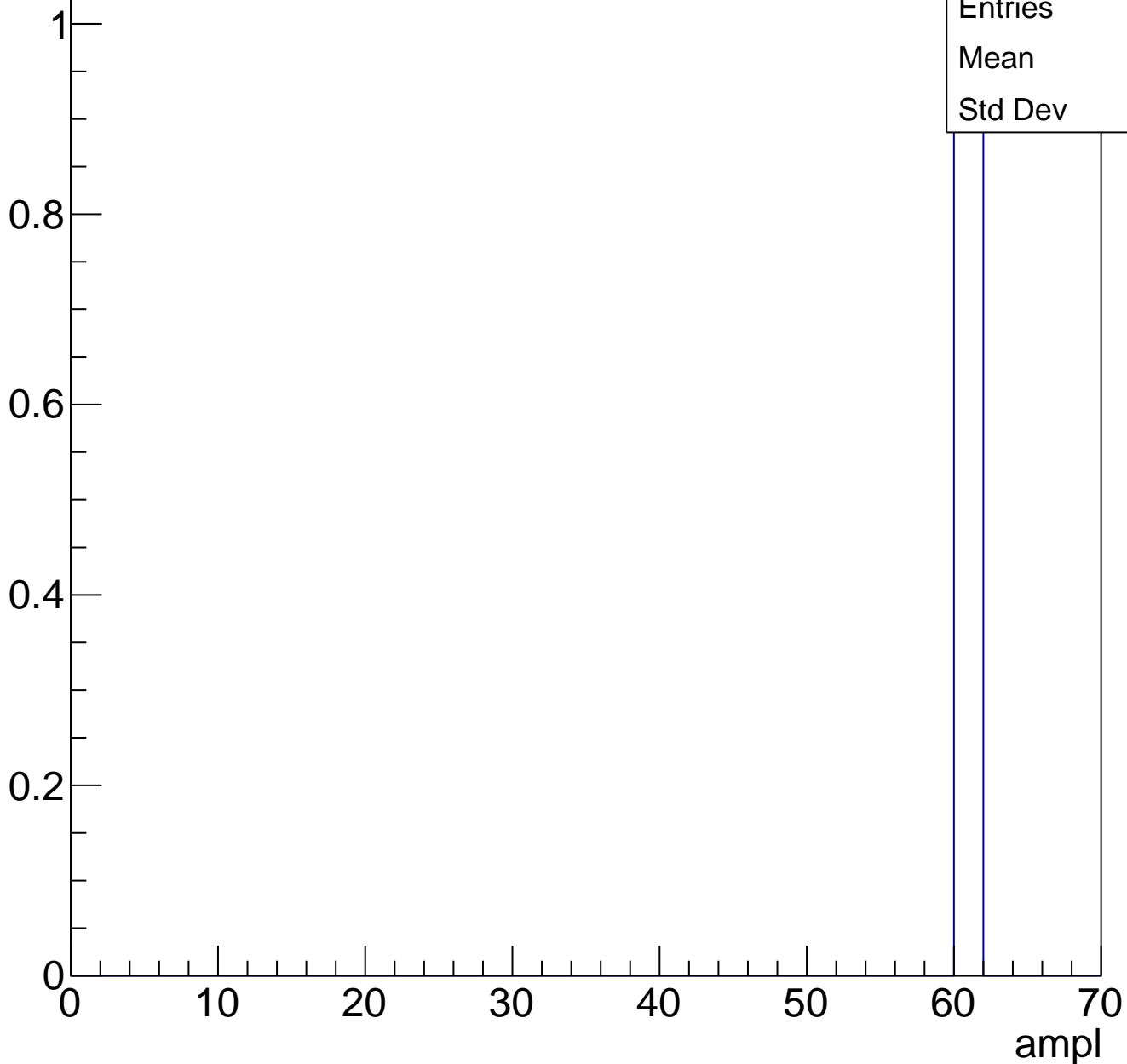
Entries	46
Mean	58.98
Std Dev	9.071



# B0L002S, U8-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch112, adc0

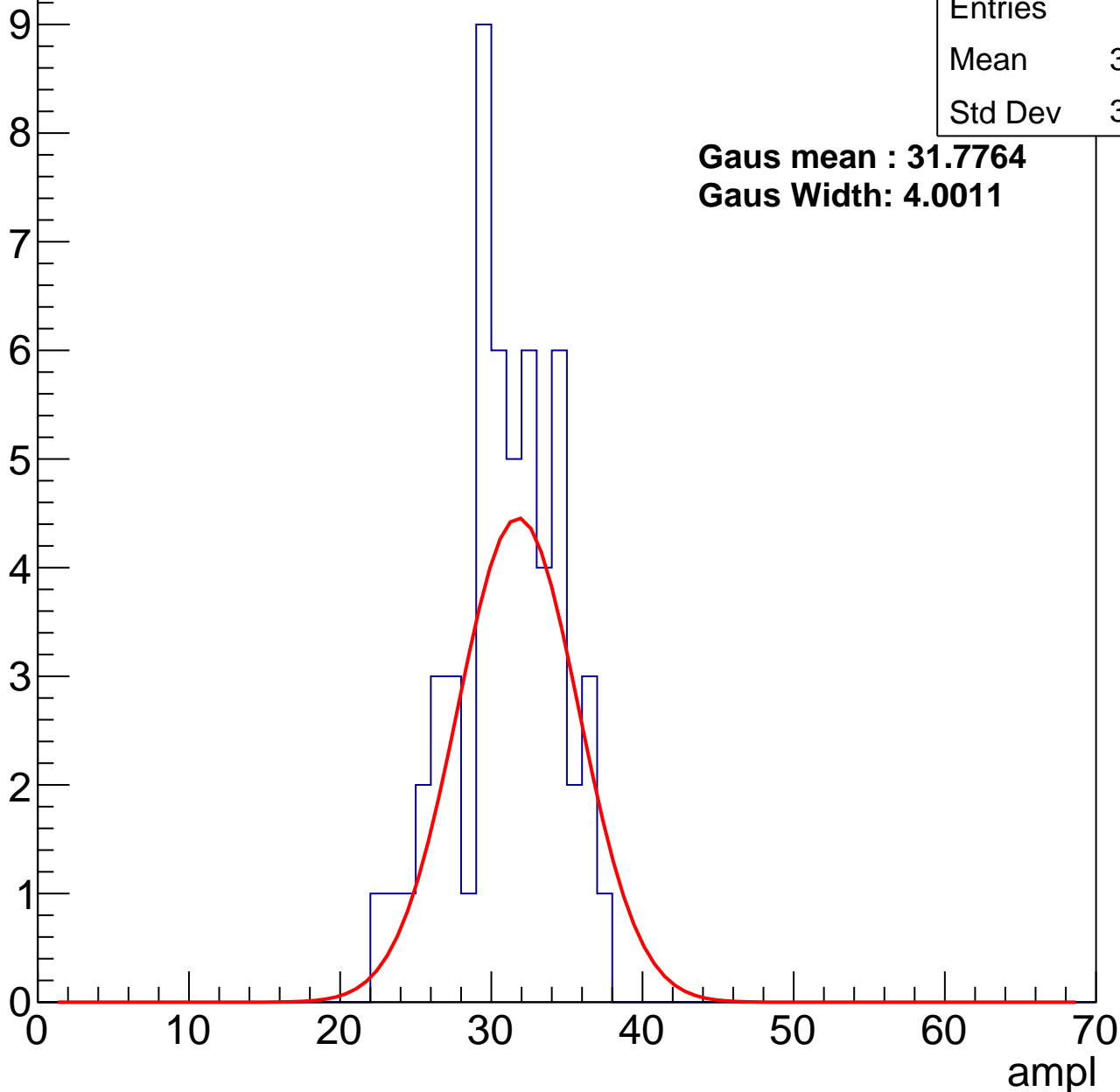
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	30.46
Std Dev	3.452

**Gaus mean : 31.7764**

**Gaus Width: 4.0011**



# B0L002S, U8-ch112, adc1

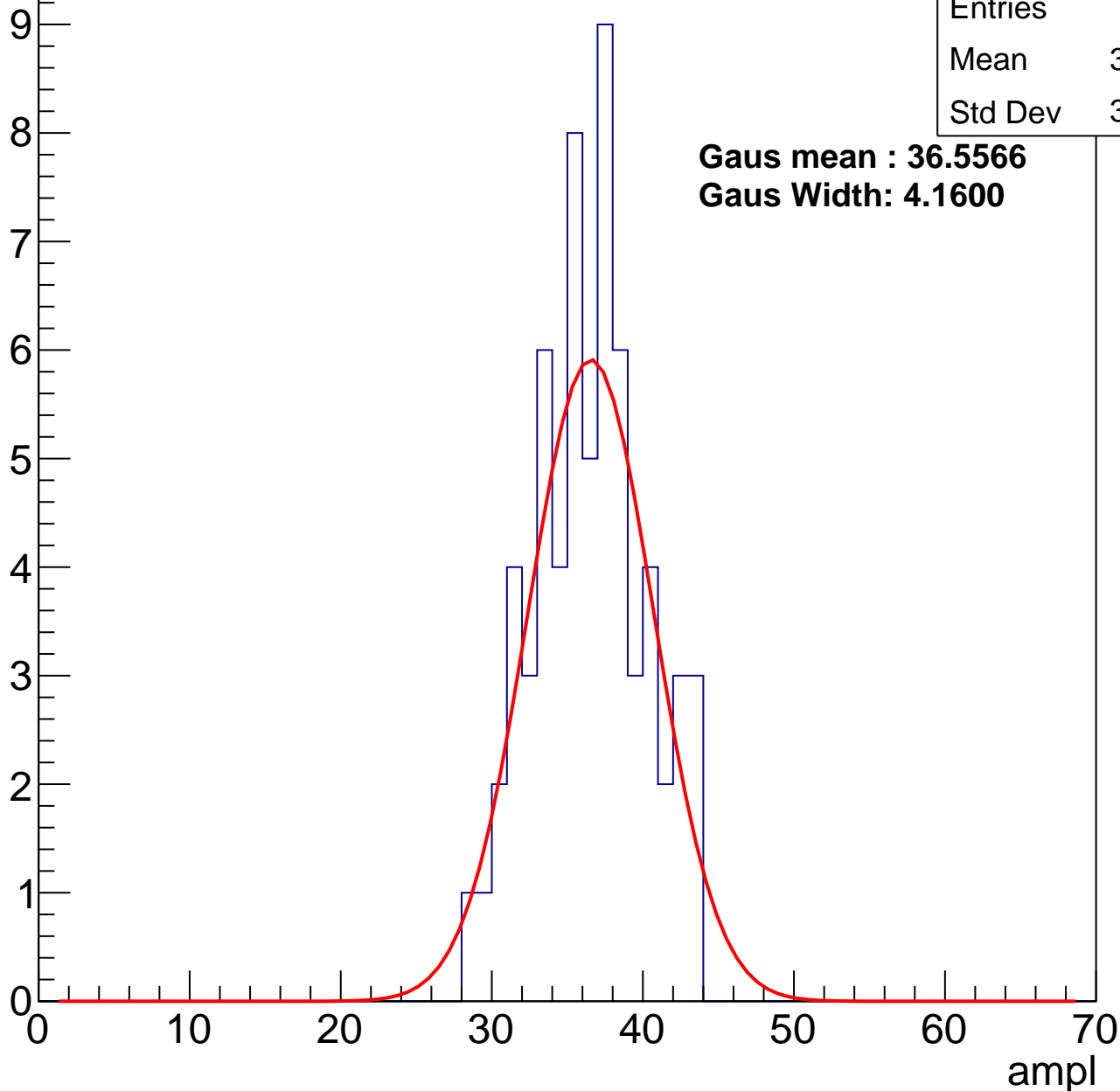
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.03
Std Dev	3.623

**Gaus mean : 36.5566**

**Gaus Width: 4.1600**



# B0L002S, U8-ch112, adc2

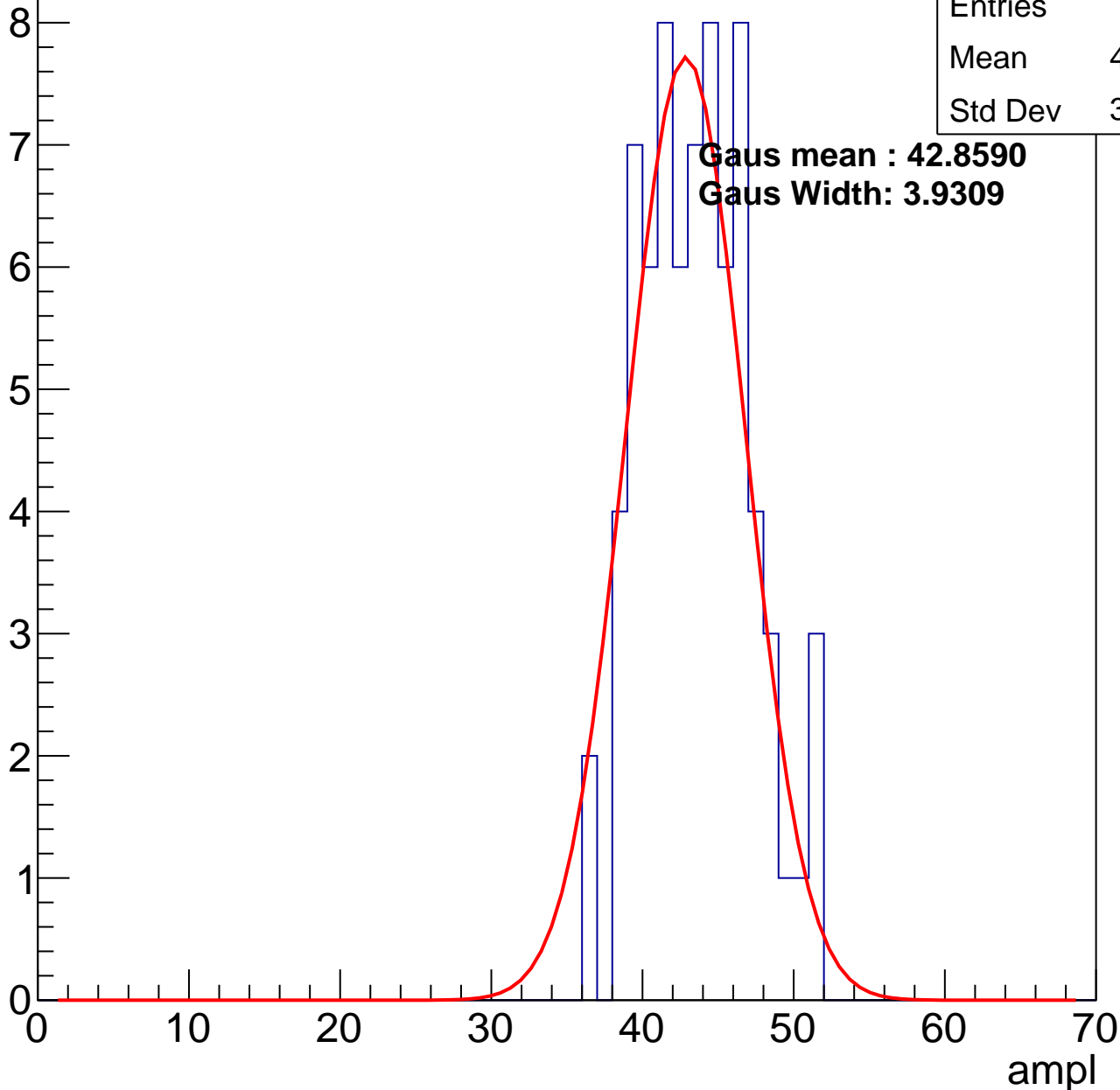
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.14
Std Dev	3.527

**Gaus mean : 42.8590**

**Gaus Width: 3.9309**

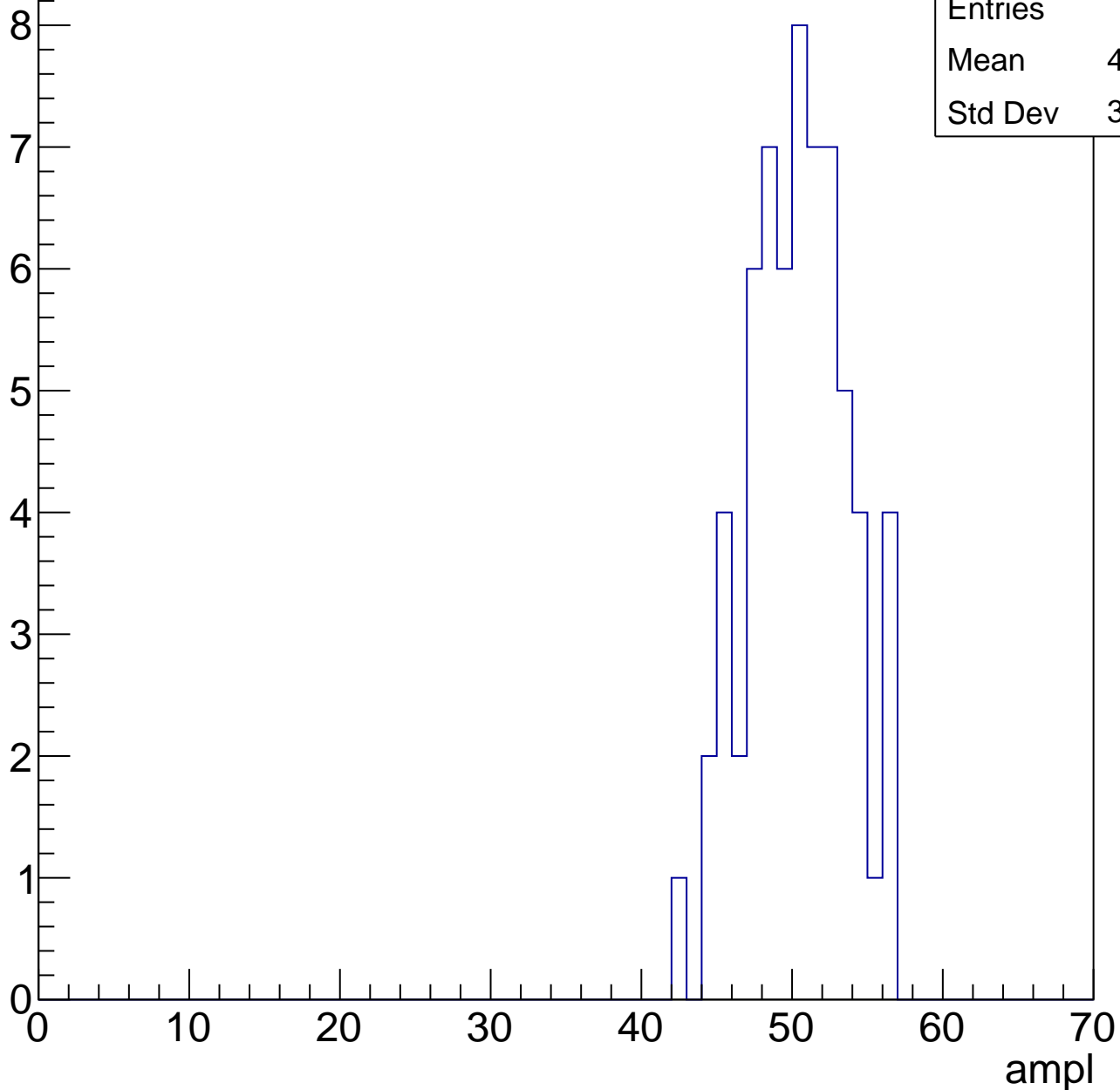


# B0L002S, U8-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.92
Std Dev	3.232

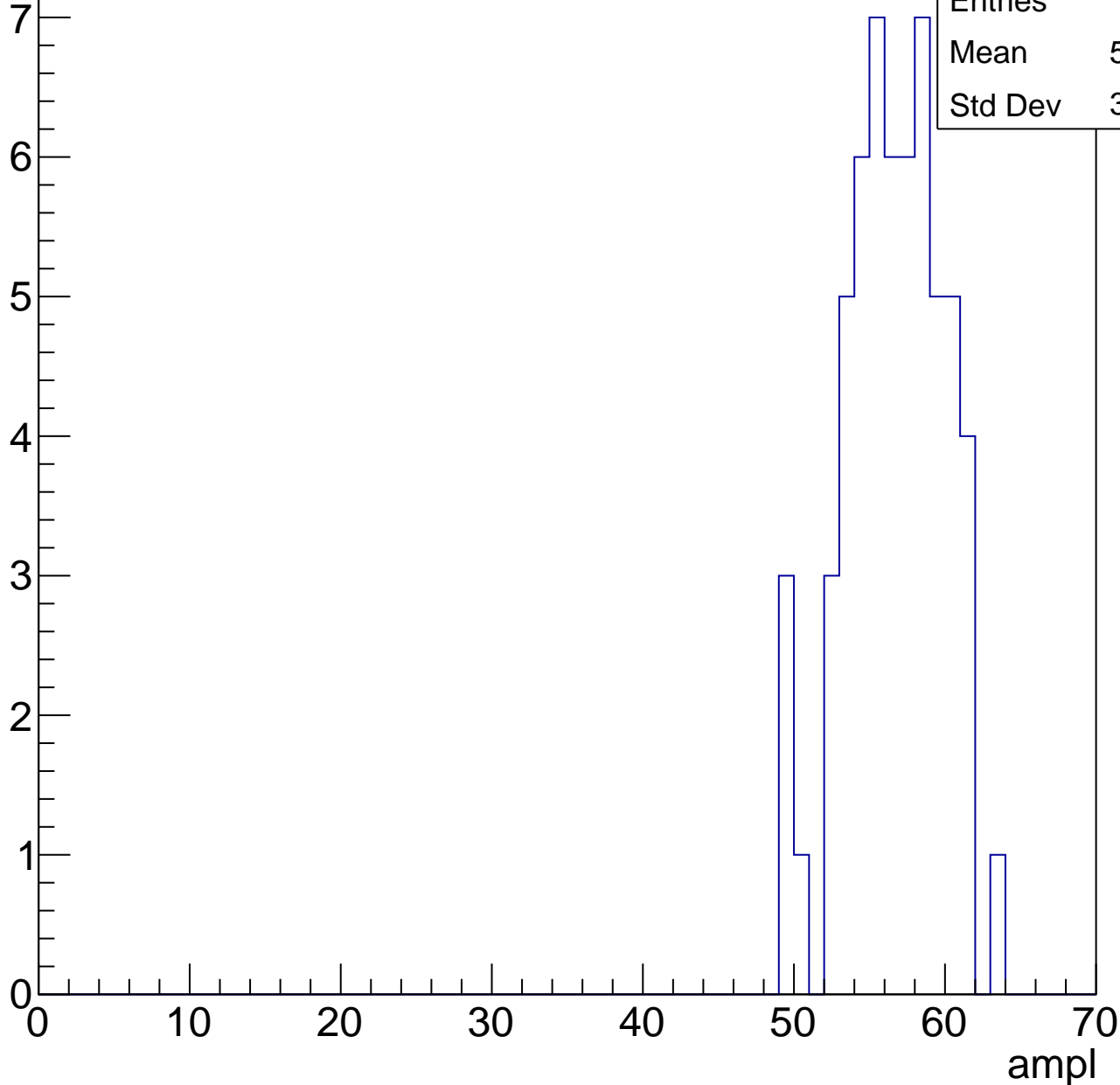


# B0L002S, U8-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.15
Std Dev	3.225

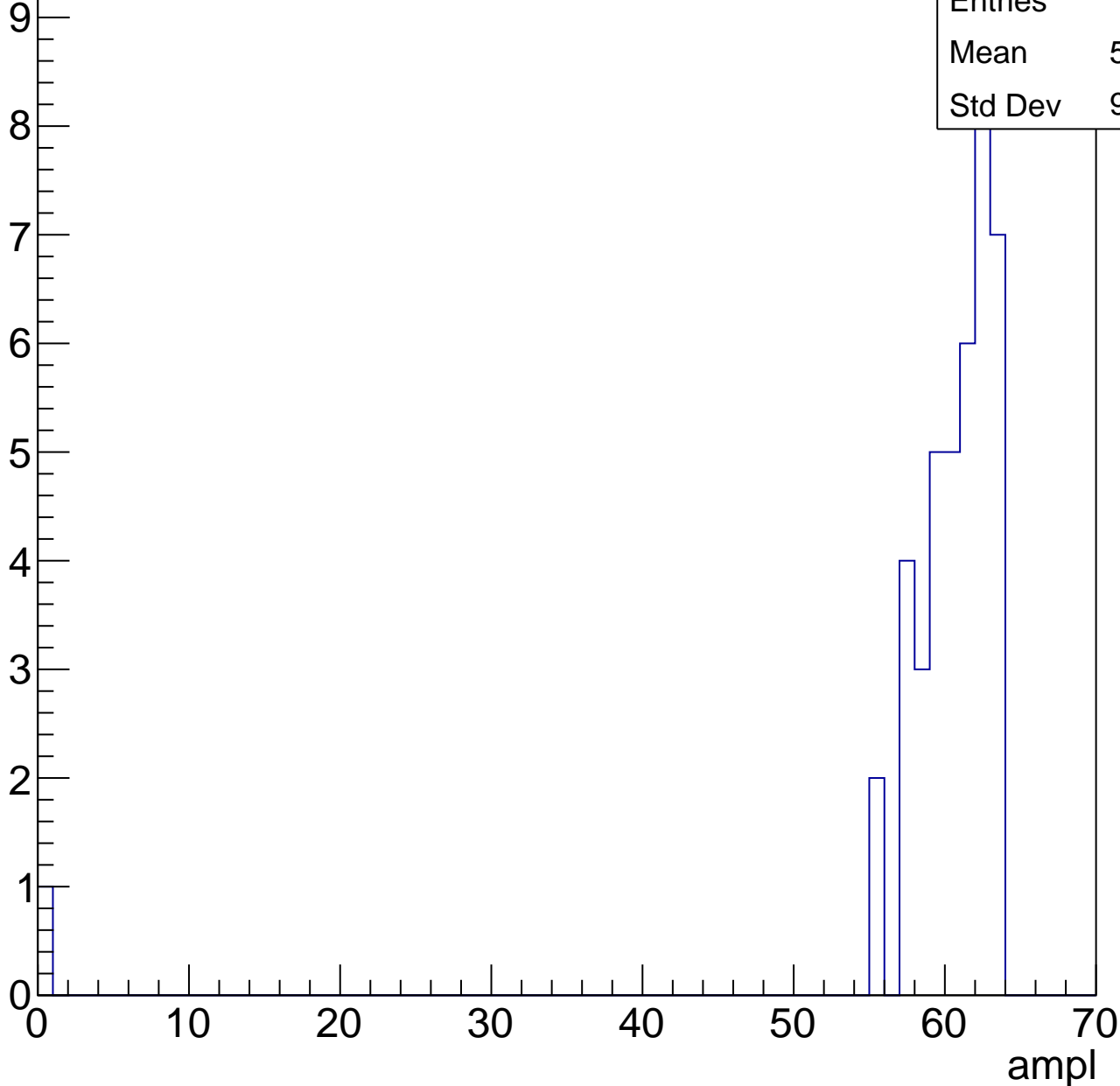


# B0L002S, U8-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.86
Std Dev	9.453



# B0L002S, U8-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

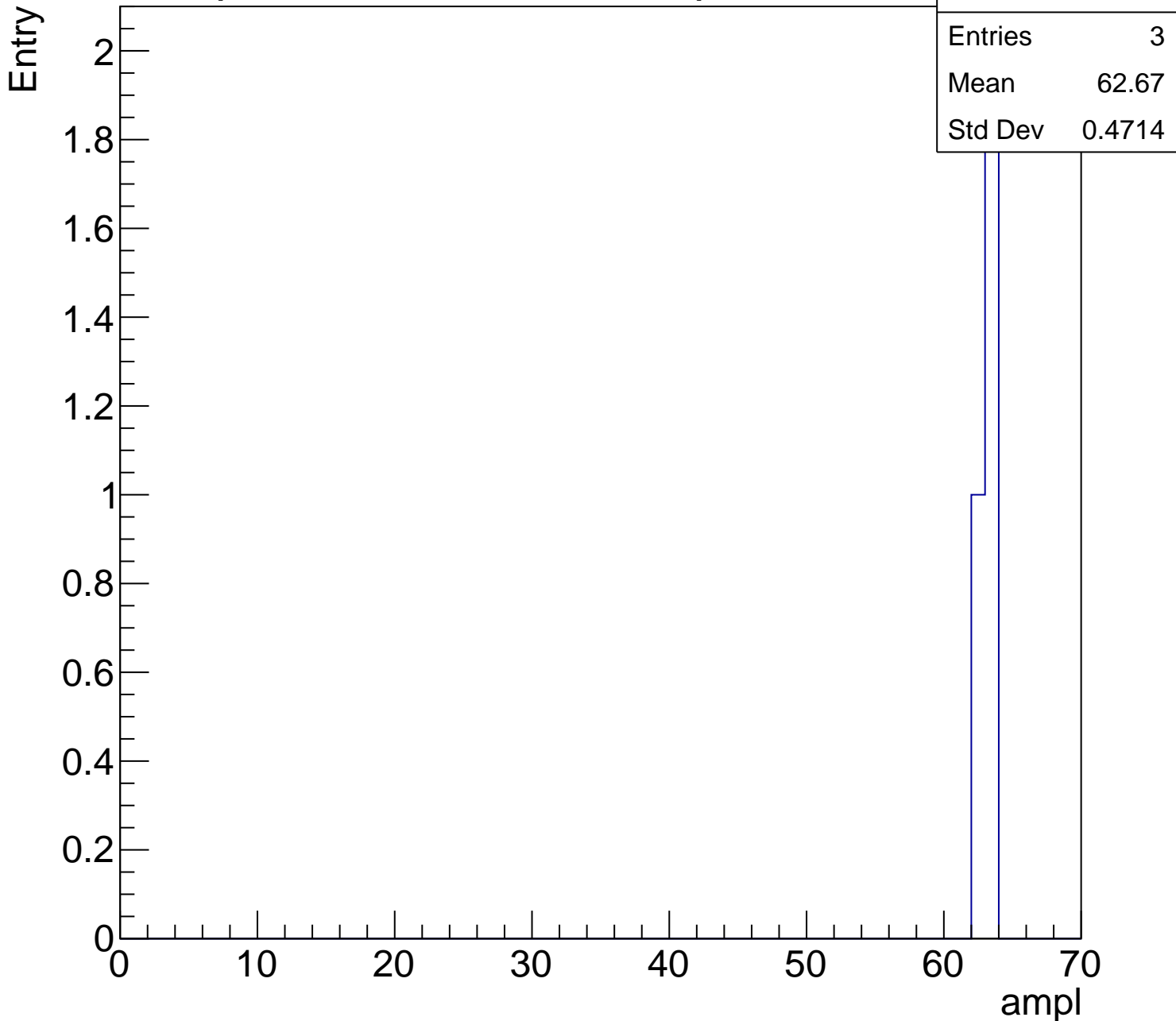
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70





# B0L002S, U8-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	0
Std Dev	0

# B0L002S, U8-ch113, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	93
Mean	30.28
Std Dev	5.759

**Gaus mean : 31.6643**

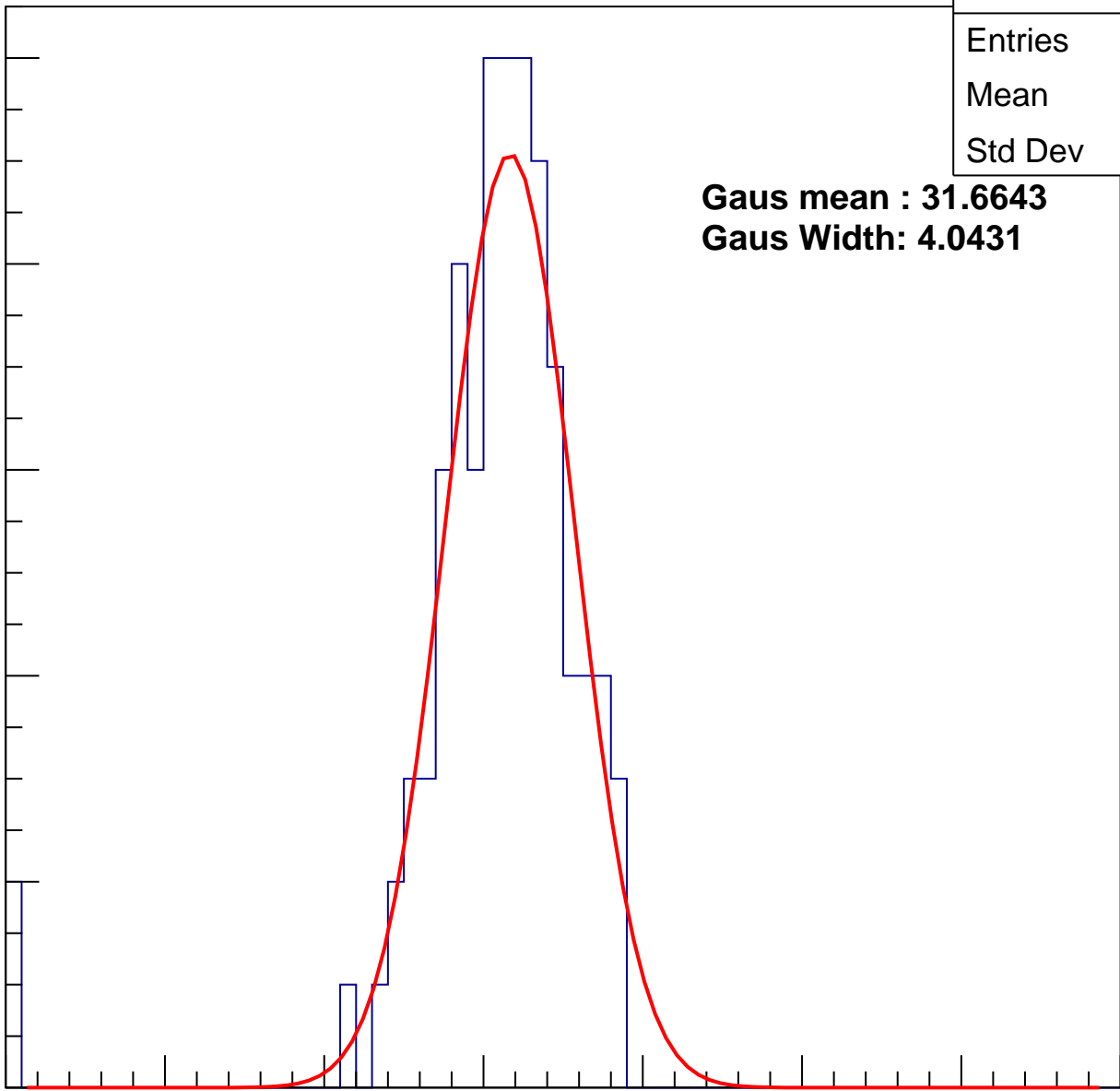
**Gaus Width: 4.0431**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



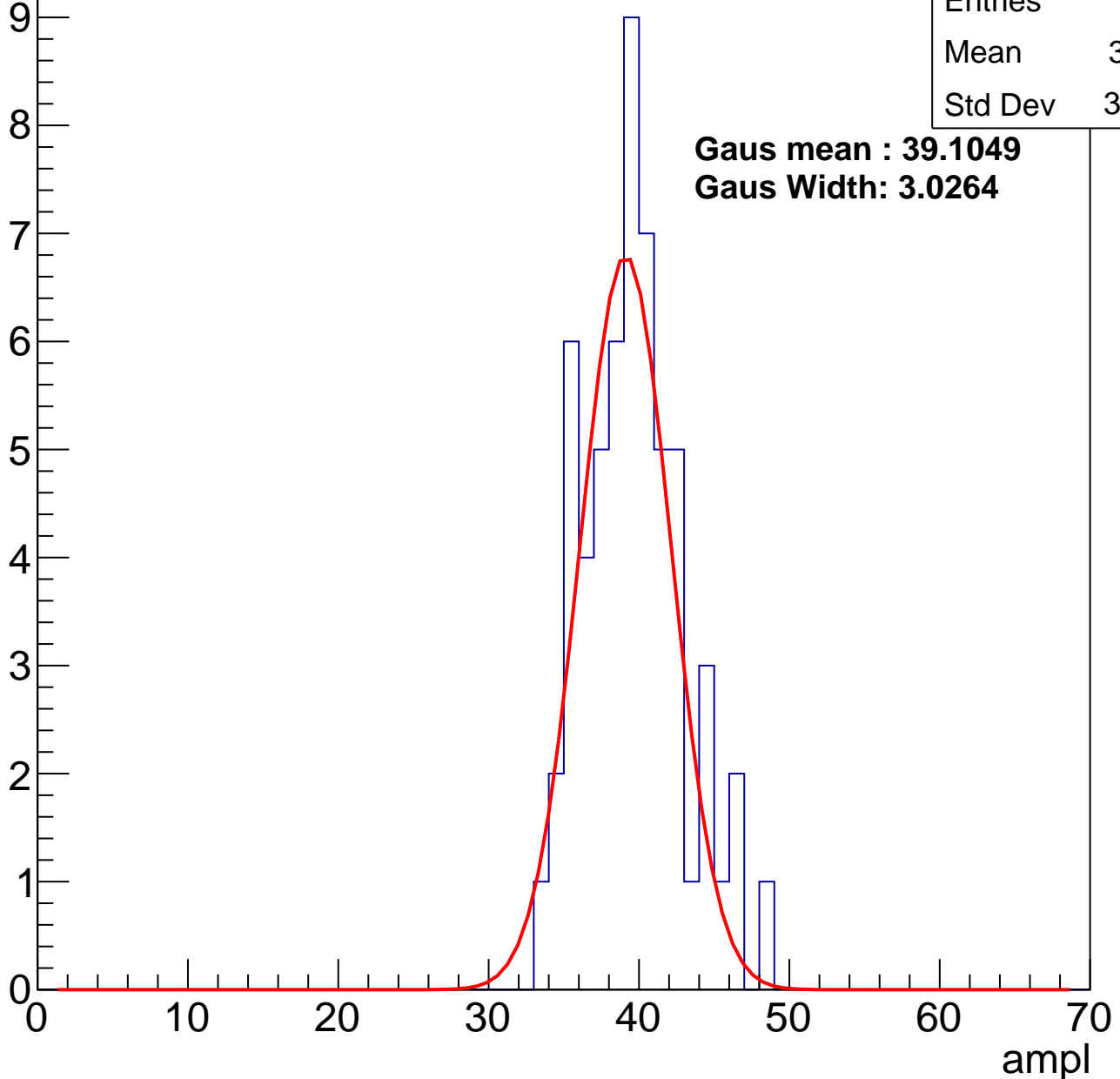
# B0L002S, U8-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	39.21
Std Dev	3.268

**Gaus mean : 39.1049**  
**Gaus Width: 3.0264**



# B0L002S, U8-ch113, adc2

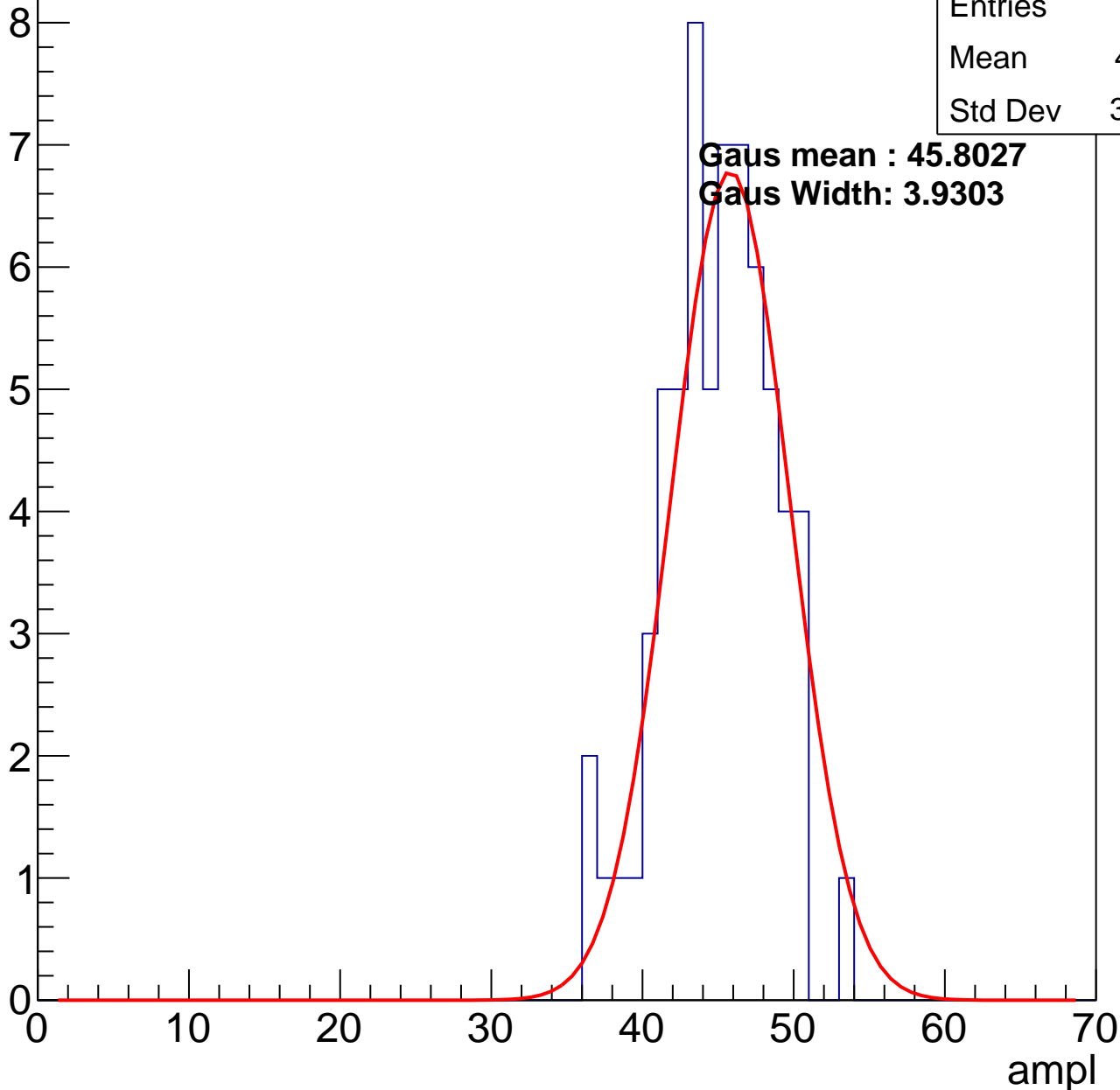
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.51
Std Dev	3.587

**Gaus mean : 45.8027**

**Gaus Width: 3.9303**

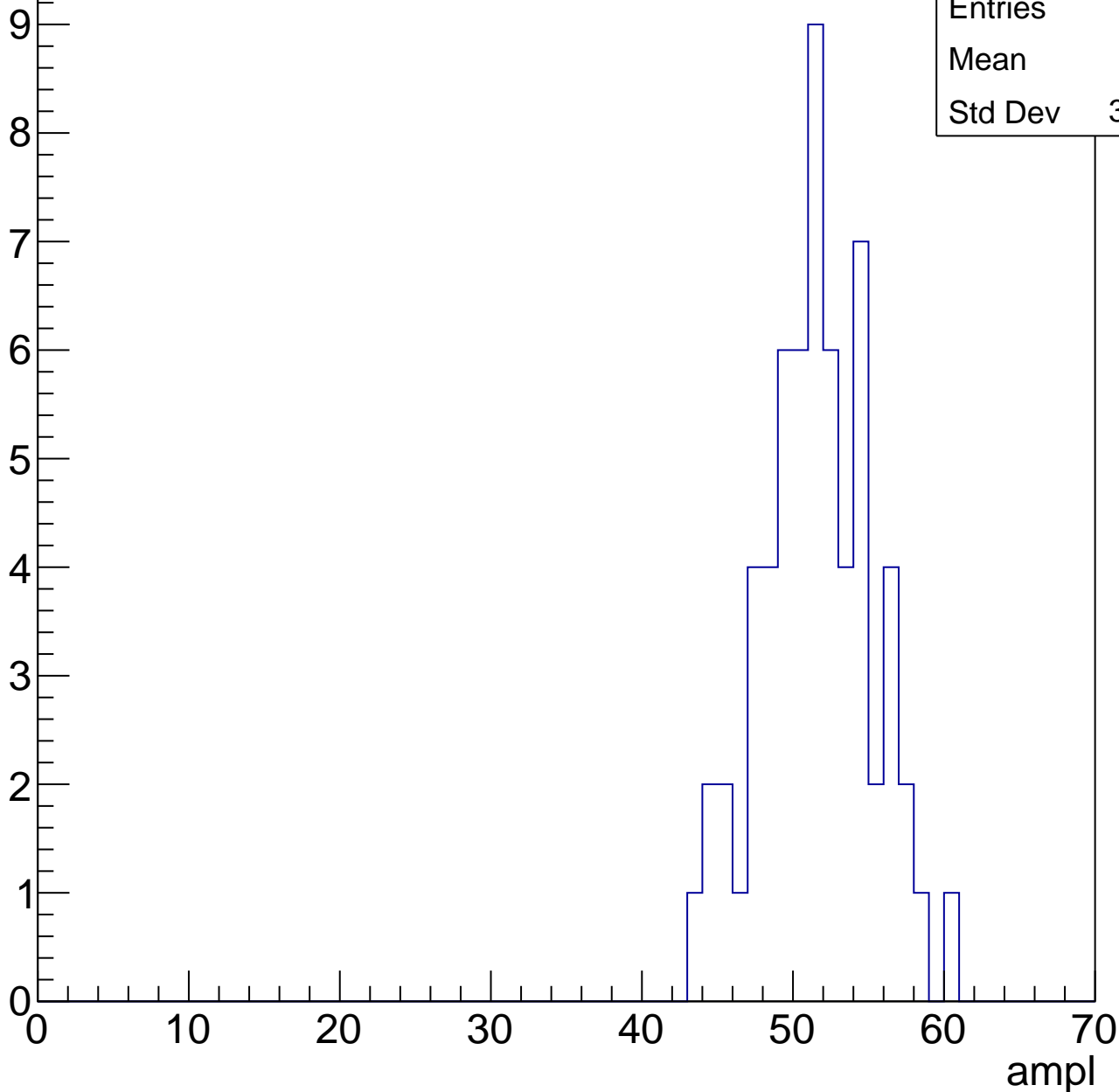


# B0L002S, U8-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.1
Std Dev	3.609

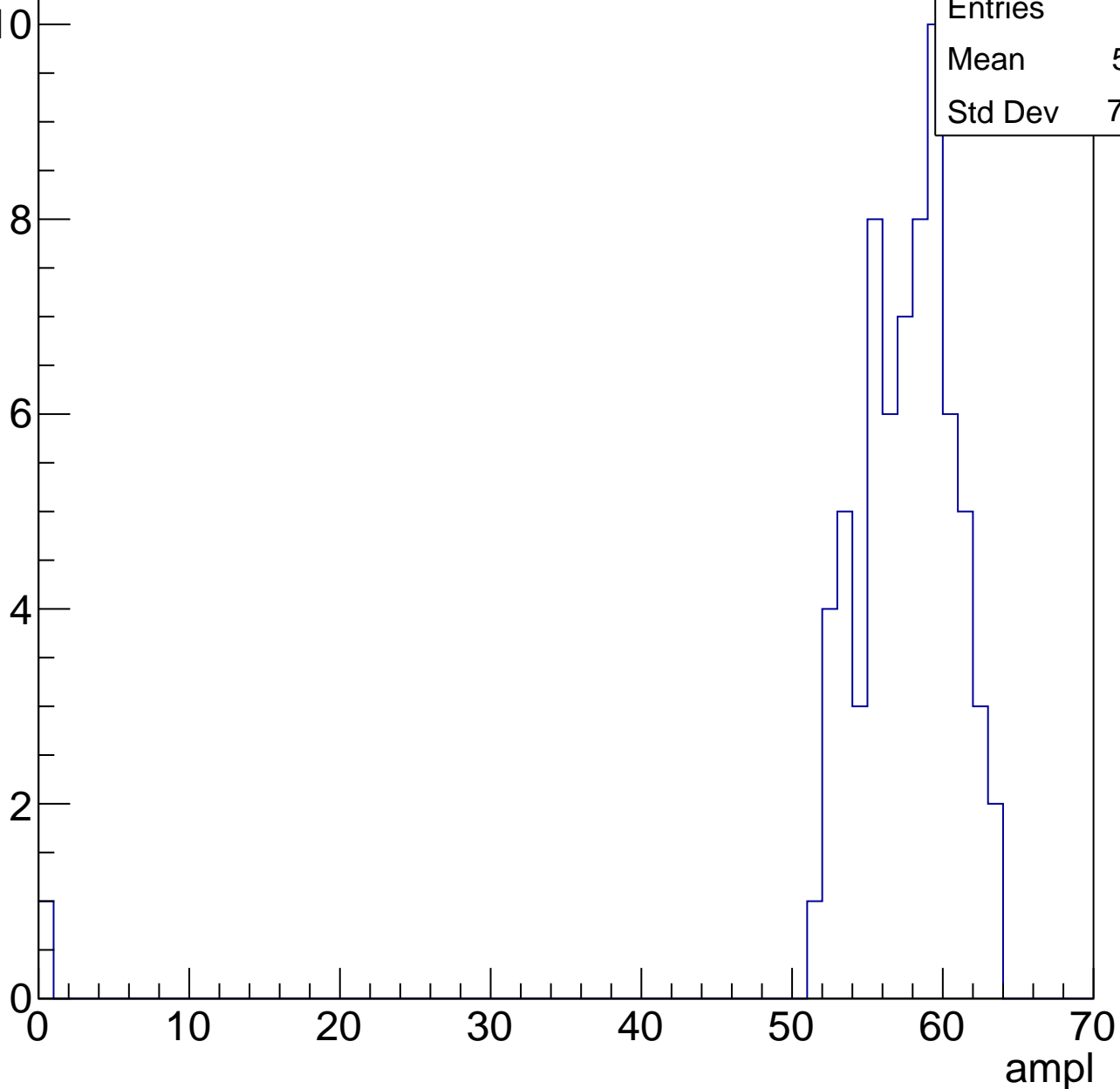


# B0L002S, U8-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	56.41
Std Dev	7.455

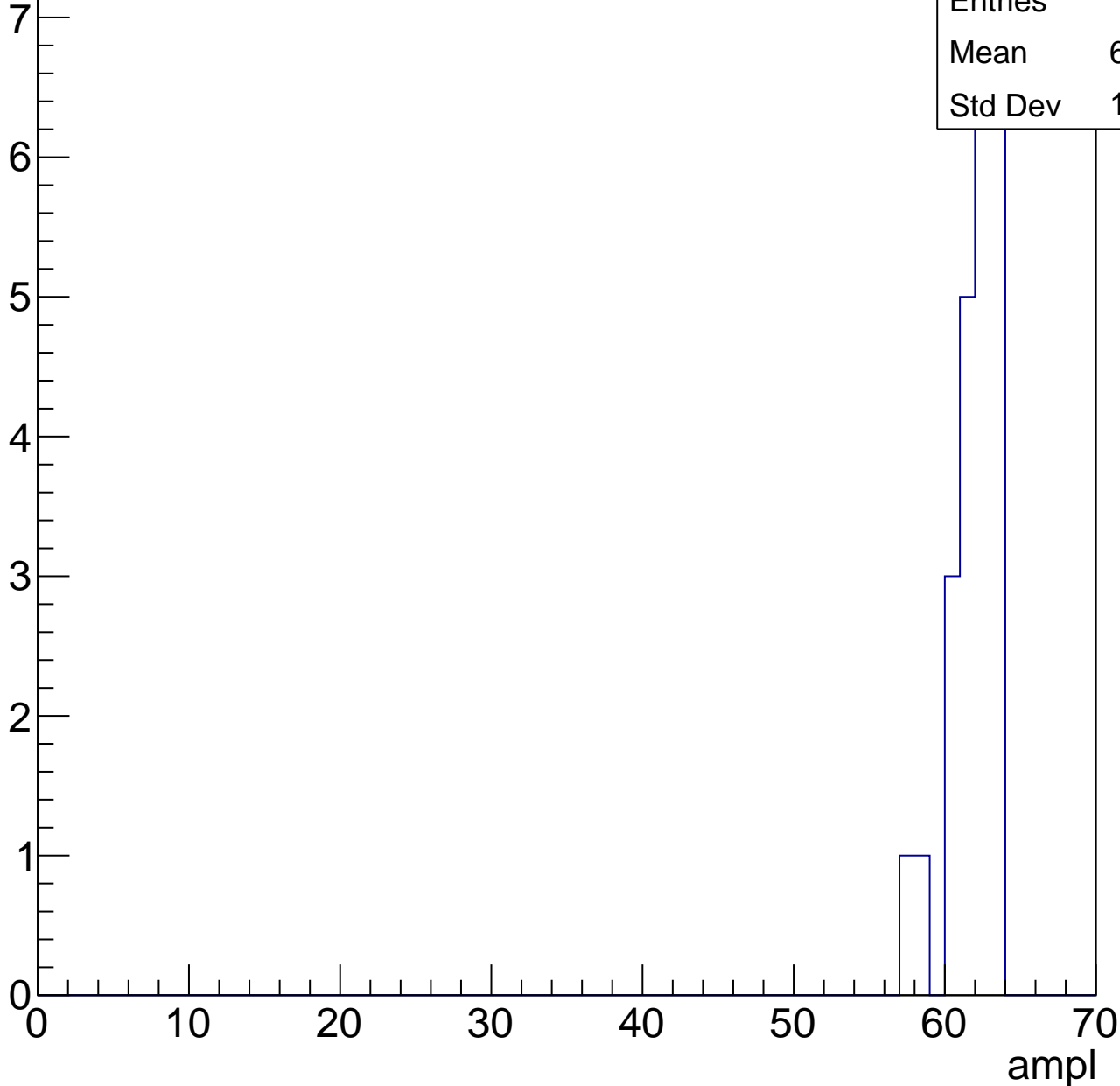


# B0L002S, U8-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.46
Std Dev	1.554



# B0L002S, U8-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch114, adc0

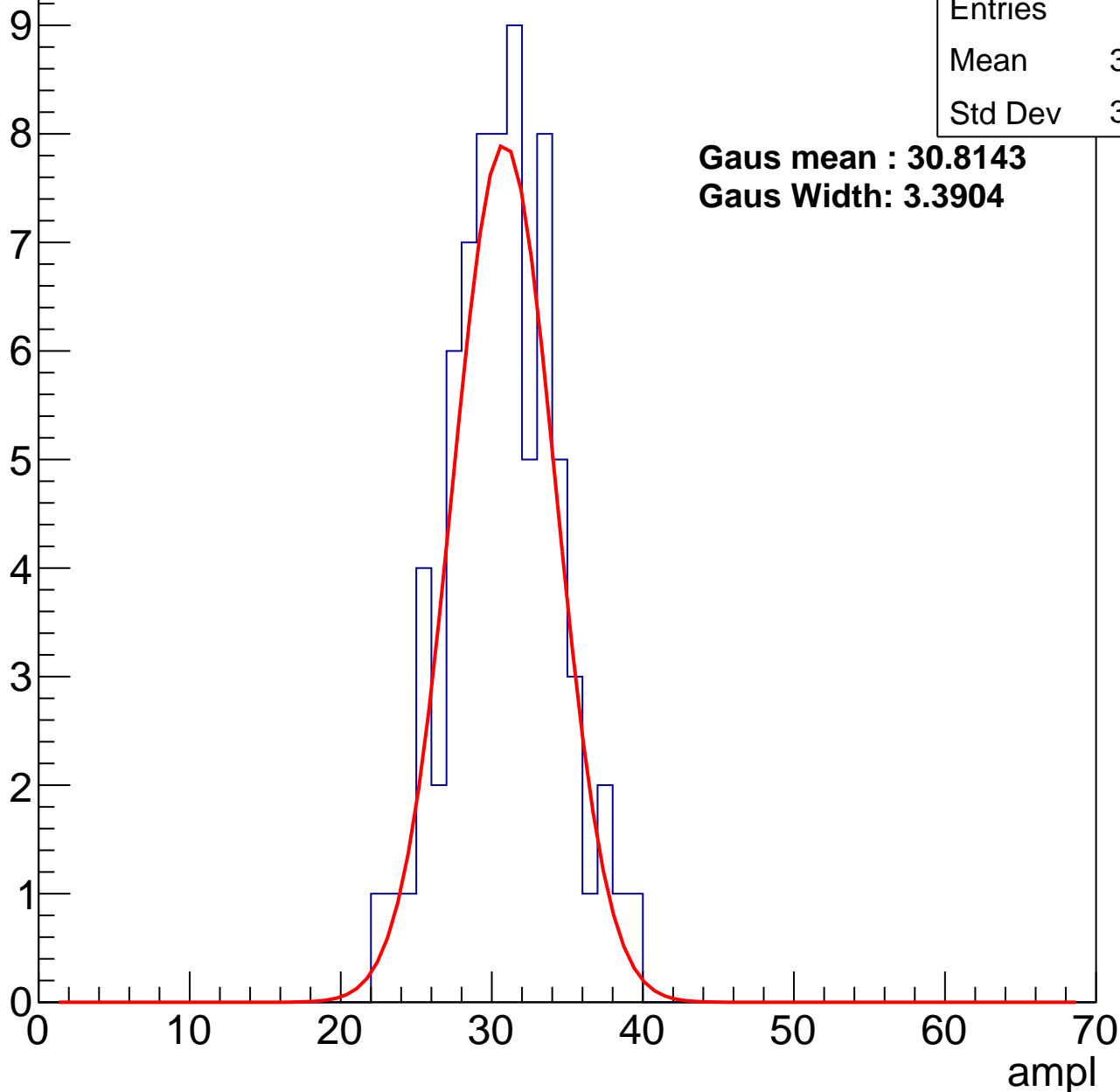
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.36
Std Dev	3.505

**Gaus mean : 30.8143**

**Gaus Width: 3.3904**



# B0L002S, U8-ch114, adc1

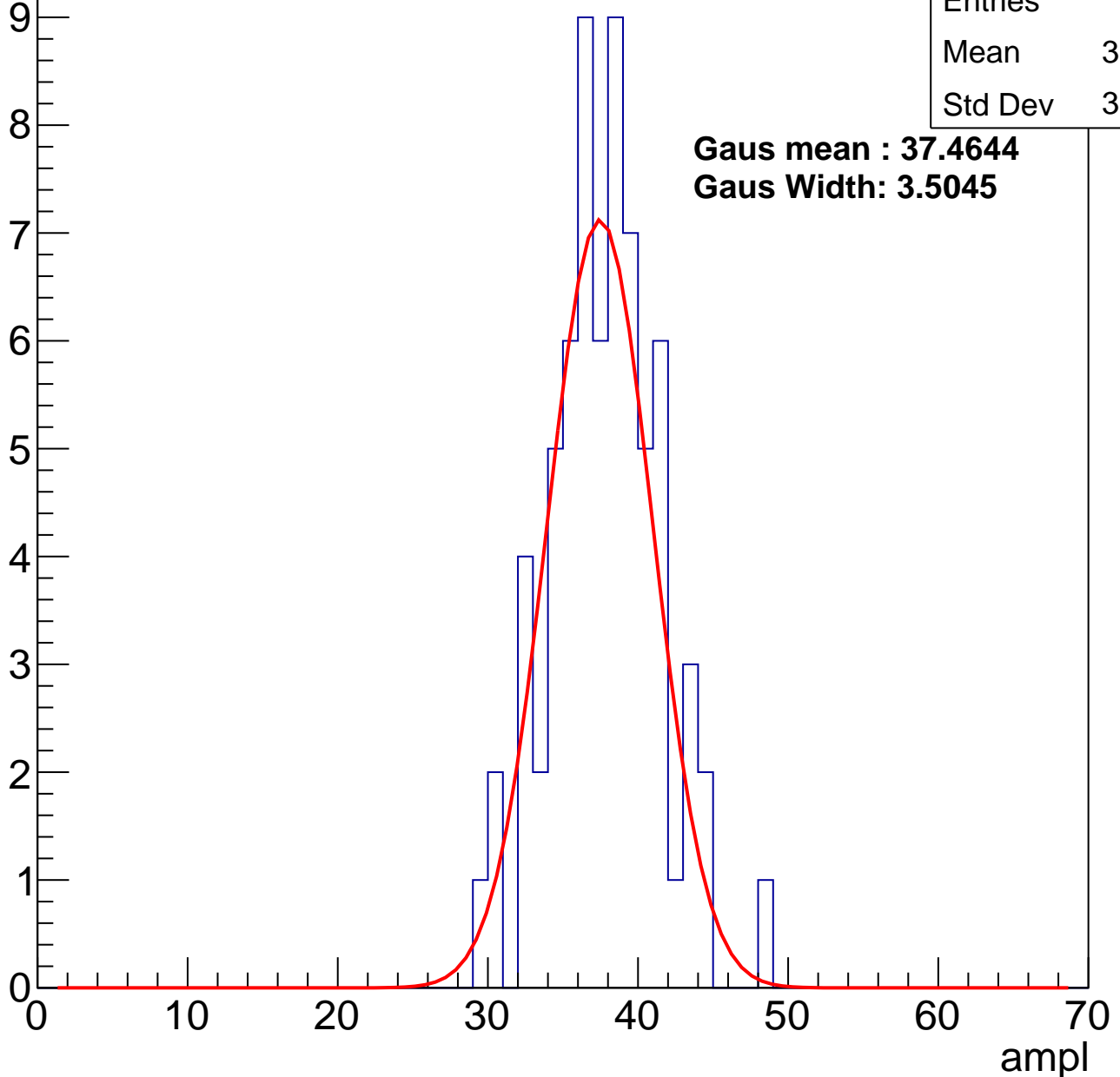
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.35
Std Dev	3.599

**Gaus mean : 37.4644**

**Gaus Width: 3.5045**



# B0L002S, U8-ch114, adc2

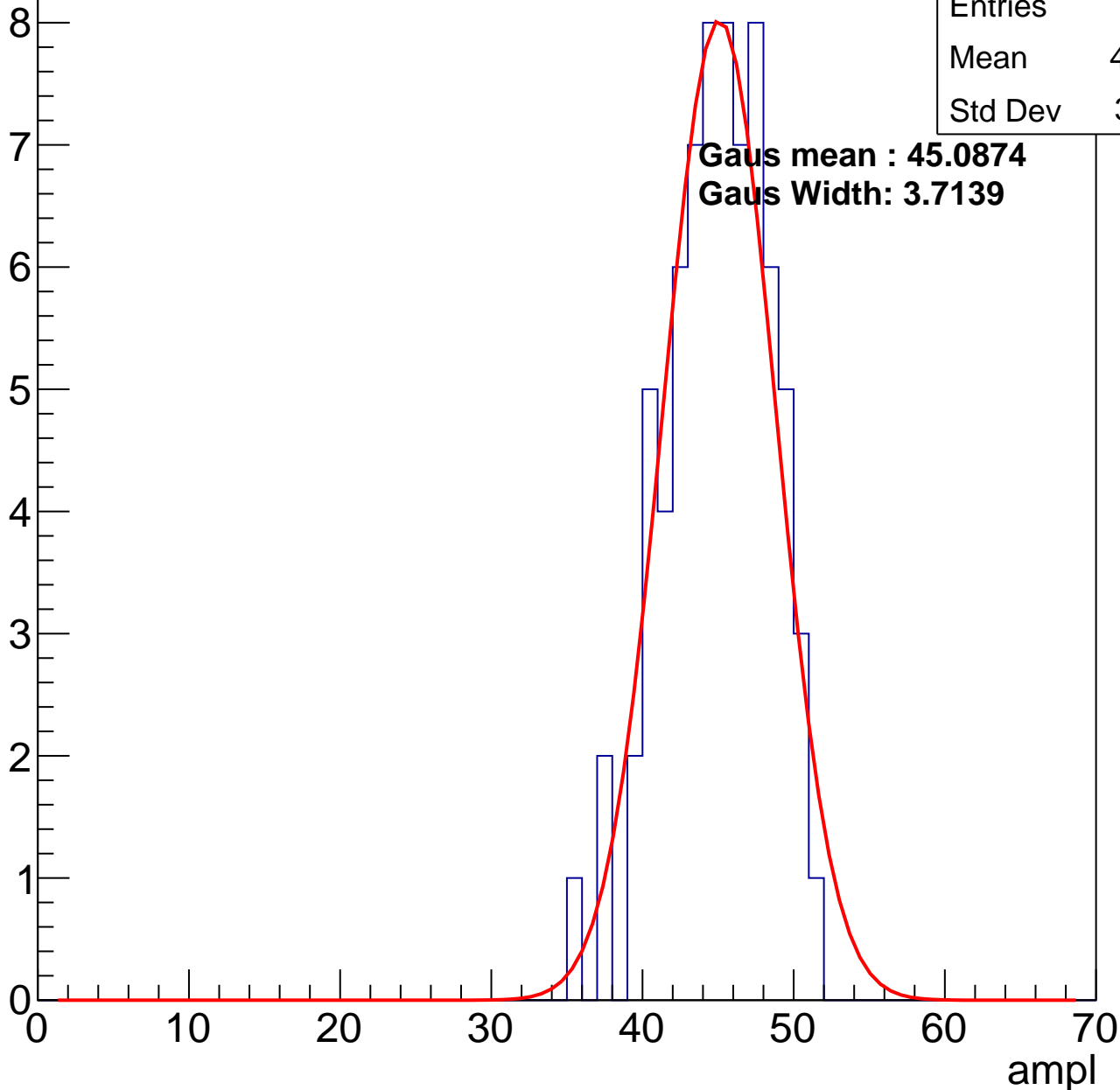
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.49
Std Dev	3.401

**Gaus mean : 45.0874**

**Gaus Width: 3.7139**

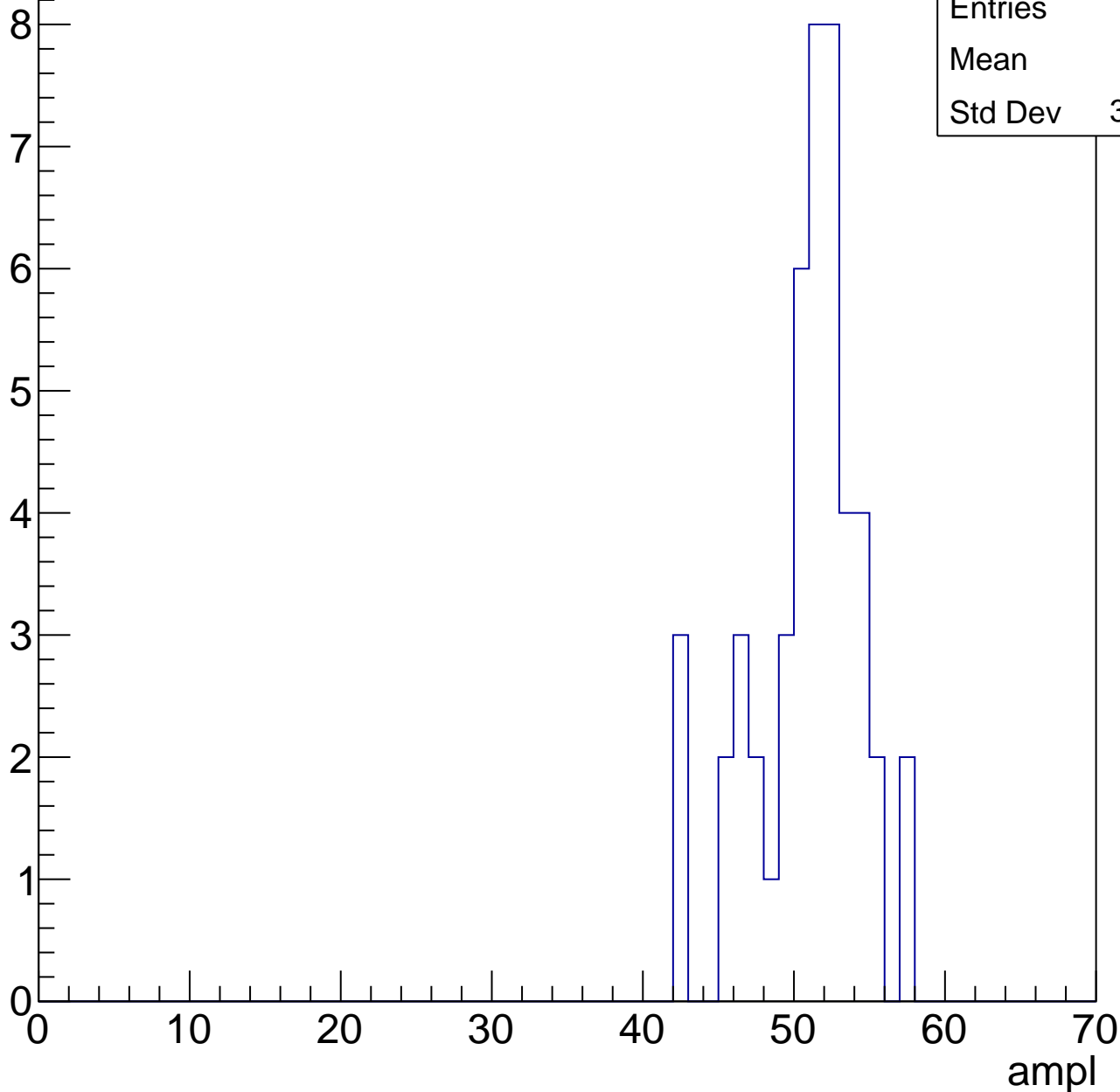


# B0L002S, U8-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

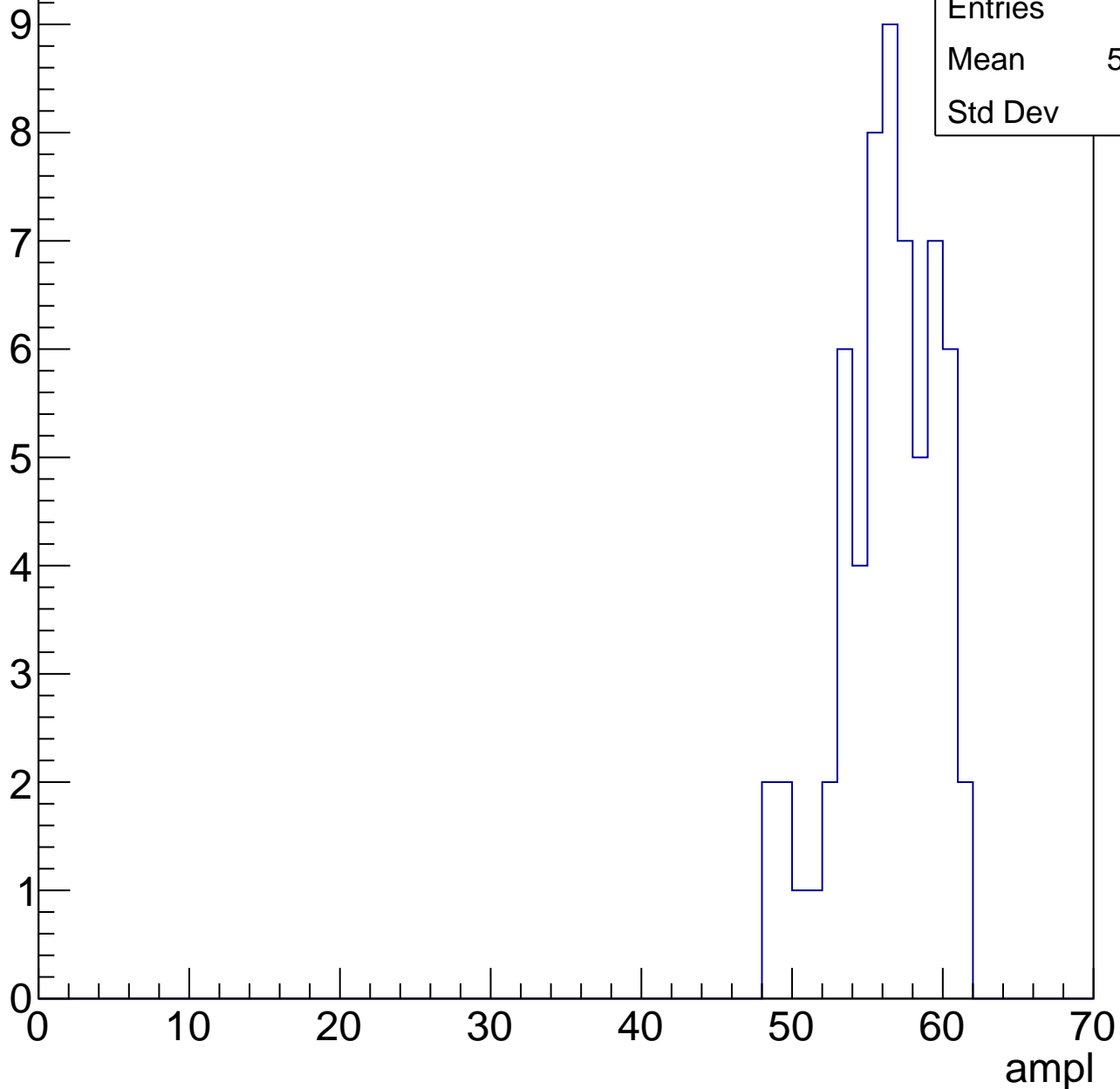
Entries	48
Mean	50.4
Std Dev	3.522



# B0L002S, U8-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



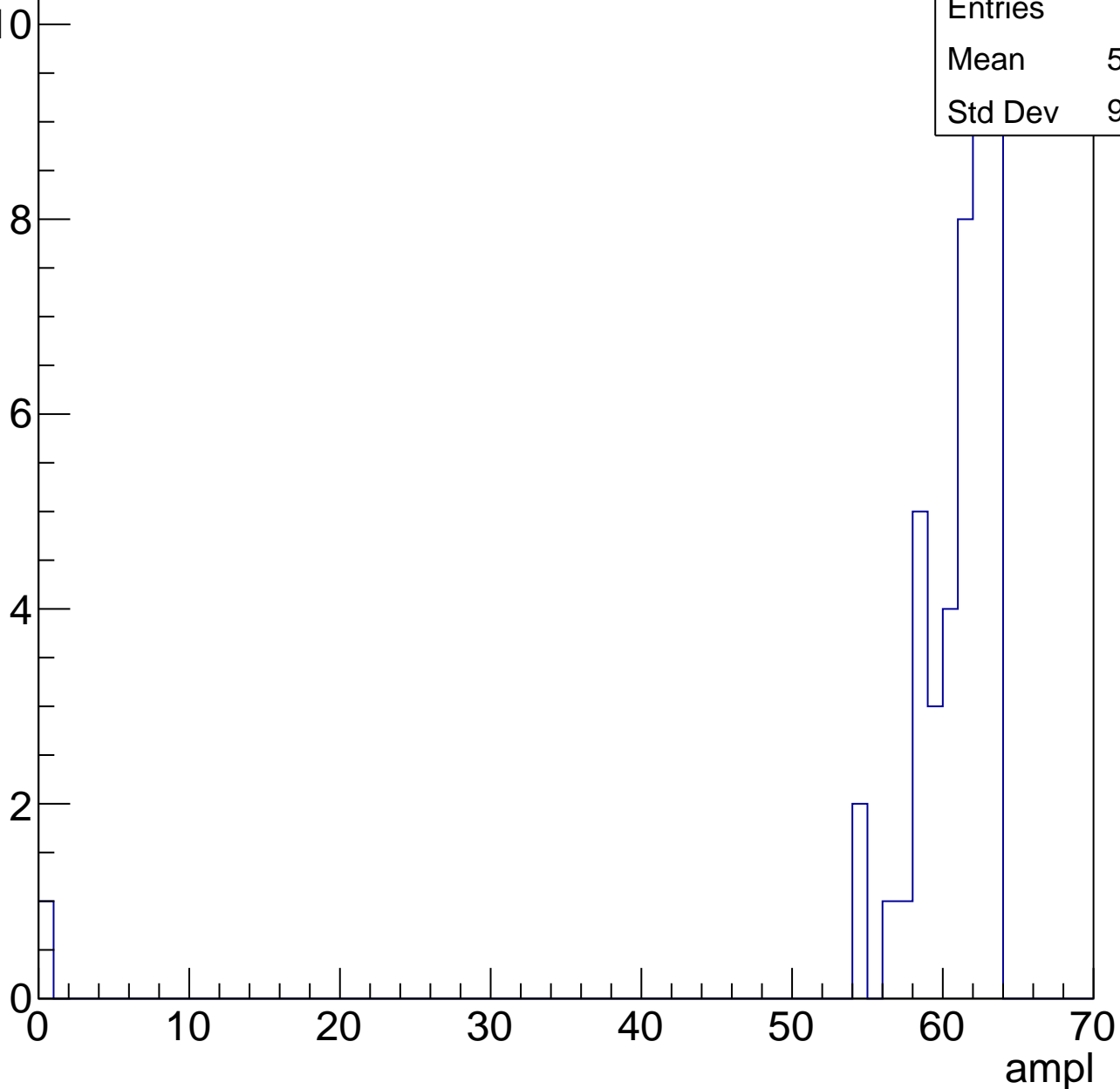
Entries	62
Mean	55.82
Std Dev	3.19

# B0L002S, U8-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	59.16
Std Dev	9.315



# B0L002S, U8-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch115, adc0

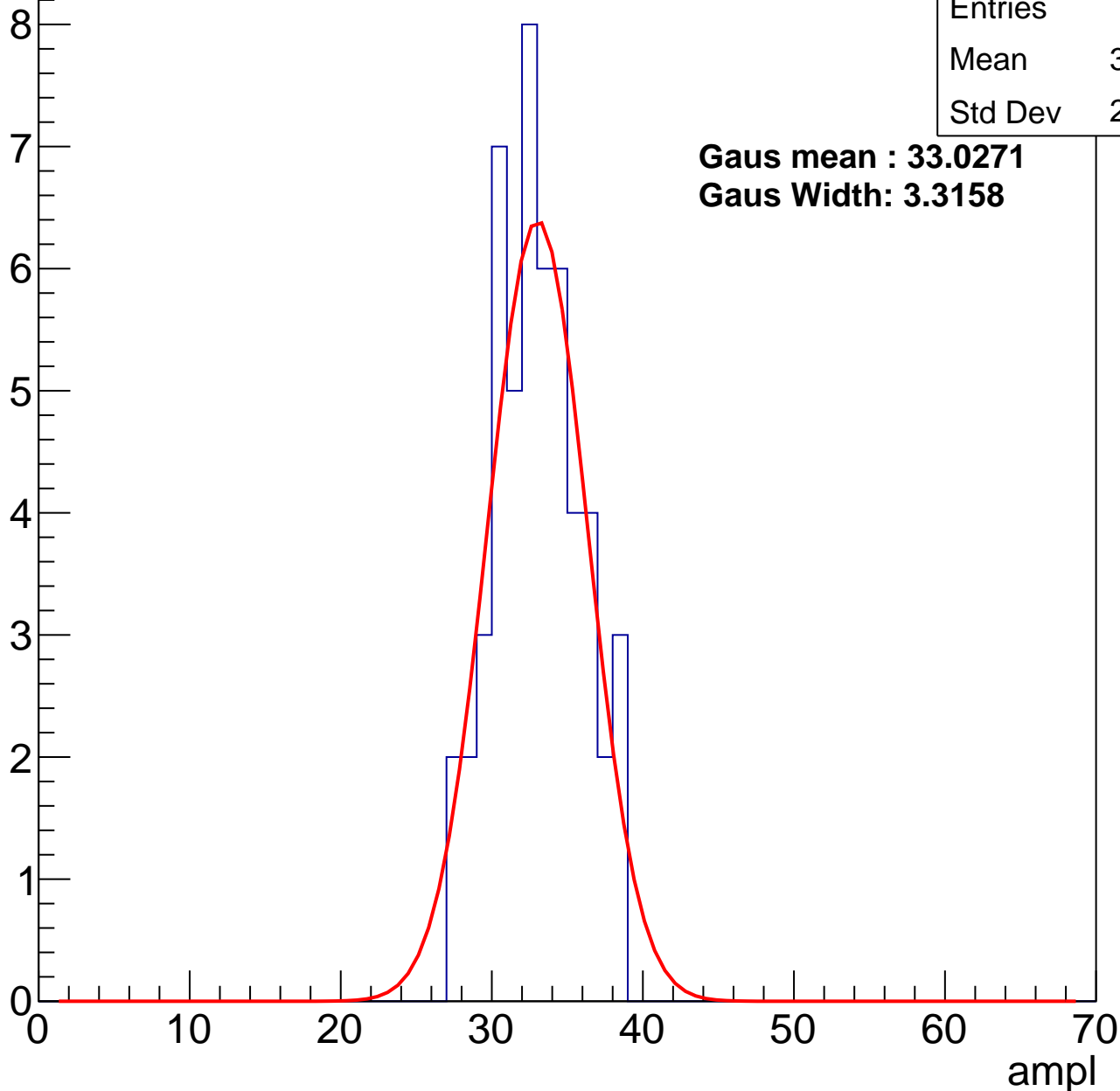
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	32.54
Std Dev	2.825

**Gaus mean : 33.0271**

**Gaus Width: 3.3158**



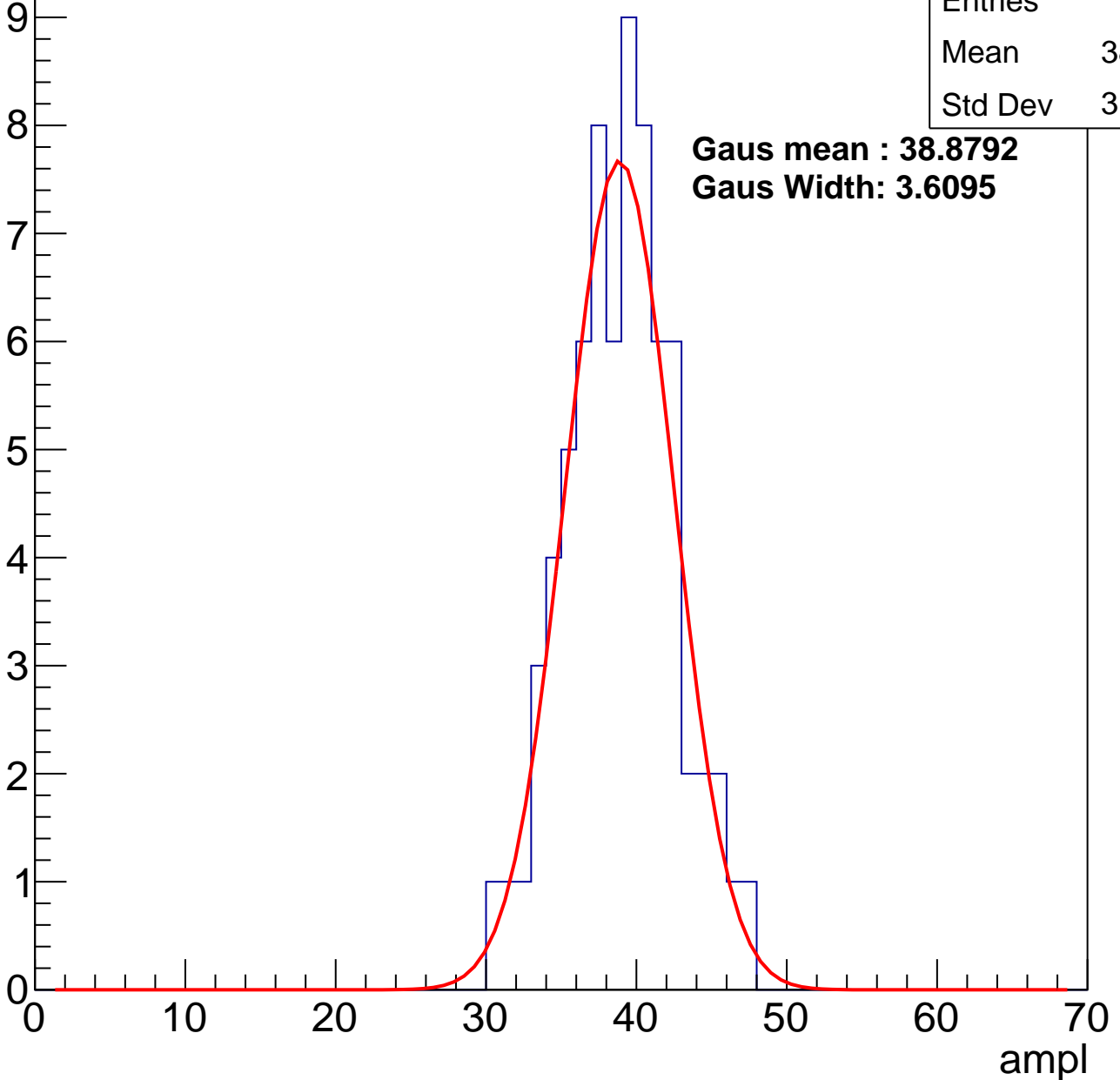
# B0L002S, U8-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	38.46
Std Dev	3.547

**Gaus mean : 38.8792**  
**Gaus Width: 3.6095**



# B0L002S, U8-ch115, adc2

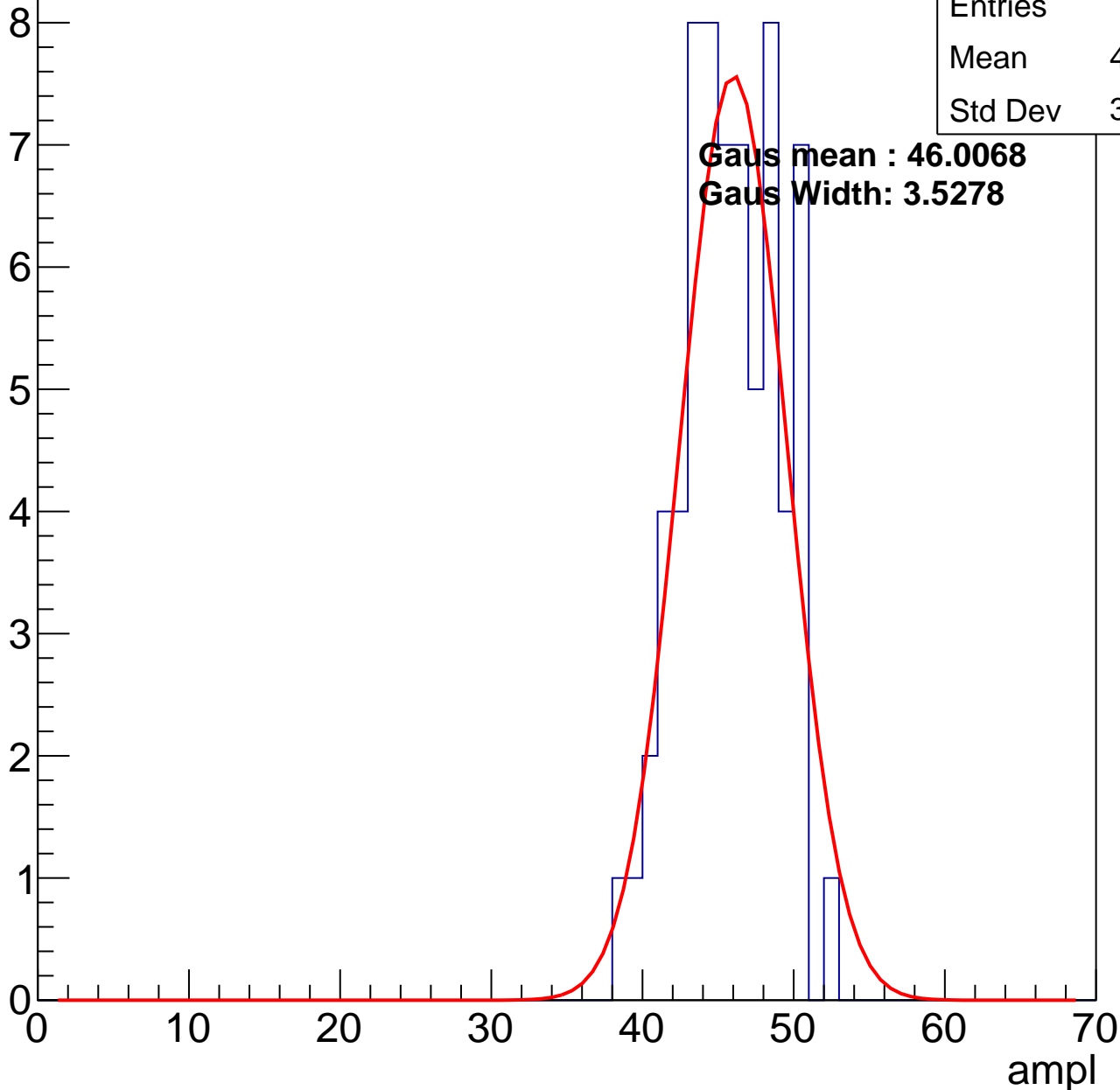
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	45.36
Std Dev	3.123

**Gaus mean : 46.0068**

**Gaus Width: 3.5278**



# B0L002S, U8-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	52.59
Std Dev	3.347

Entry

10

8

6

4

2

0

0

10

20

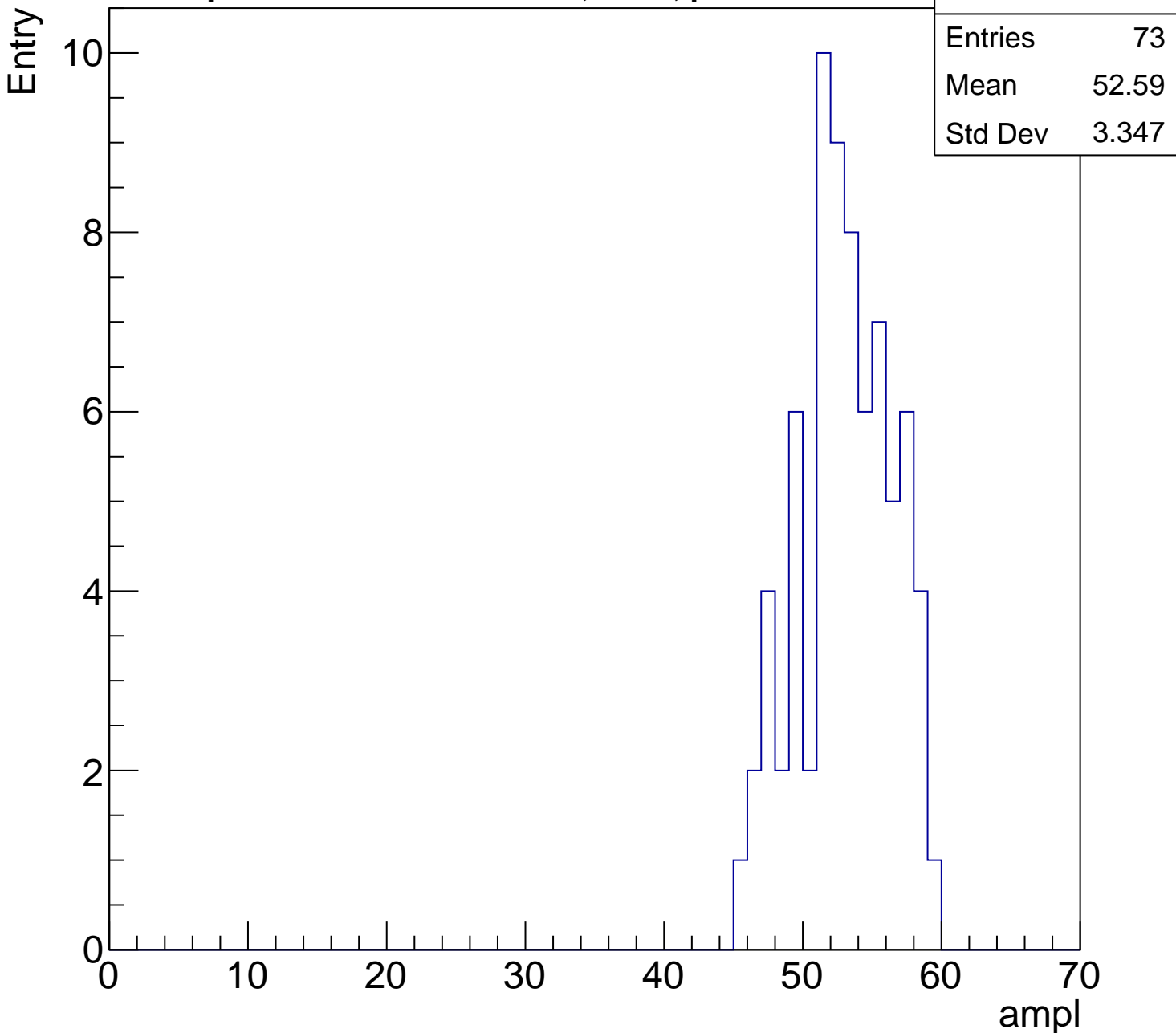
30

40

50

60

ampl

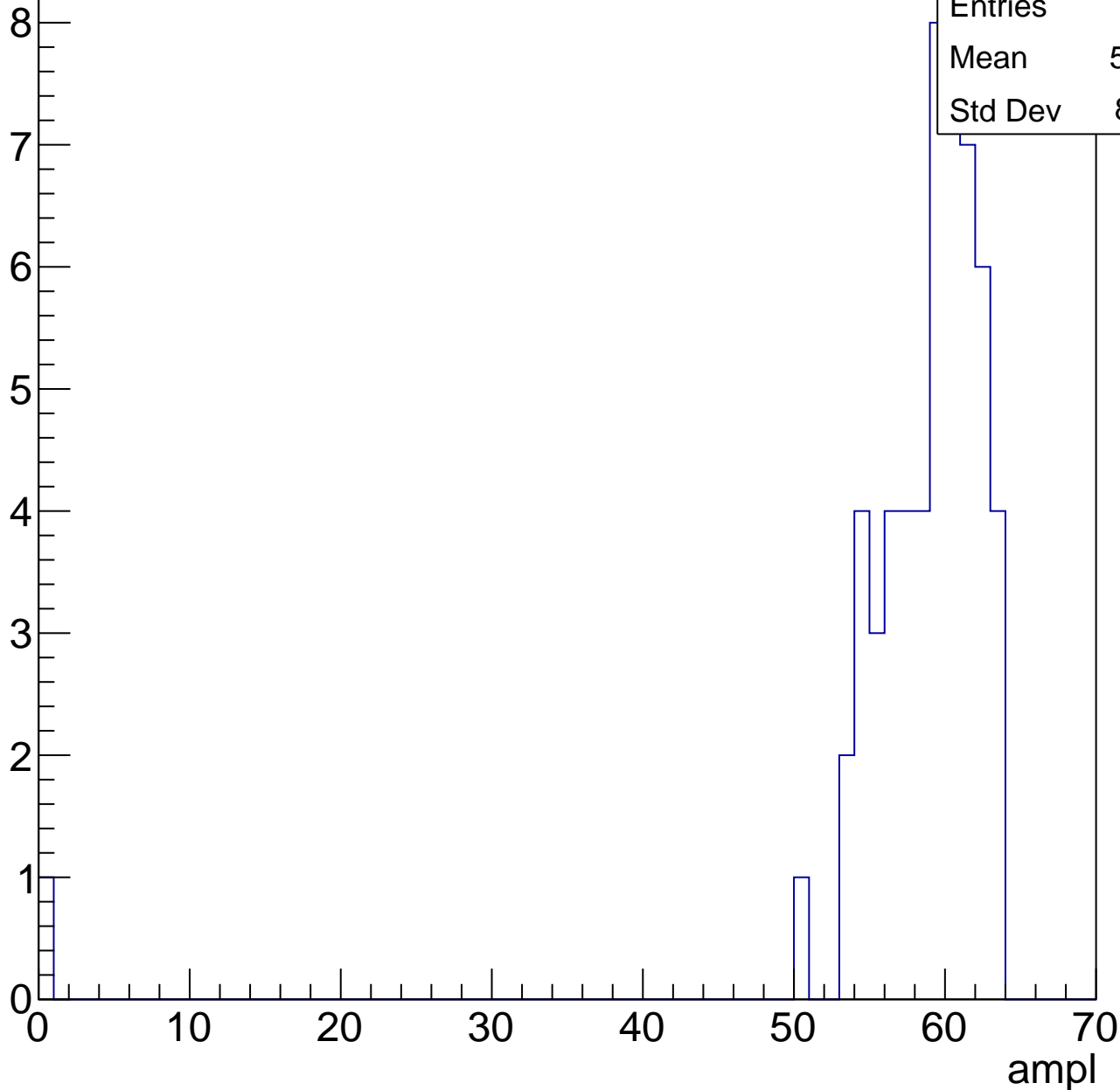


# B0L002S, U8-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.57
Std Dev	8.321

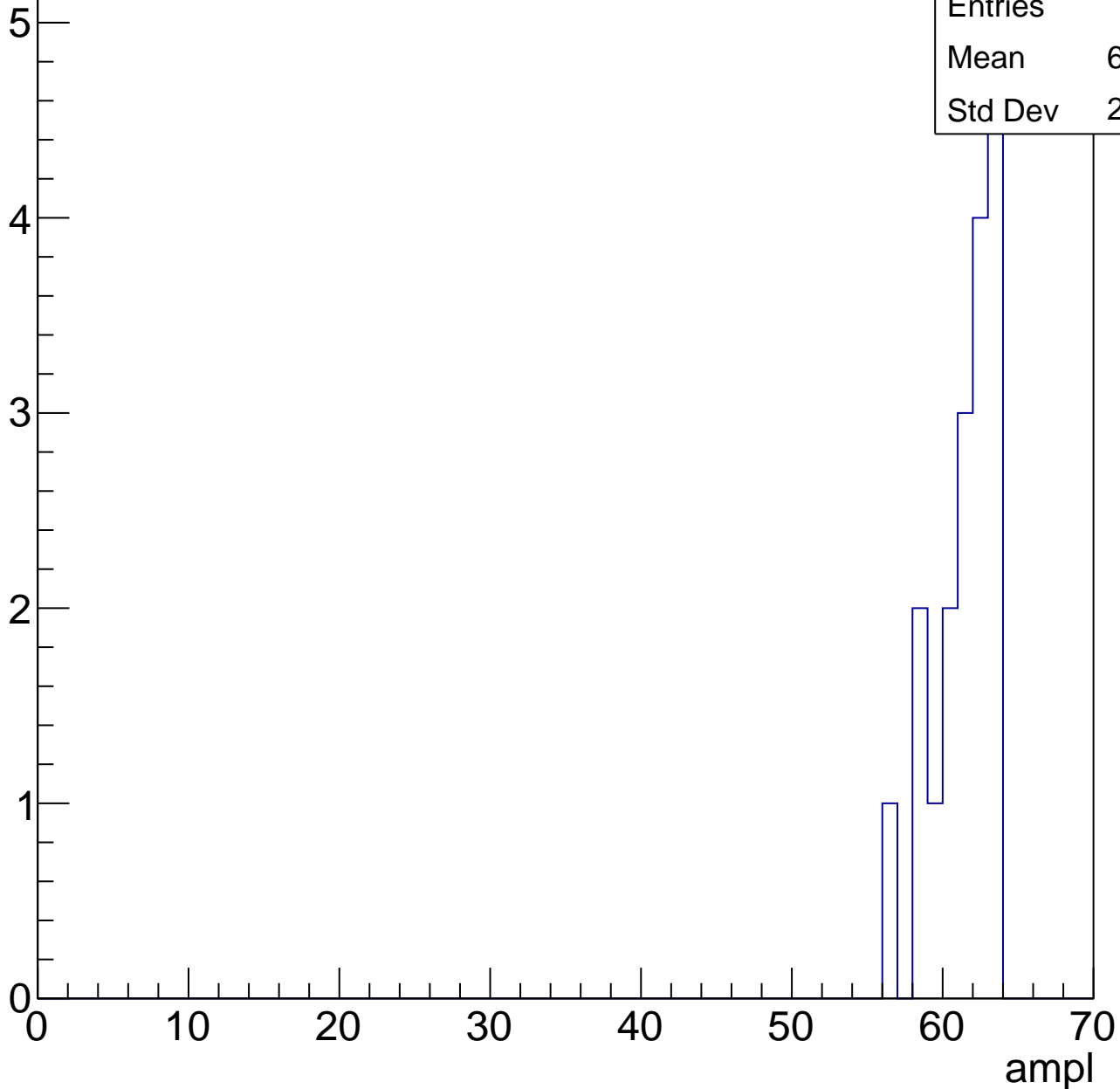


# B0L002S, U8-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	18
Mean	60.94
Std Dev	2.013



# B0L002S, U8-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U8-ch116, adc0

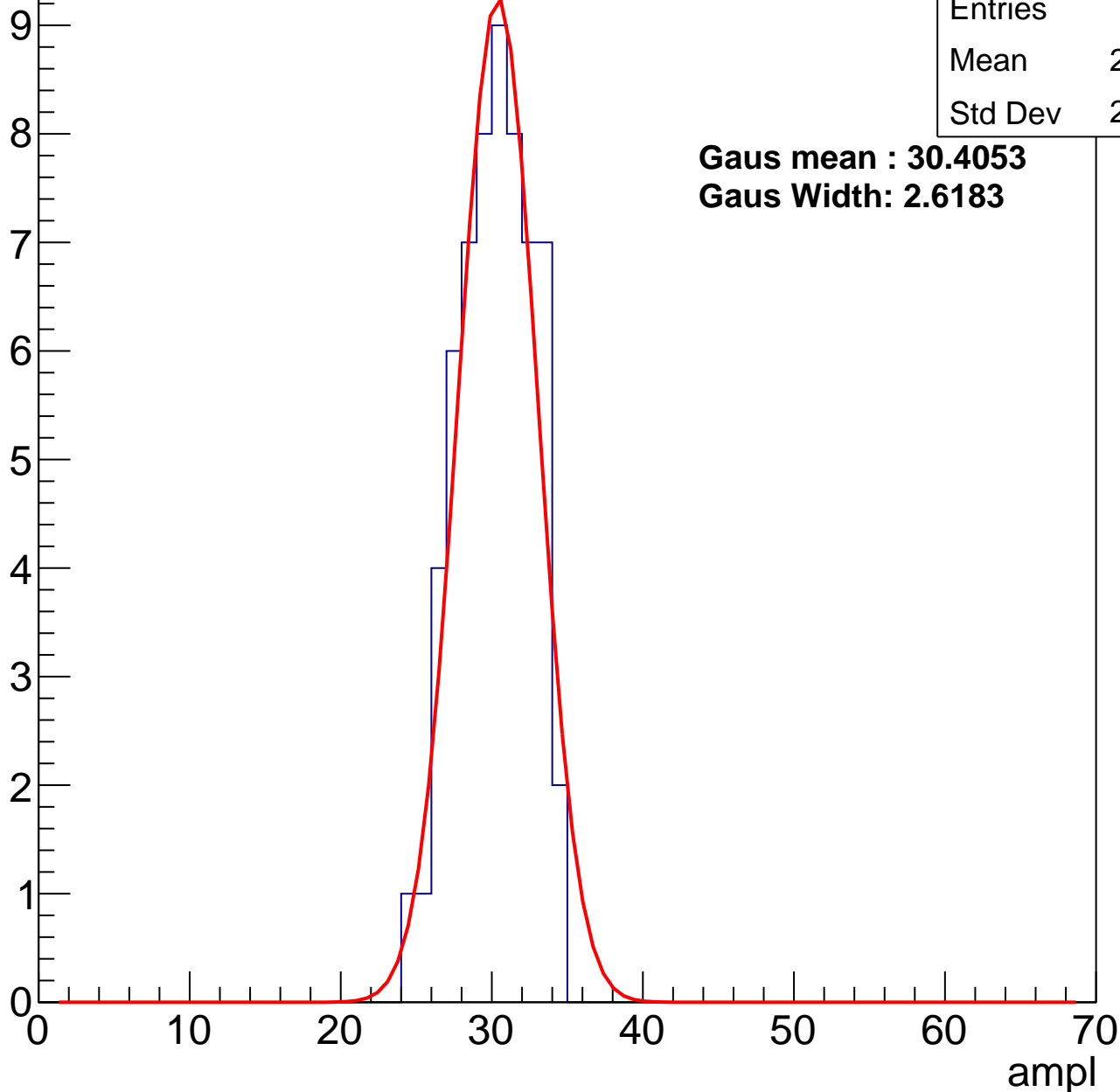
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.73
Std Dev	2.387

**Gaus mean : 30.4053**

**Gaus Width: 2.6183**



# B0L002S, U8-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	36.88
Std Dev	3.381

**Gaus mean : 37.3596**

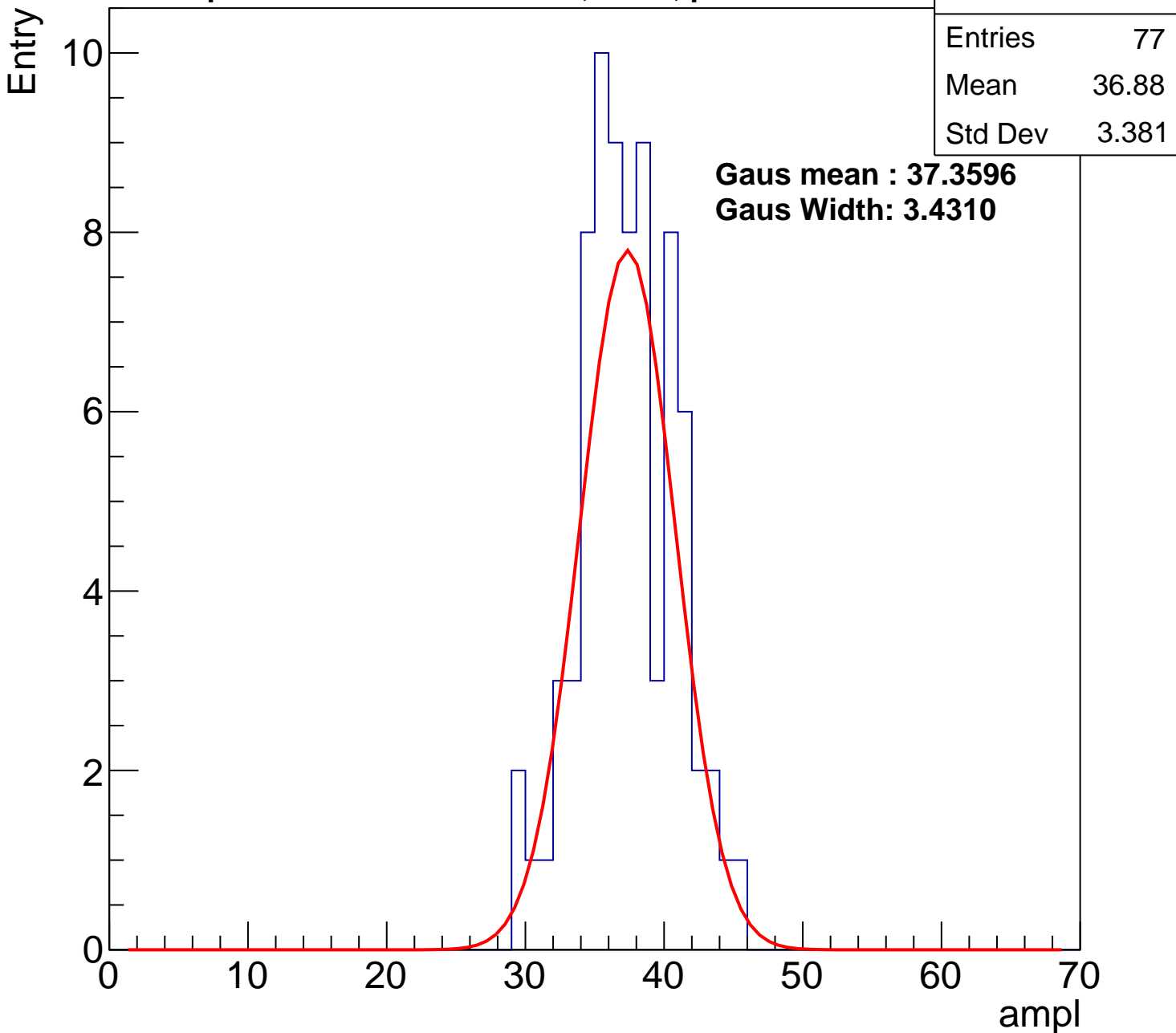
**Gaus Width: 3.4310**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U8-ch116, adc2

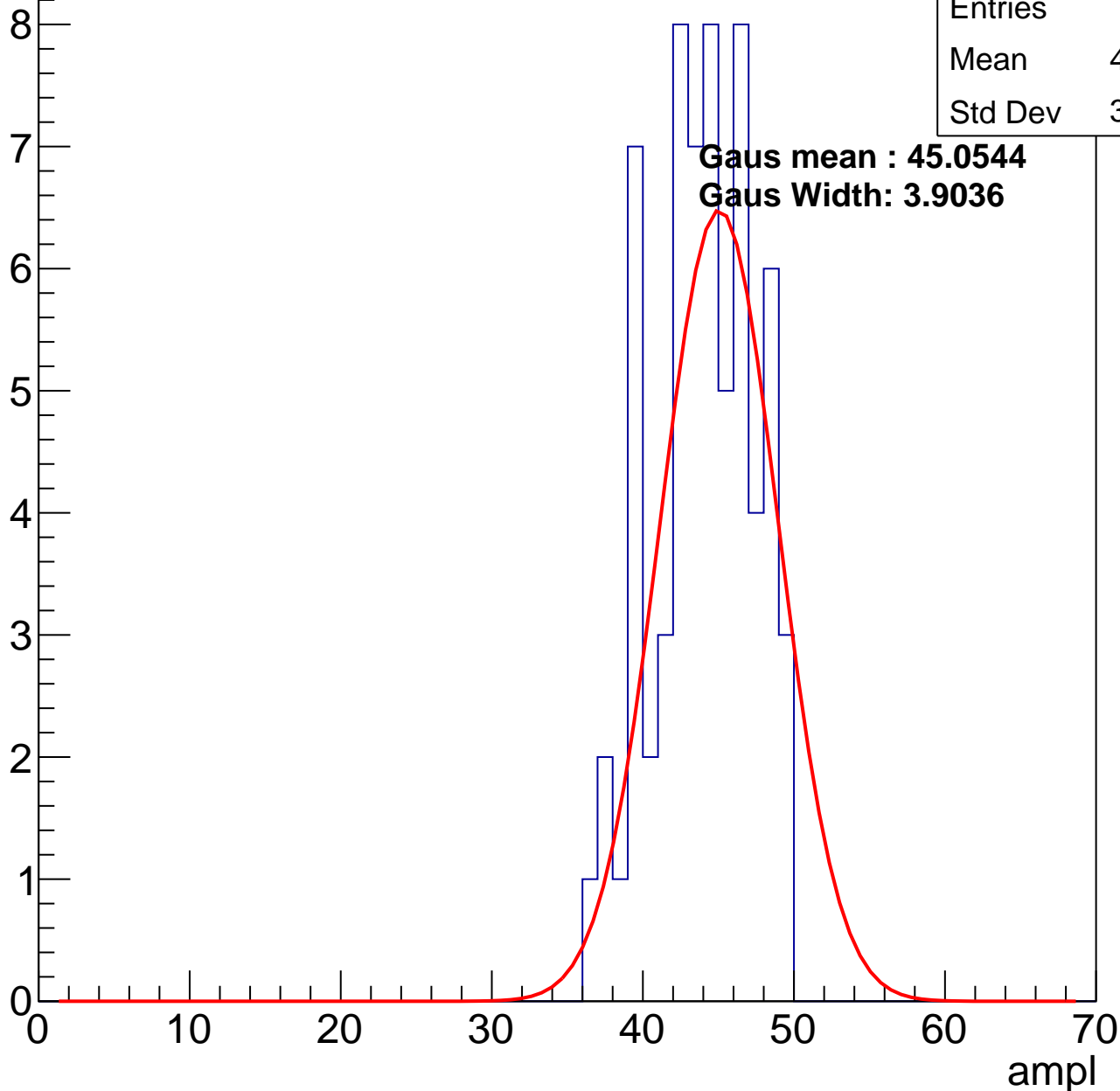
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.52
Std Dev	3.282

**Gaus mean : 45.0544**

**Gaus Width: 3.9036**

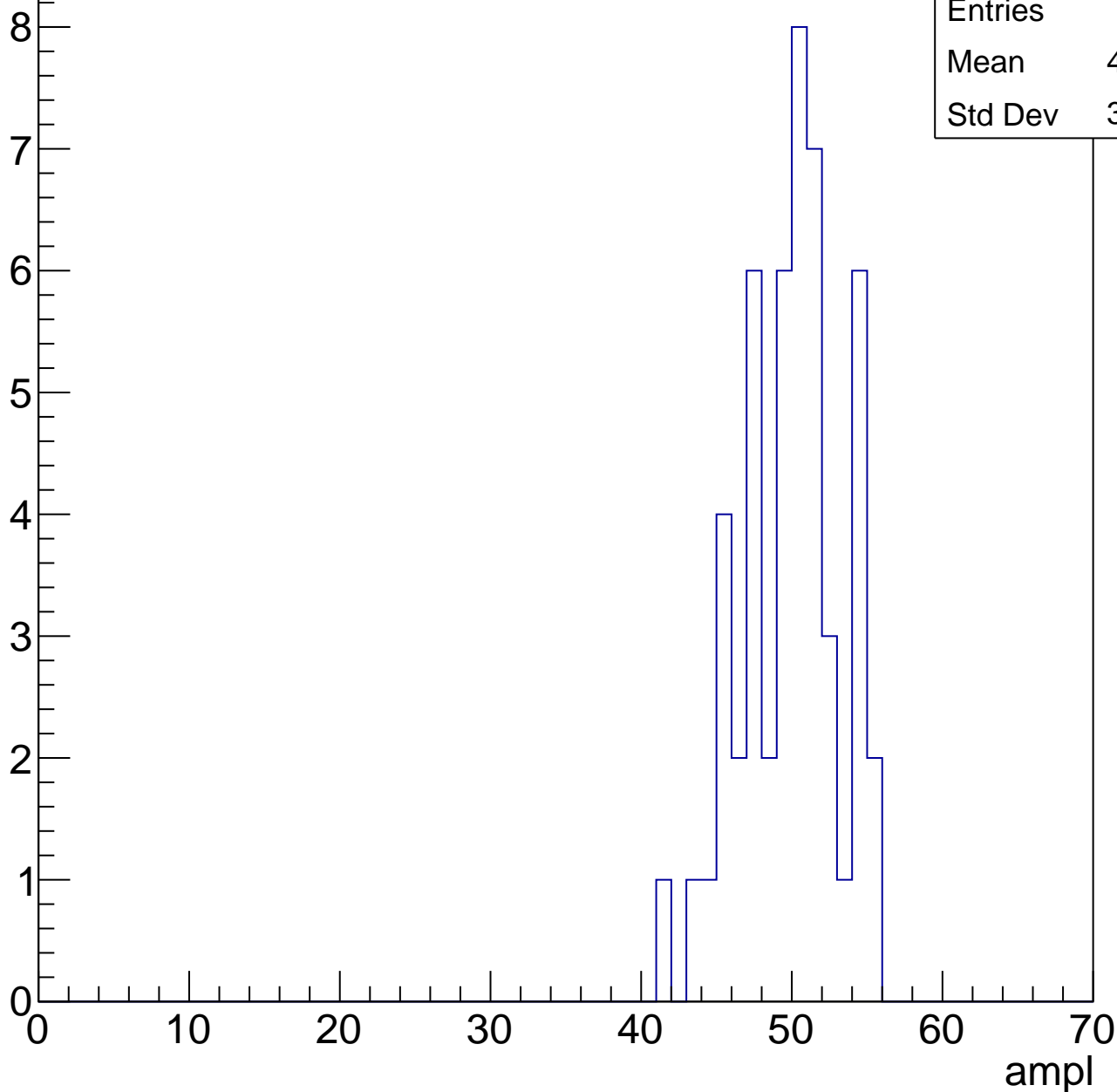


# B0L002S, U8-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

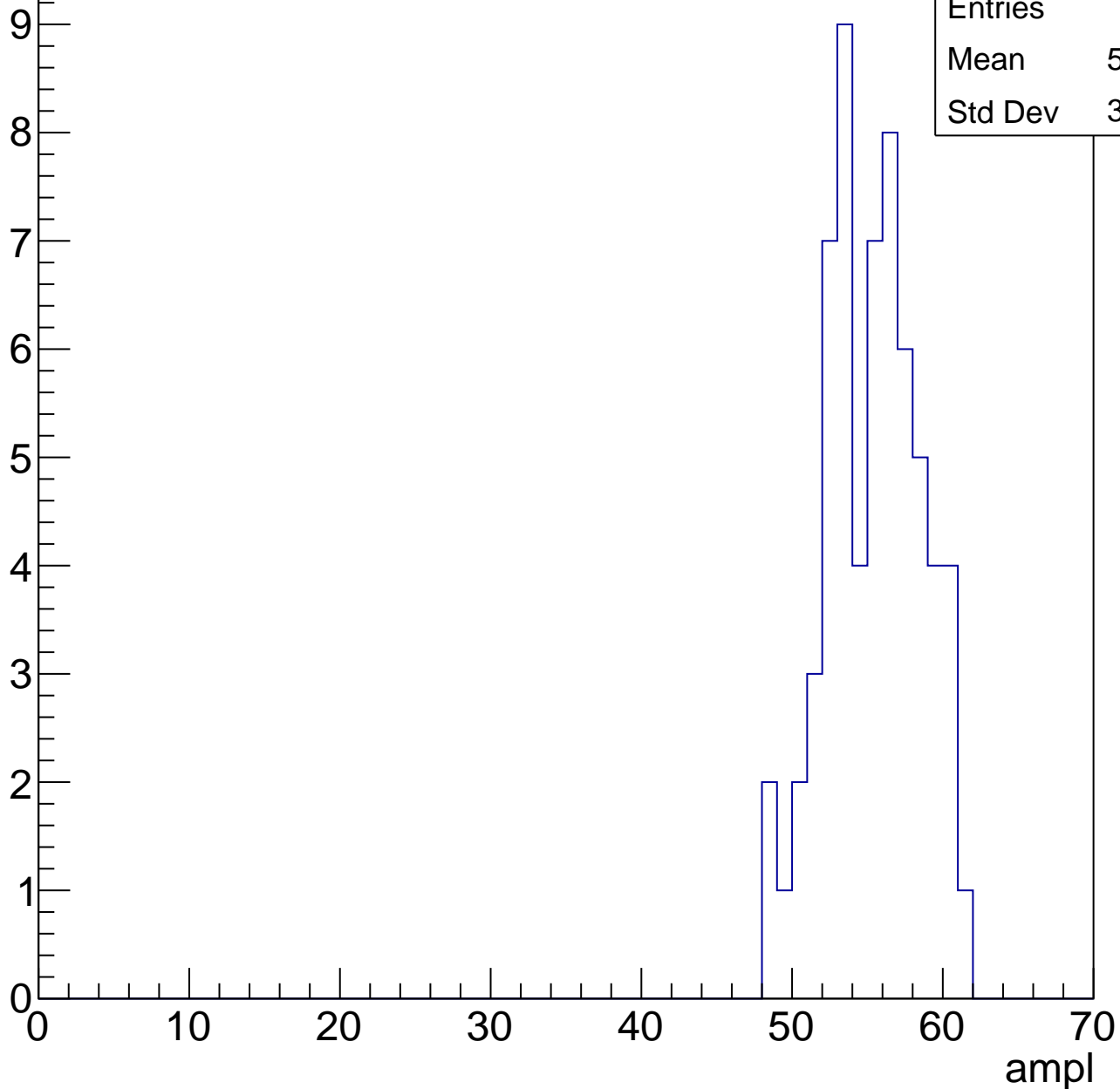
Entries	50
Mean	49.44
Std Dev	3.238



# B0L002S, U8-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

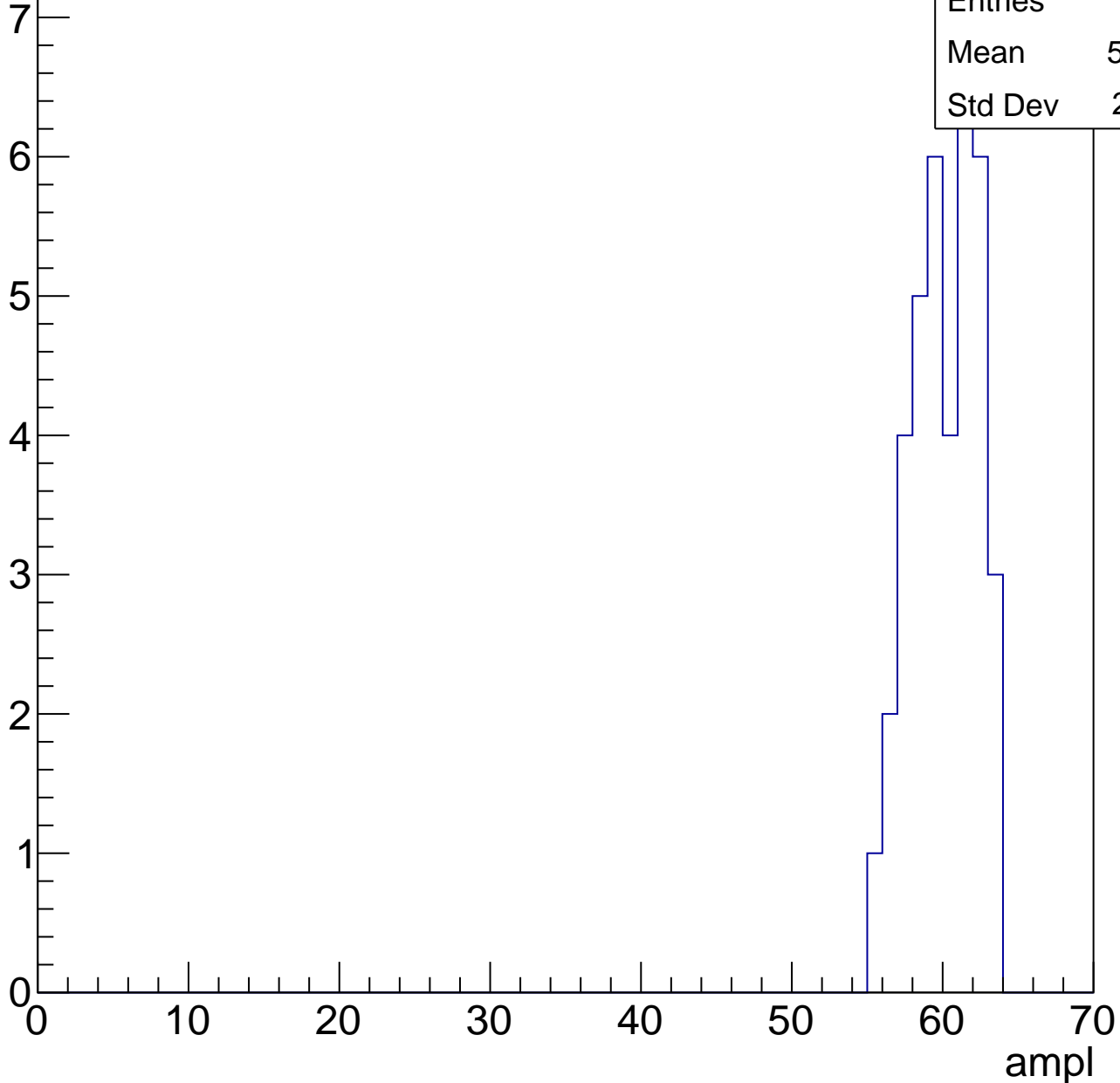


# B0L002S, U8-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.66
Std Dev	2.131

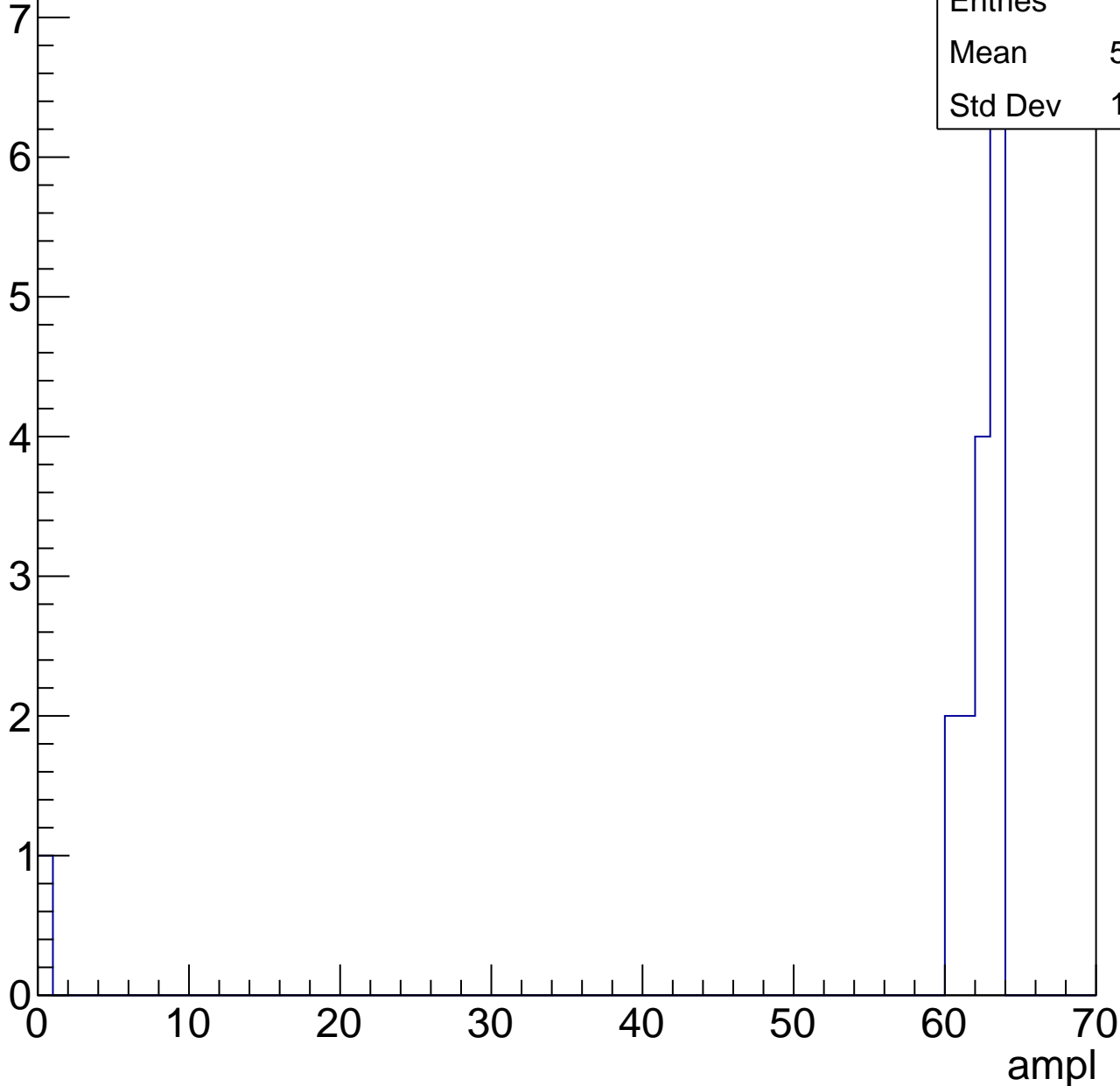


# B0L002S, U8-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	16
Mean	58.19
Std Dev	15.06





# B0L002S, U8-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L002S, U8-ch117, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	28.63
Std Dev	4.641

**Gaus mean : 29.4307**

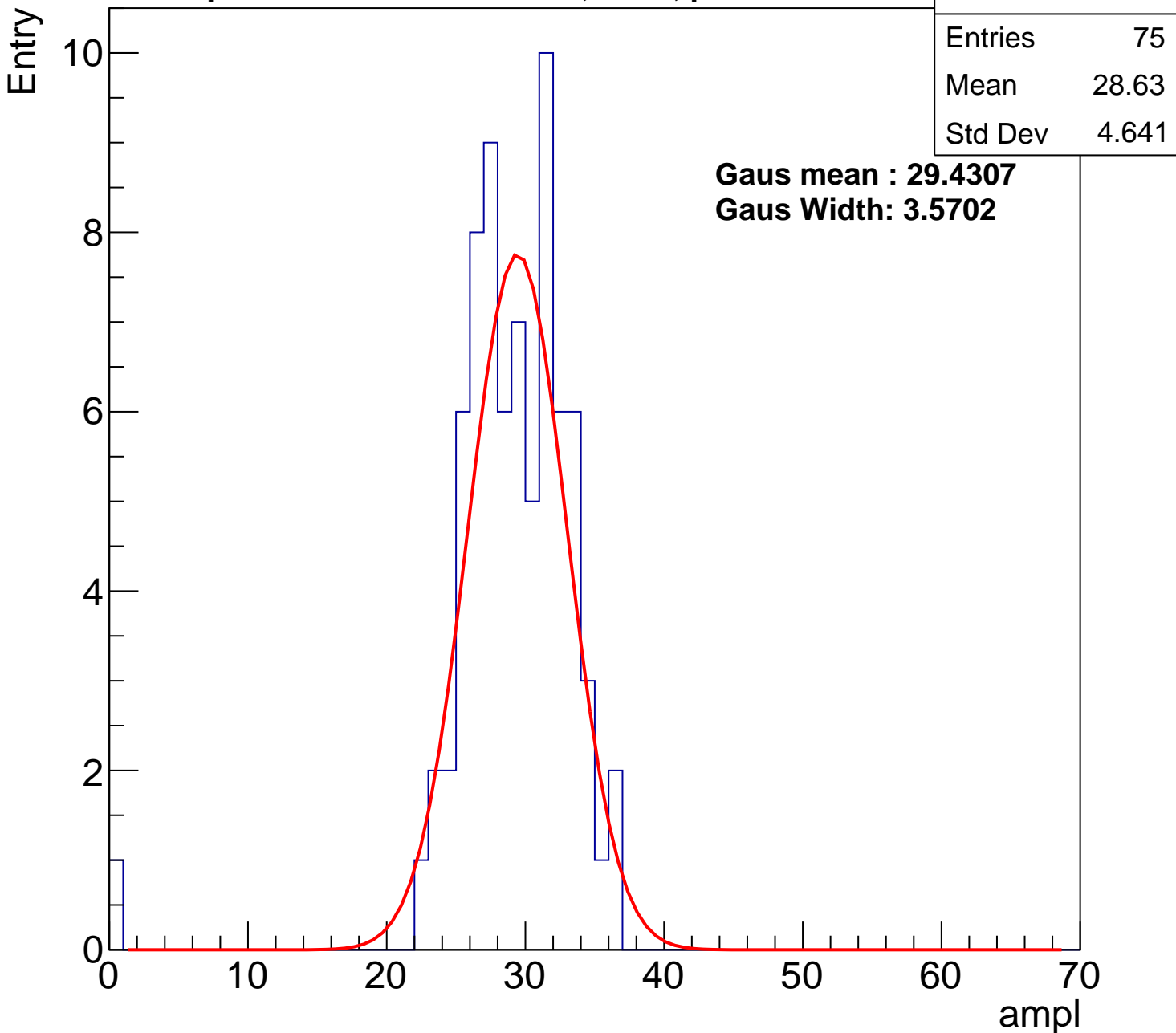
**Gaus Width: 3.5702**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch117, adc1

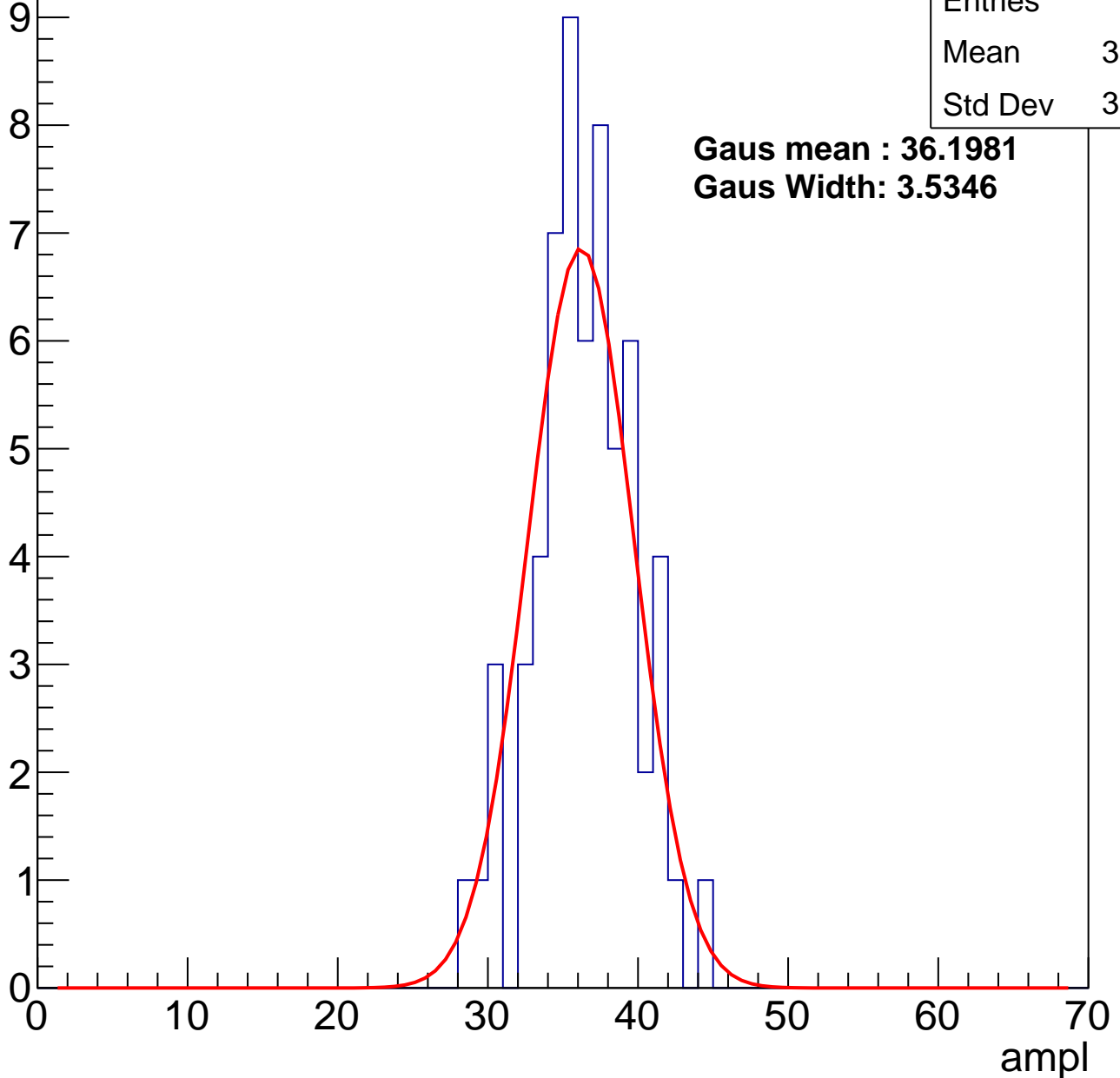
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	35.97
Std Dev	3.279

**Gaus mean : 36.1981**

**Gaus Width: 3.5346**



# B0L002S, U8-ch117, adc2

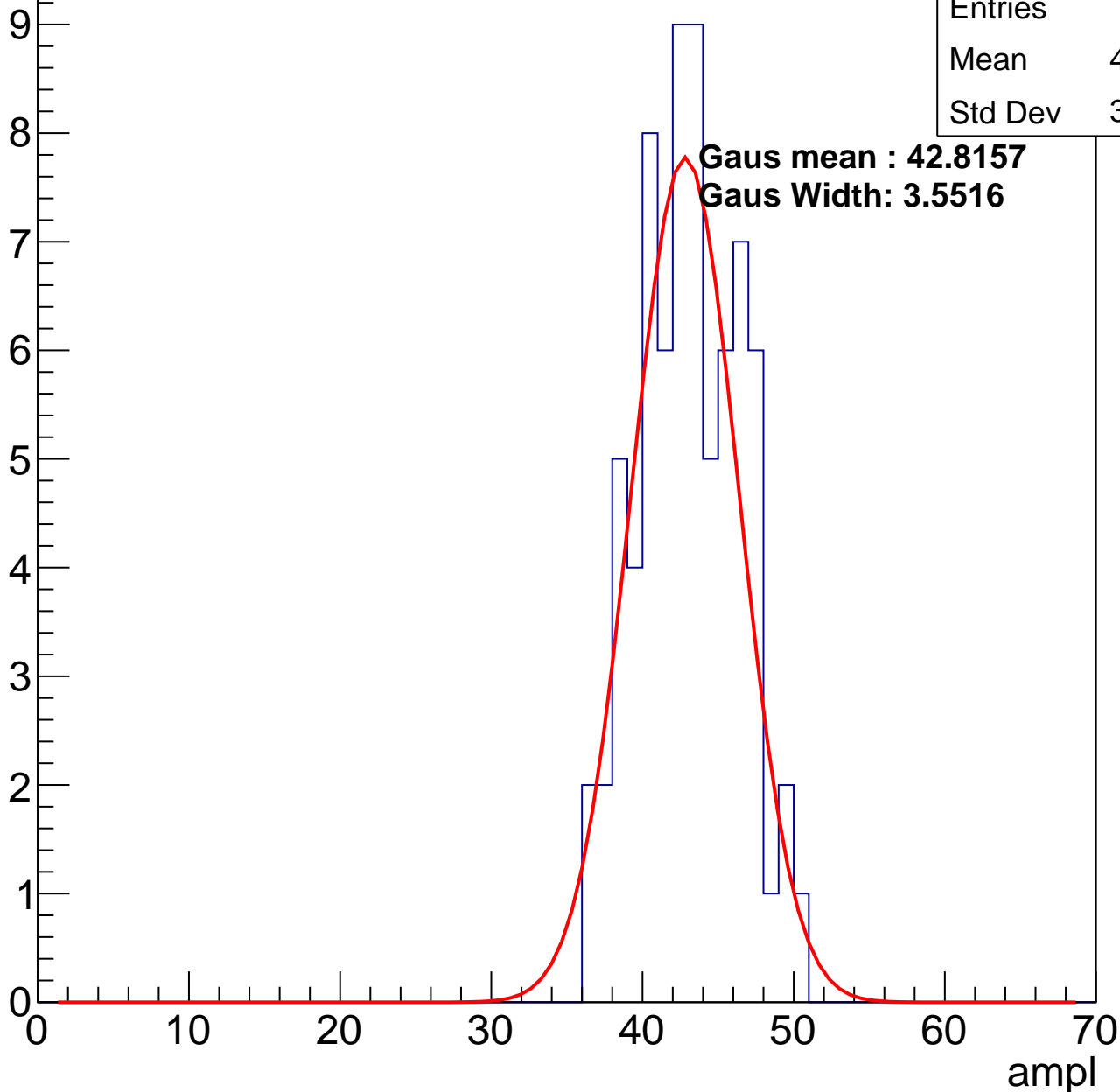
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.64
Std Dev	3.287

**Gaus mean : 42.8157**

**Gaus Width: 3.5516**

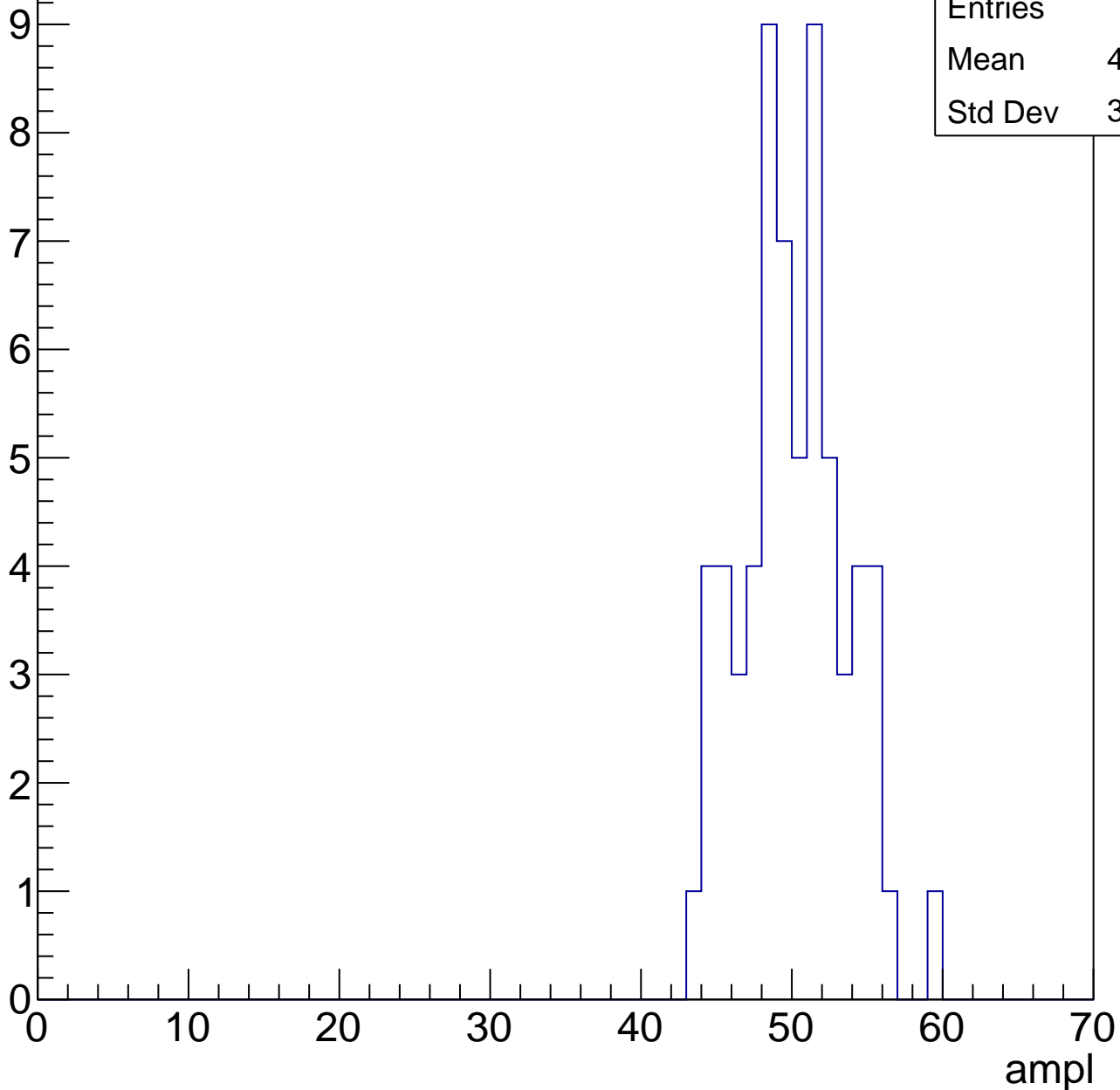


# B0L002S, U8-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.67
Std Dev	3.424

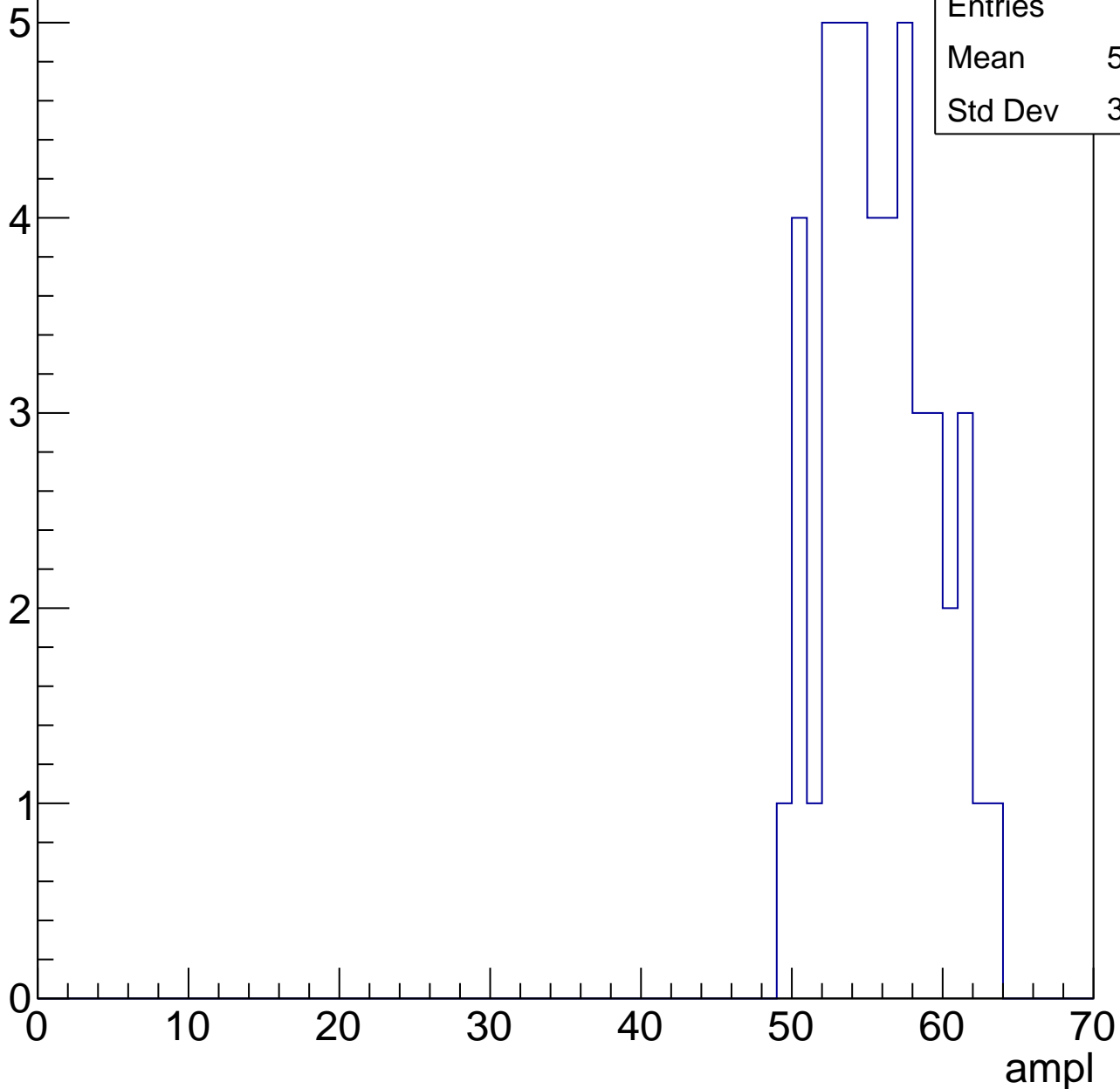


# B0L002S, U8-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	55.38
Std Dev	3.528



# B0L002S, U8-ch117, adc5

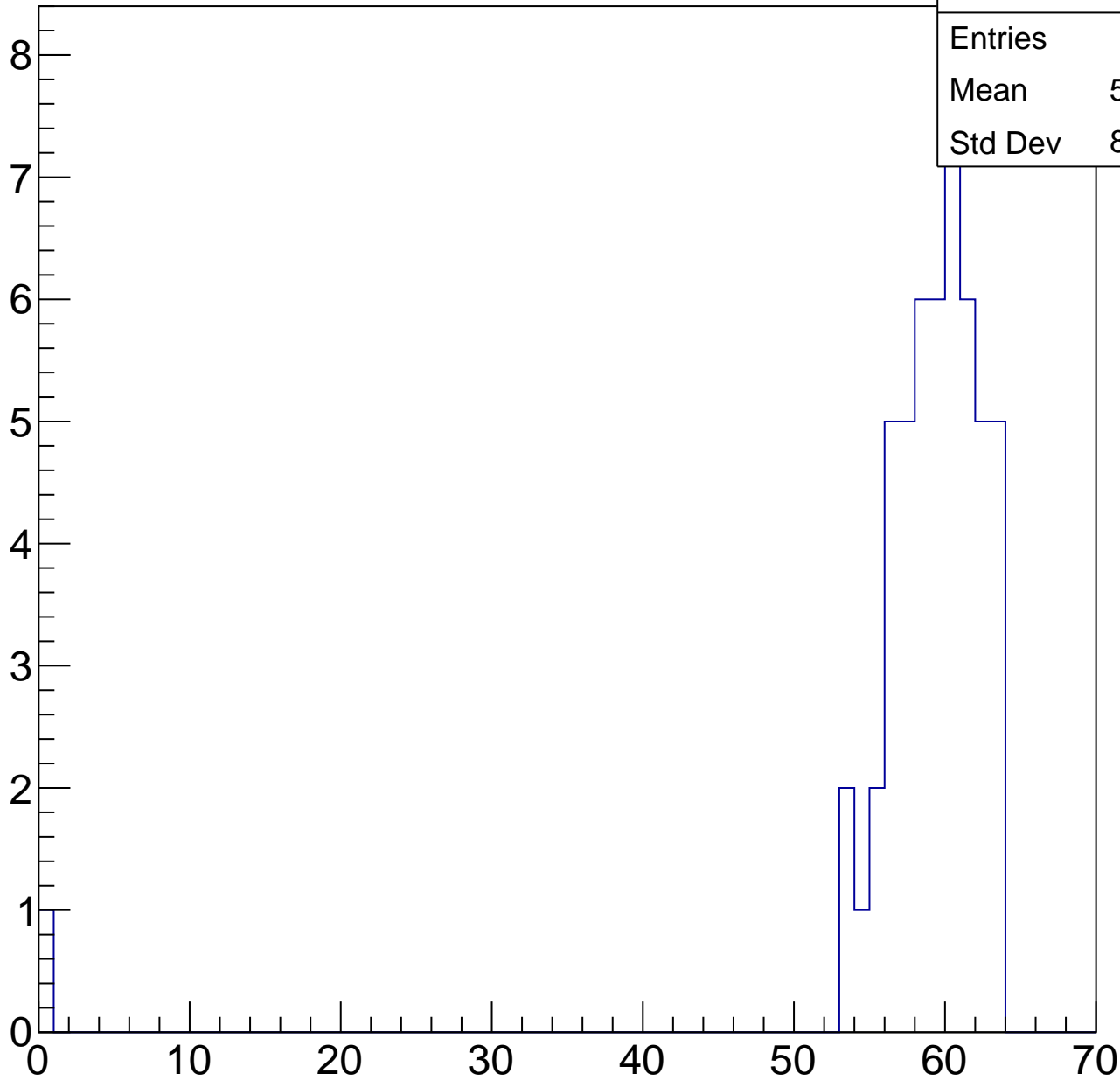
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	57.85
Std Dev	8.513

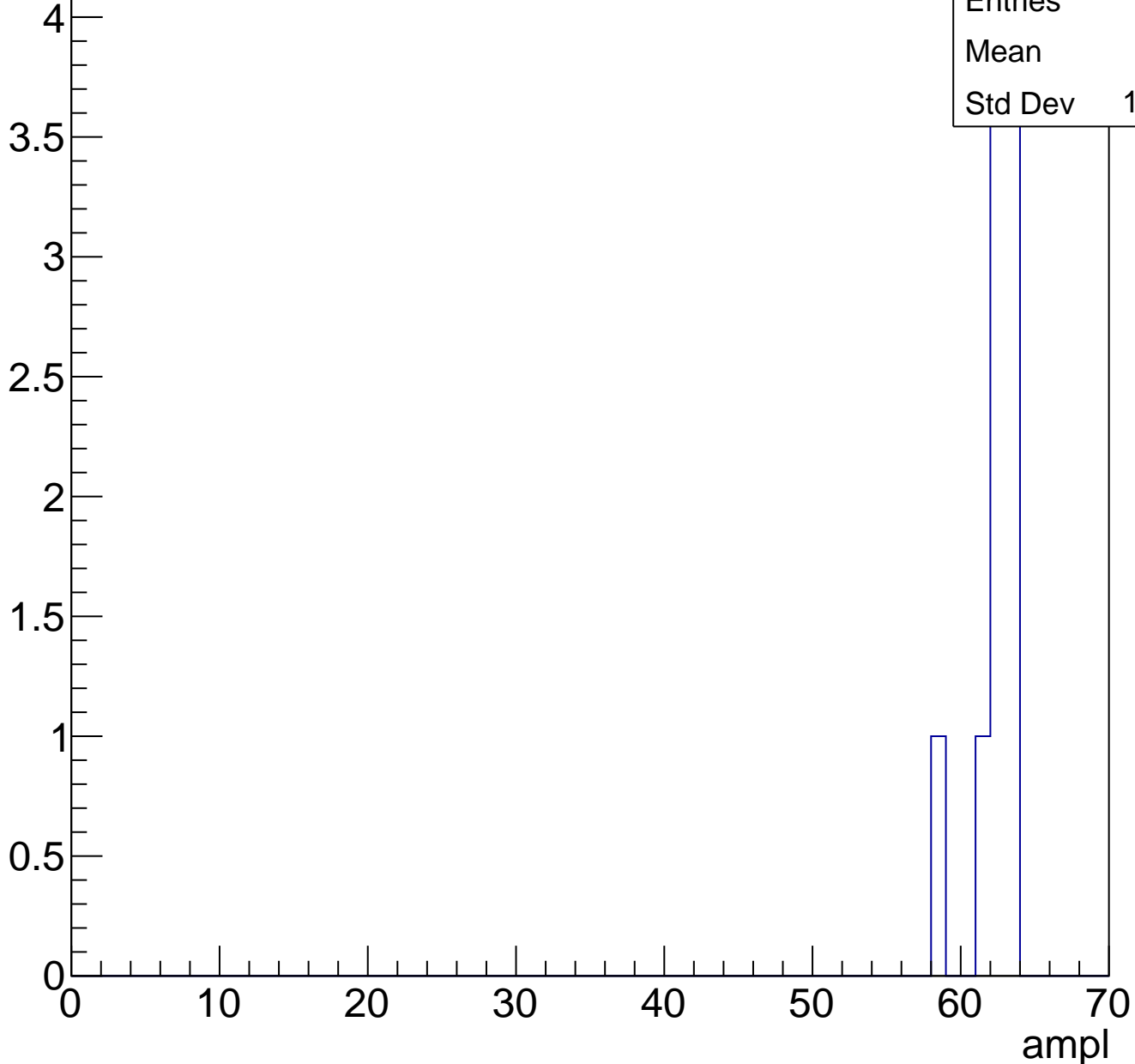
ampl



# B0L002S, U8-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	10
Mean	61.9
Std Dev	1.446



# B0L002S, U8-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch118, adc0

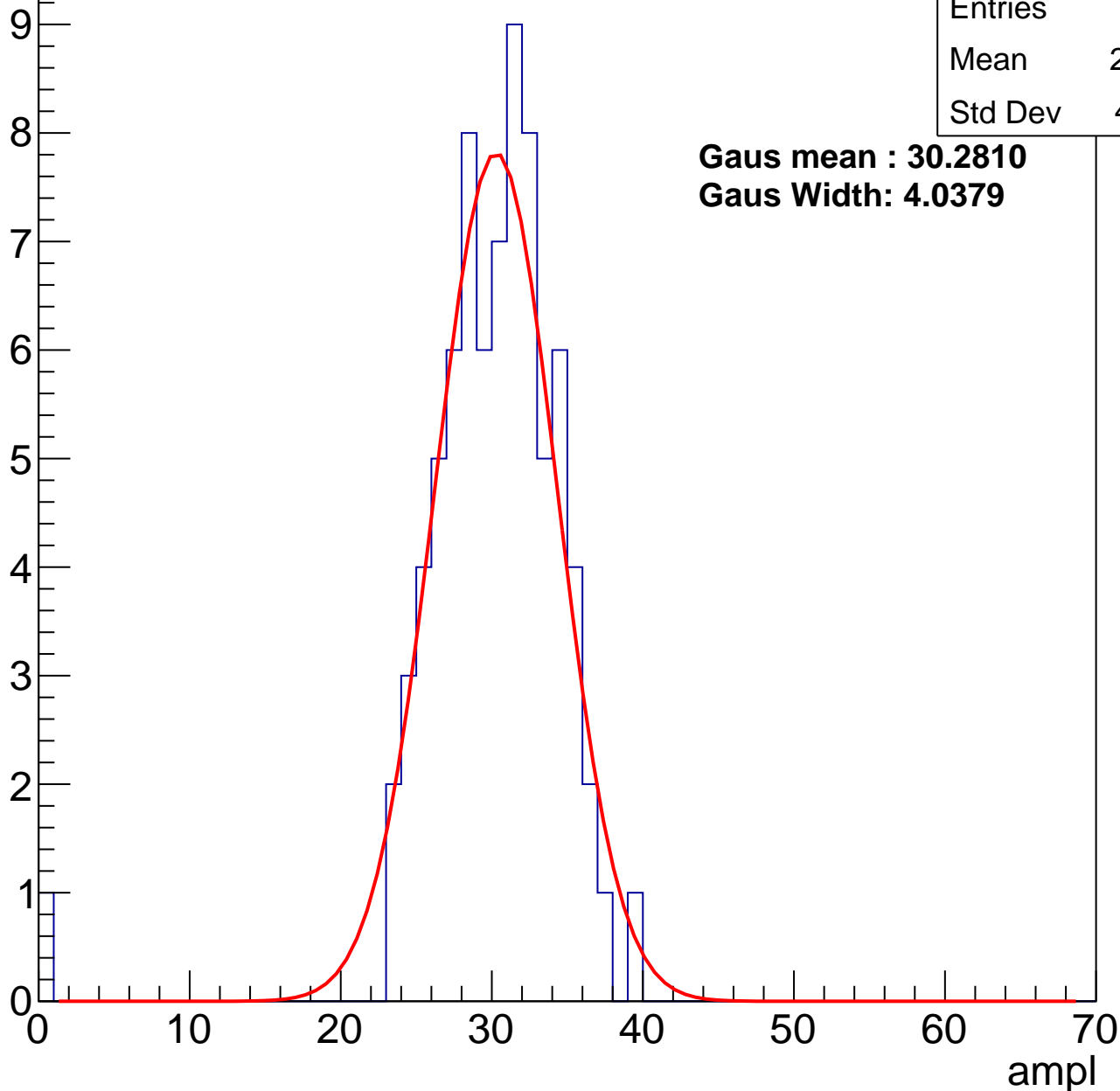
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.62
Std Dev	4.871

**Gaus mean : 30.2810**

**Gaus Width: 4.0379**



# B0L002S, U8-ch118, adc1

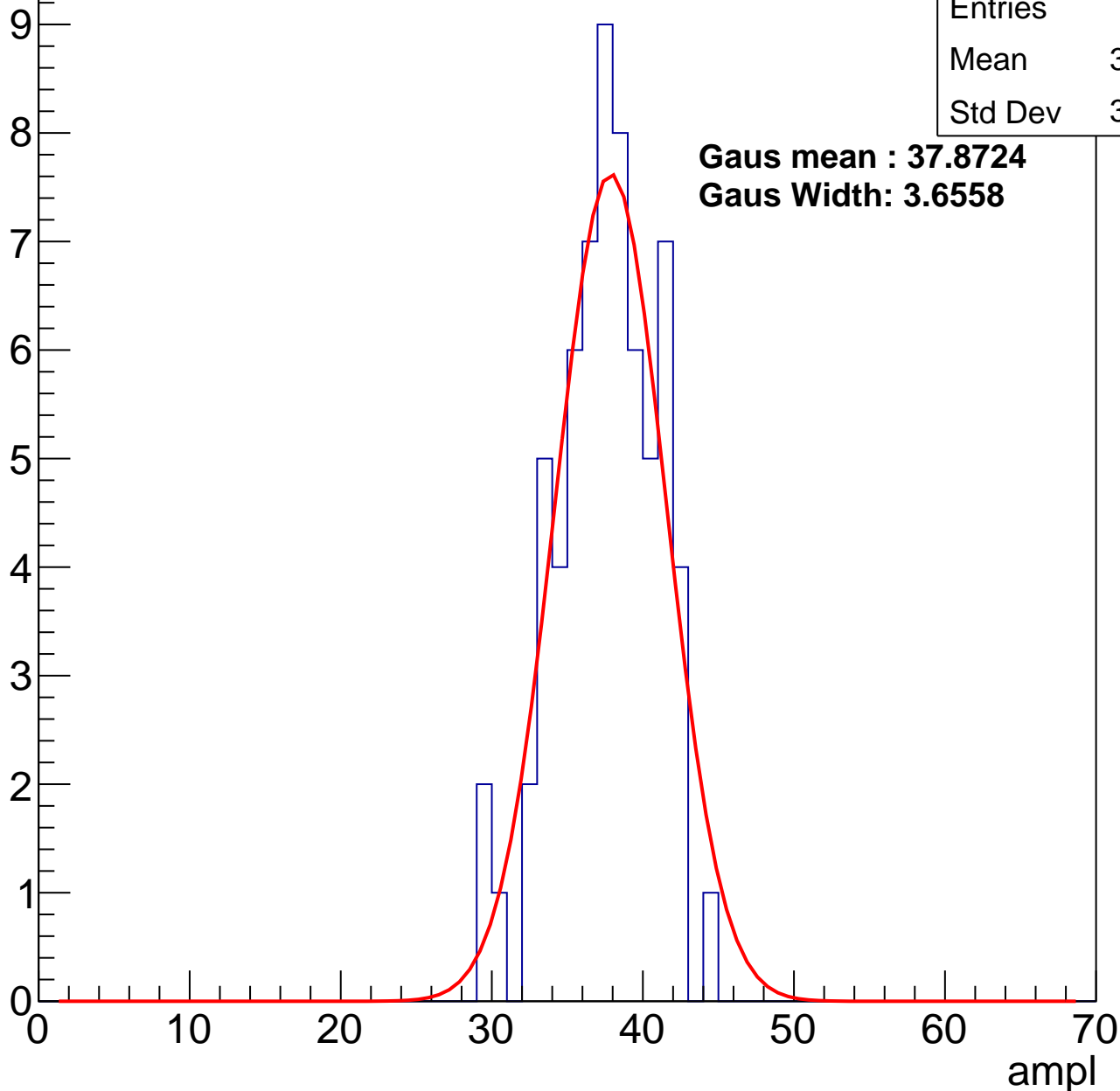
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.09
Std Dev	3.259

**Gaus mean : 37.8724**

**Gaus Width: 3.6558**



# B0L002S, U8-ch118, adc2

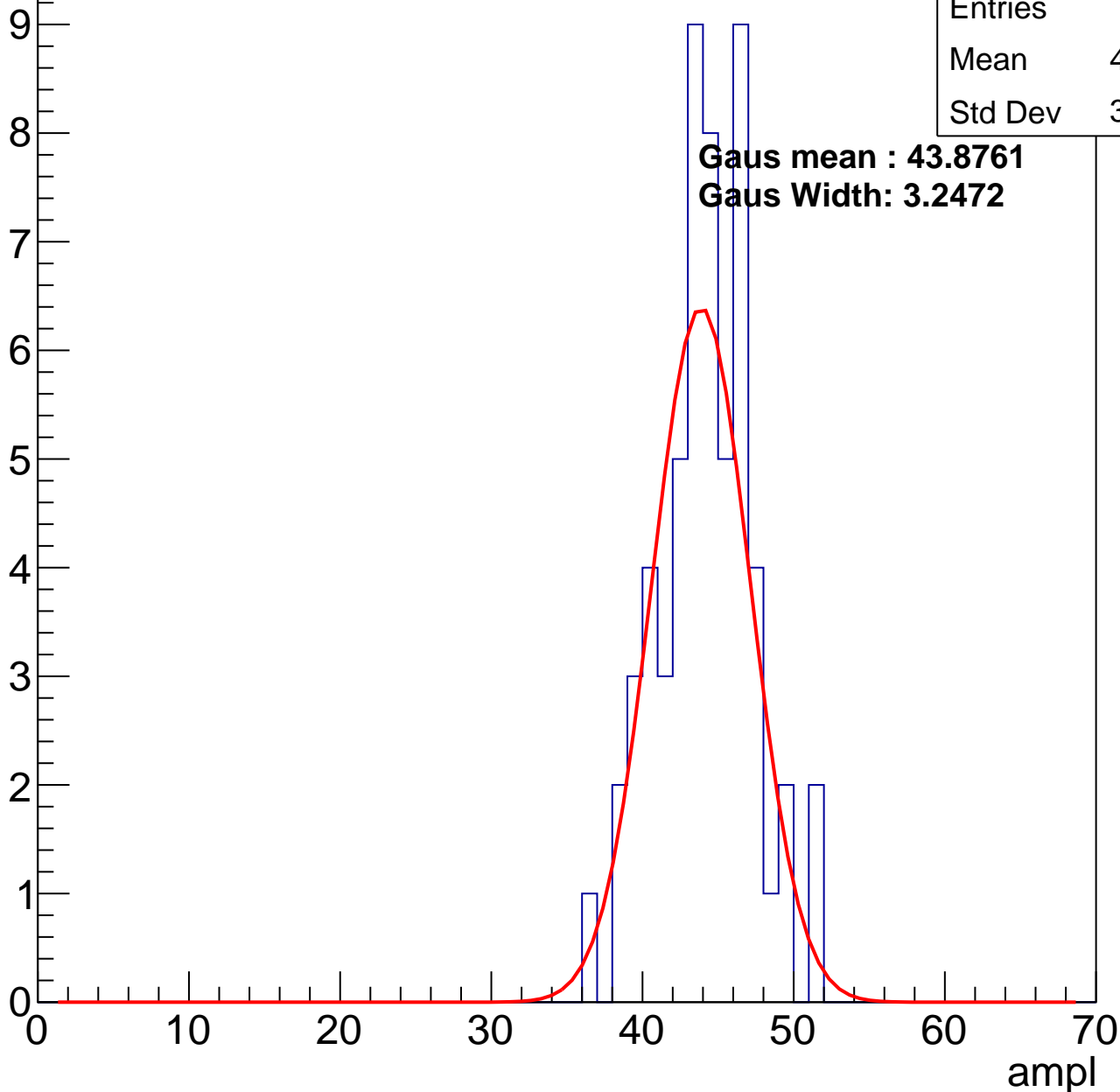
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.72
Std Dev	3.128

**Gaus mean : 43.8761**

**Gaus Width: 3.2472**

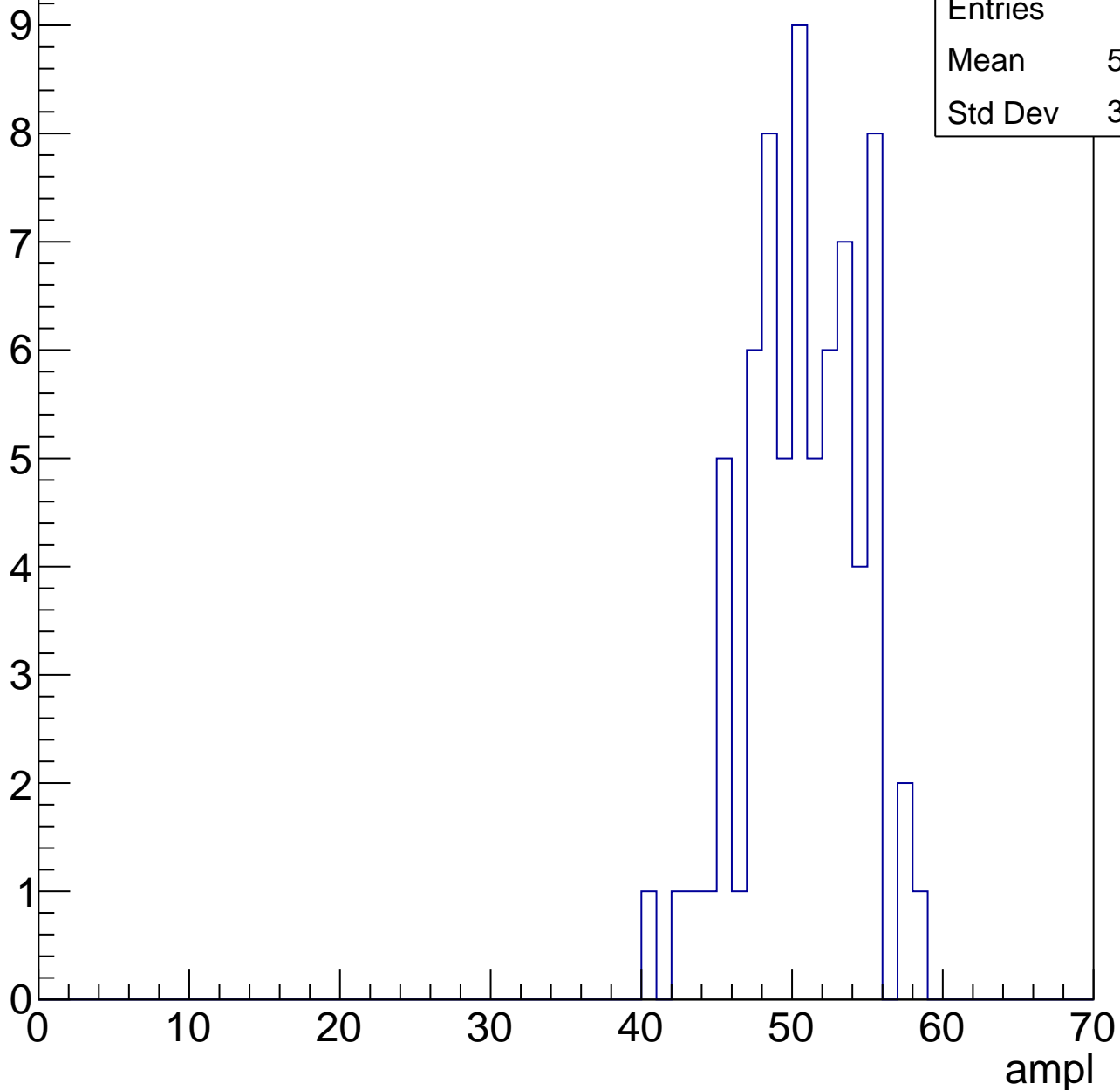


# B0L002S, U8-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.24
Std Dev	3.743

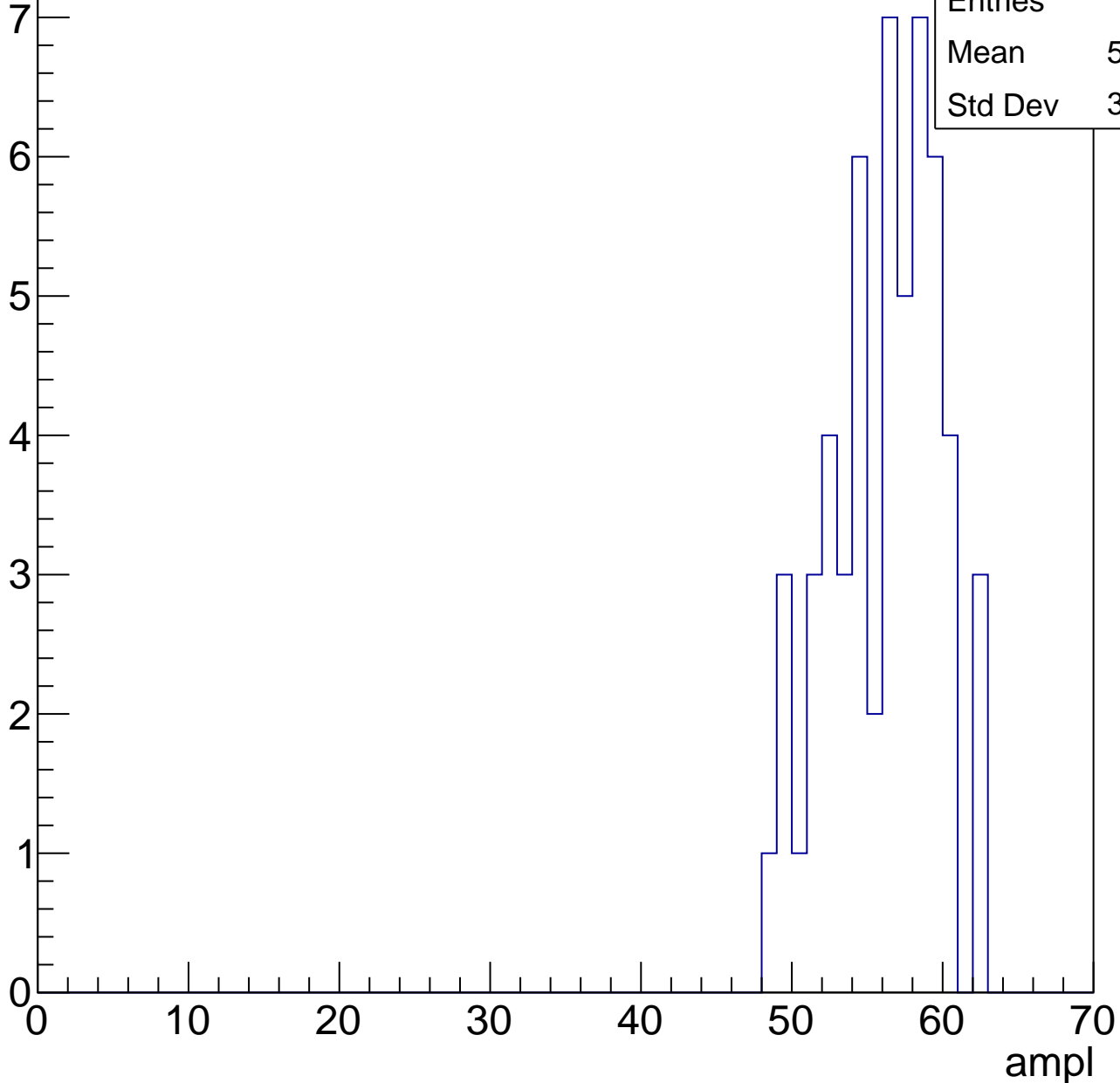


# B0L002S, U8-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	55.67
Std Dev	3.547

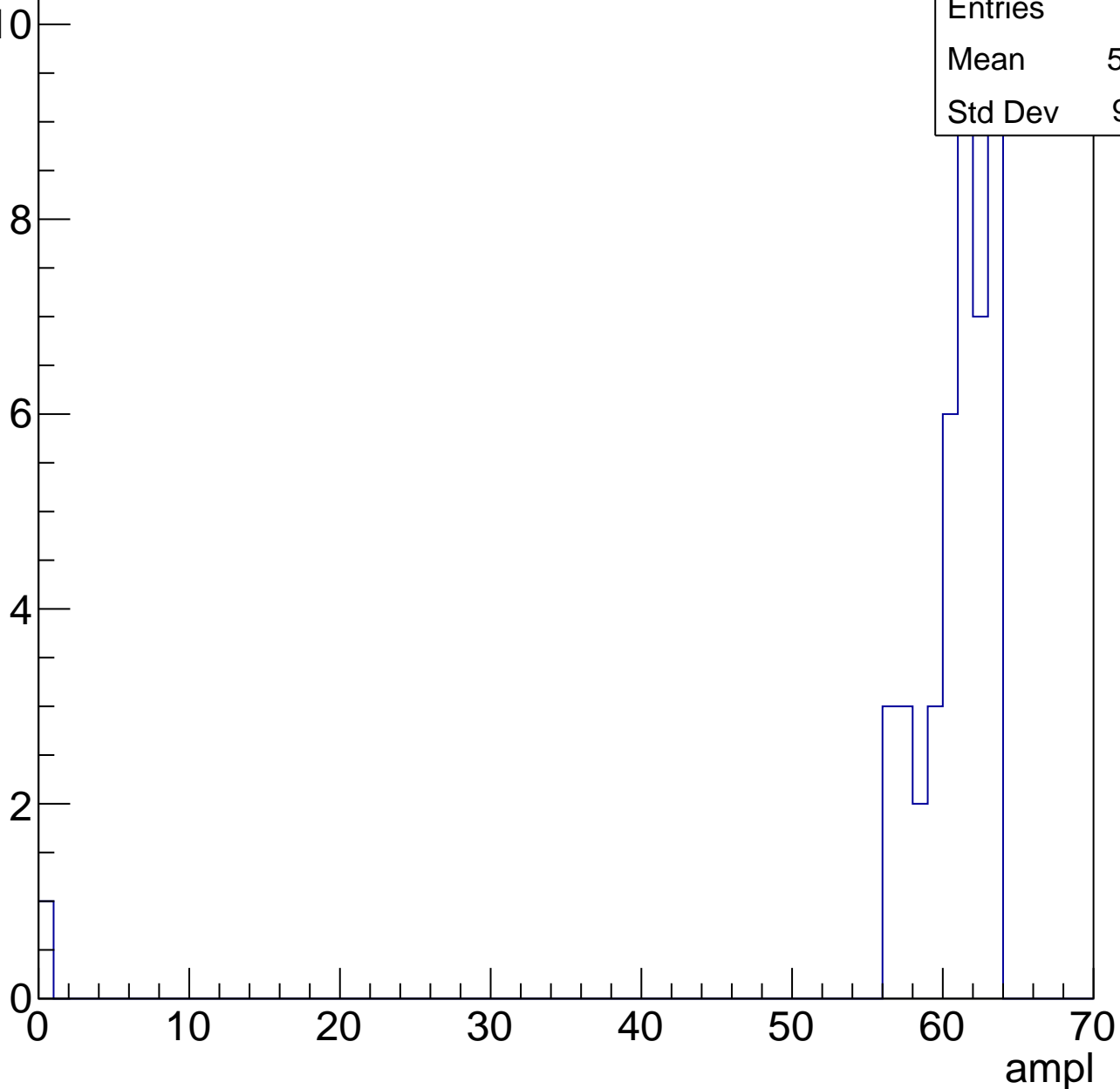


# B0L002S, U8-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

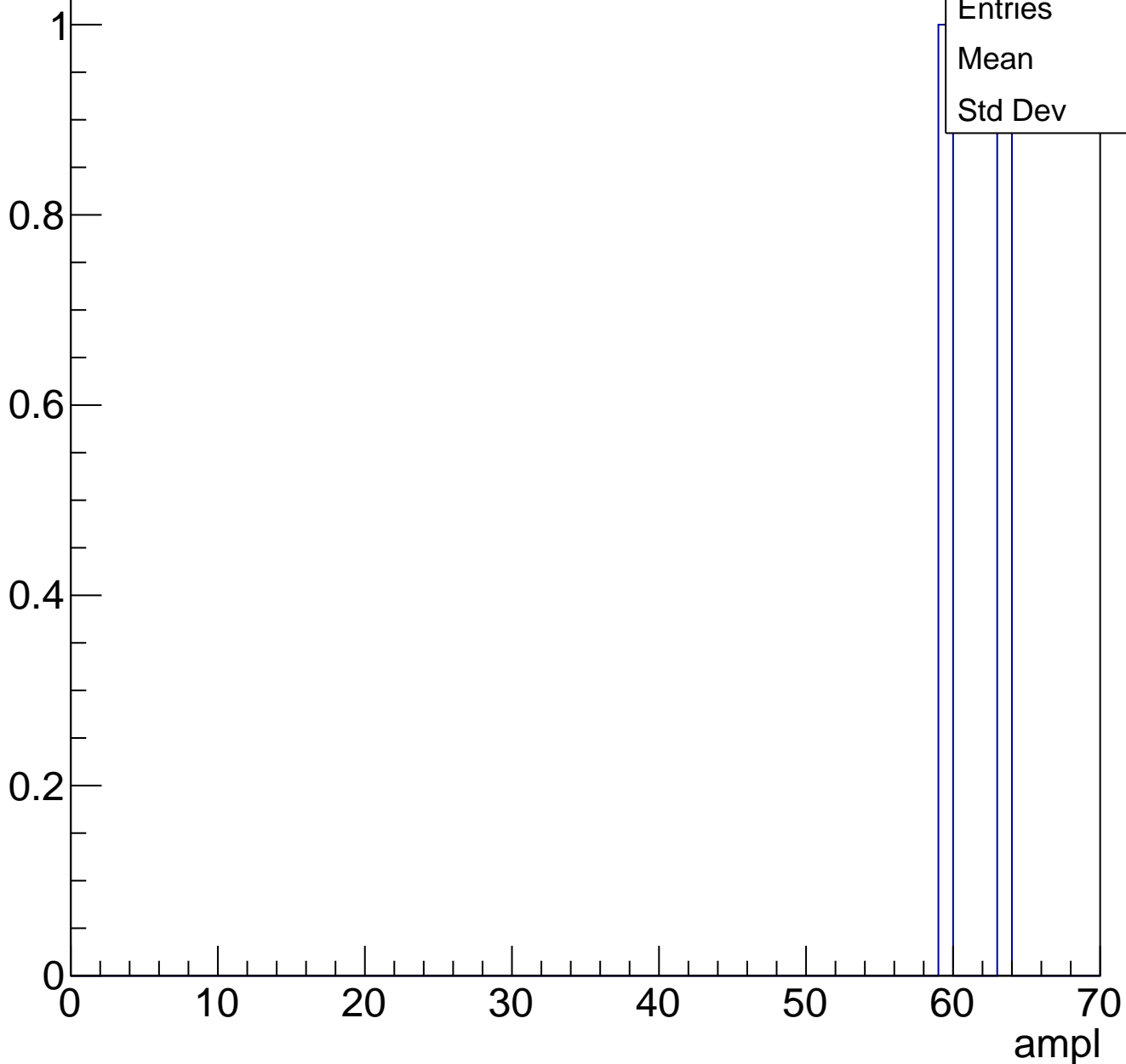
Entries	44
Mean	59.16
Std Dev	9.261



# B0L002S, U8-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

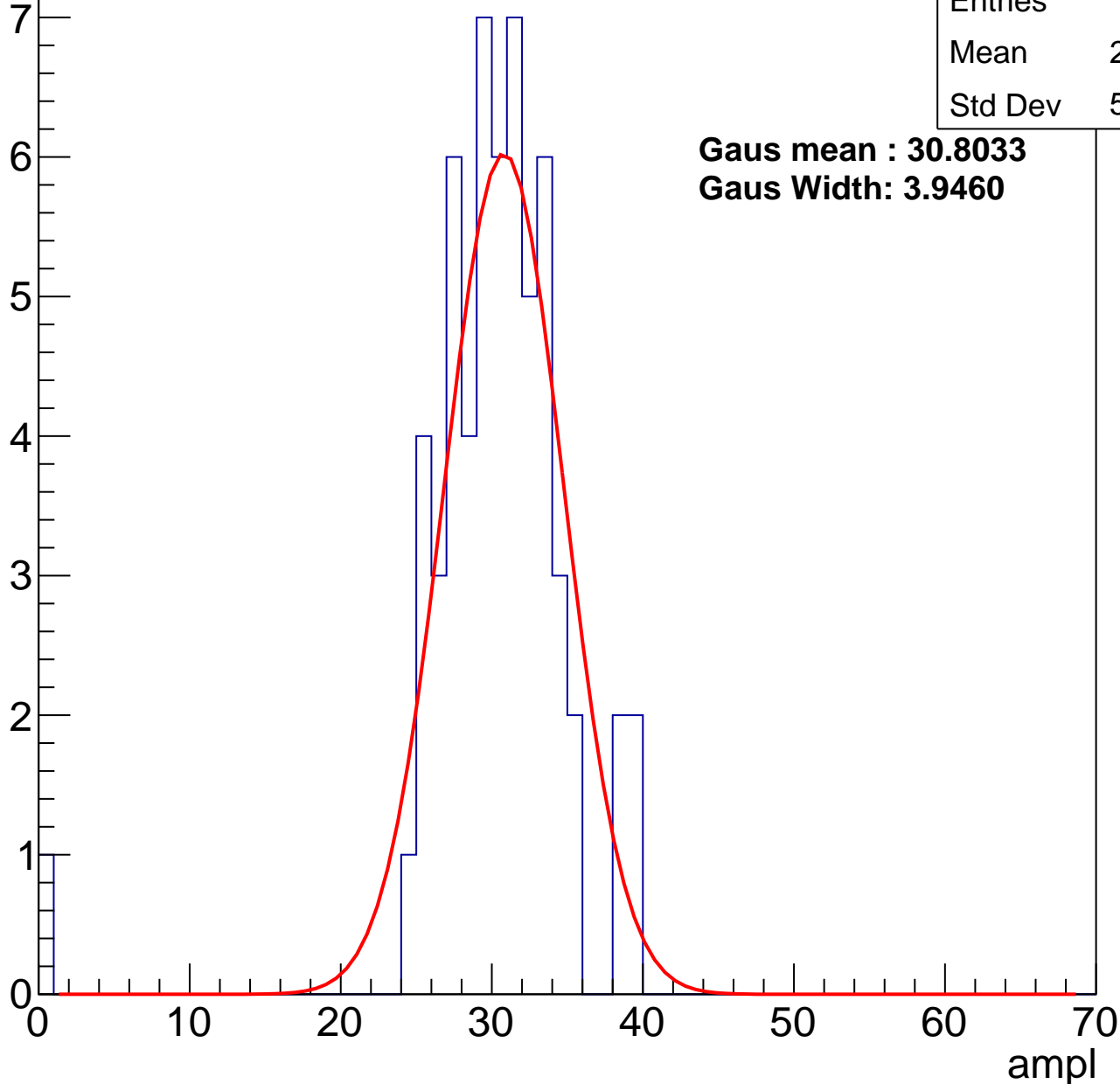
# B0L002S, U8-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.83
Std Dev	5.253

**Gaus mean : 30.8033**  
**Gaus Width: 3.9460**



# B0L002S, U8-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	86
Mean	36.67
Std Dev	3.856

**Gaus mean : 36.8914**

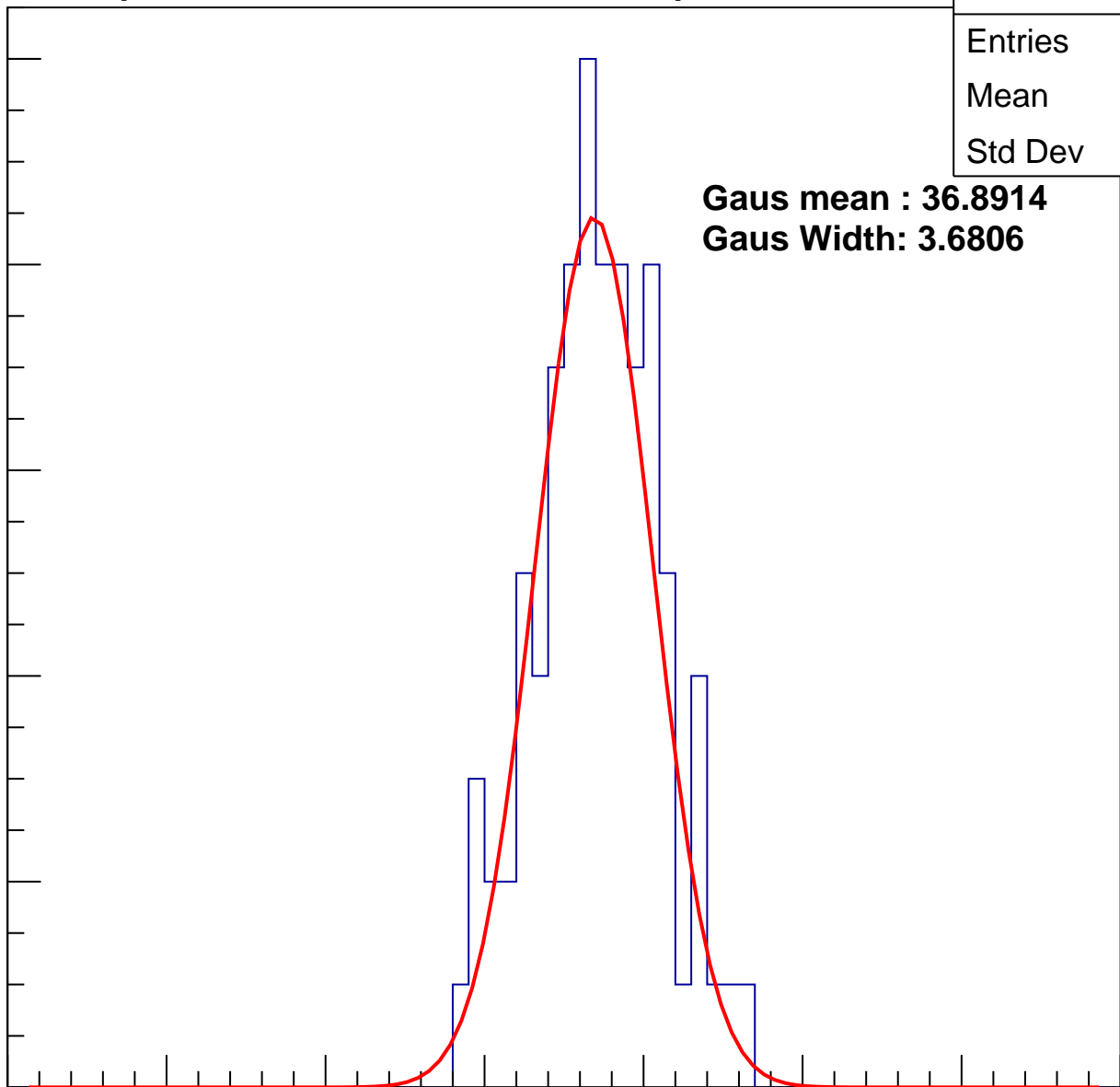
**Gaus Width: 3.6806**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch119, adc2

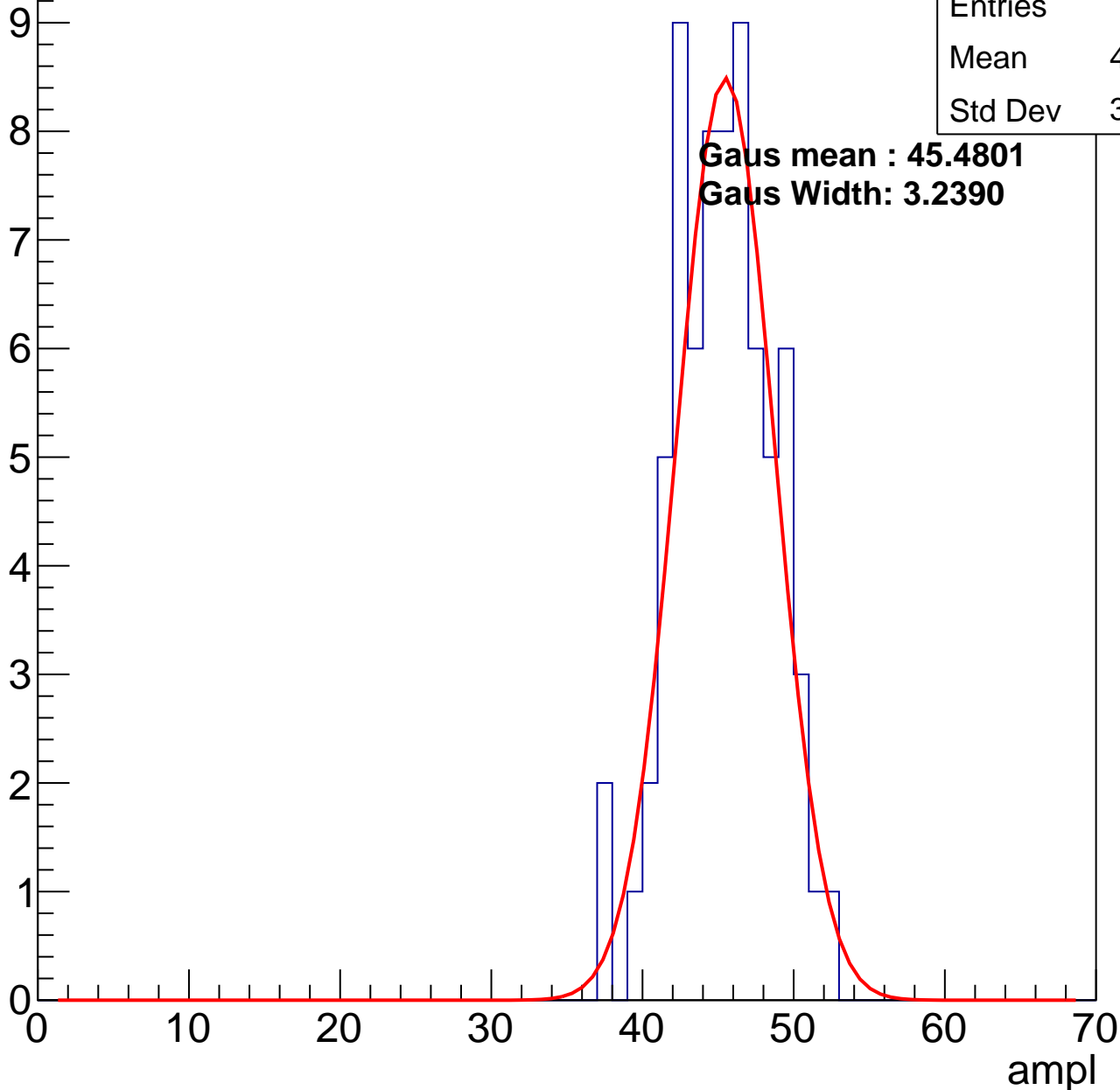
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.85
Std Dev	3.204

**Gaus mean : 45.4801**

**Gaus Width: 3.2390**

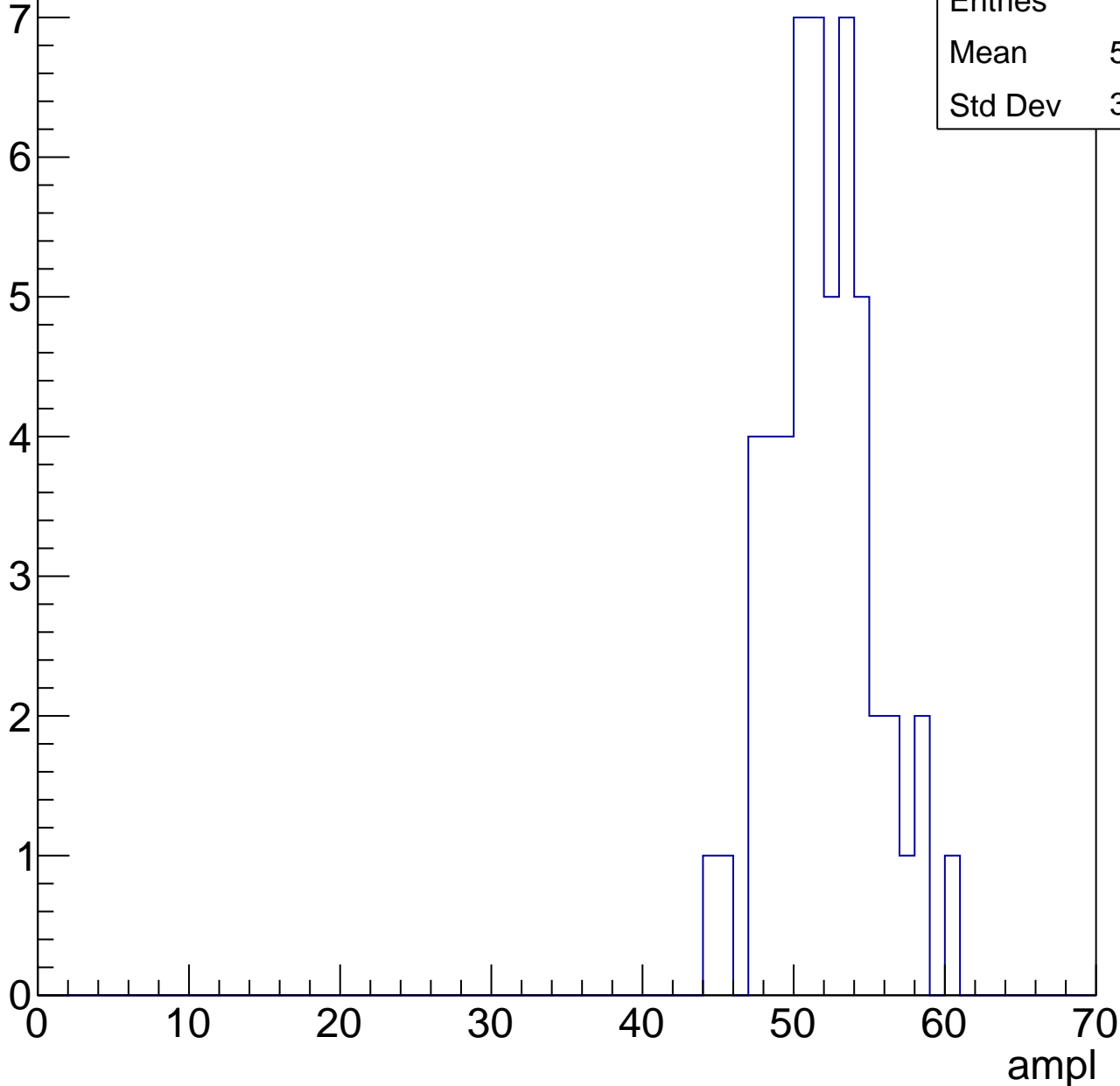


# B0L002S, U8-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	51.47
Std Dev	3.283

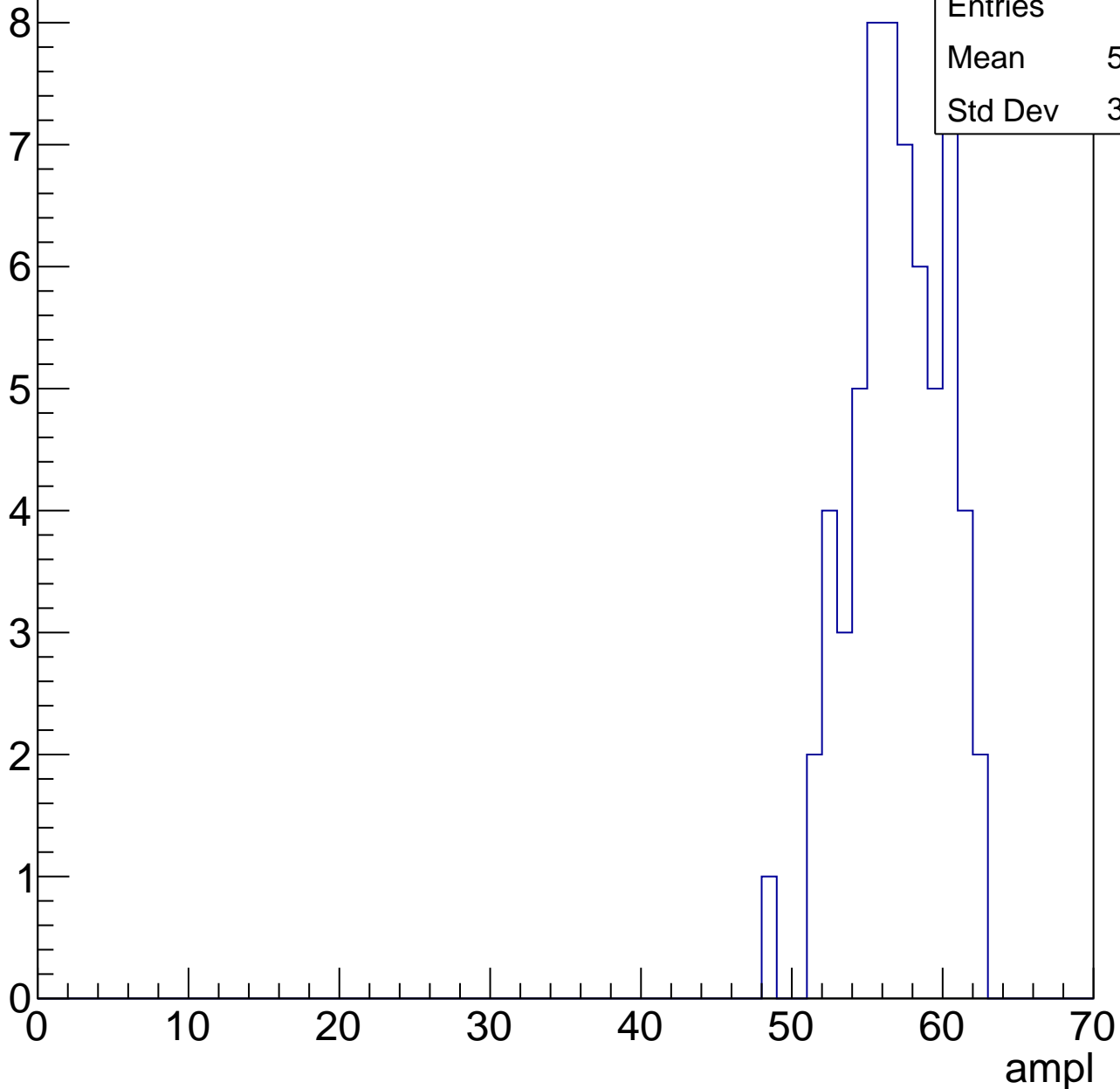


# B0L002S, U8-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.59
Std Dev	3.053

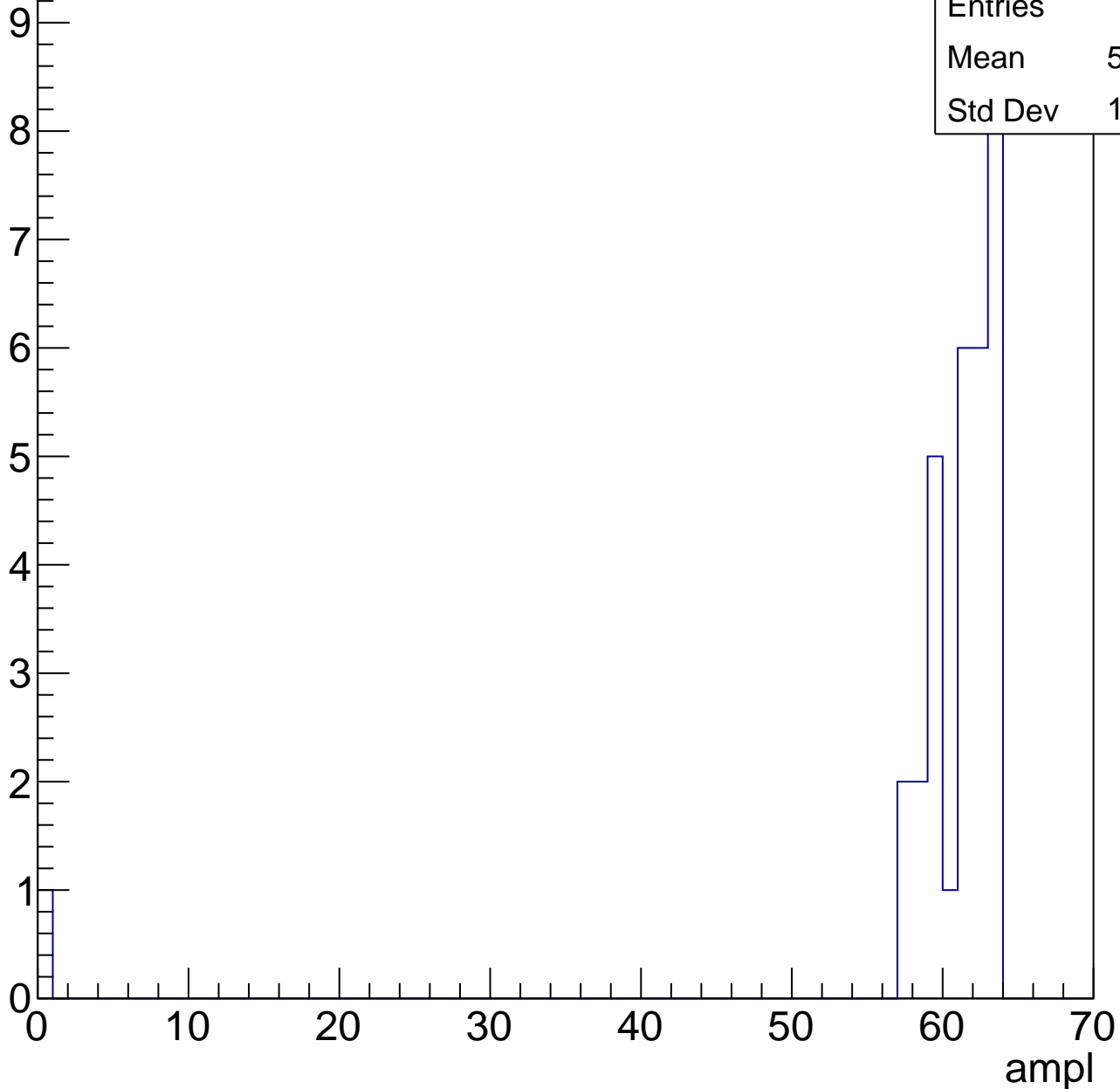


# B0L002S, U8-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.06
Std Dev	10.77



# B0L002S, U8-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	91
Mean	29.37
Std Dev	5.674

**Gaus mean : 30.2847**

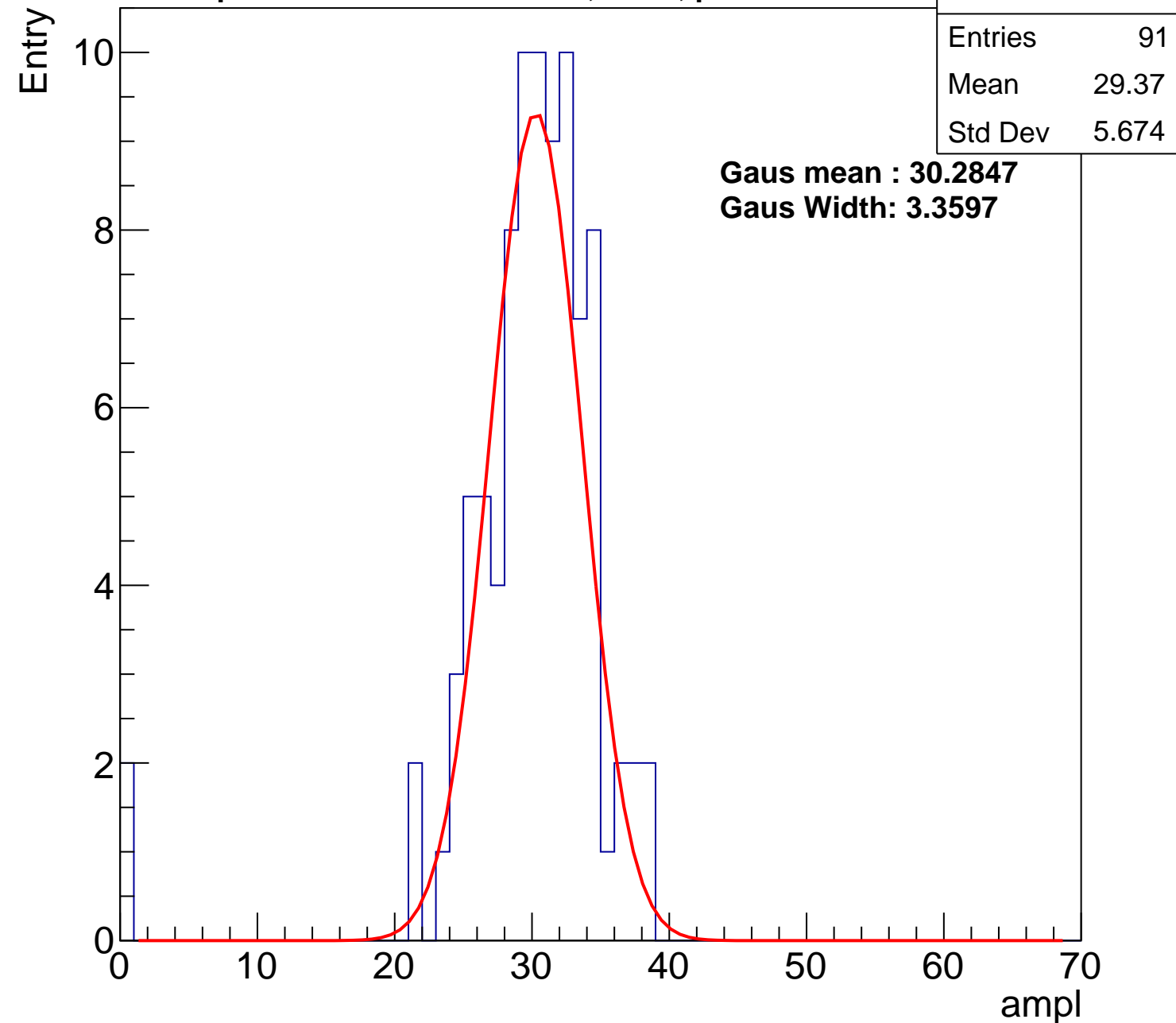
**Gaus Width: 3.3597**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch120, adc1

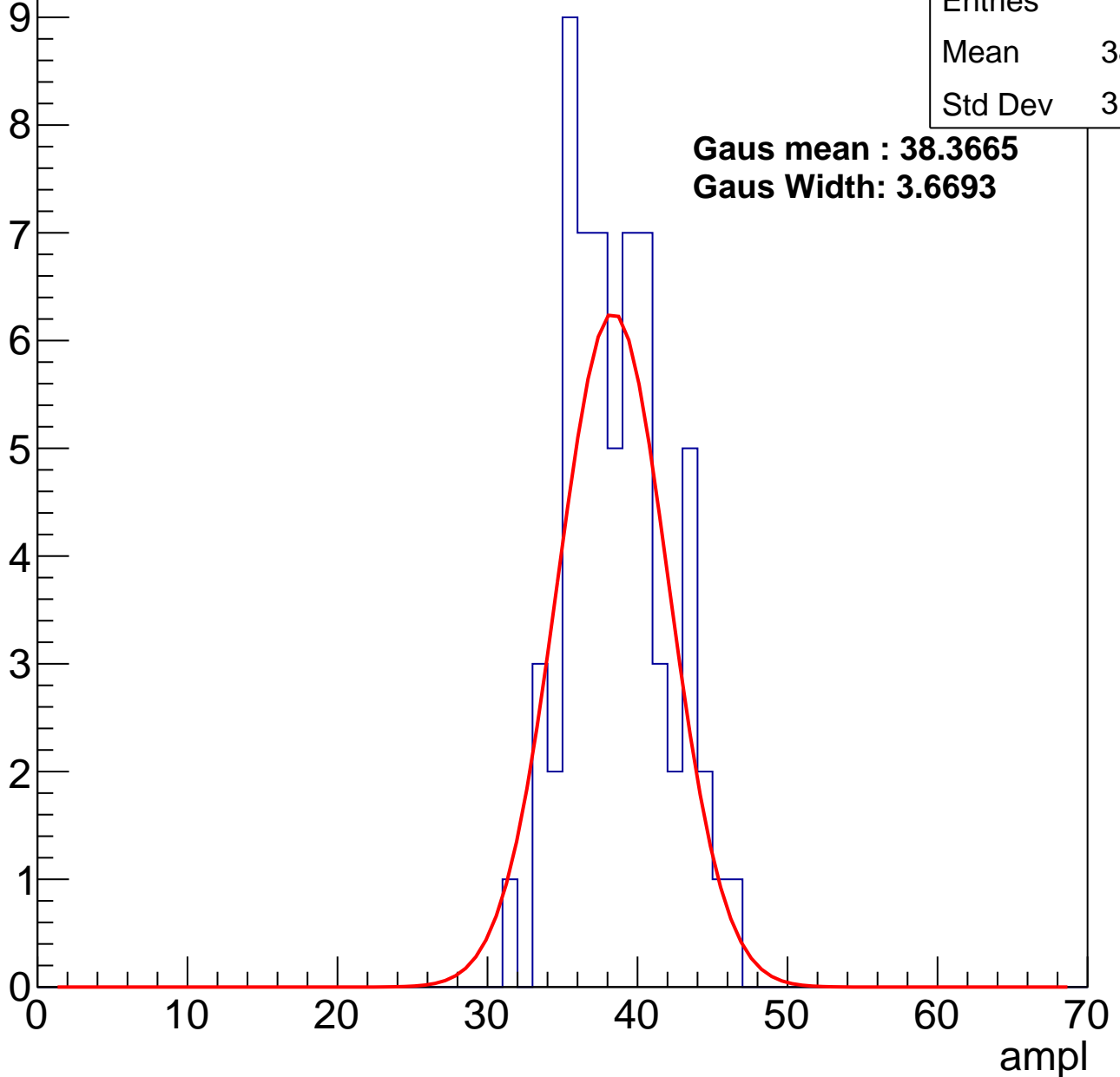
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.19
Std Dev	3.296

**Gaus mean : 38.3665**

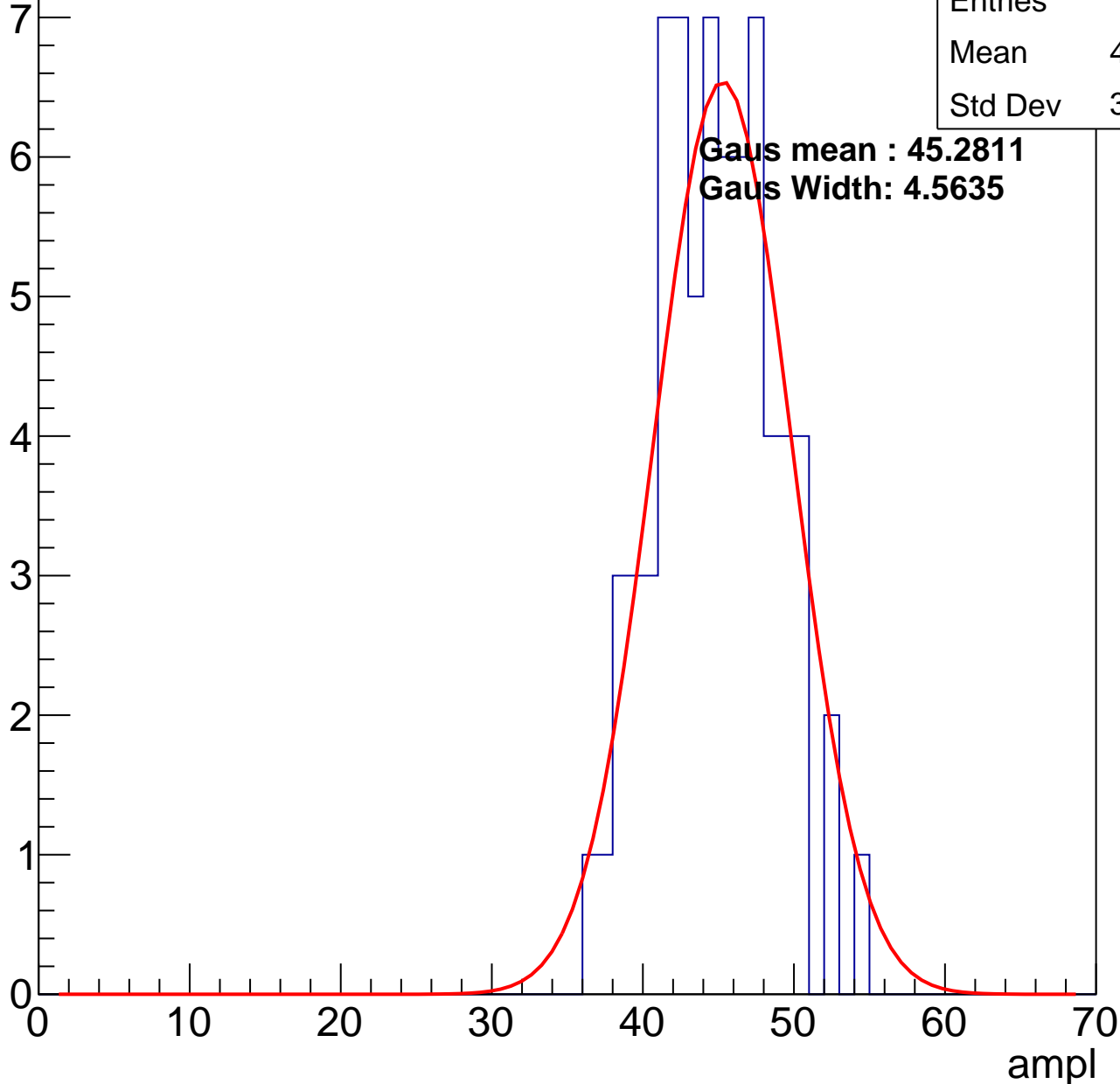
**Gaus Width: 3.6693**



# B0L002S, U8-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



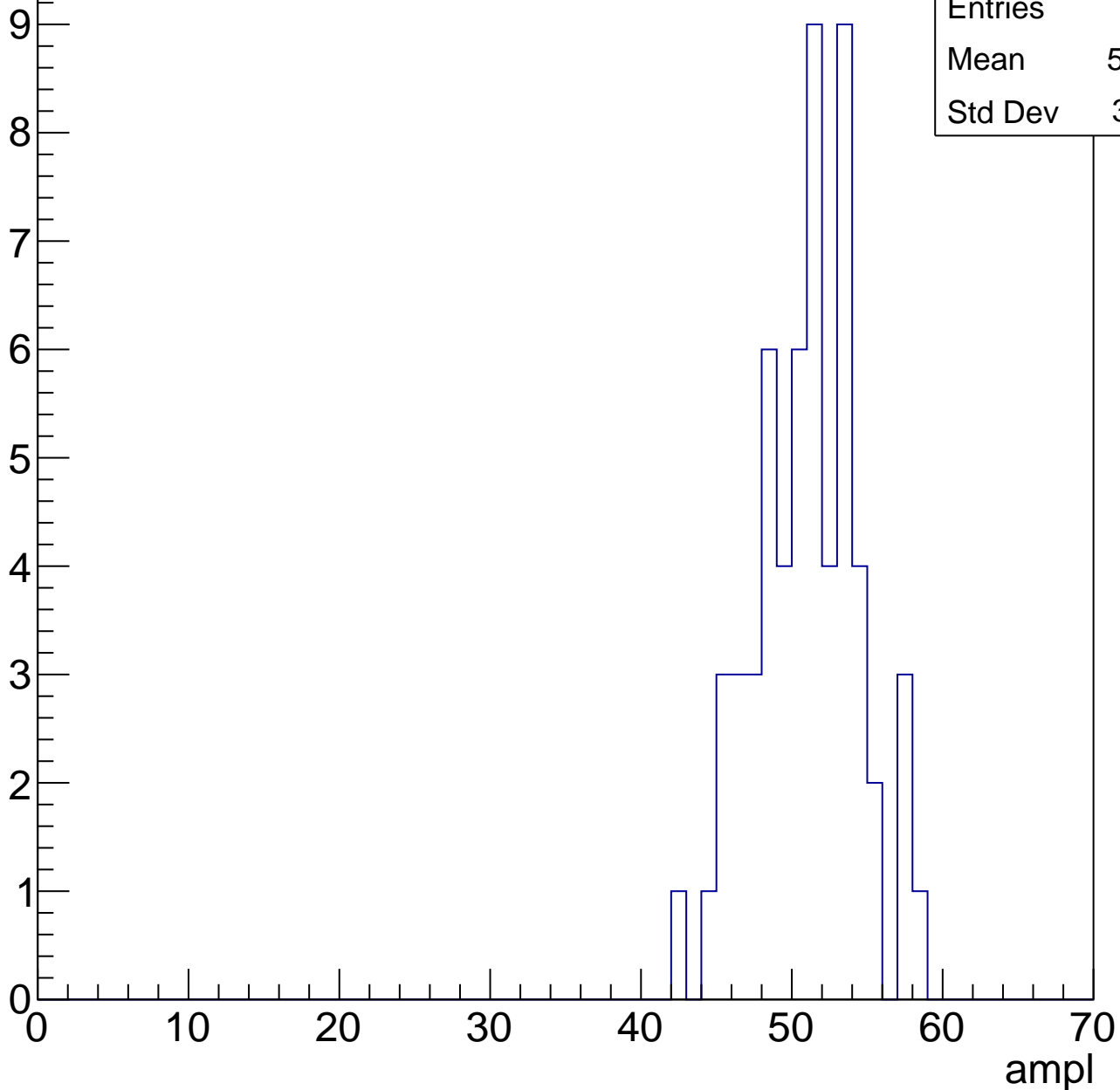
Entries	71
Mean	44.35
Std Dev	3.864

# B0L002S, U8-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

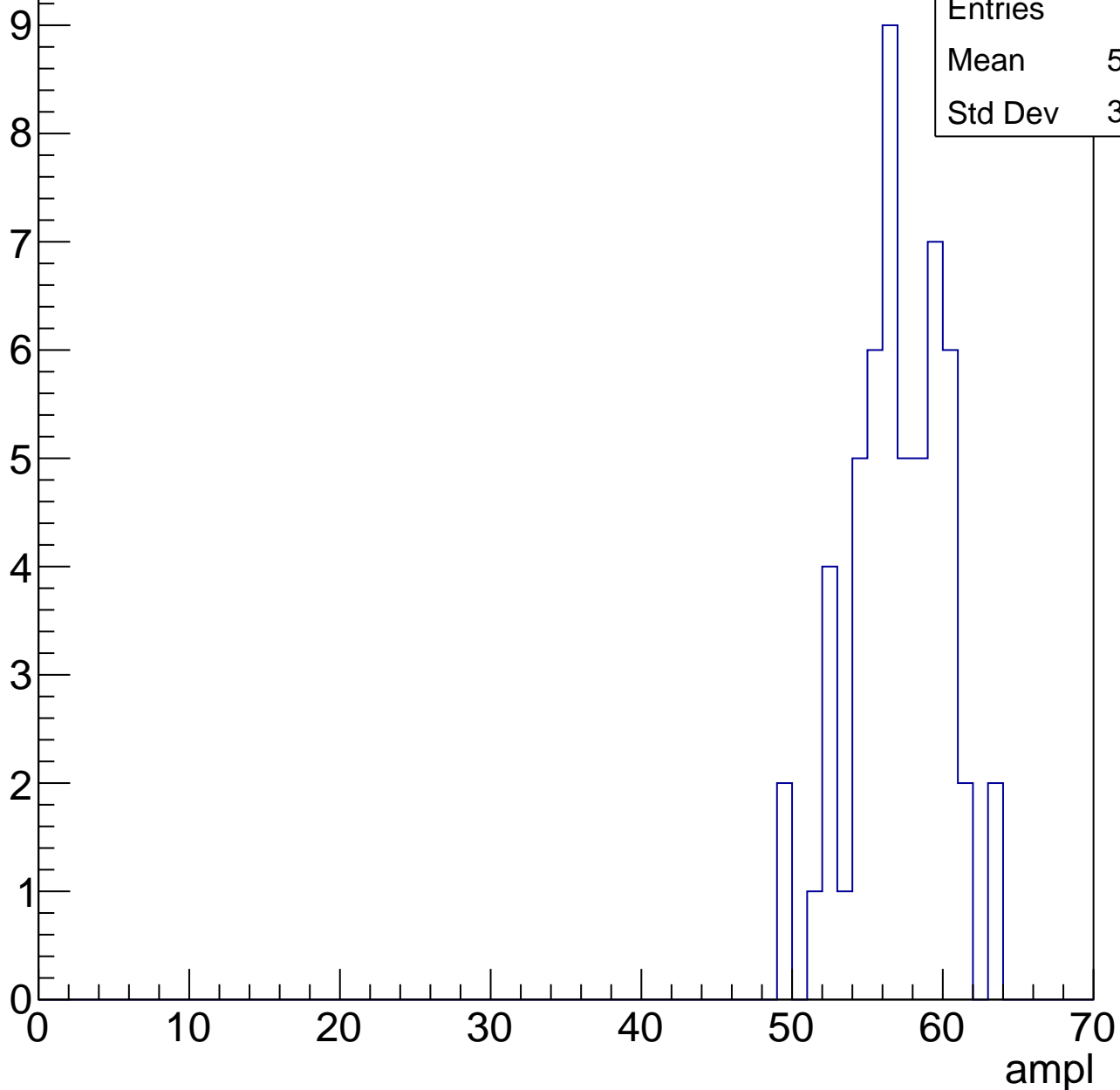
Entries	59
Mean	50.56
Std Dev	3.421



# B0L002S, U8-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



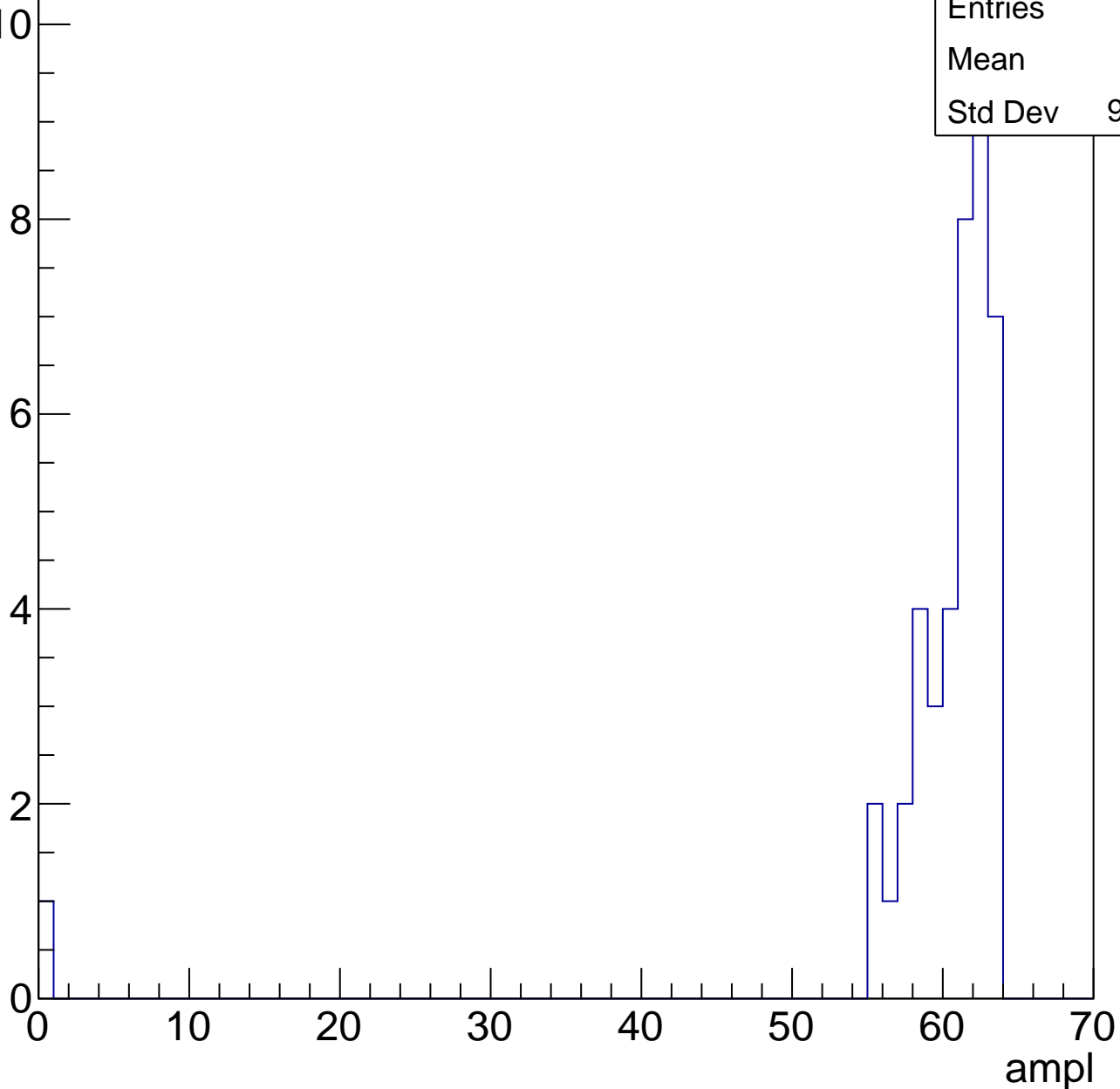
Entries	55
Mean	56.55
Std Dev	3.127

# B0L002S, U8-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59
Std Dev	9.477



# B0L002S, U8-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L002S, U8-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch121, adc0

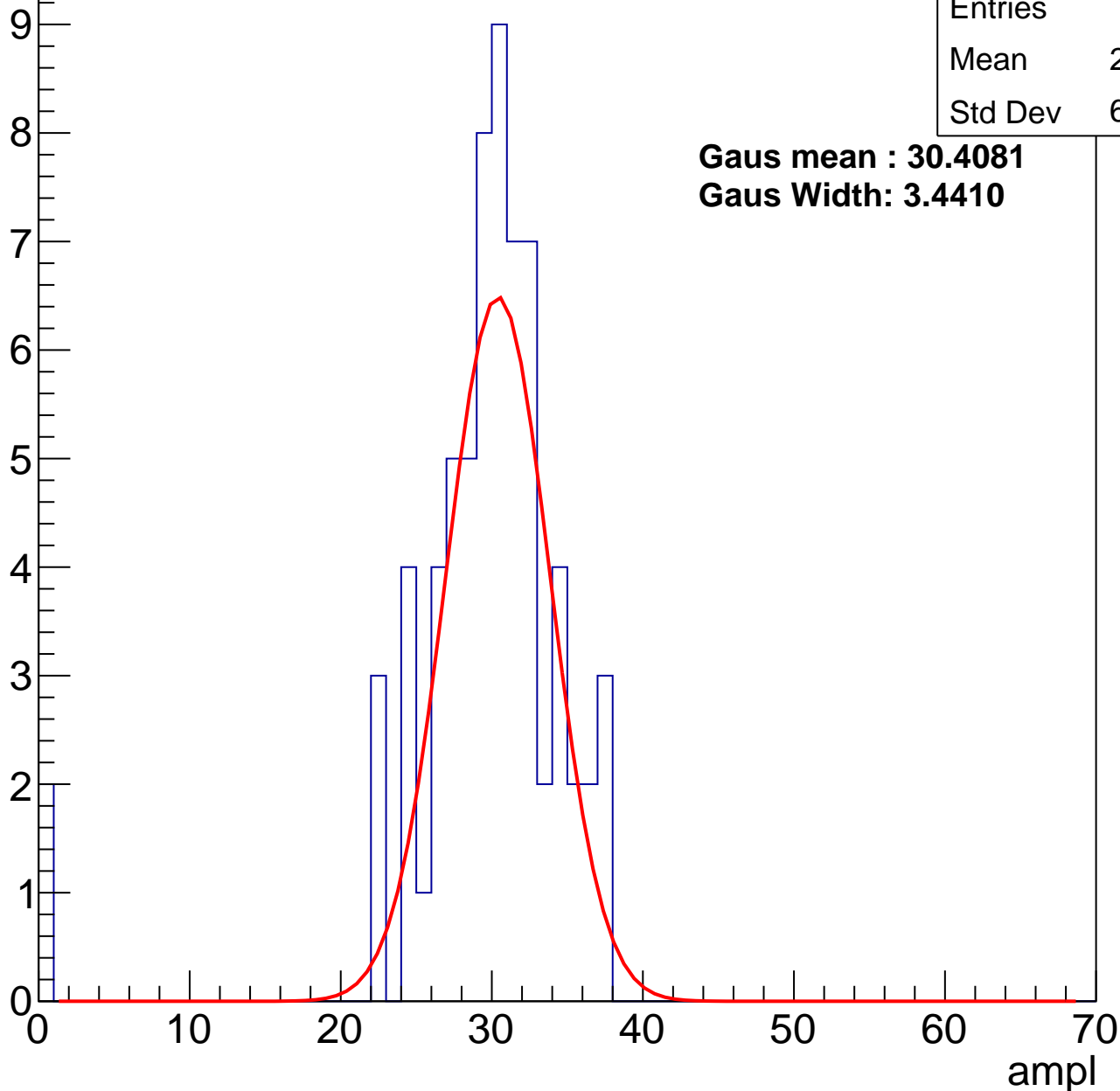
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.88
Std Dev	6.182

**Gaus mean : 30.4081**

**Gaus Width: 3.4410**



# B0L002S, U8-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	36.68
Std Dev	3.92

**Gaus mean : 36.8470**

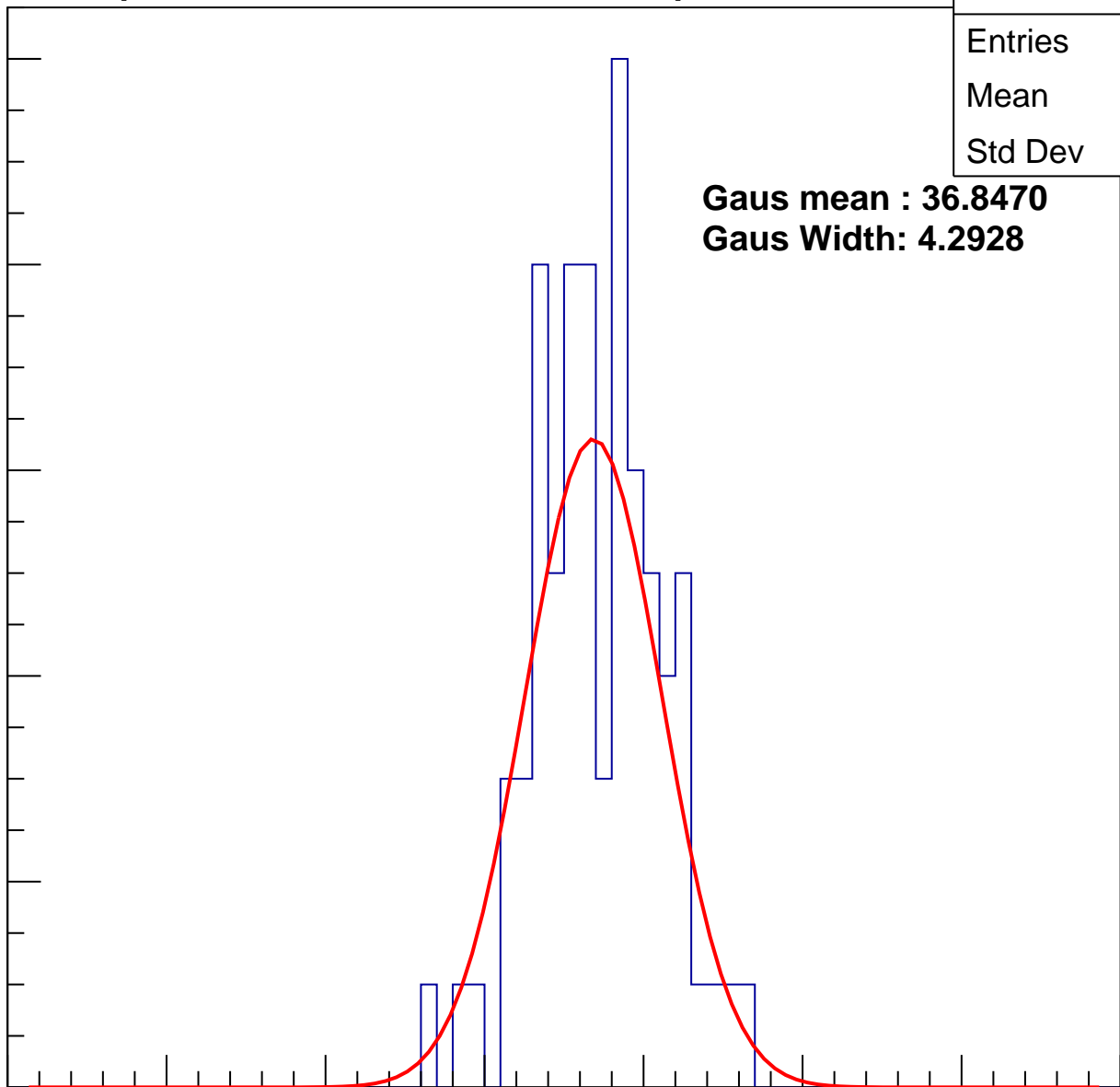
**Gaus Width: 4.2928**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch121, adc2

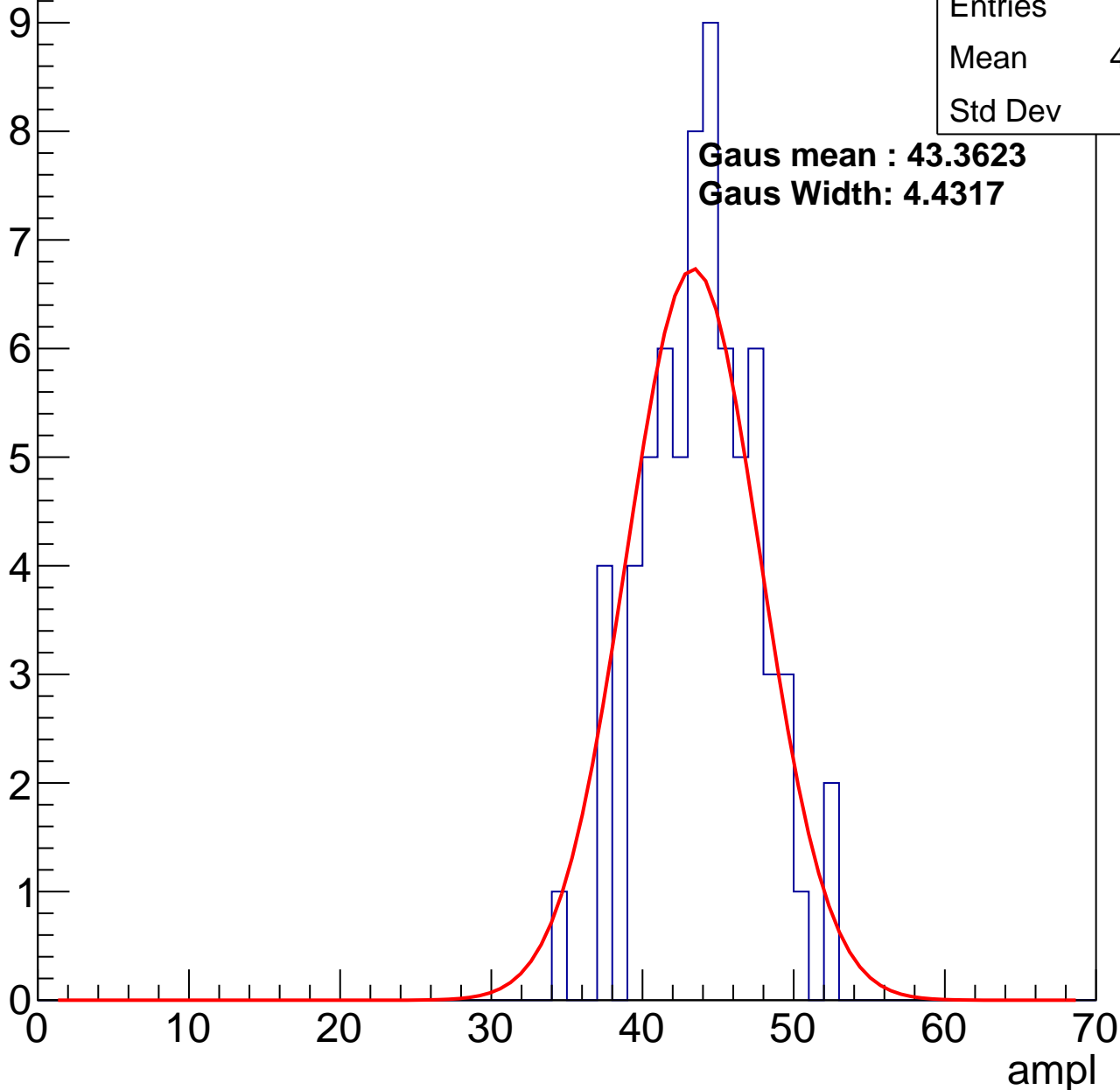
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.54
Std Dev	3.66

**Gaus mean : 43.3623**

**Gaus Width: 4.4317**

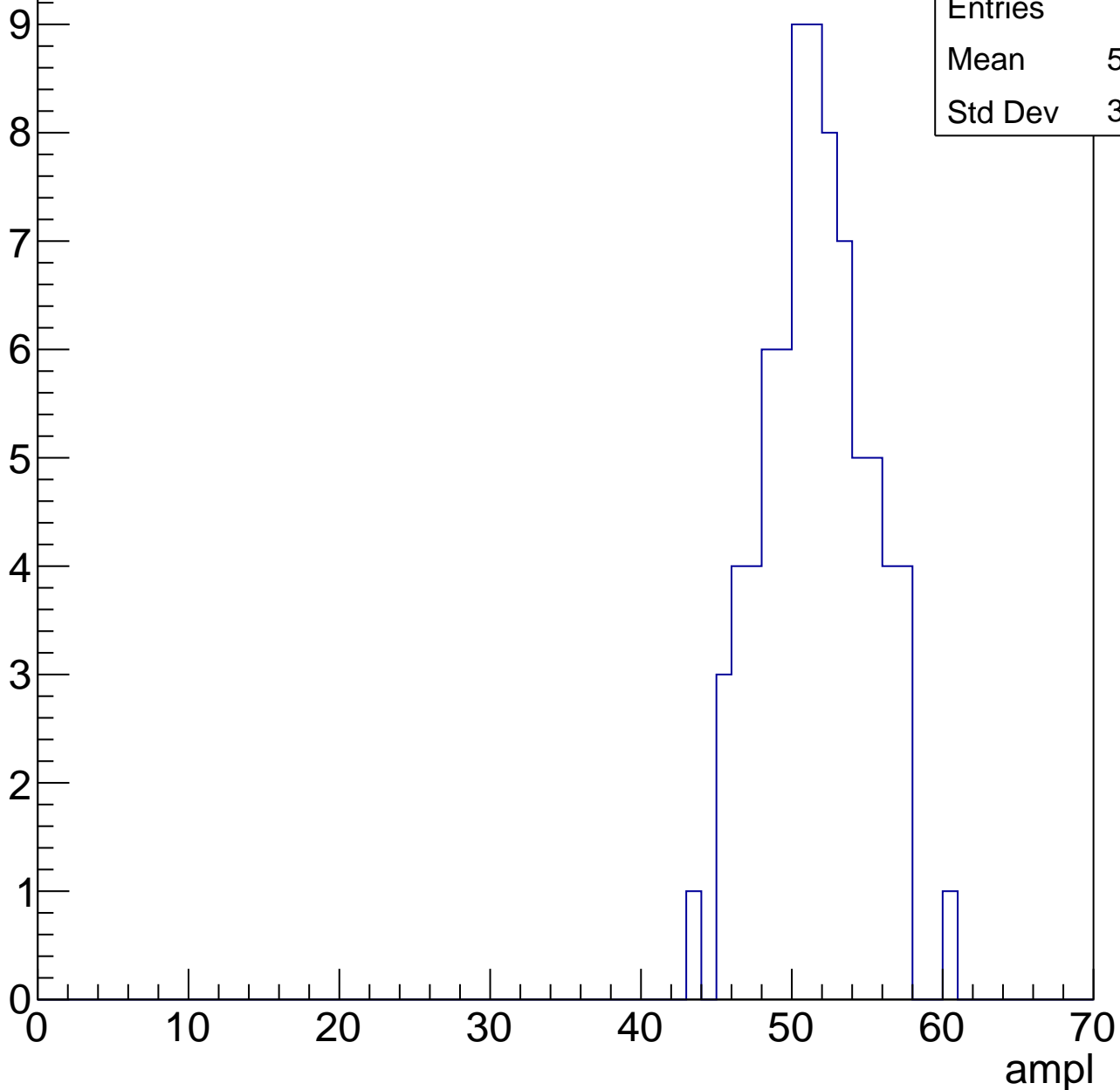


# B0L002S, U8-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

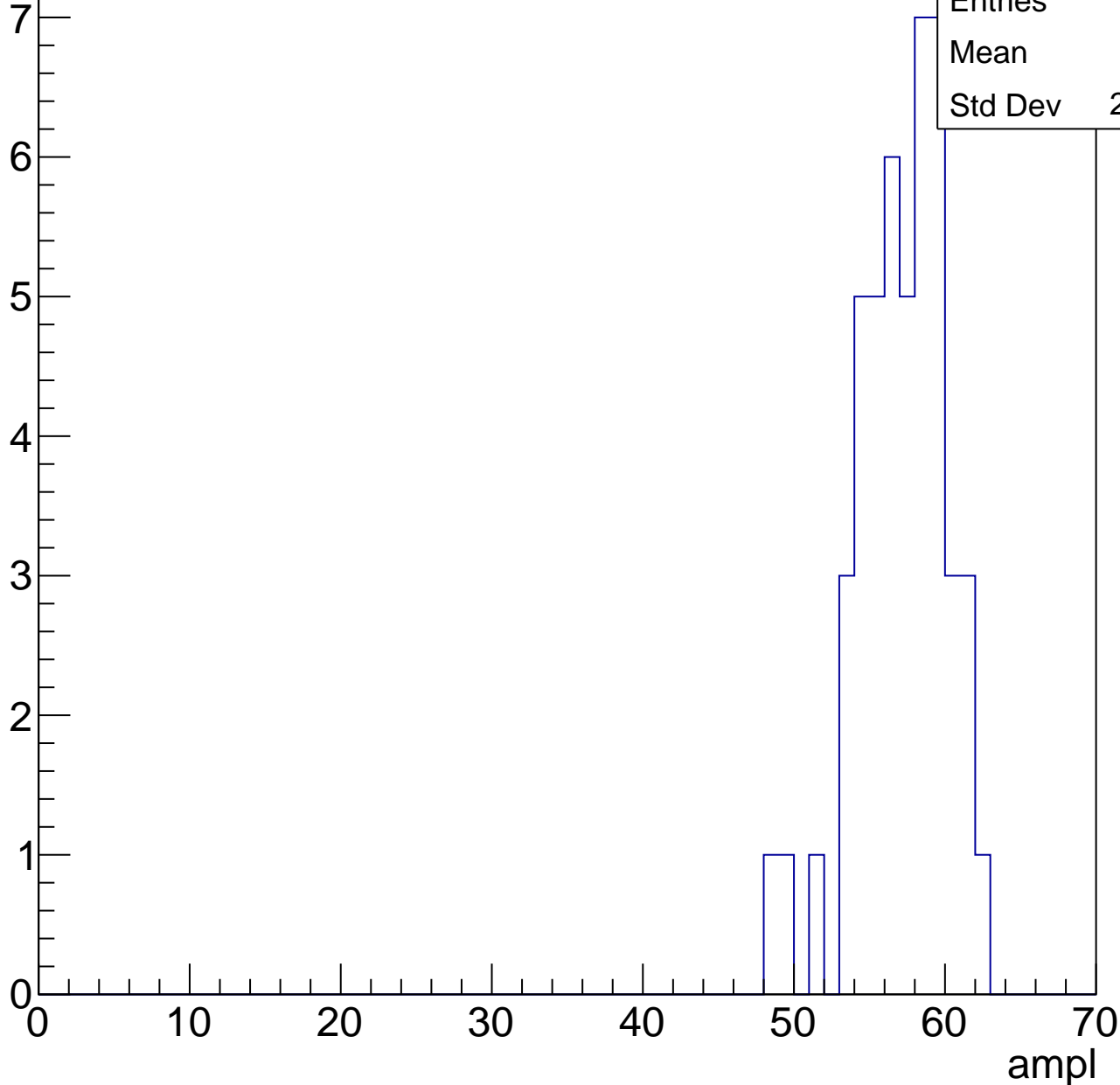
Entries	76
Mean	51.12
Std Dev	3.456



# B0L002S, U8-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

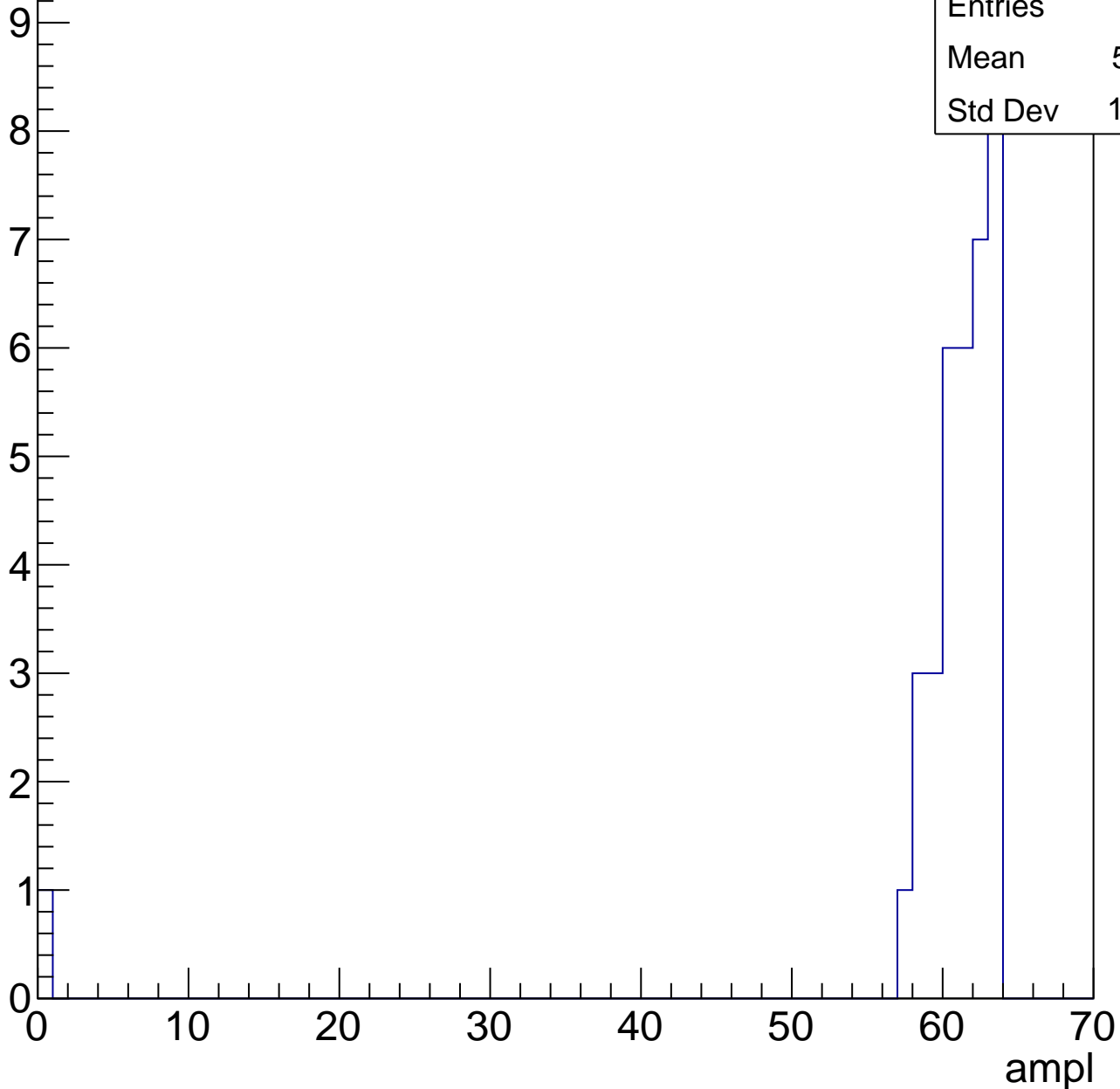


# B0L002S, U8-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.31
Std Dev	10.17



# B0L002S, U8-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch122, adc0

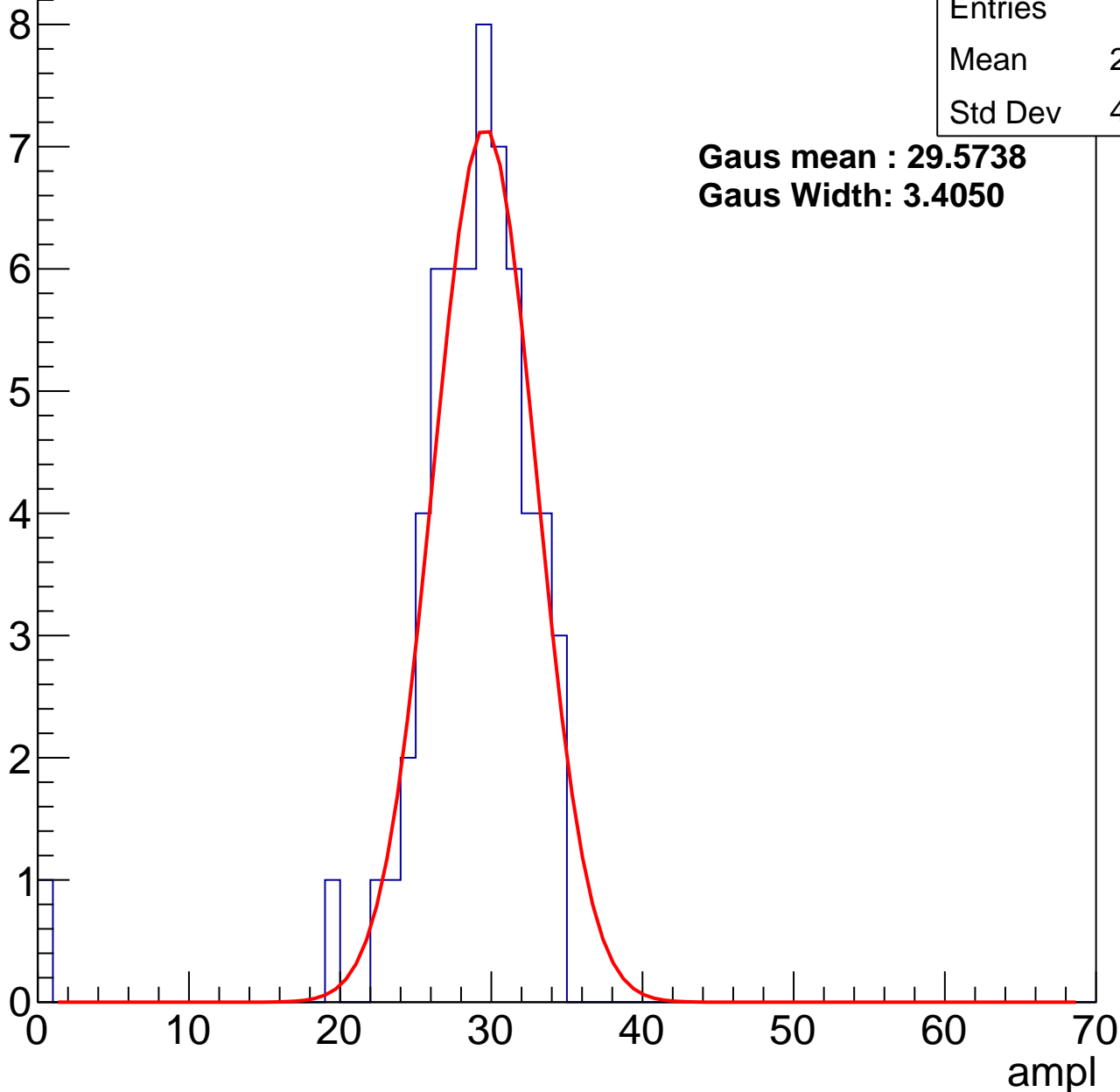
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28.13
Std Dev	4.804

**Gaus mean : 29.5738**

**Gaus Width: 3.4050**



# B0L002S, U8-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	35.64
Std Dev	3.343

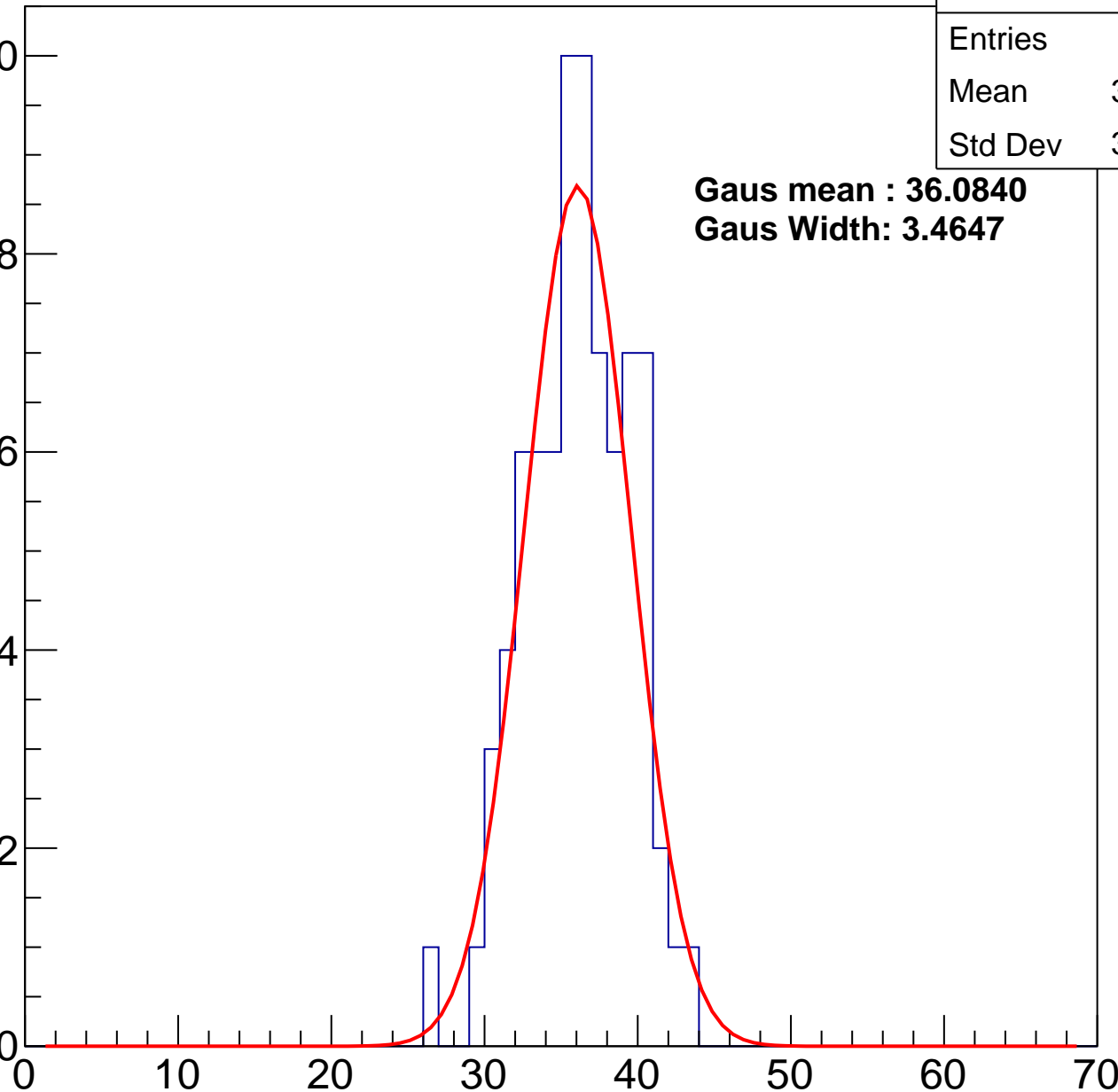
**Gaus mean : 36.0840**

**Gaus Width: 3.4647**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L002S, U8-ch122, adc2

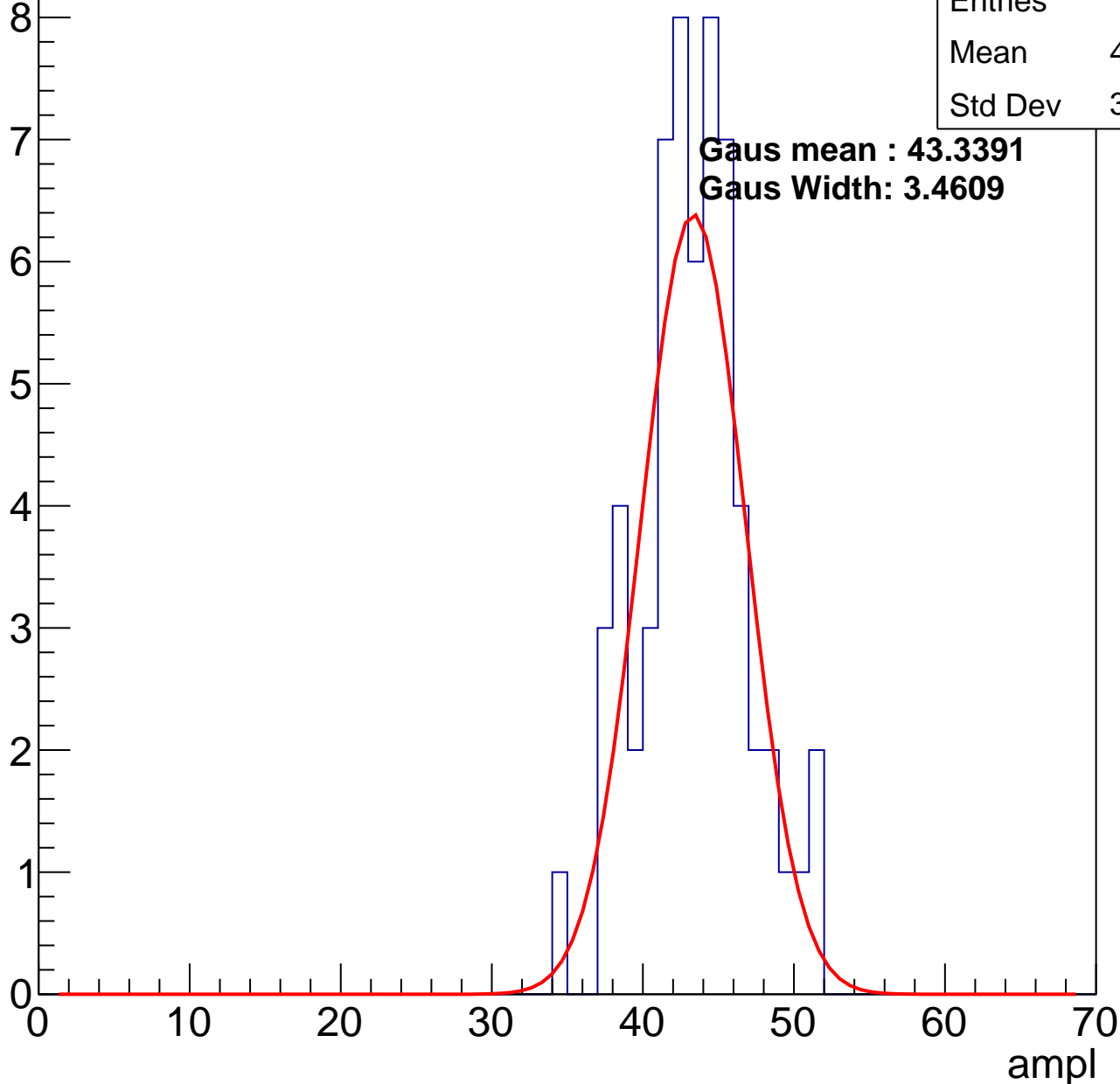
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.92
Std Dev	3.508

**Gaus mean : 43.3391**

**Gaus Width: 3.4609**

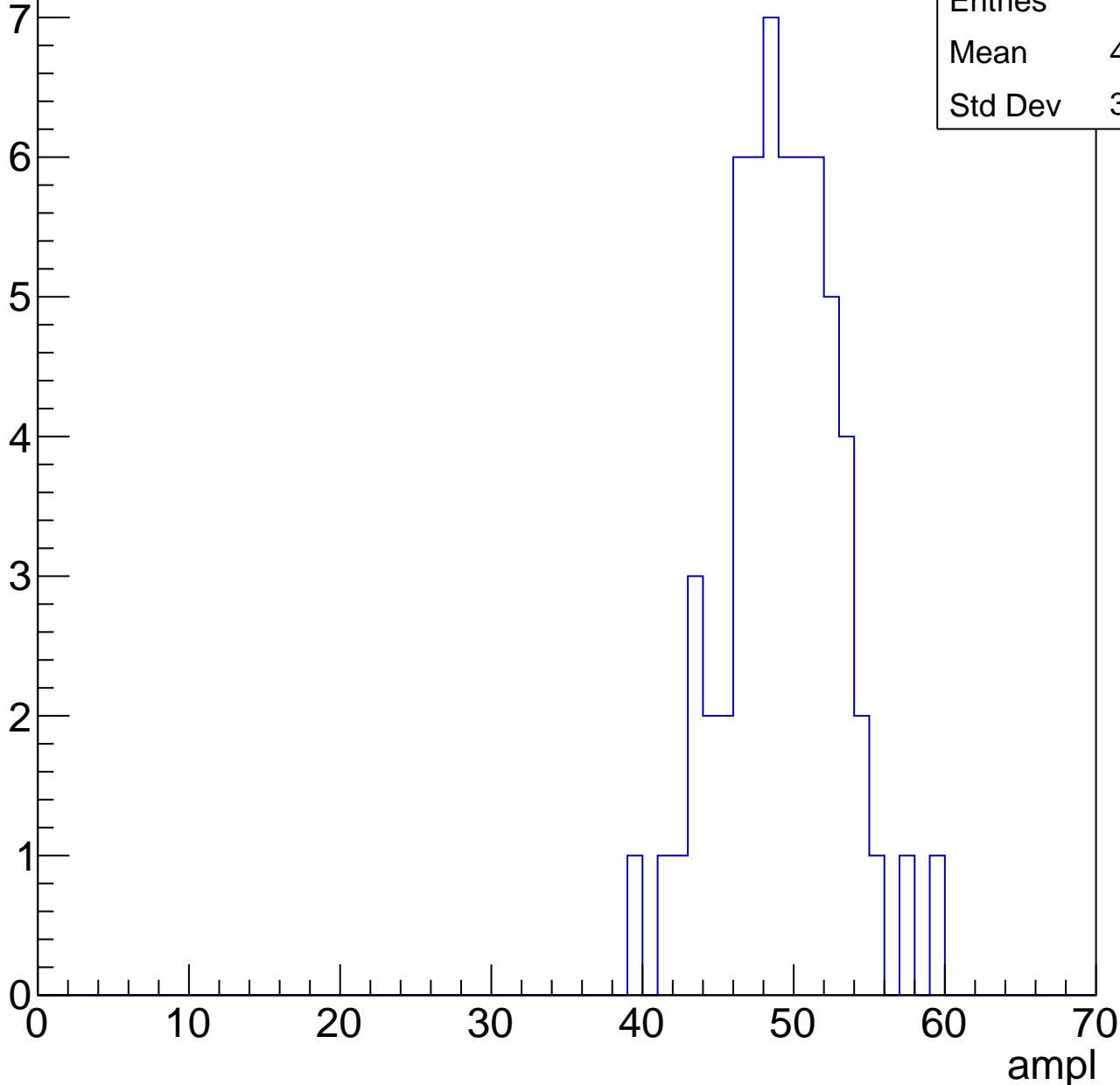


# B0L002S, U8-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	48.75
Std Dev	3.784

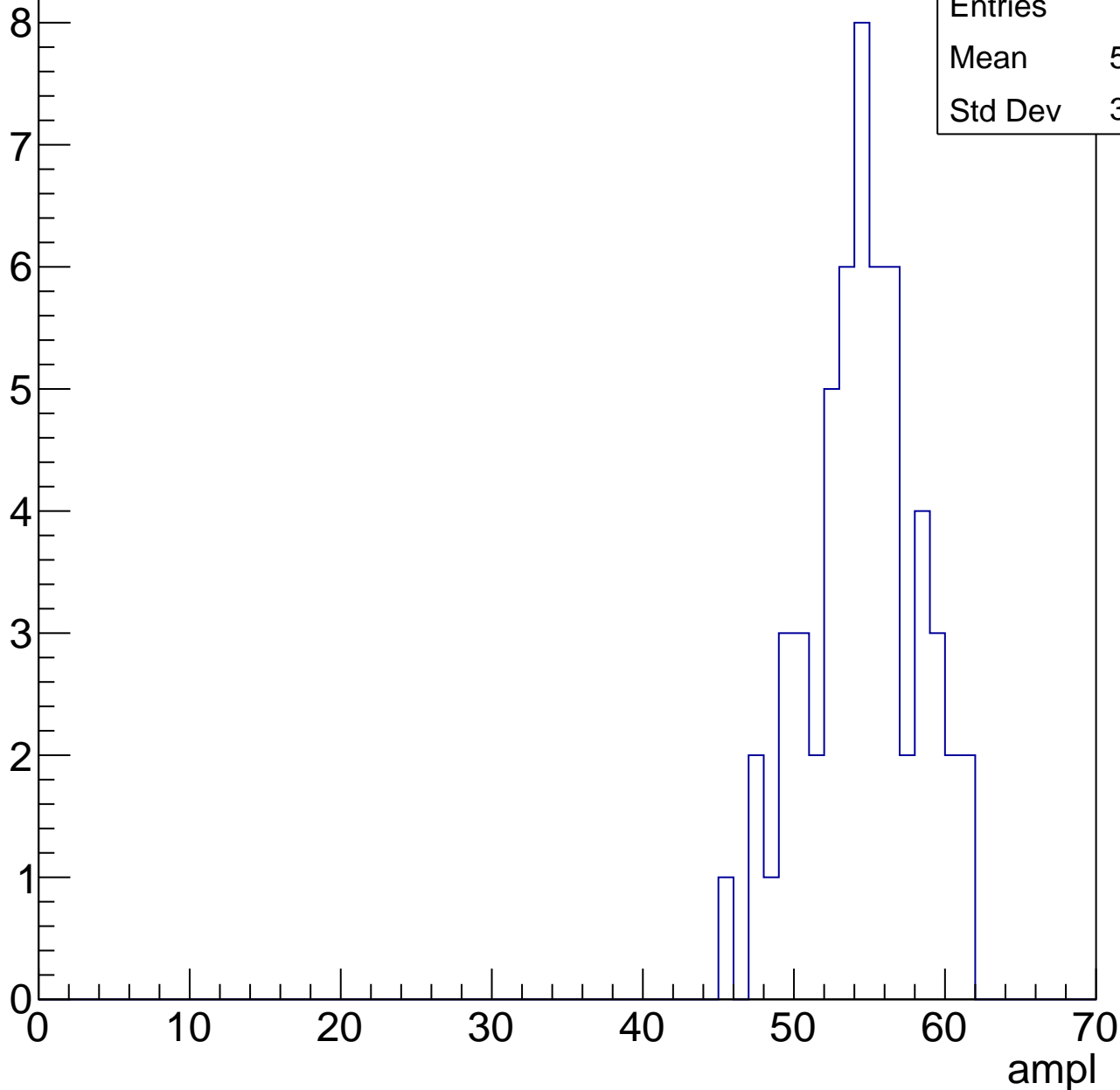


# B0L002S, U8-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	54.05
Std Dev	3.632

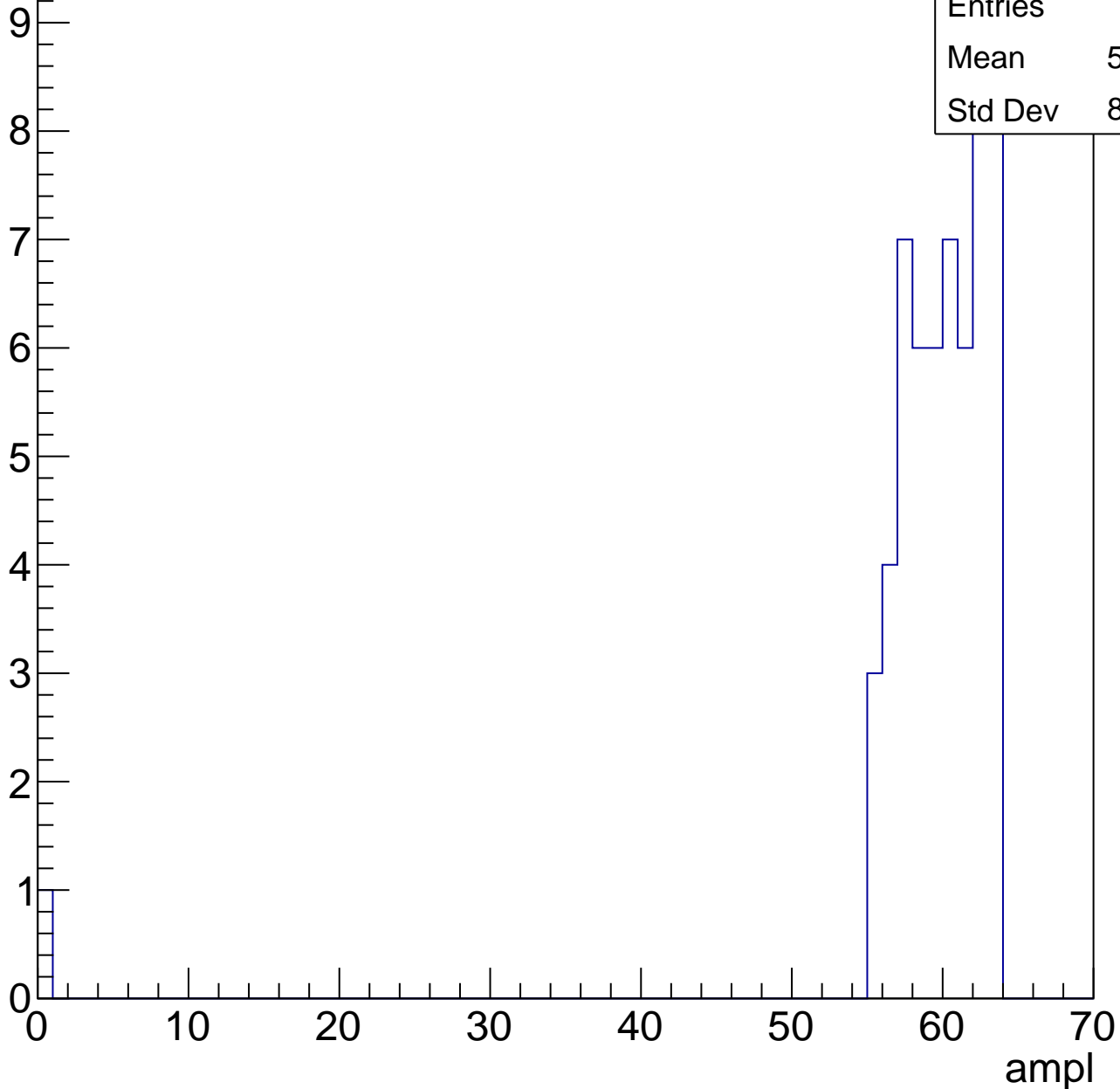


# B0L002S, U8-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.56
Std Dev	8.195



# B0L002S, U8-ch122, adc6

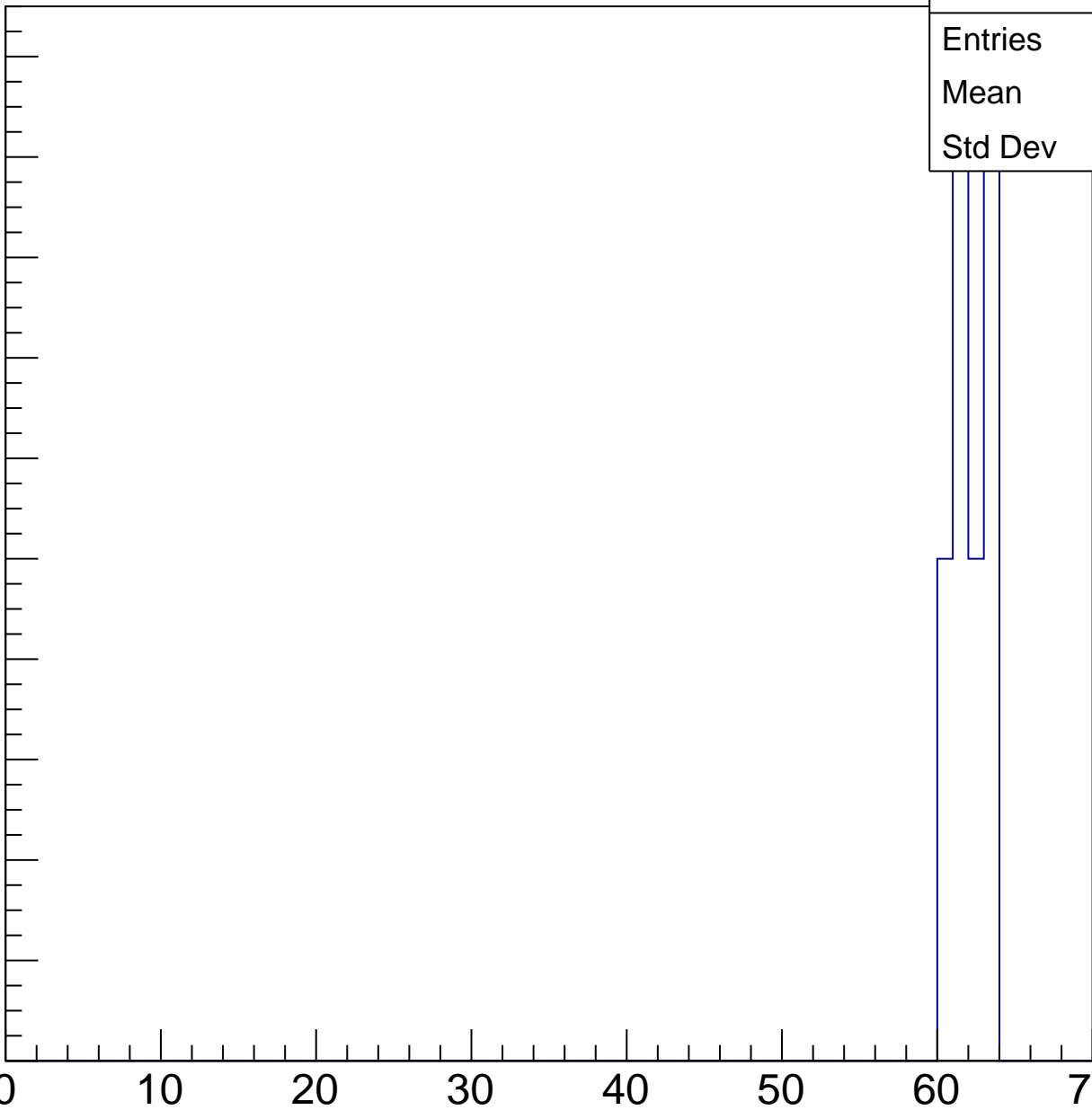
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl





# B0L002S, U8-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch123, adc0

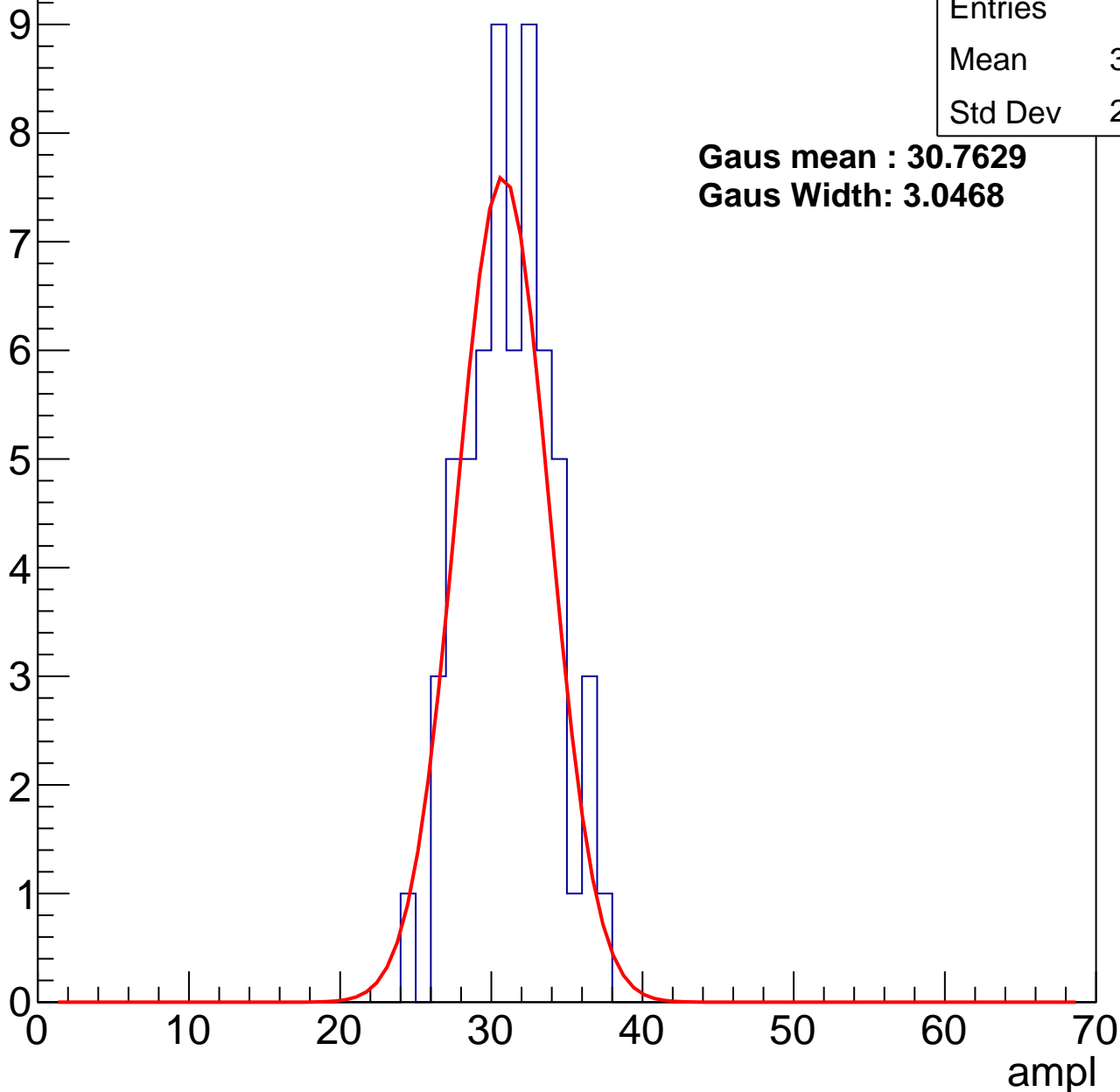
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	30.72
Std Dev	2.835

**Gaus mean : 30.7629**

**Gaus Width: 3.0468**



# B0L002S, U8-ch123, adc1

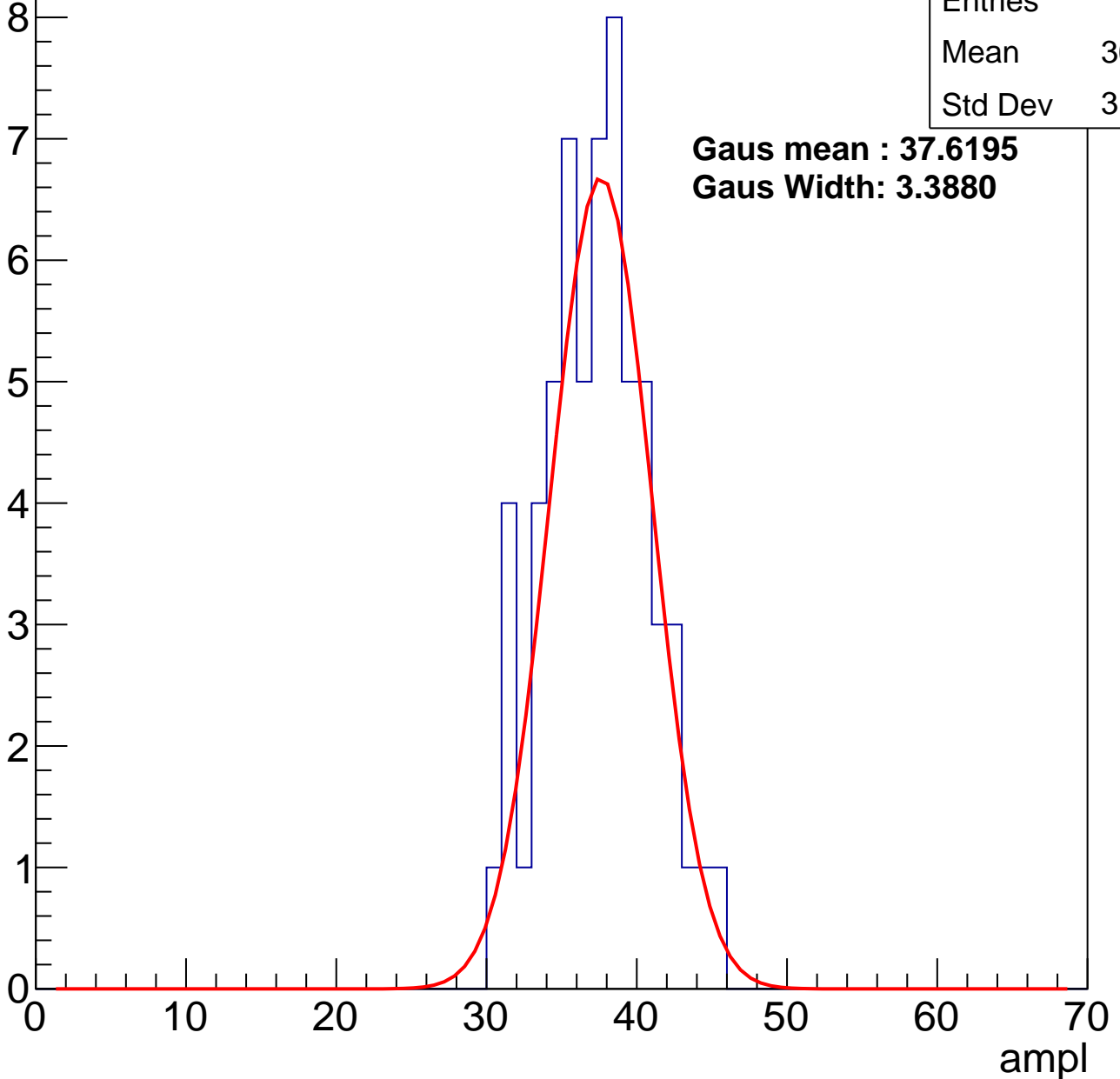
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.92
Std Dev	3.399

**Gaus mean : 37.6195**

**Gaus Width: 3.3880**



# B0L002S, U8-ch123, adc2

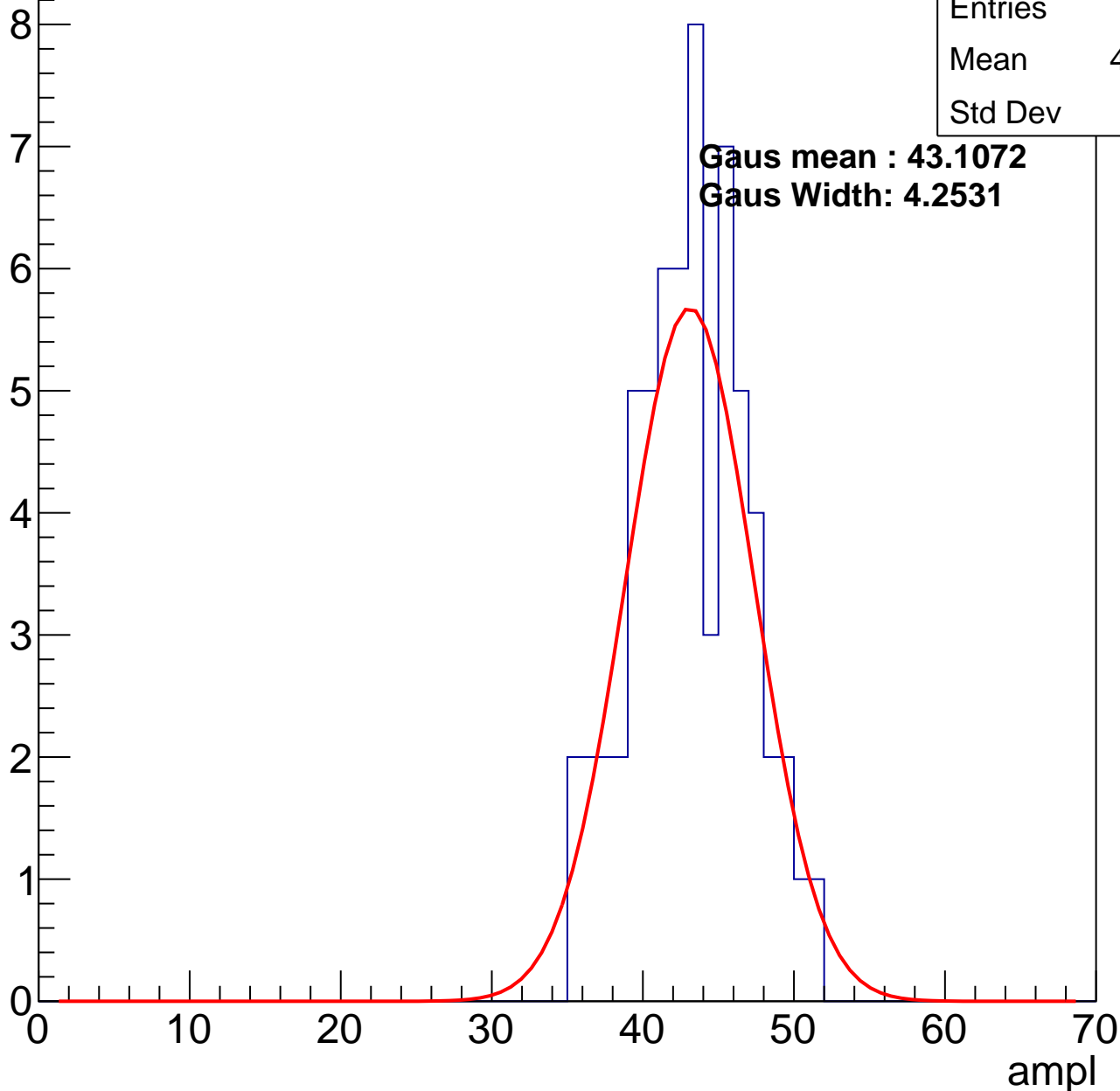
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.68
Std Dev	3.72

**Gaus mean : 43.1072**

**Gaus Width: 4.2531**

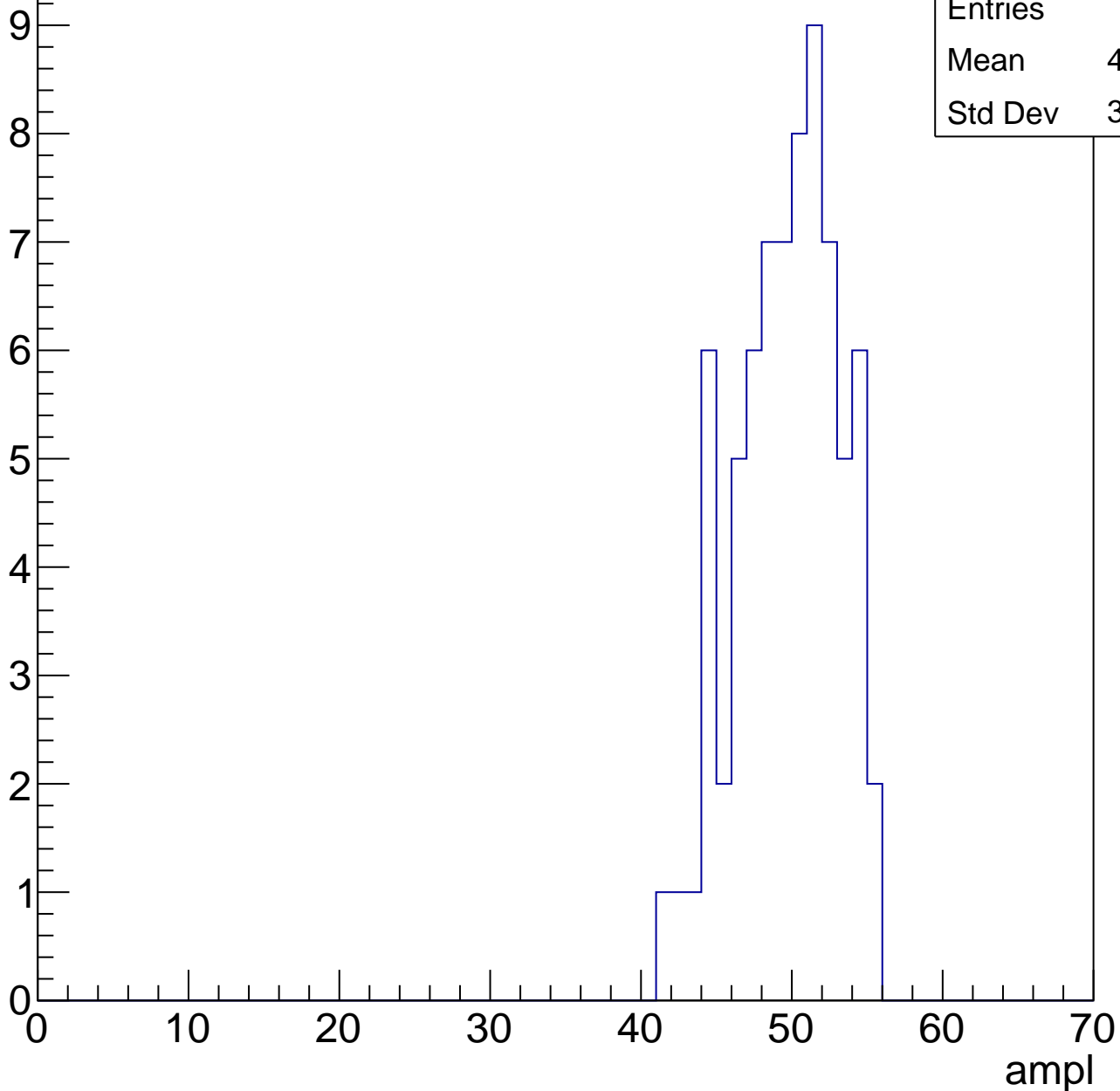


# B0L002S, U8-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.22
Std Dev	3.336

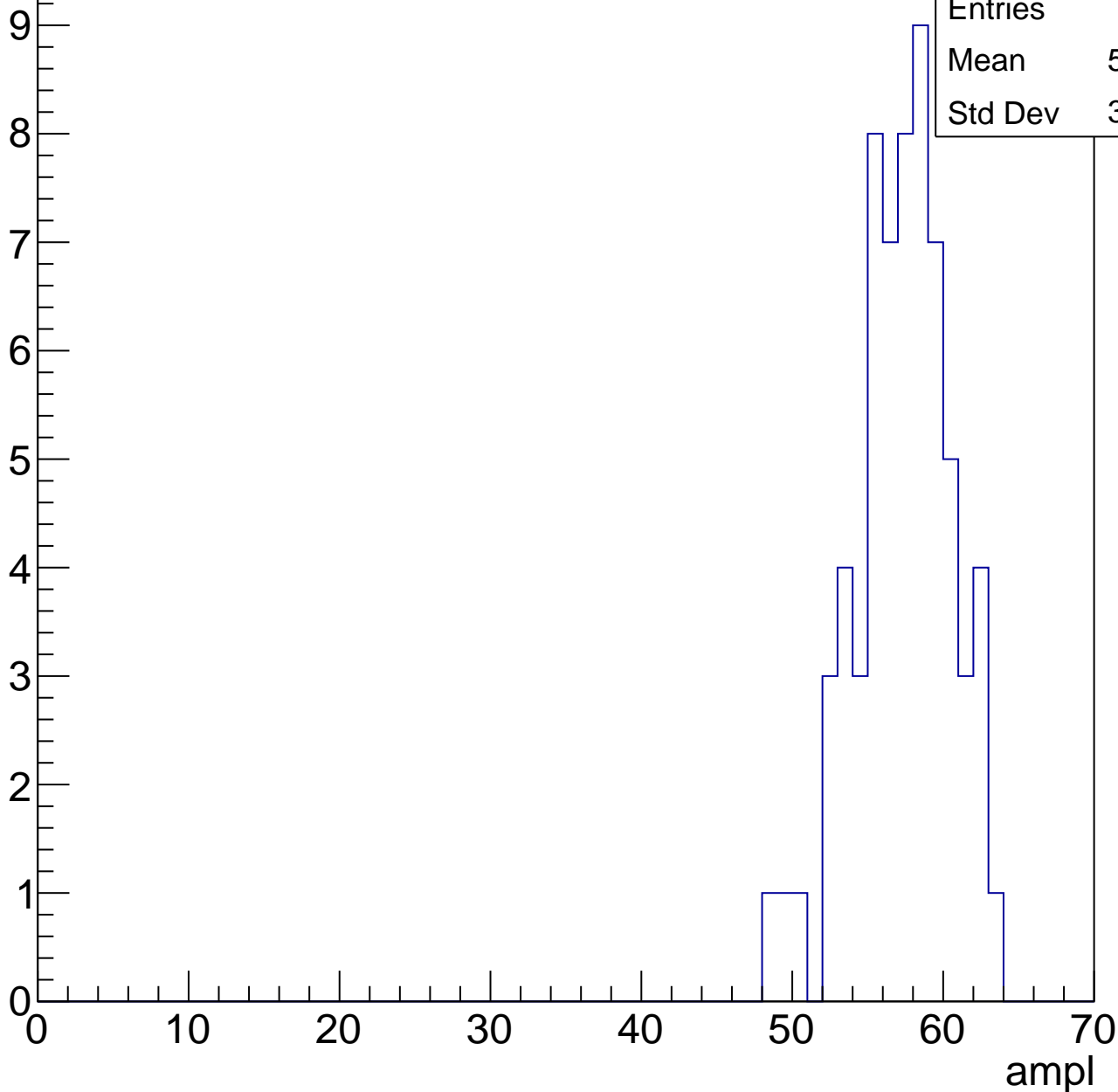


# B0L002S, U8-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

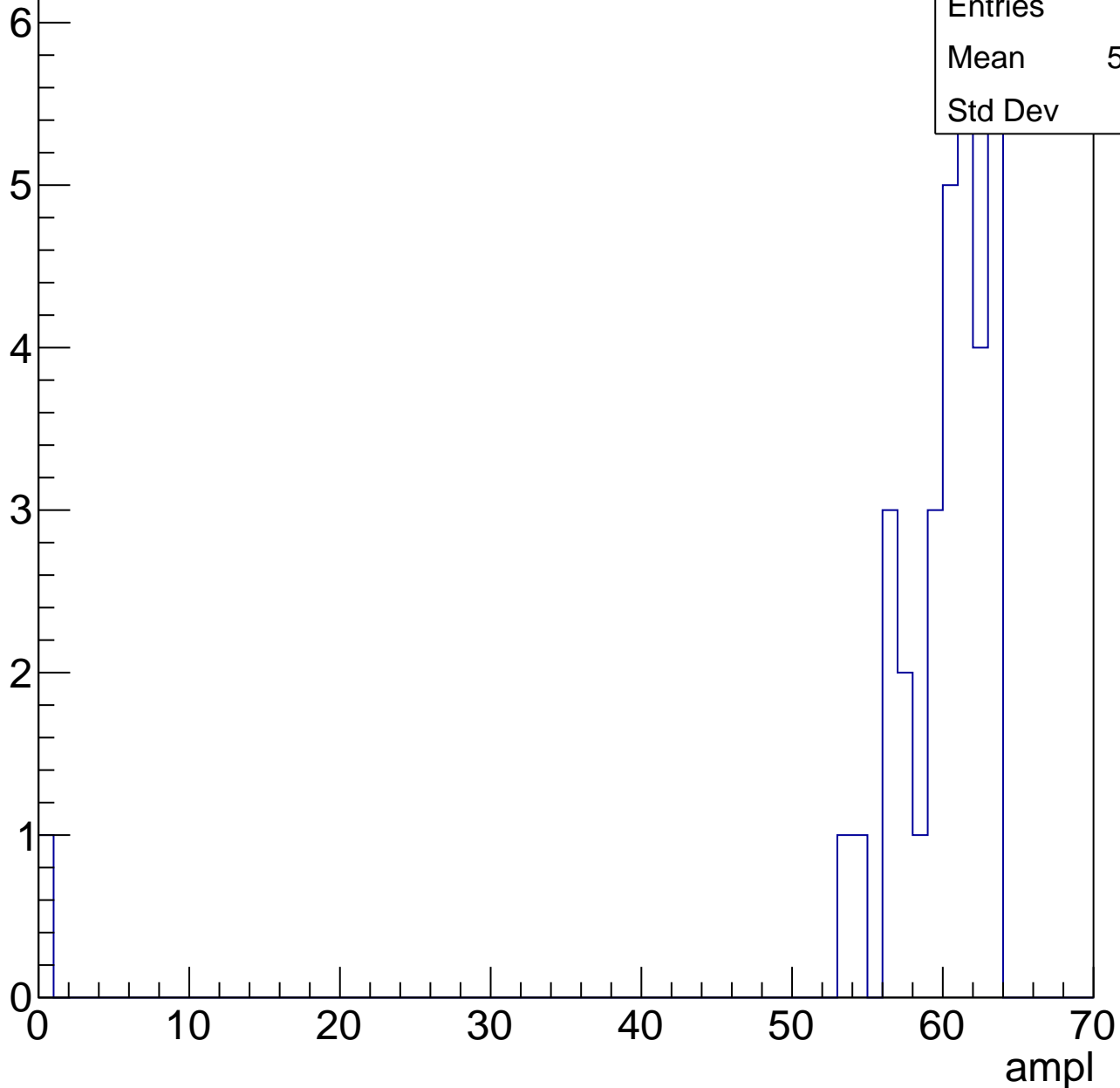
Entries	65
Mean	56.83
Std Dev	3.194



# B0L002S, U8-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

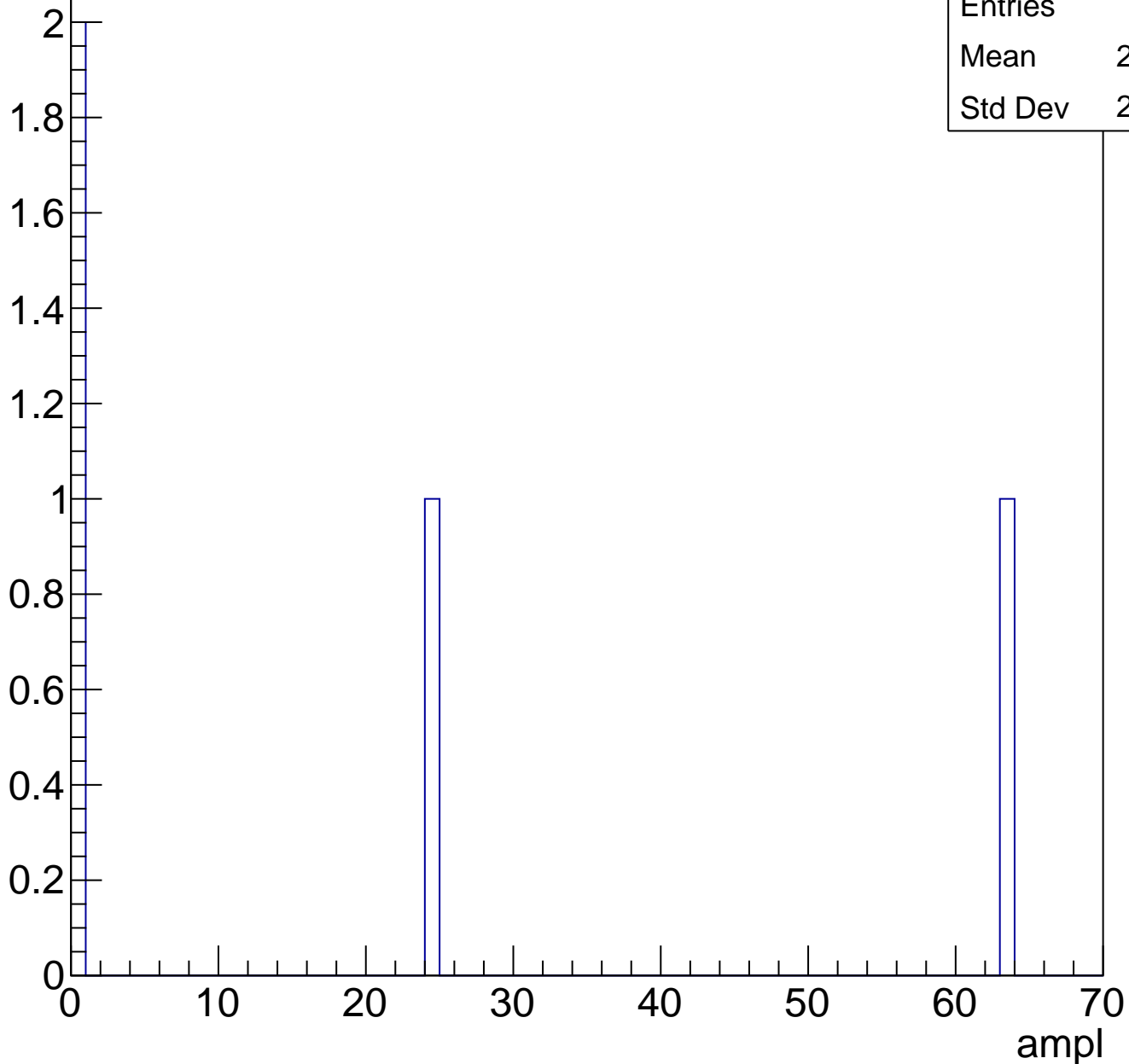
0 10 20 30 40 50 60 70



# B0L002S, U8-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	21.75
Std Dev	25.75

# B0L002S, U8-ch124, adc0

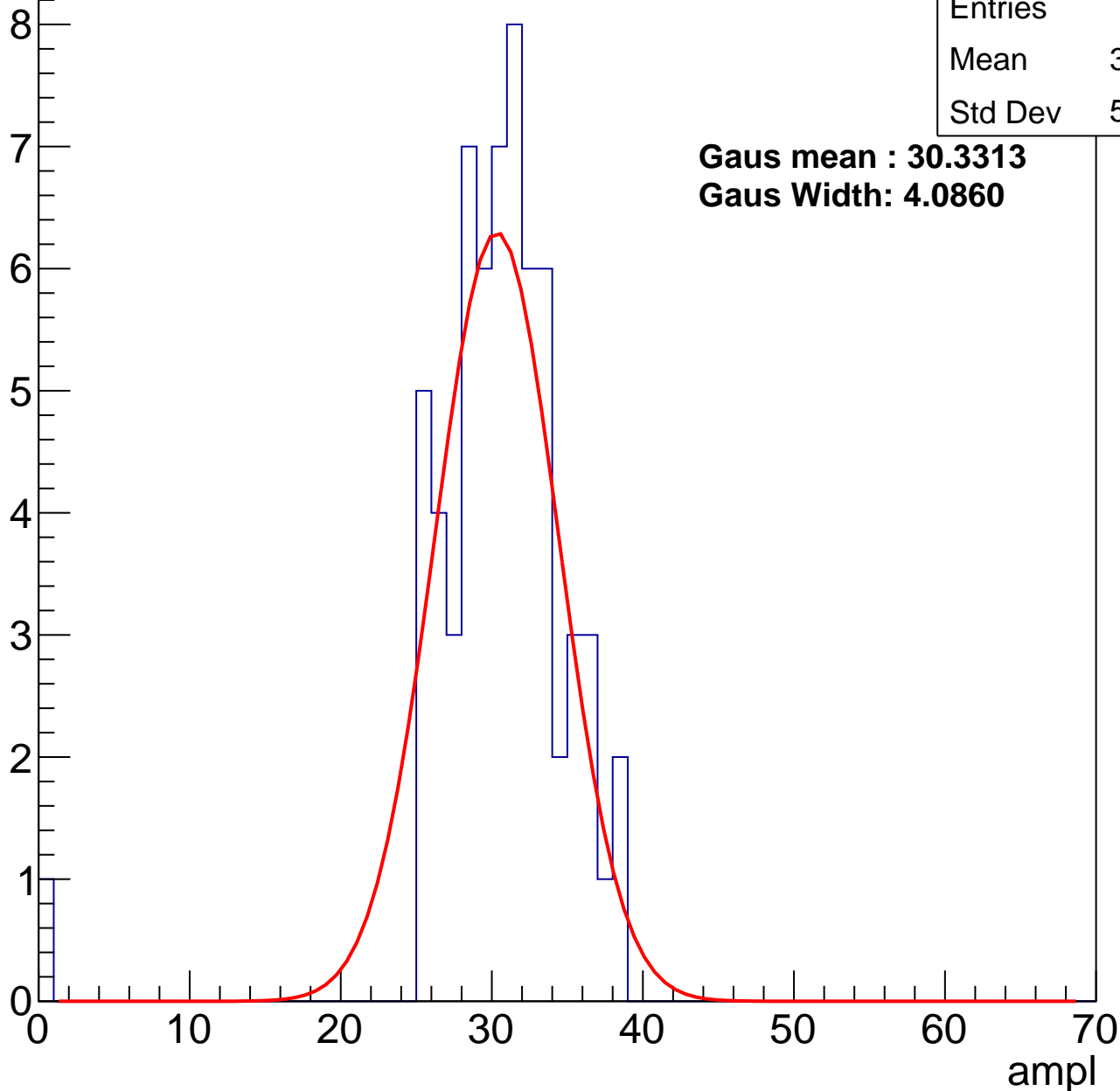
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.03
Std Dev	5.043

**Gaus mean : 30.3313**

**Gaus Width: 4.0860**



# B0L002S, U8-ch124, adc1

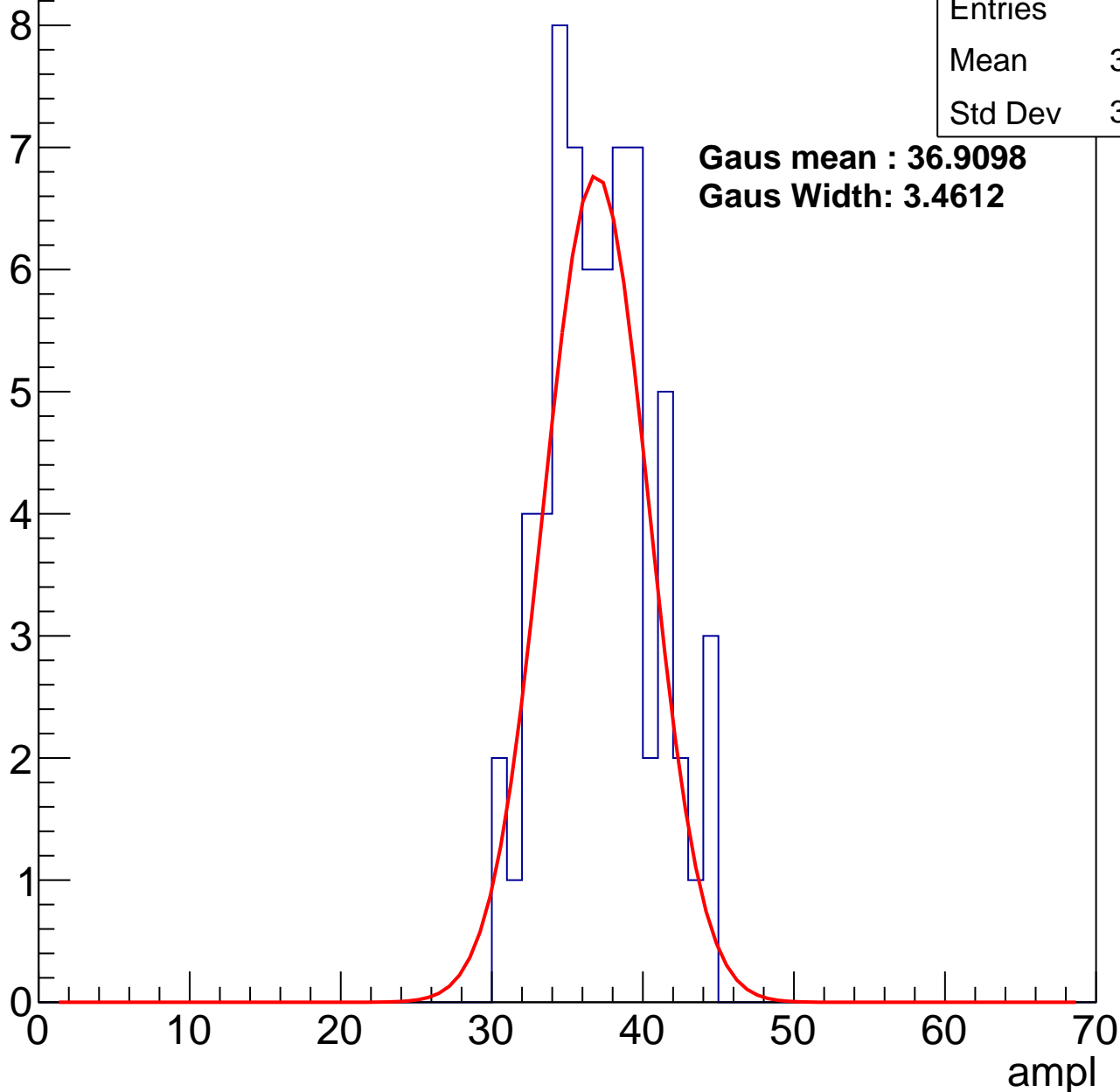
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.75
Std Dev	3.433

**Gaus mean : 36.9098**

**Gaus Width: 3.4612**



# B0L002S, U8-ch124, adc2

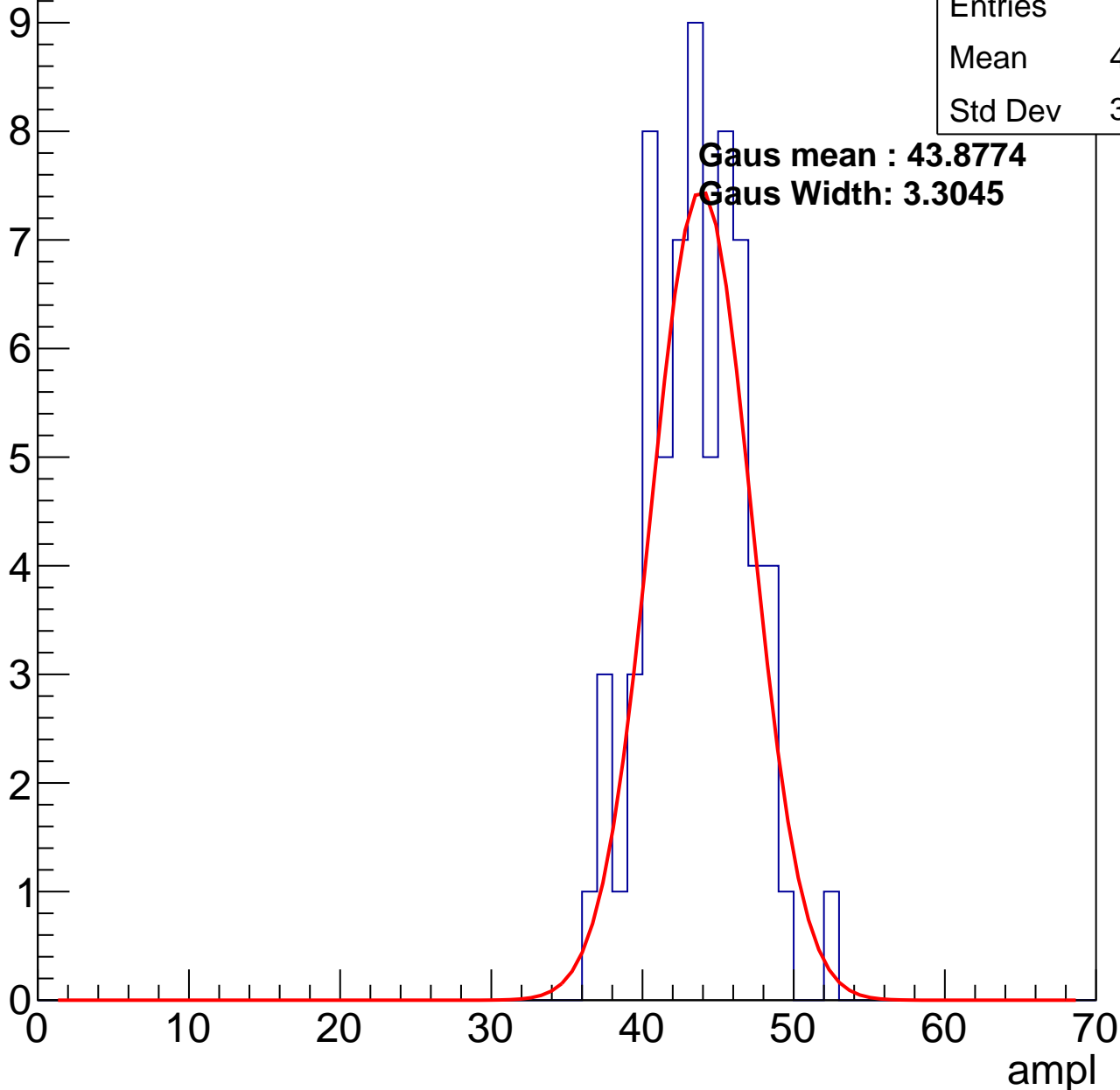
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.15
Std Dev	3.252

**Gaus mean : 43.8774**

**Gaus Width: 3.3045**

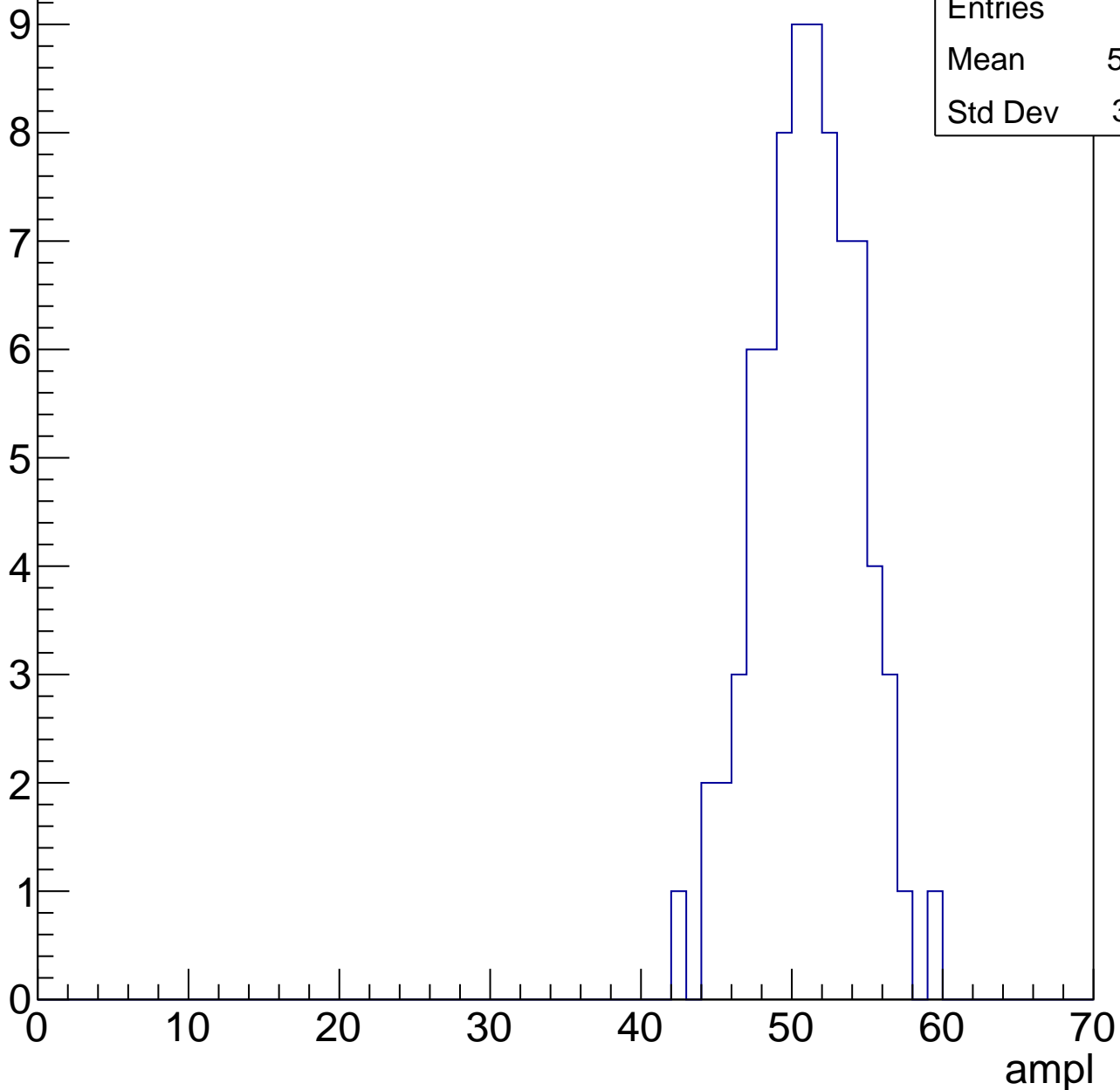


# B0L002S, U8-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

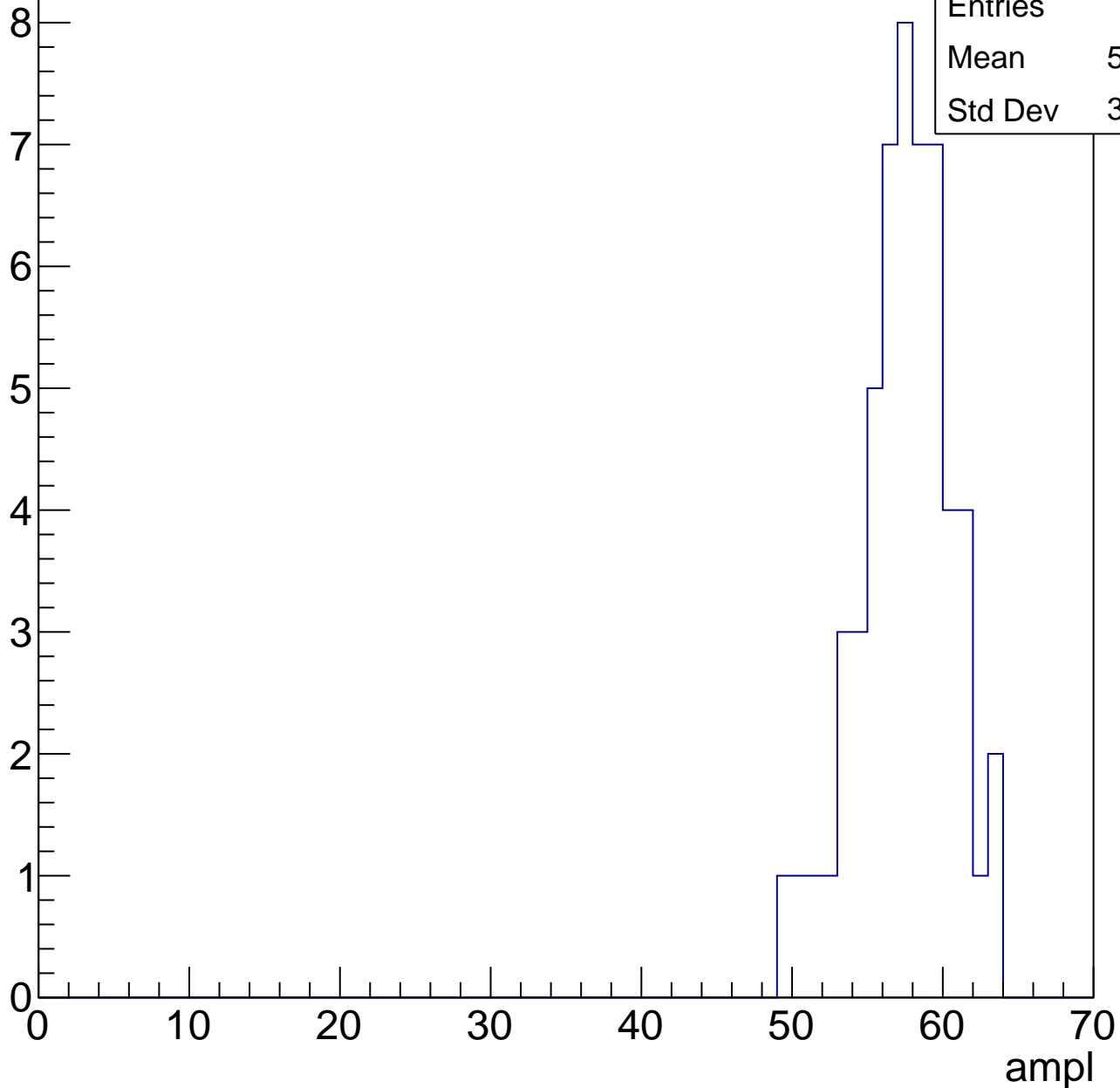
Entries	77
Mean	50.62
Std Dev	3.311



# B0L002S, U8-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



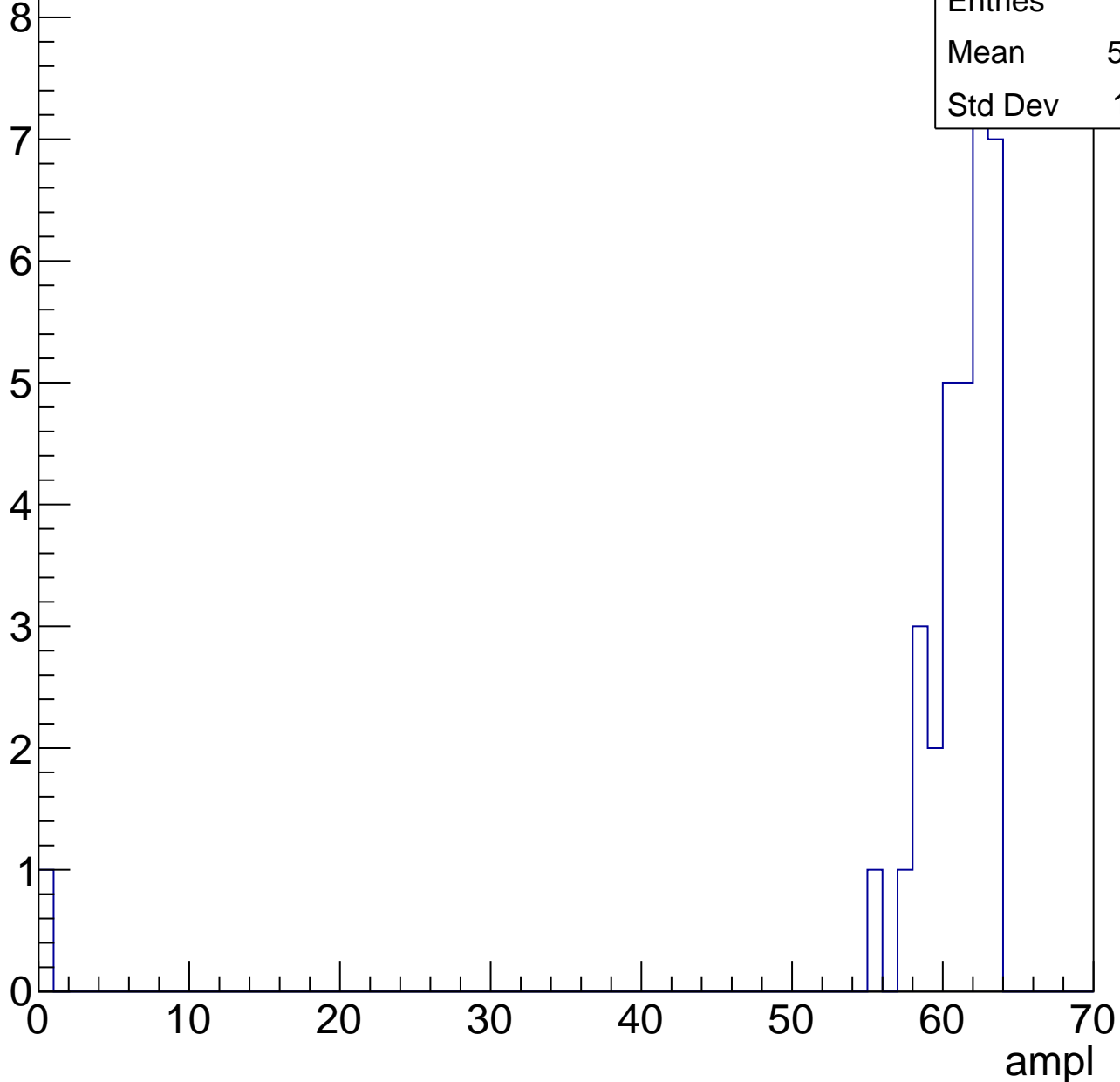
Entries	55
Mean	57.04
Std Dev	3.039

# B0L002S, U8-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.97
Std Dev	10.61



# B0L002S, U8-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U8-ch125, adc0

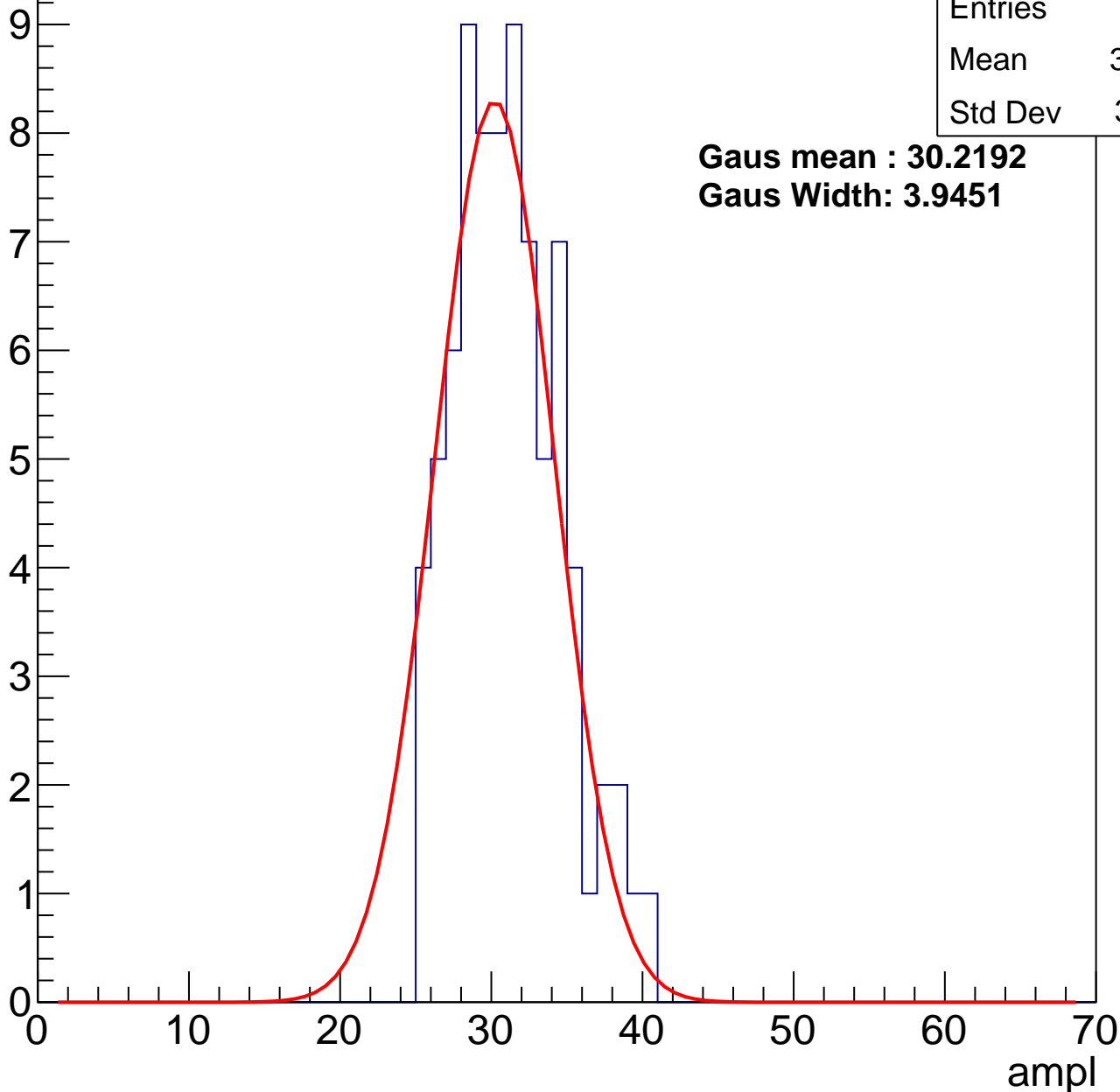
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.72
Std Dev	3.511

**Gaus mean : 30.2192**

**Gaus Width: 3.9451**



# B0L002S, U8-ch125, adc1

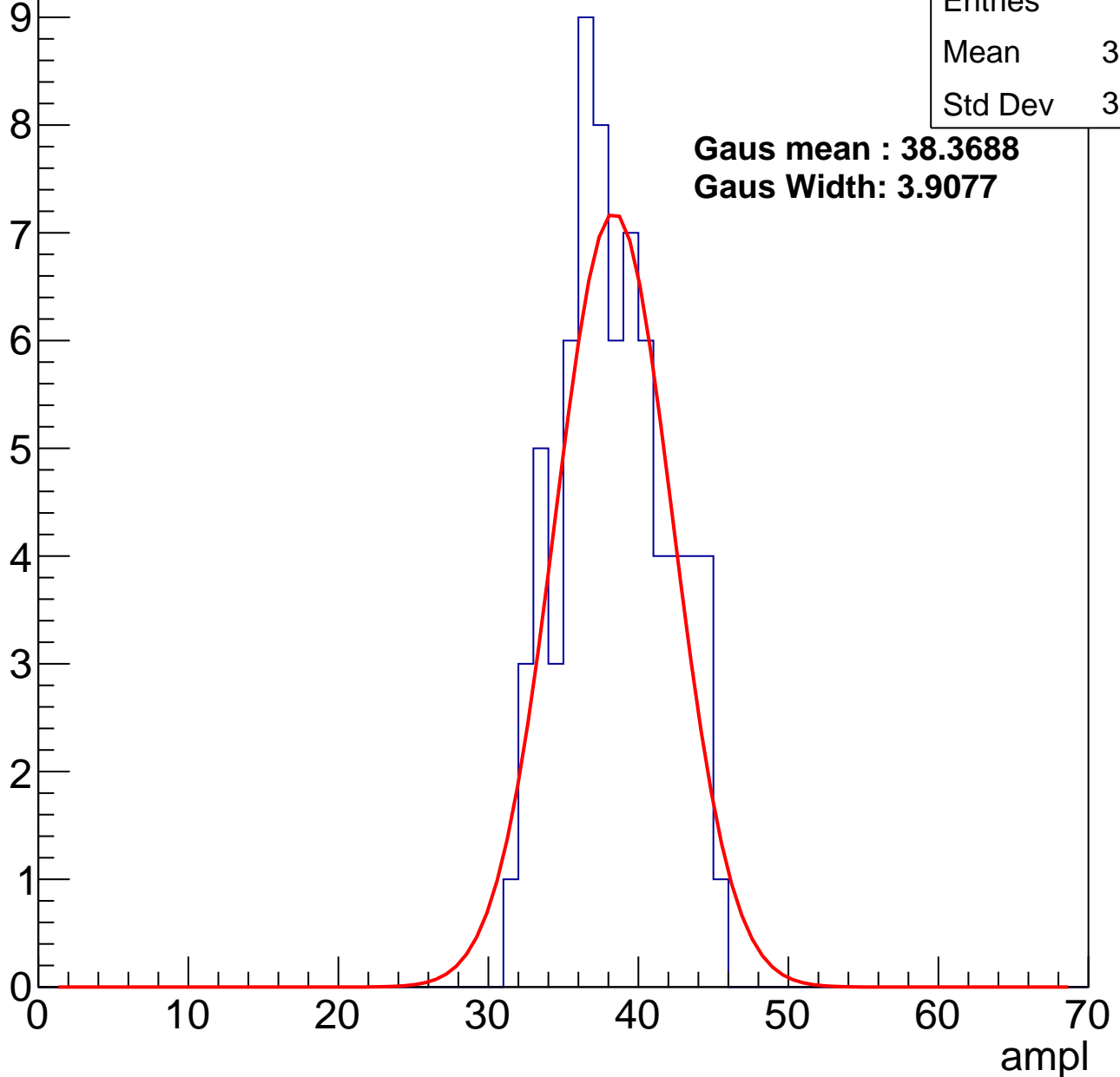
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.89
Std Dev	3.462

**Gaus mean : 38.3688**

**Gaus Width: 3.9077**



# B0L002S, U8-ch125, adc2

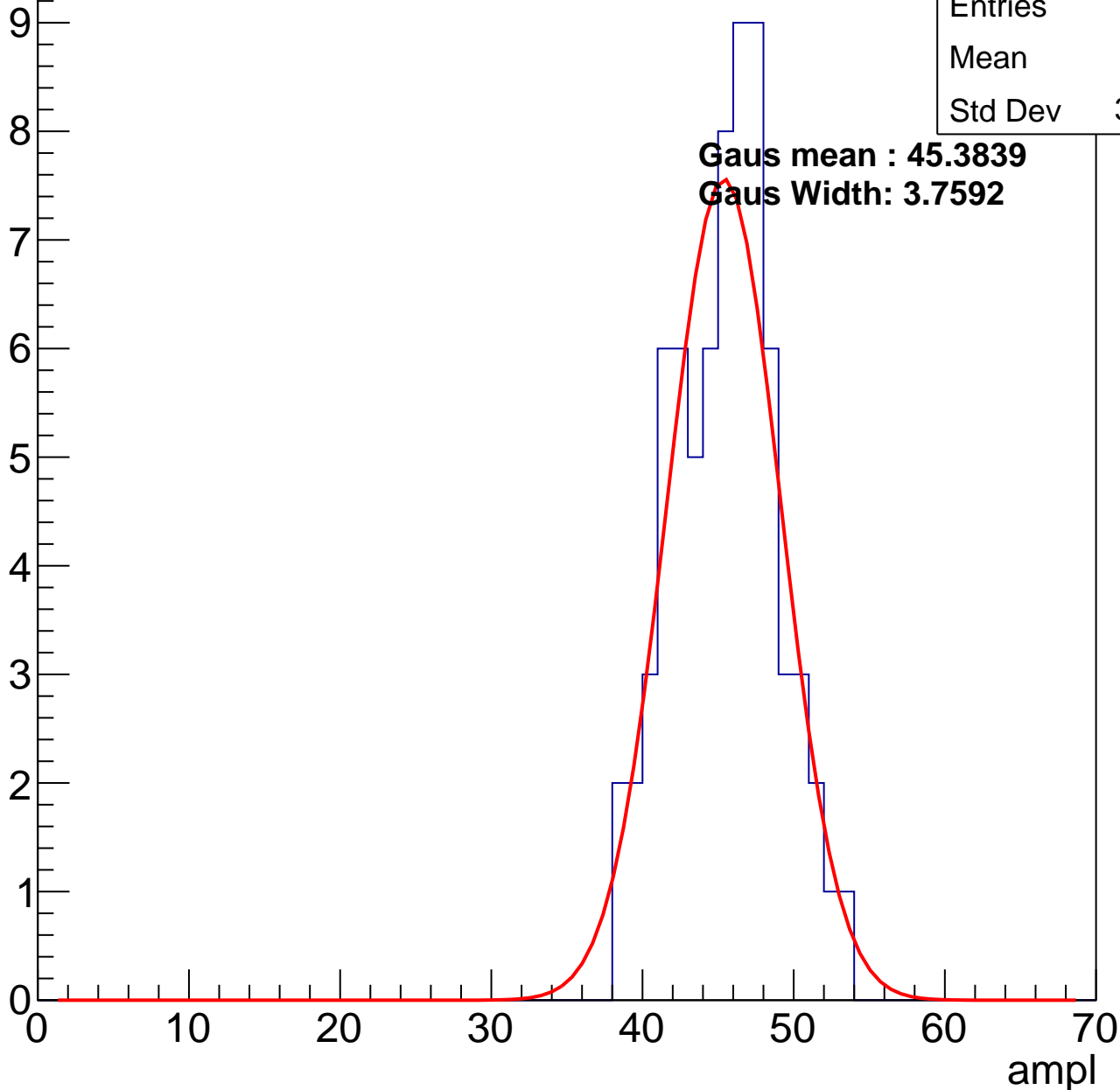
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45
Std Dev	3.391

**Gaus mean : 45.3839**

**Gaus Width: 3.7592**

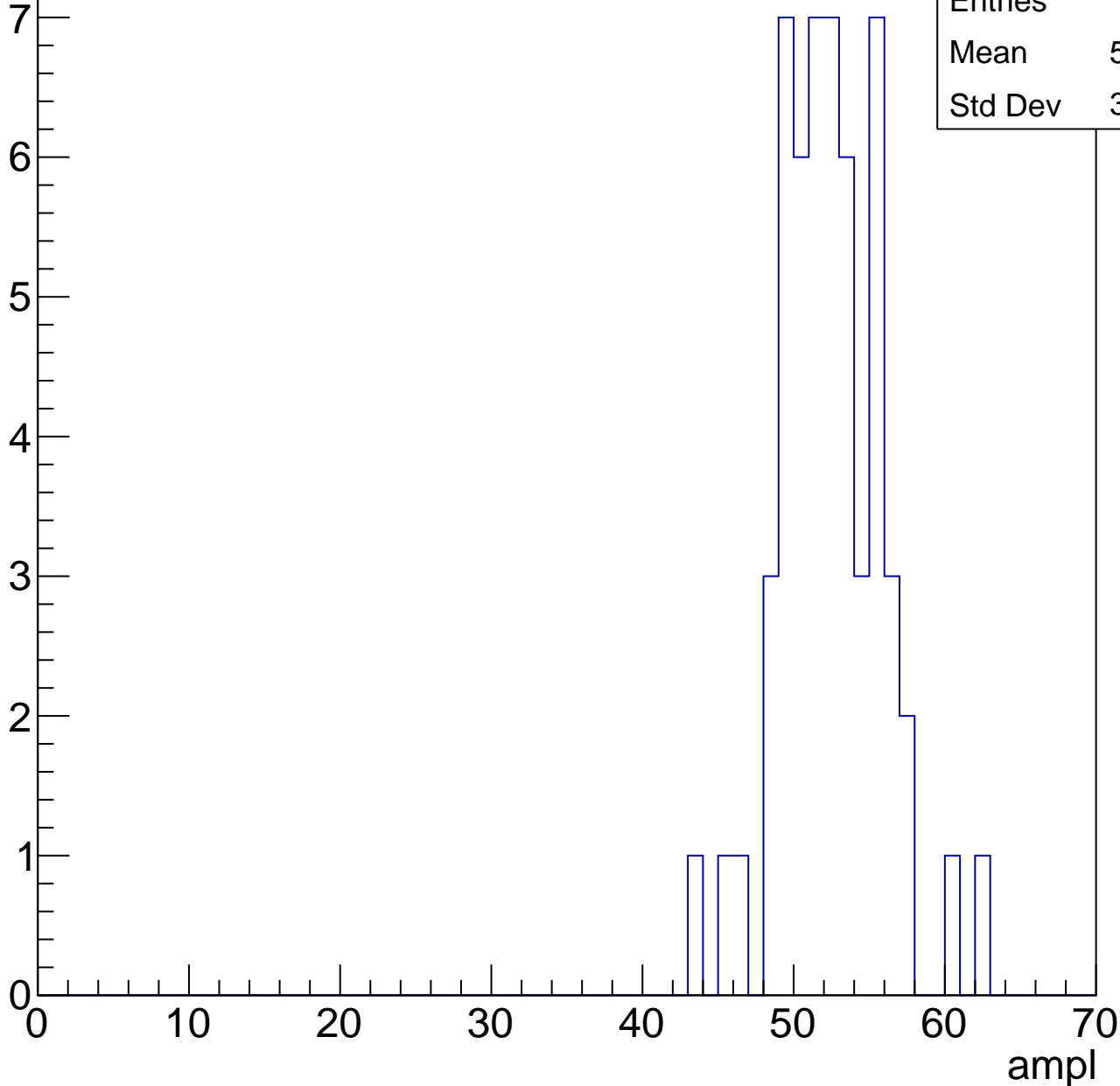


# B0L002S, U8-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	51.98
Std Dev	3.409

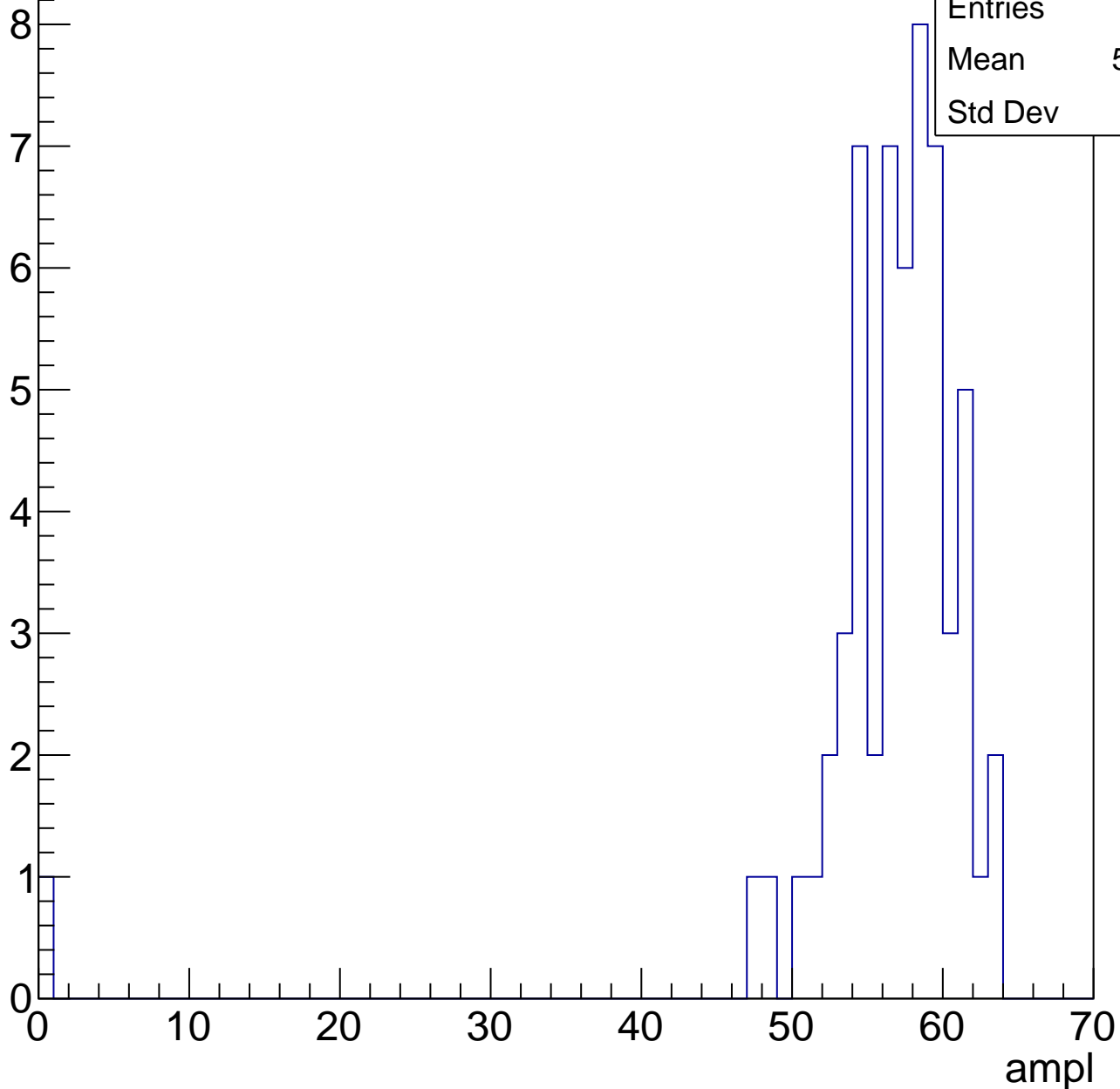


# B0L002S, U8-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.71
Std Dev	8.13

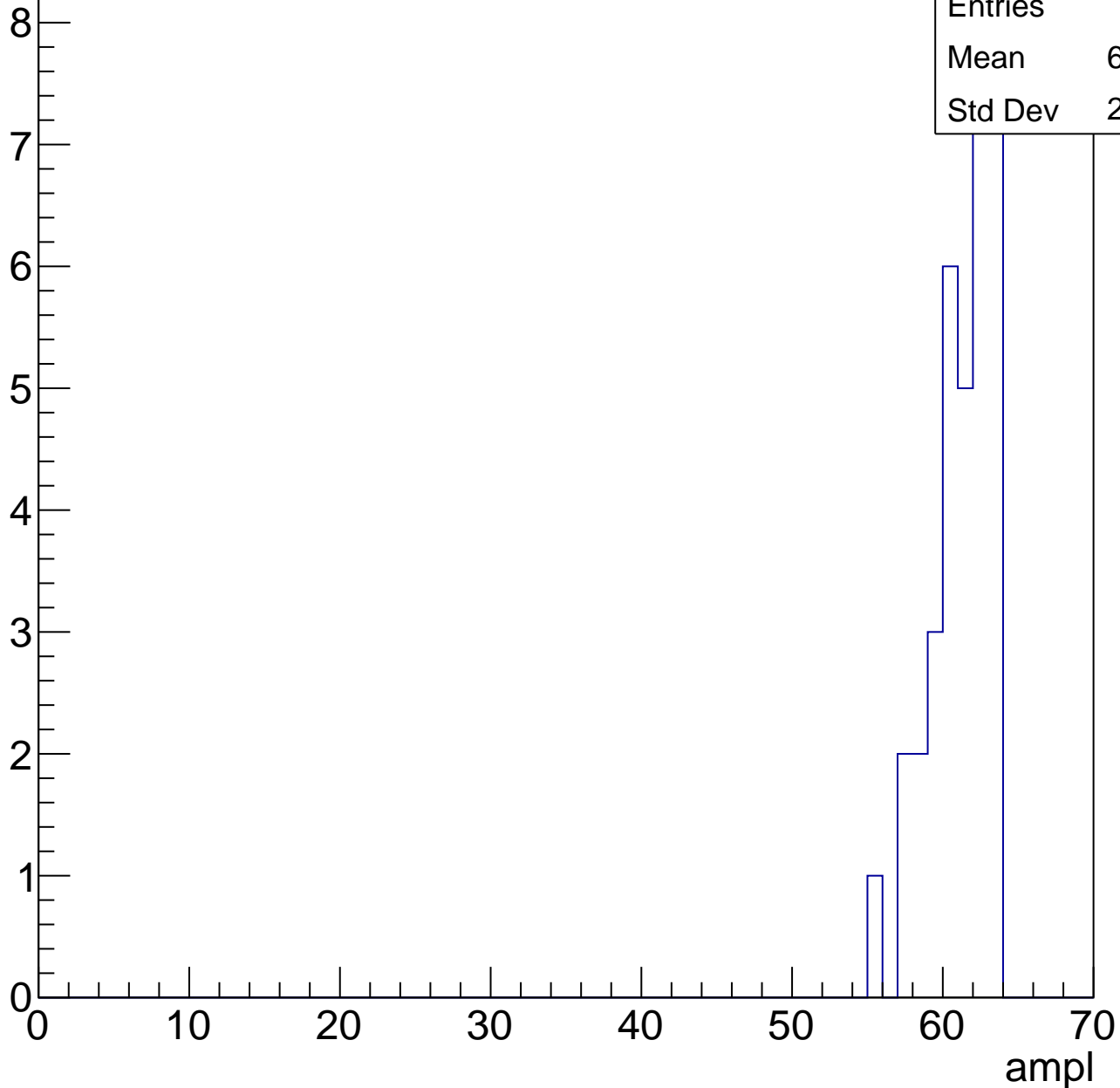


# B0L002S, U8-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.77
Std Dev	2.015



# B0L002S, U8-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U8-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L002S, U8-ch126, adc0

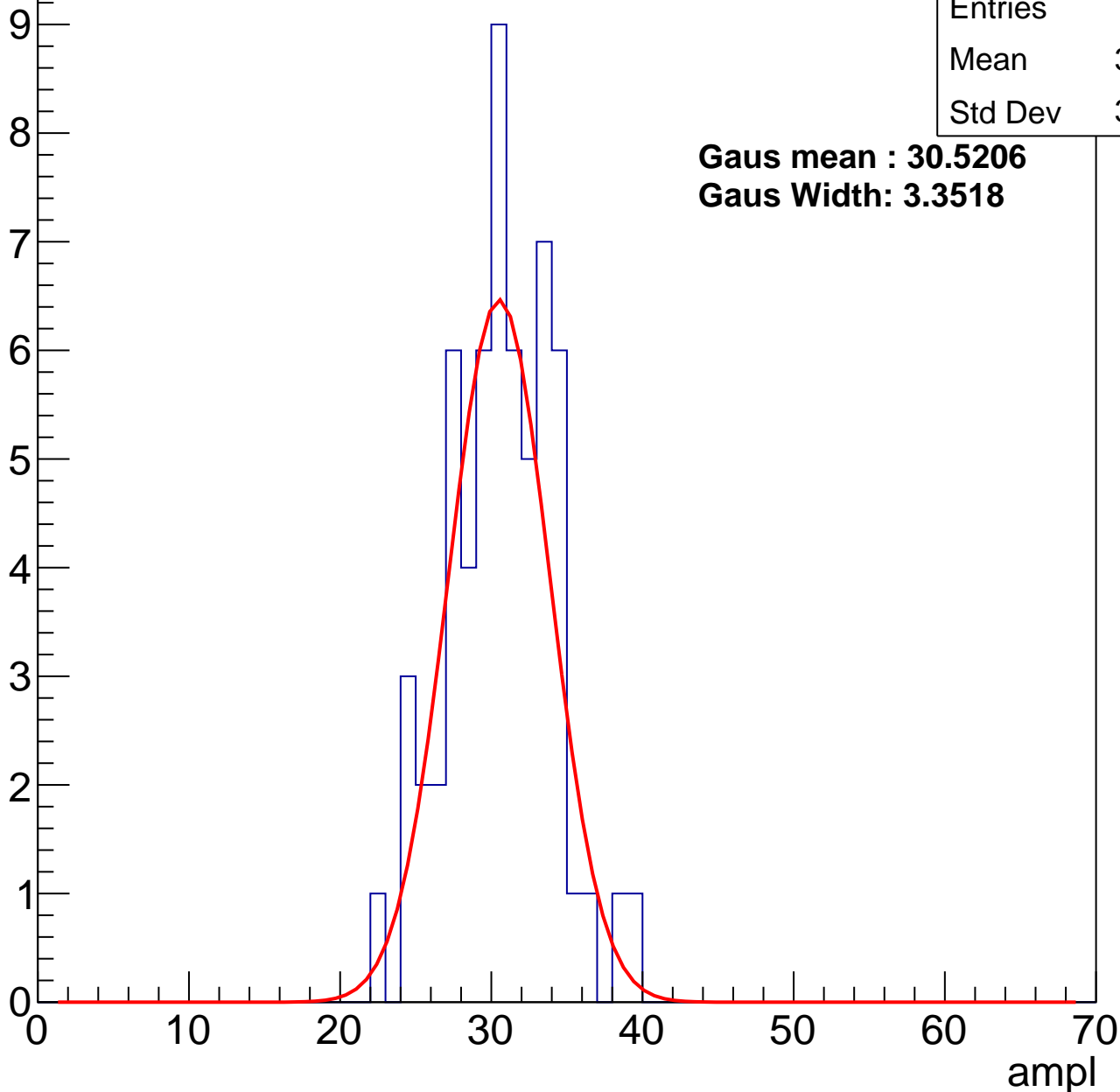
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.21
Std Dev	3.431

**Gaus mean : 30.5206**

**Gaus Width: 3.3518**



# B0L002S, U8-ch126, adc1

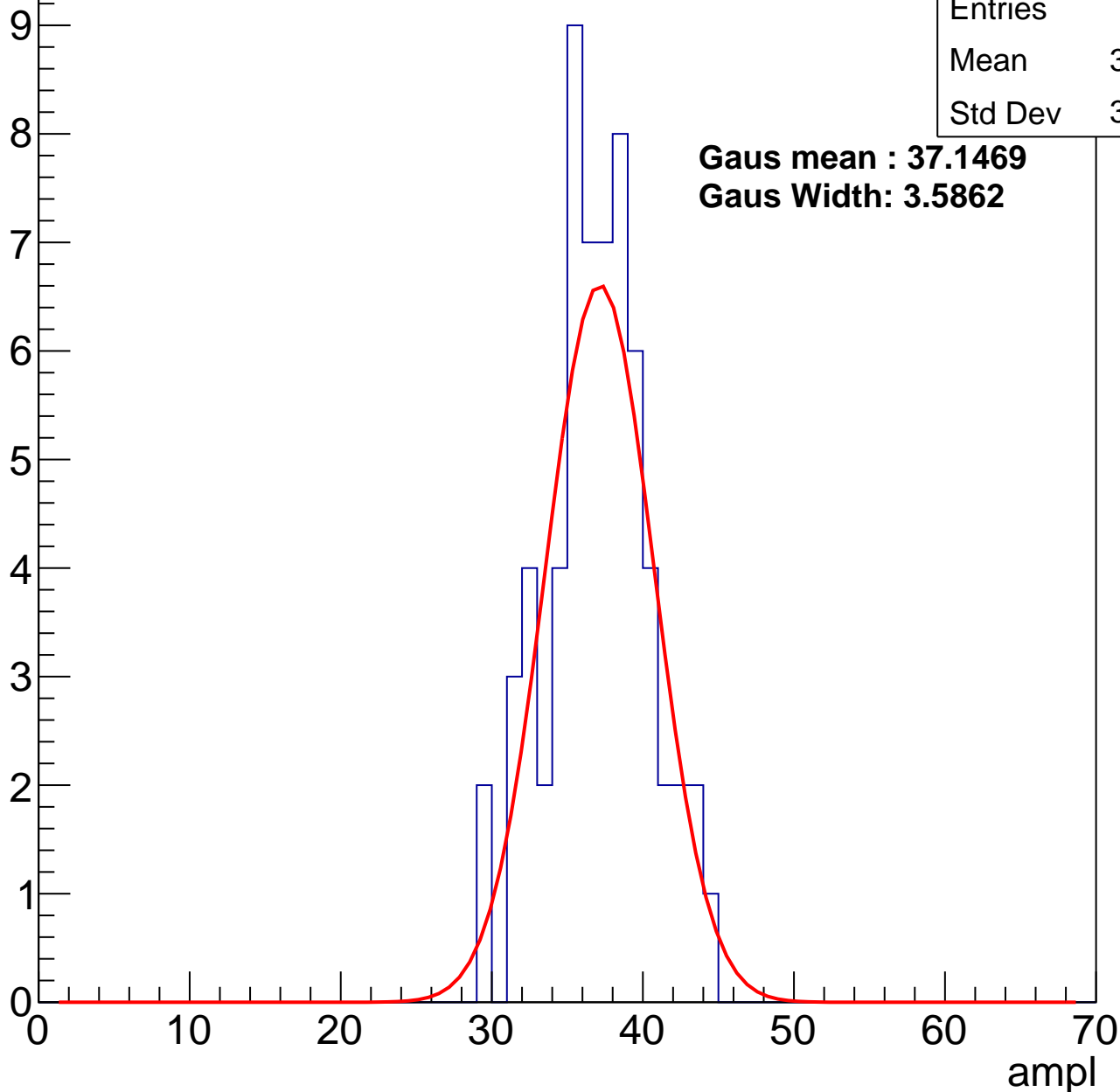
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.52
Std Dev	3.342

**Gaus mean : 37.1469**

**Gaus Width: 3.5862**



# B0L002S, U8-ch126, adc2

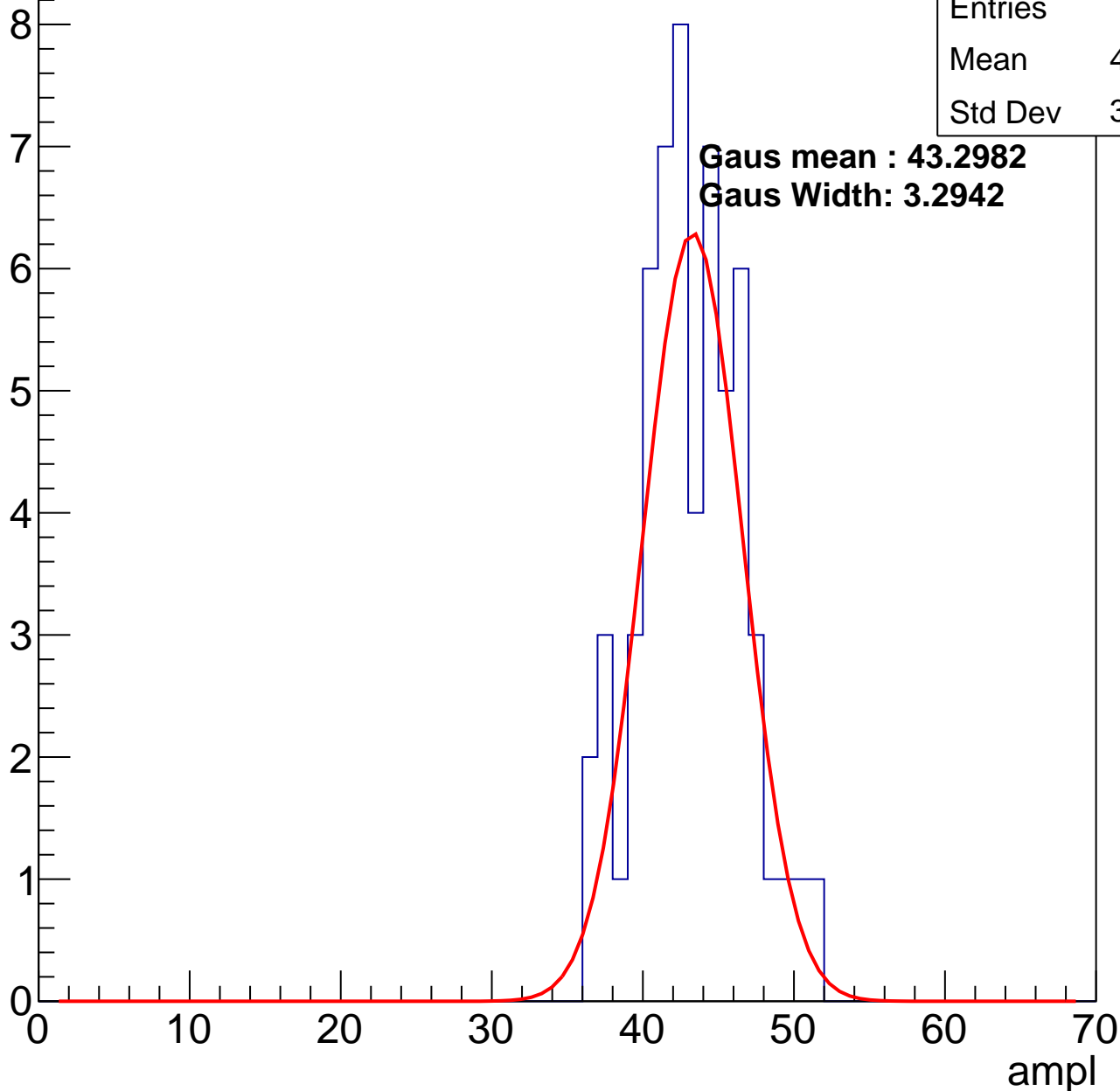
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	42.73
Std Dev	3.364

**Gaus mean : 43.2982**

**Gaus Width: 3.2942**

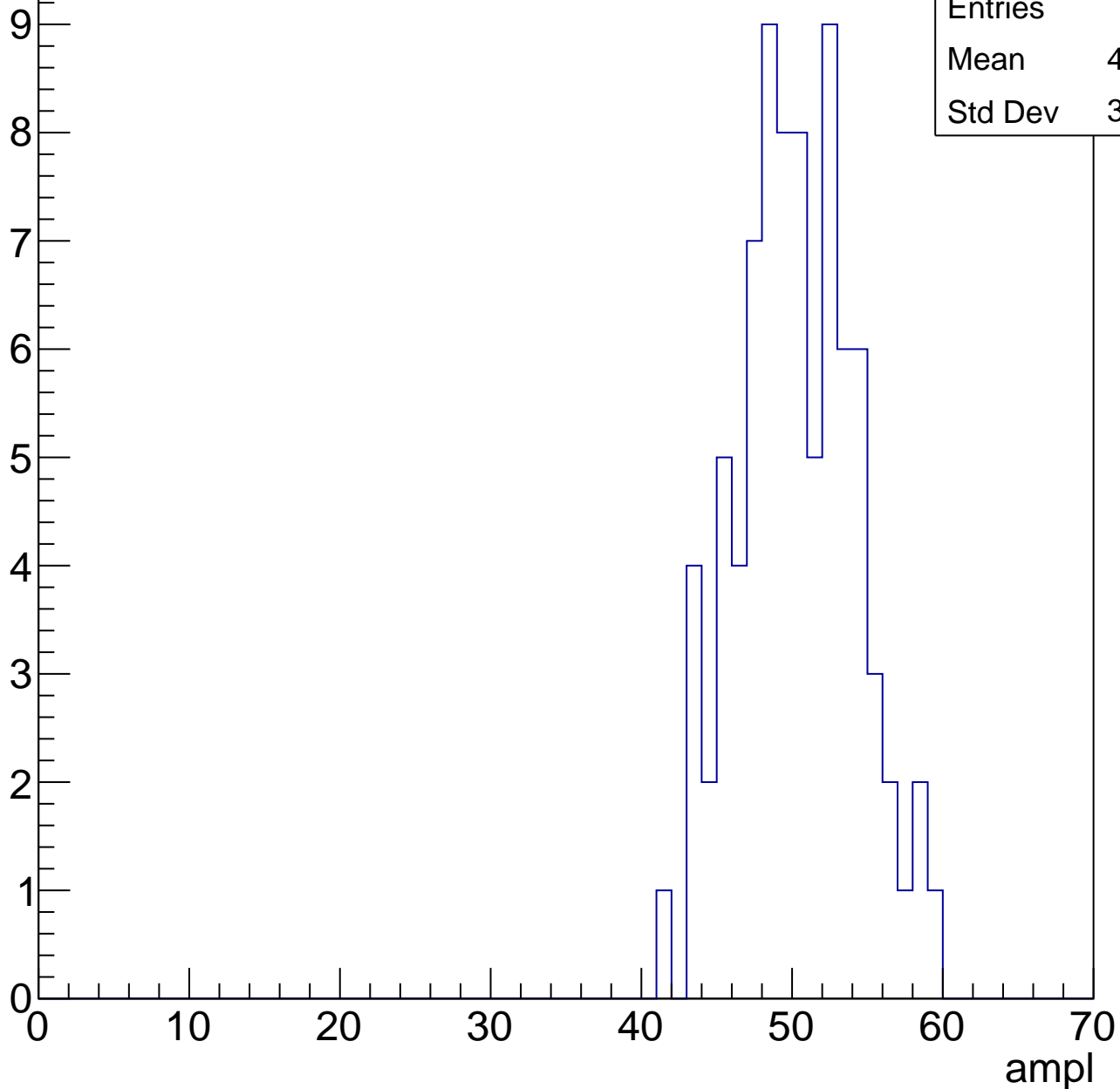


# B0L002S, U8-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	83
Mean	49.84
Std Dev	3.864

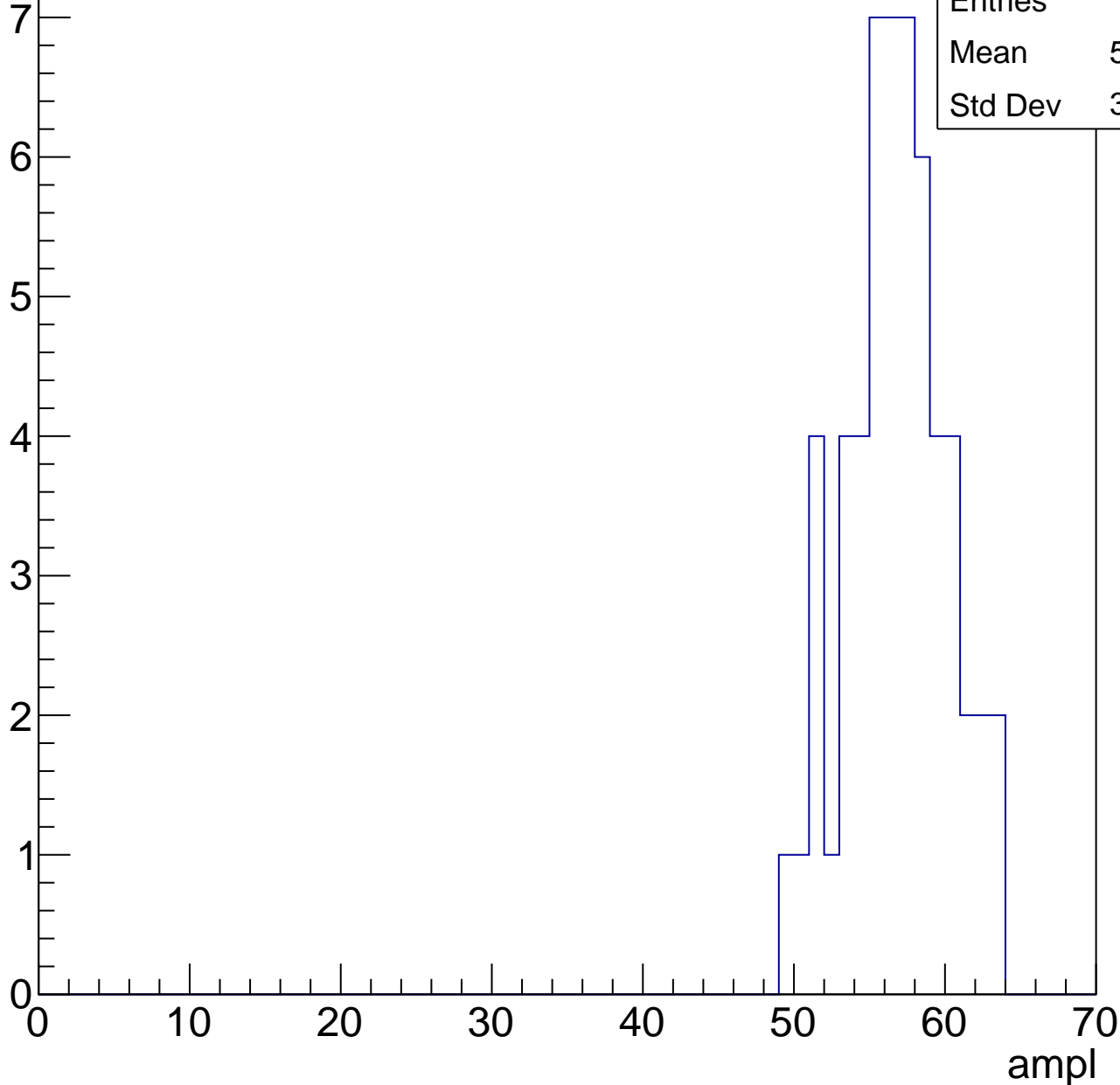


# B0L002S, U8-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.34
Std Dev	3.286

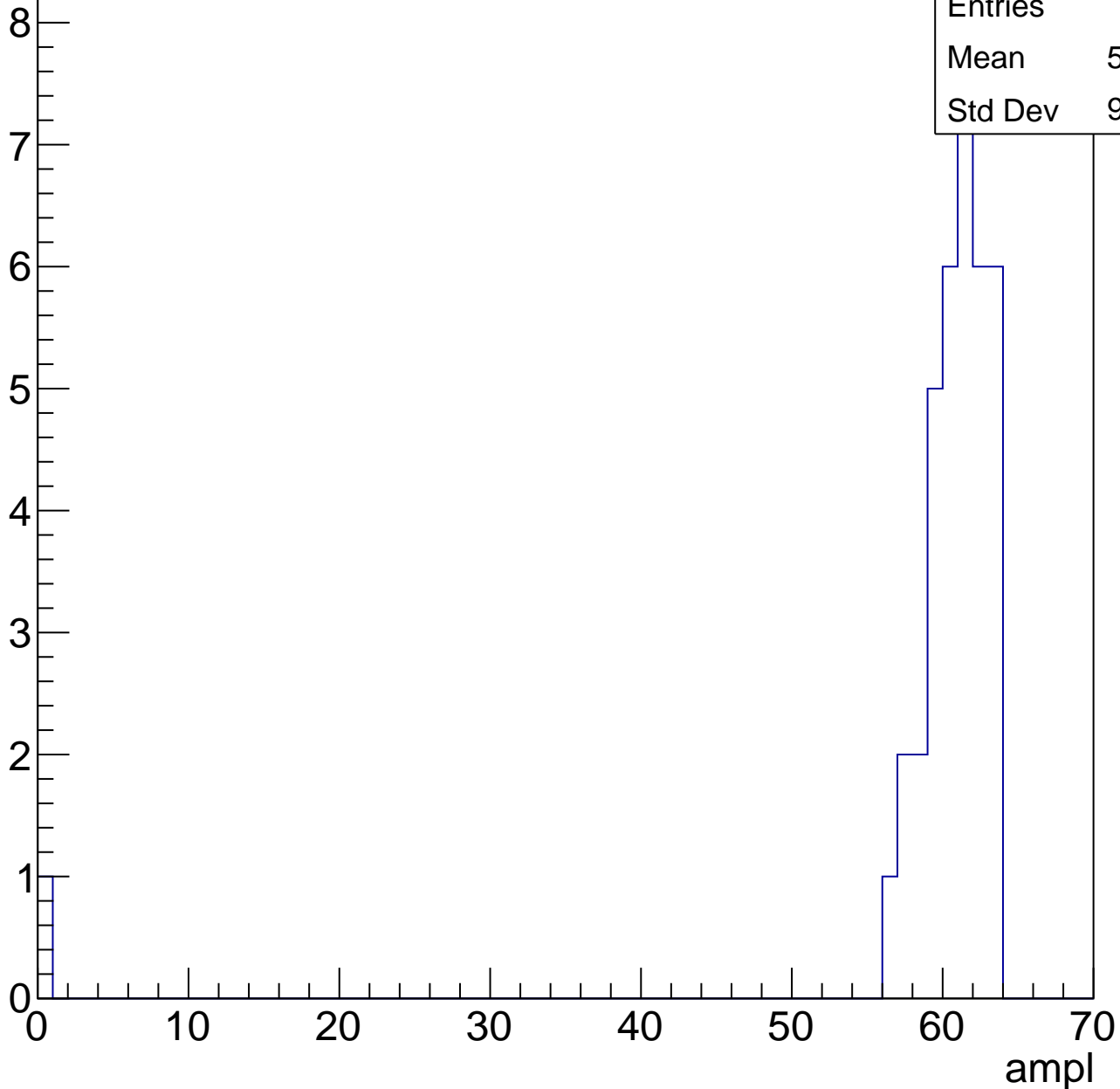


# B0L002S, U8-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.89
Std Dev	9.983



# B0L002S, U8-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U8-ch127, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	30.21
Std Dev	4.834

**Gaus mean : 30.7777**

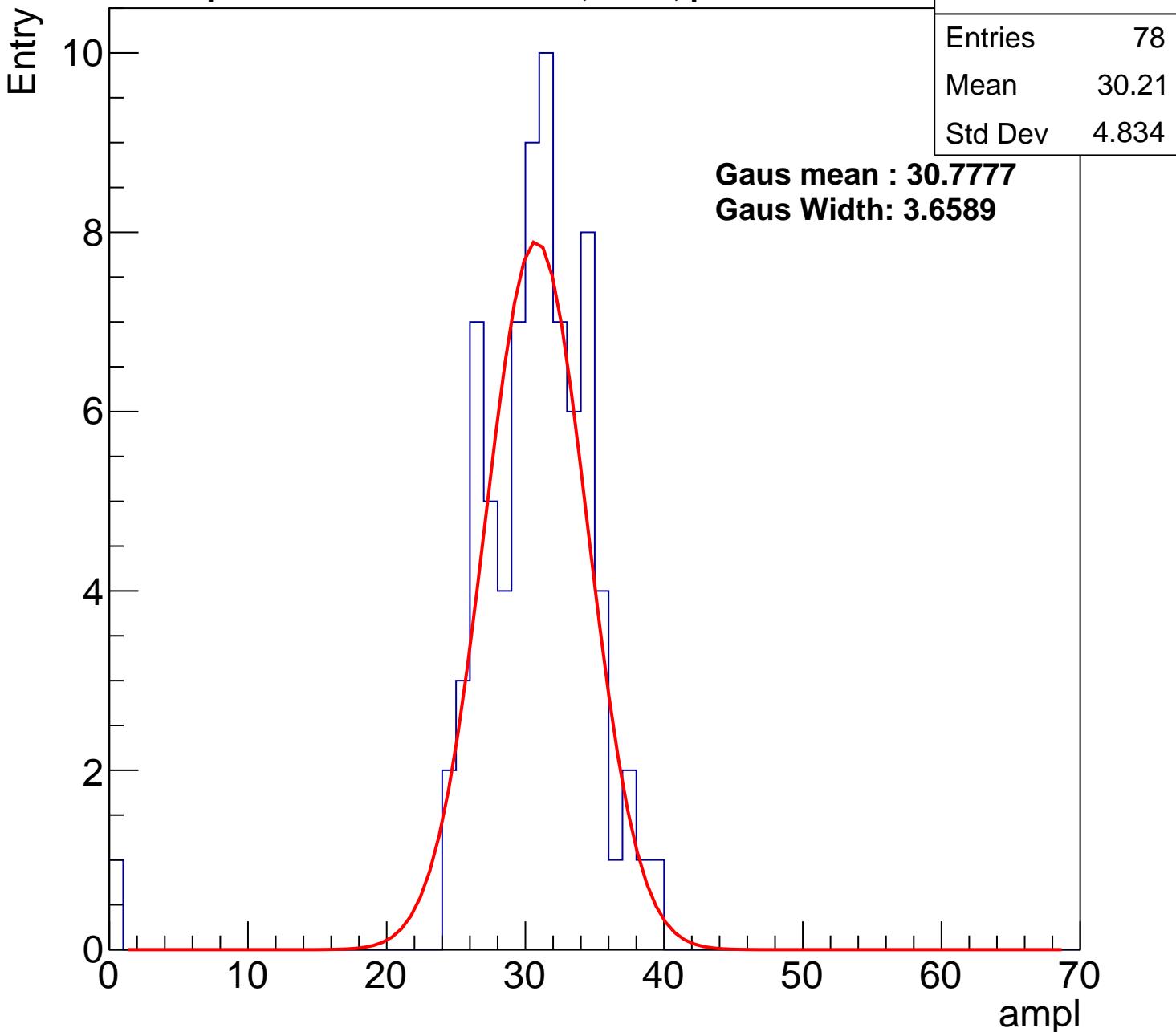
**Gaus Width: 3.6589**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U8-ch127, adc1

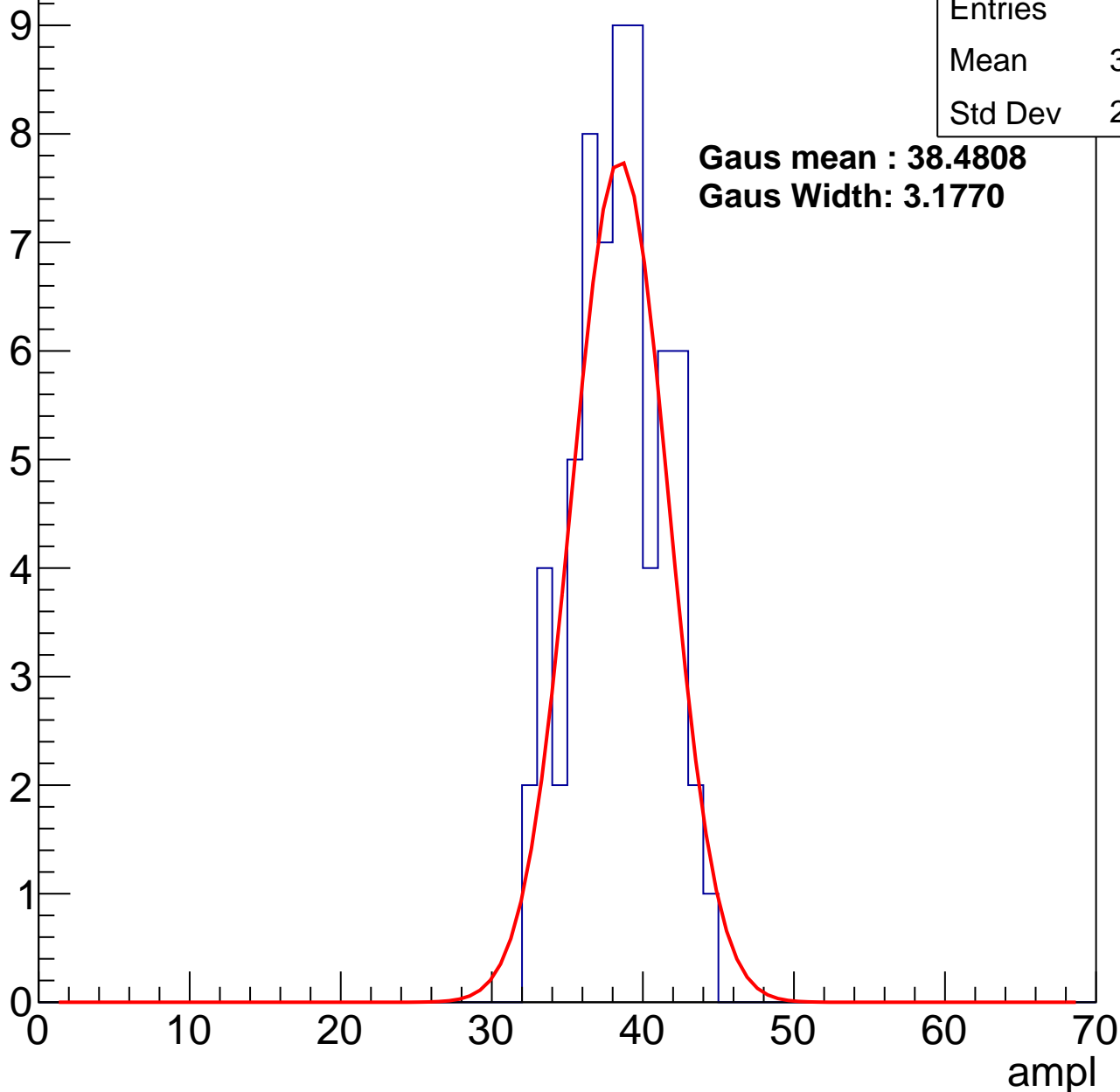
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.95
Std Dev	2.906

**Gaus mean : 38.4808**

**Gaus Width: 3.1770**



# B0L002S, U8-ch127, adc2

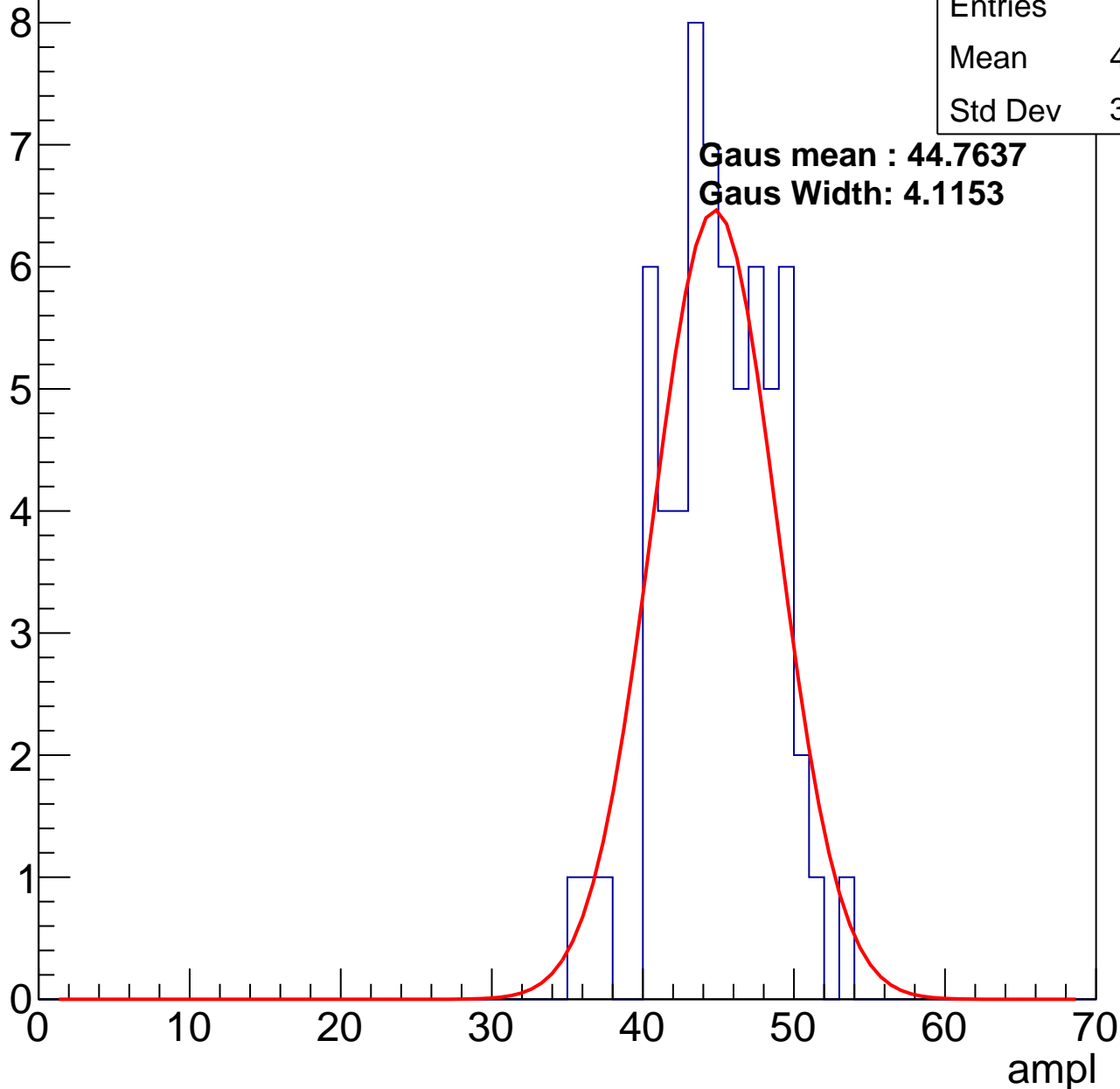
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.56
Std Dev	3.627

**Gaus mean : 44.7637**

**Gaus Width: 4.1153**

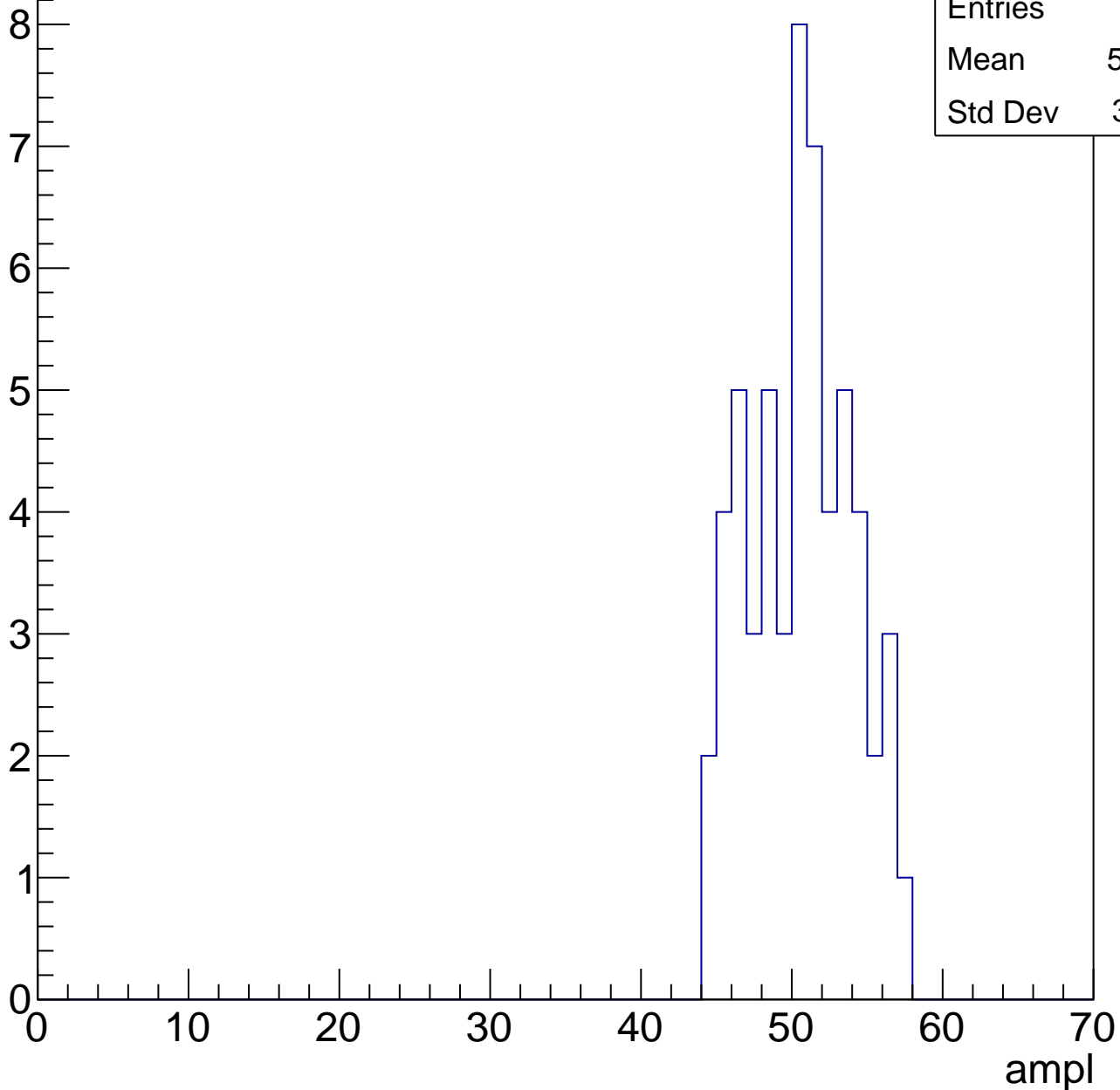


# B0L002S, U8-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	50.12
Std Dev	3.381

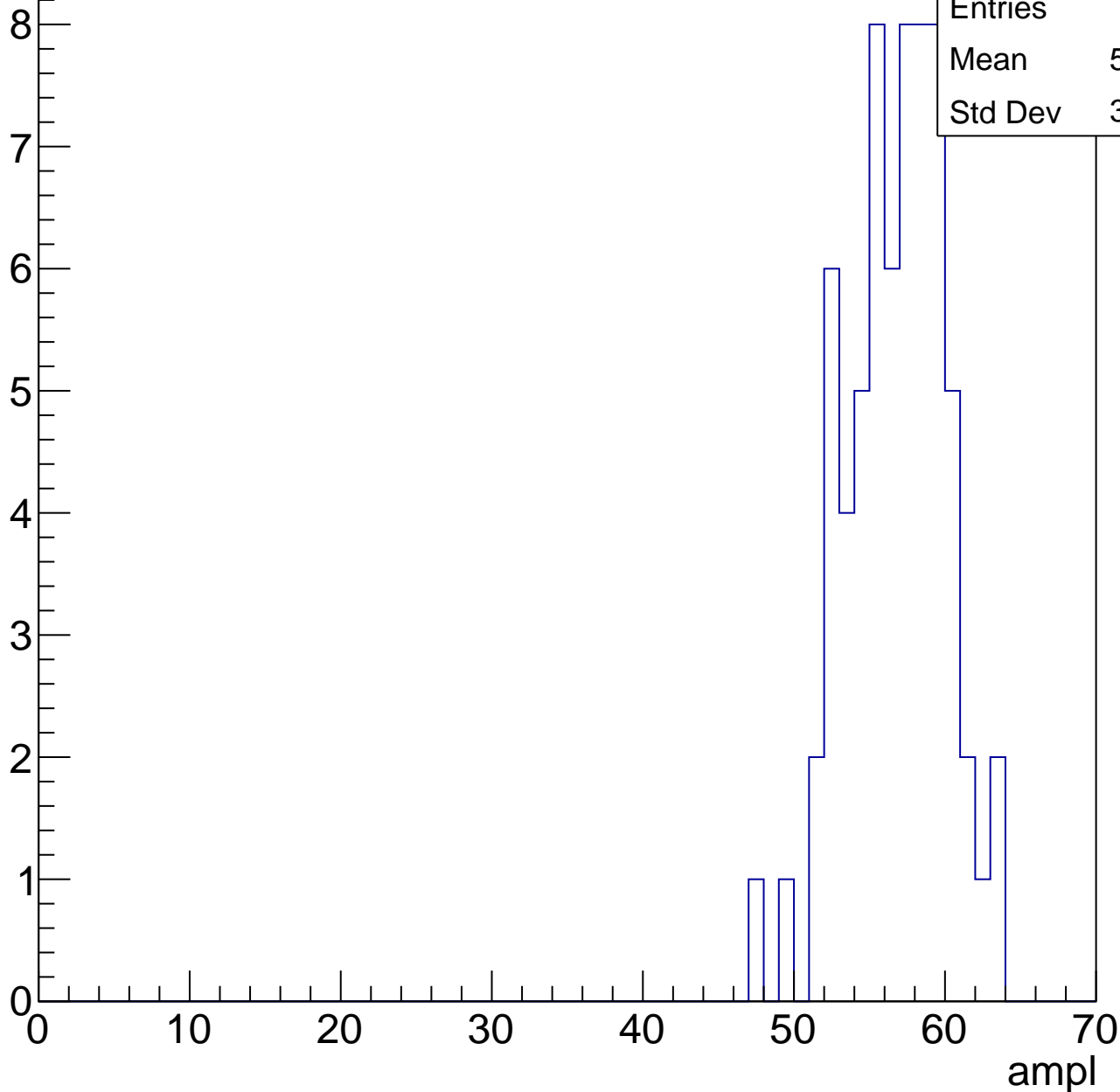


# B0L002S, U8-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.27
Std Dev	3.258

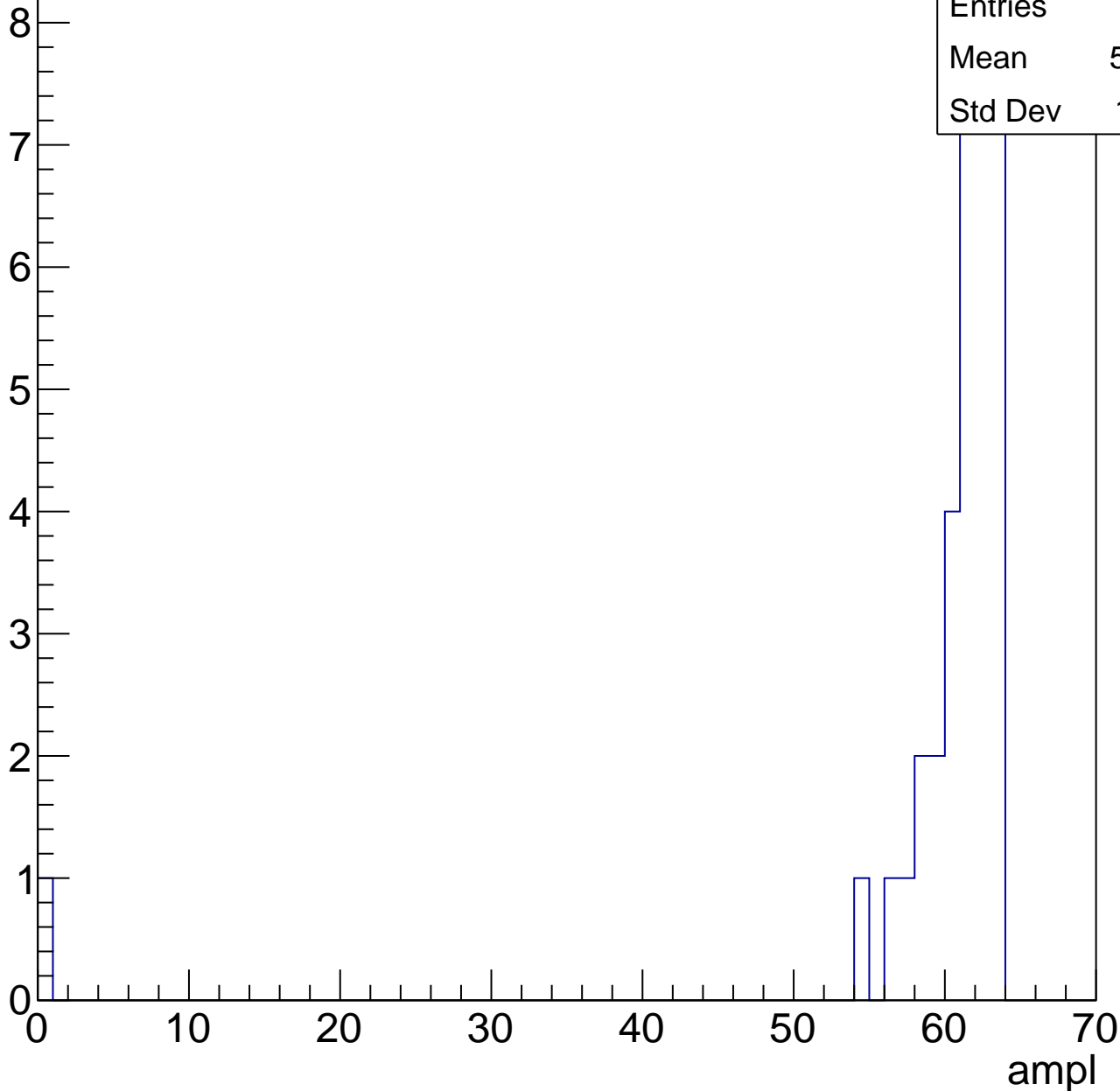


# B0L002S, U8-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.14
Std Dev	10.21



# B0L002S, U8-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U8-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

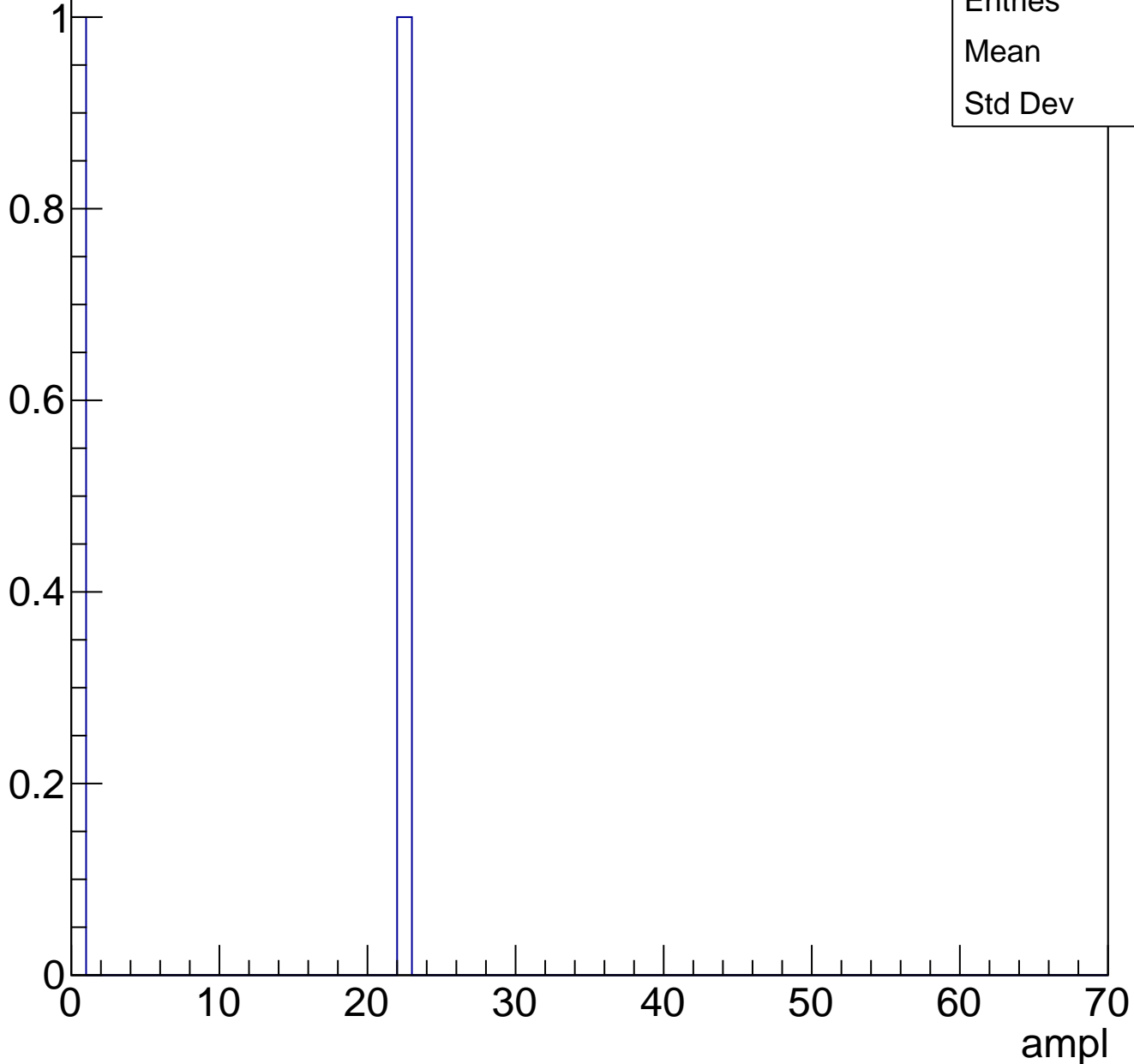


Entries	2
Mean	11
Std Dev	11

# B0L002S, U8-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11