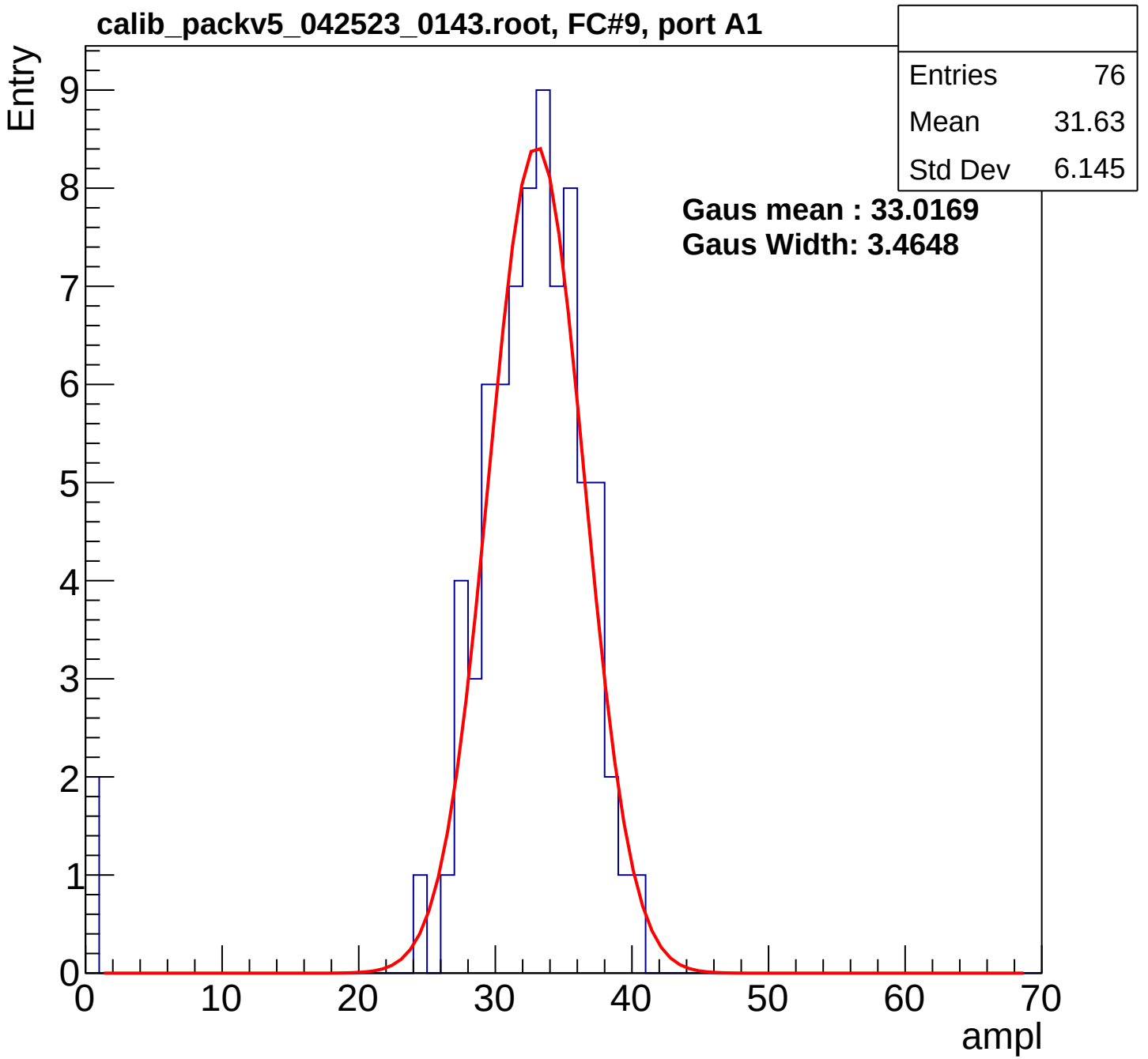
B0L001S, U8-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 31.63
Std Dev 6.145
Gaus mean : 33.0169
Gaus Width: 3.4648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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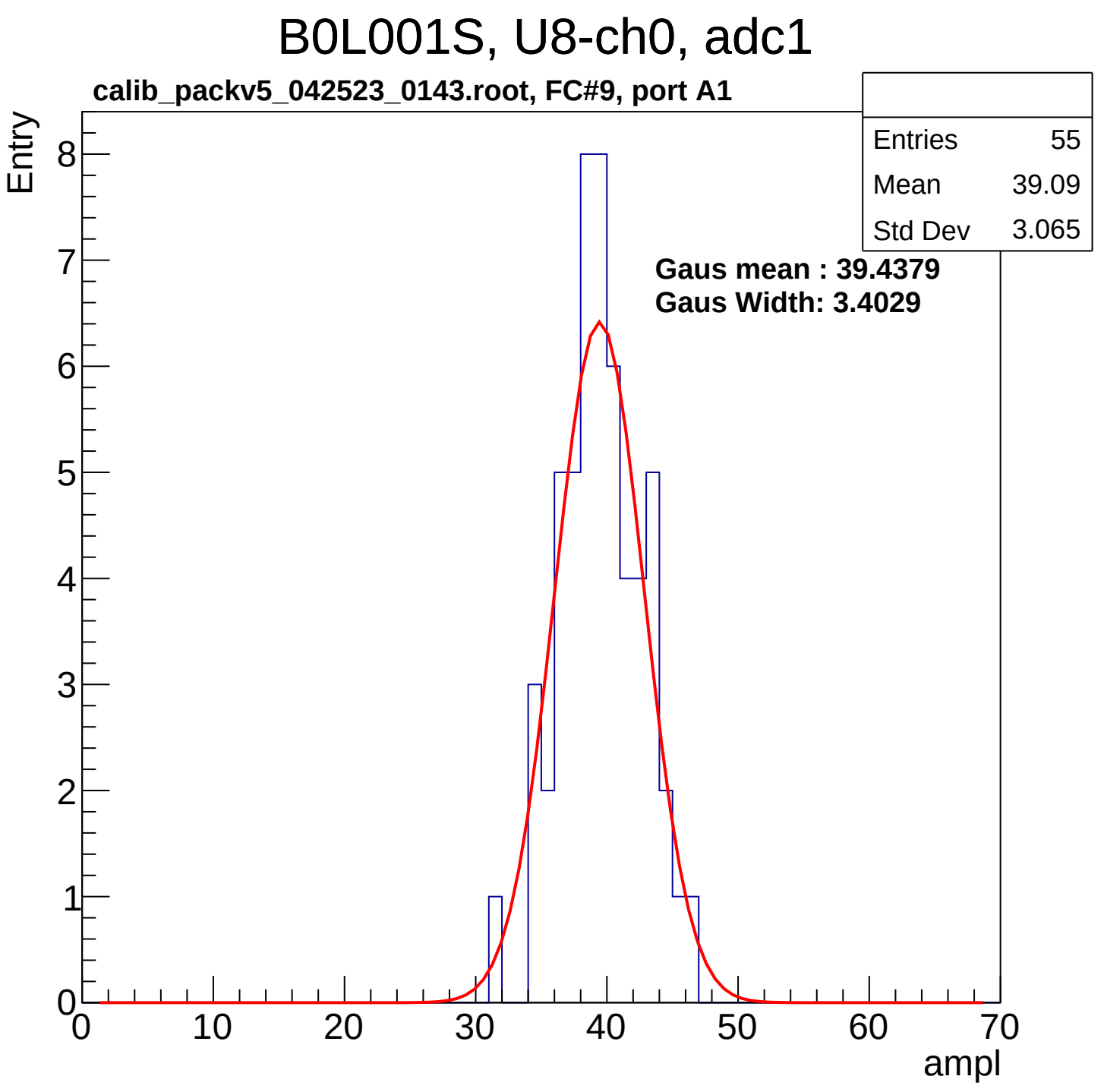
B0L001S, U8-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 39.09
Std Dev 3.065
Gaus mean : 39.4379
Gaus Width: 3.4029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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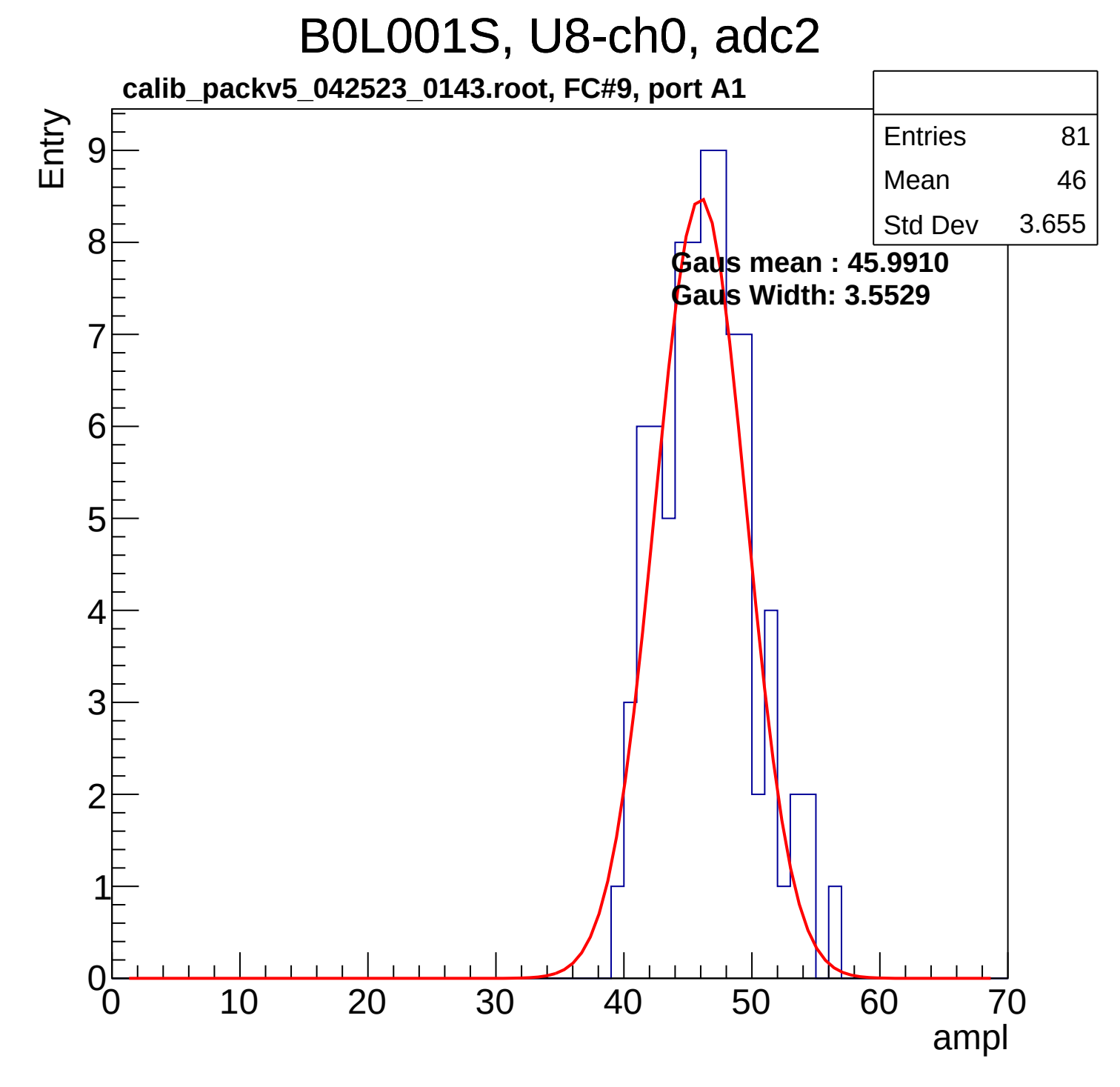
B0L001S, U8-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 46
Std Dev 3.655
Gaus mean : 45.9910
Gaus Width: 3.5529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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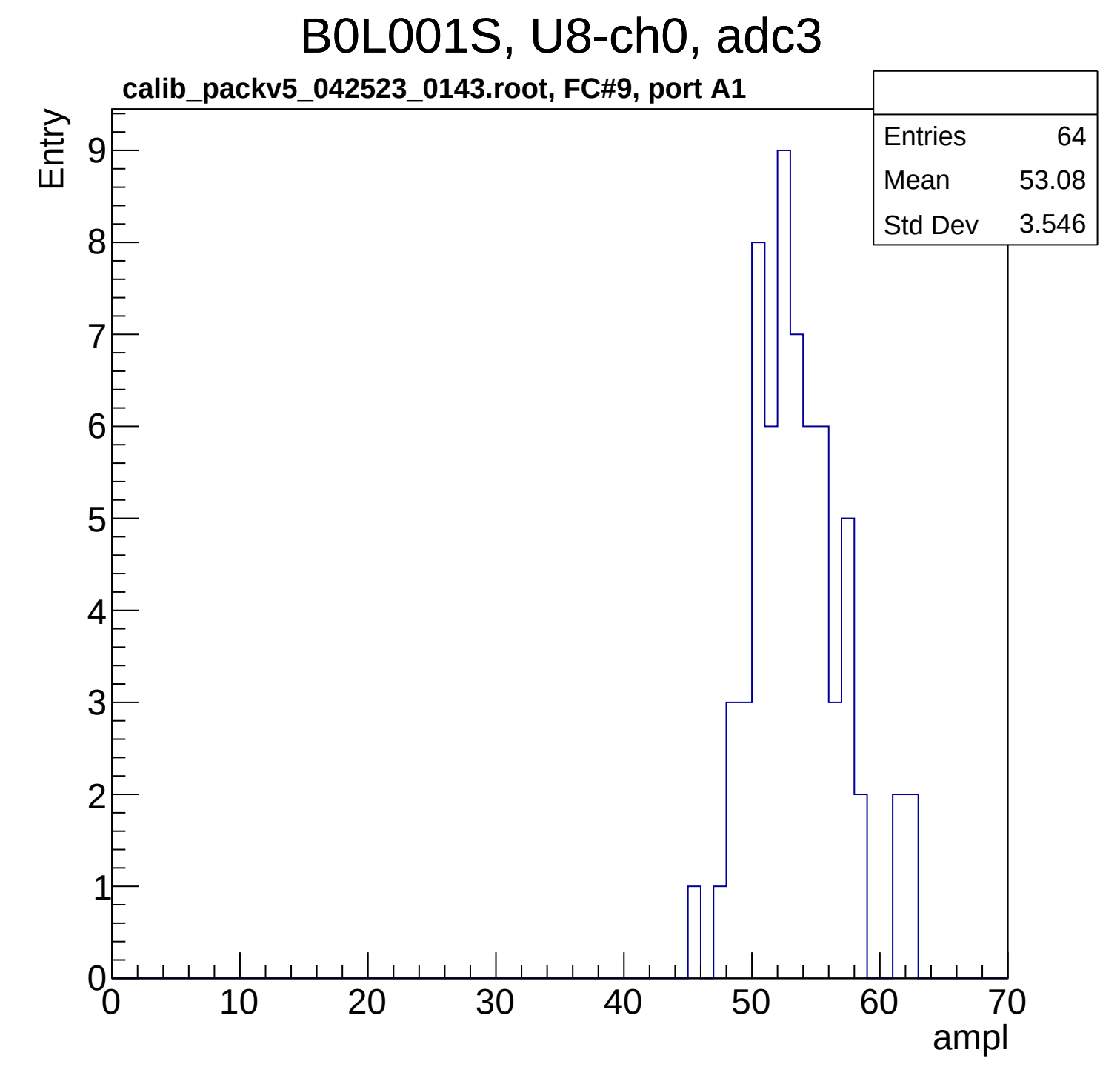
B0L001S, U8-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 53.08
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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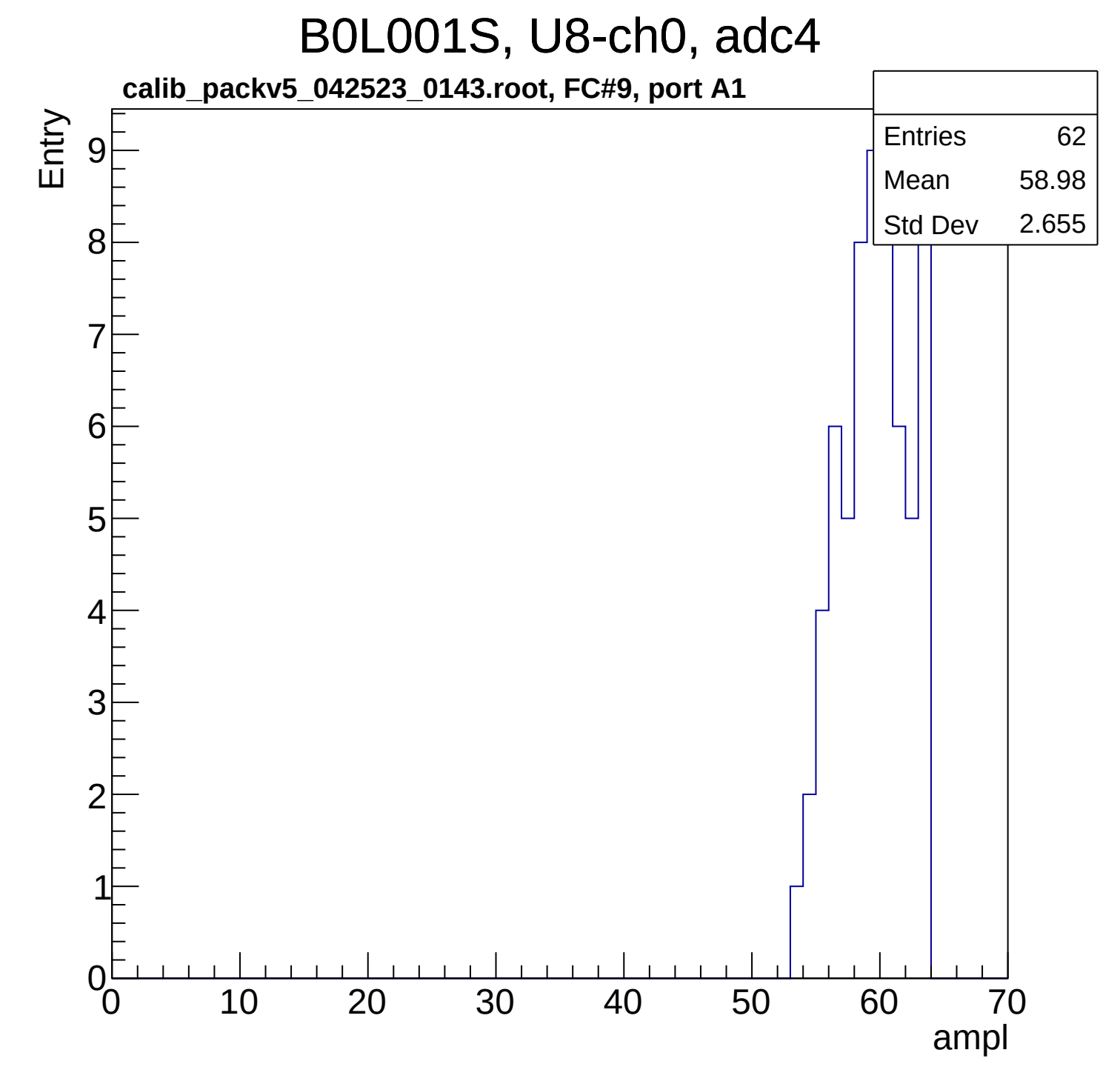
B0L001S, U8-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 58.98
Std Dev 2.655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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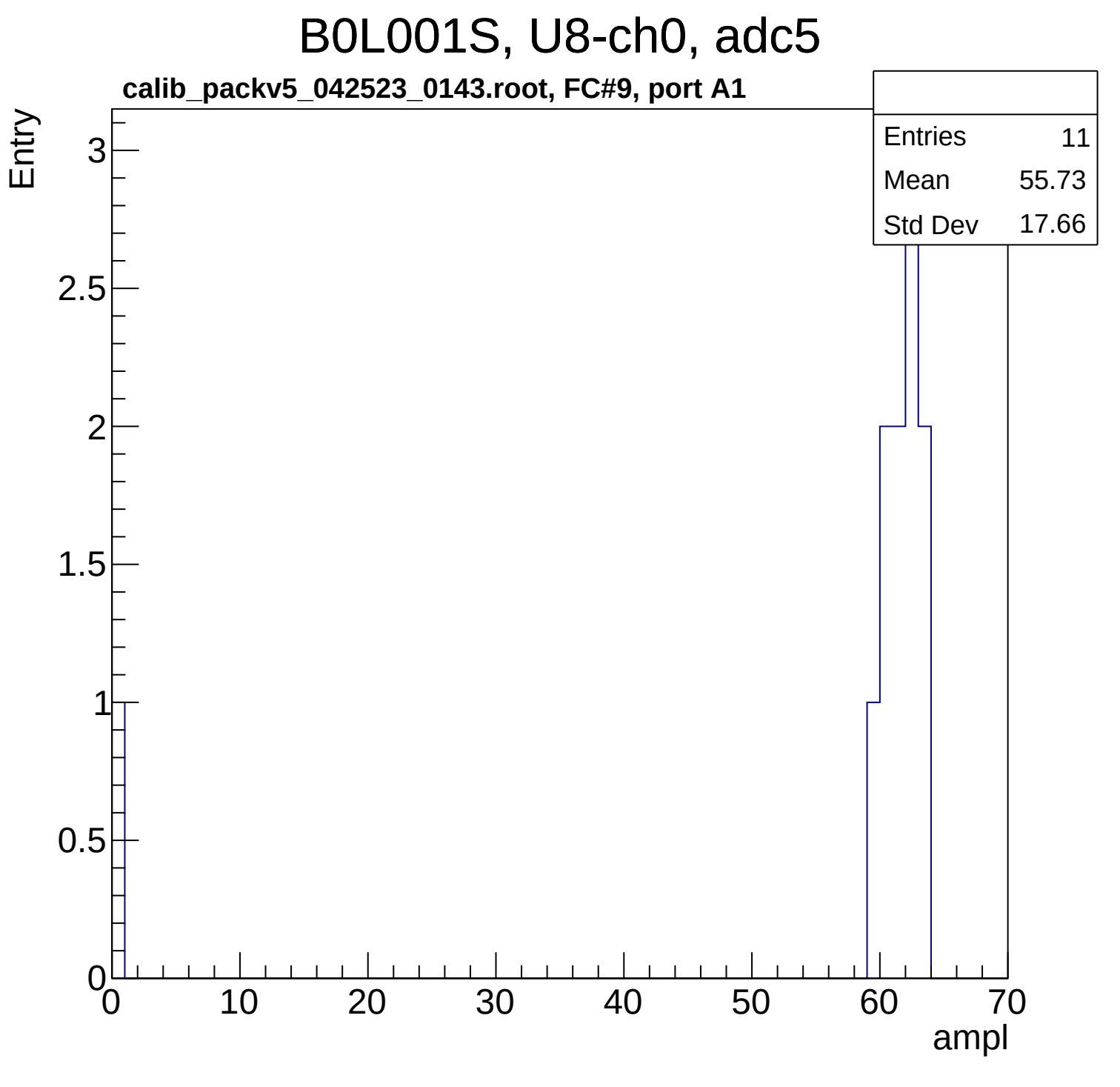
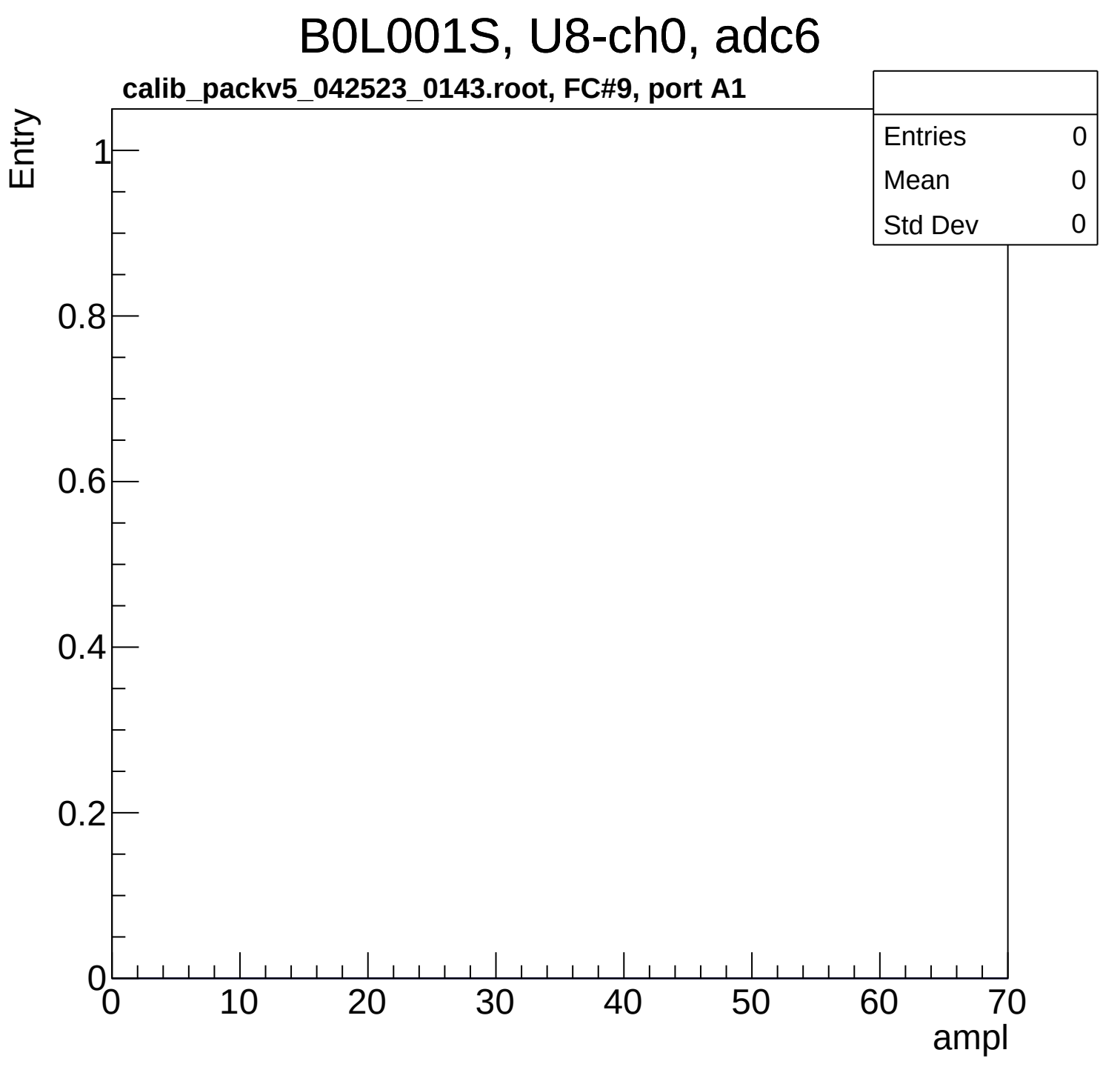
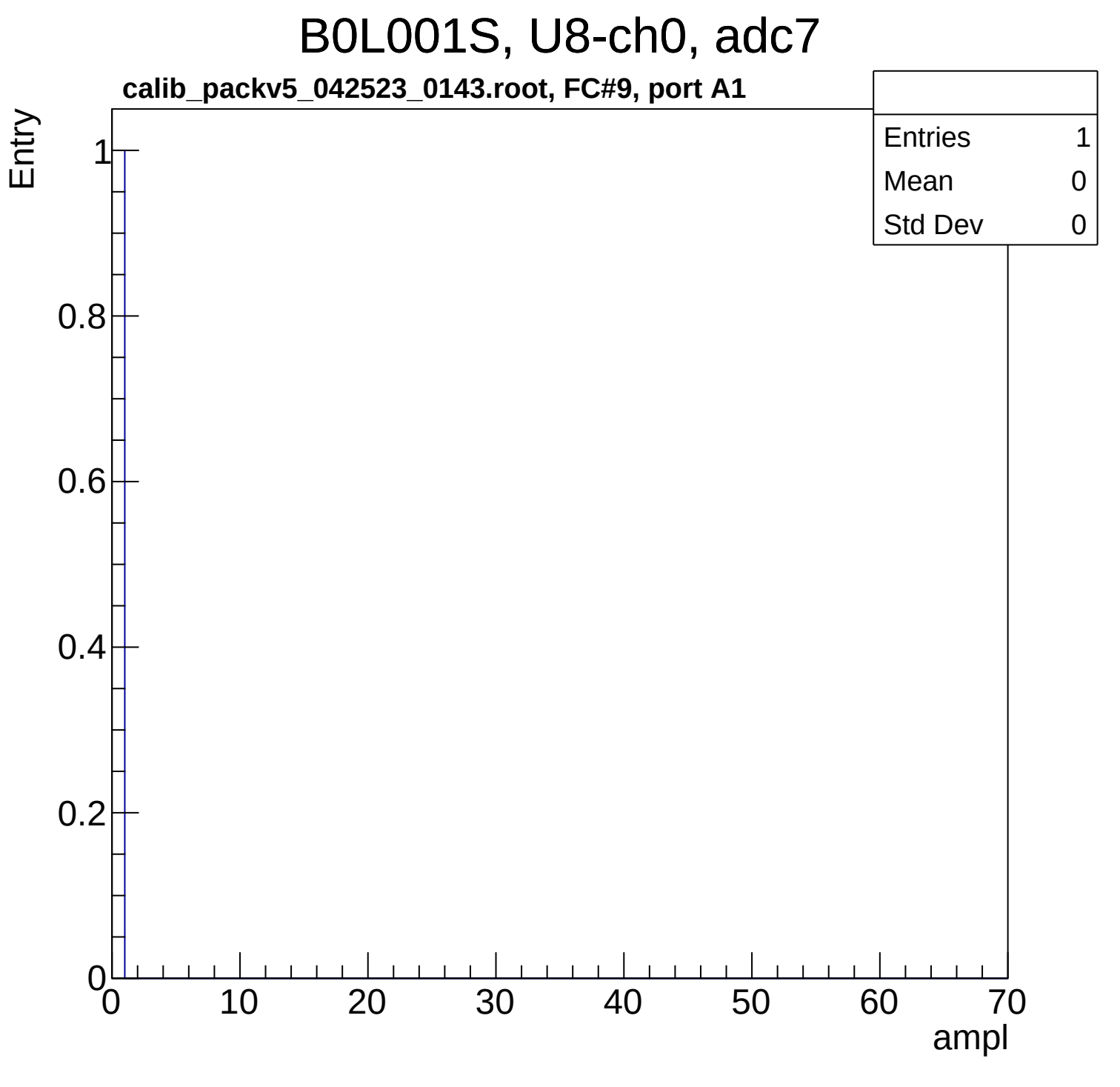
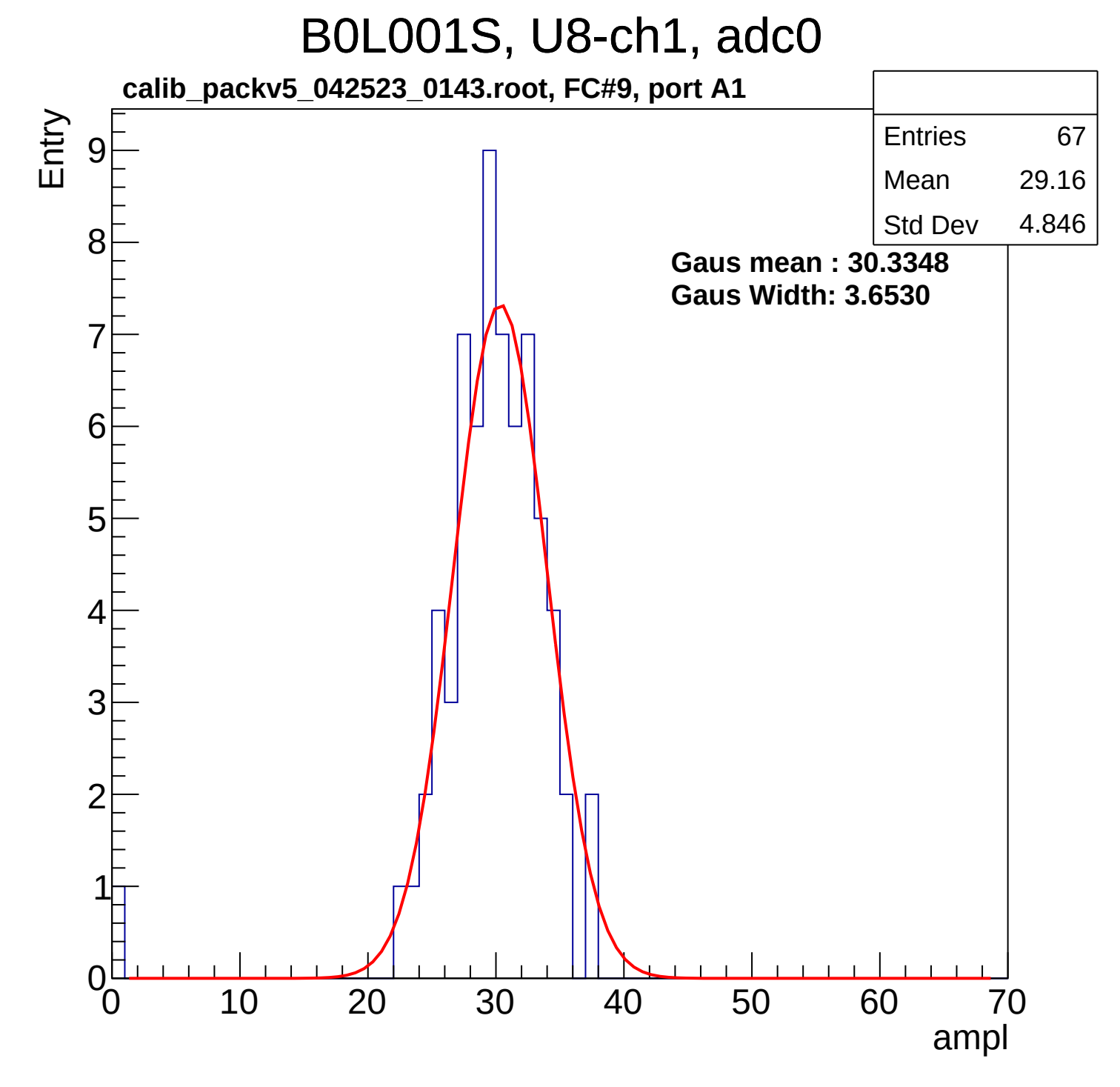
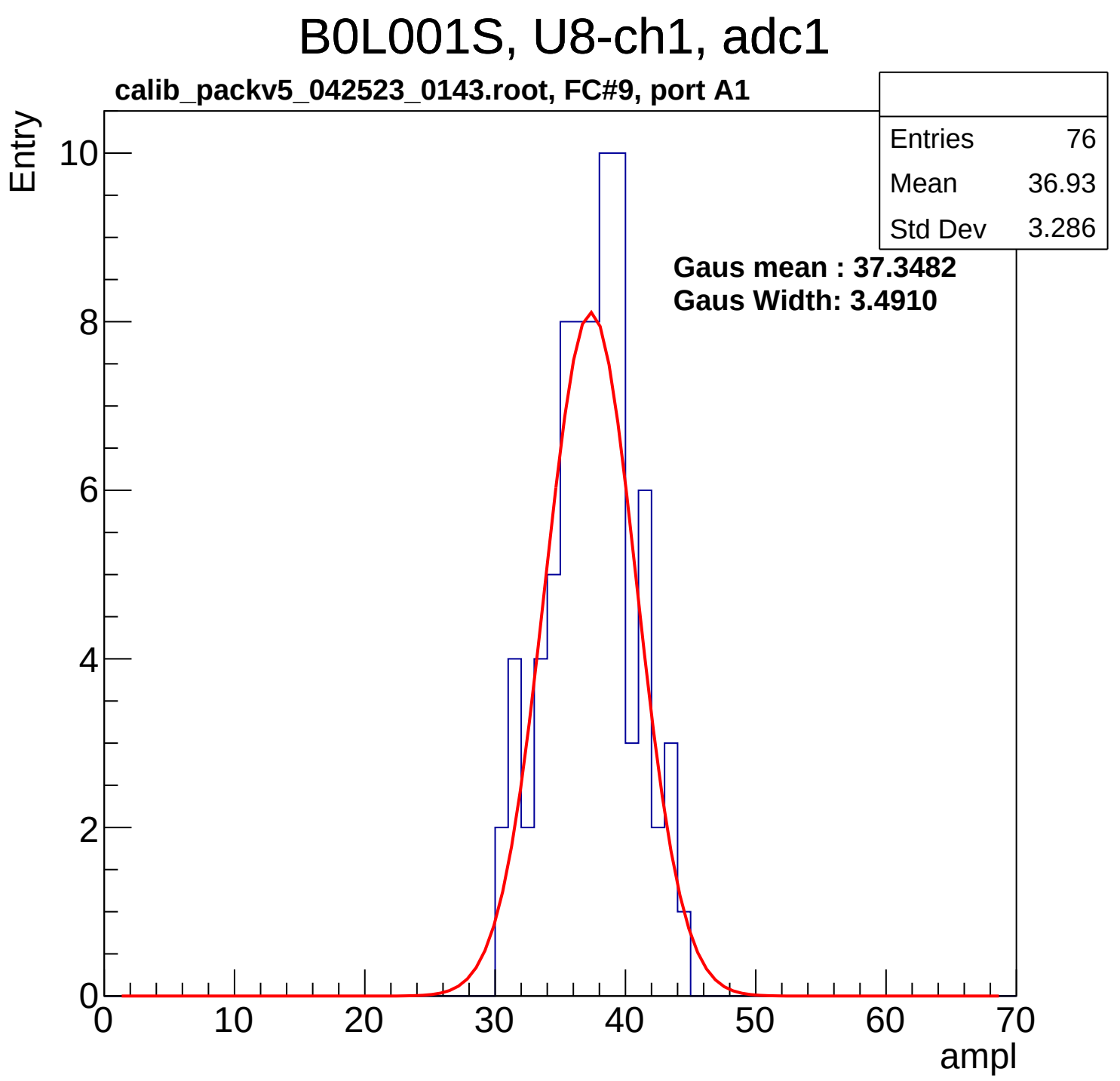
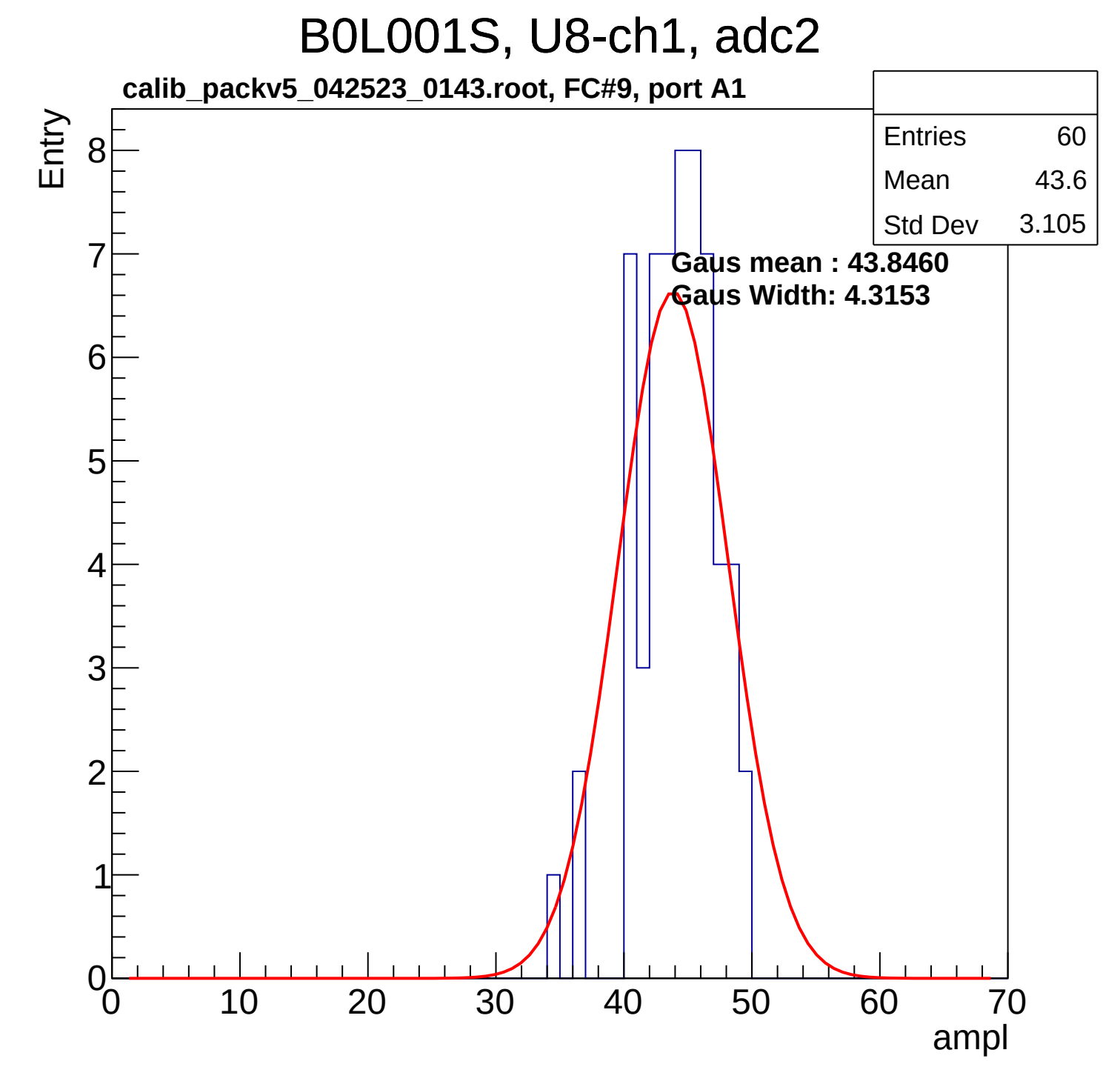

B0L001S, U8-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 55.73
Std Dev 17.66
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.16
Std Dev 4.846
Gaus mean : 30.3348
Gaus Width: 3.6530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.93
Std Dev 3.286
Gaus mean : 37.3482
Gaus Width: 3.4910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.6
Std Dev 3.105
Gaus mean : 43.8460
Gaus Width: 4.3153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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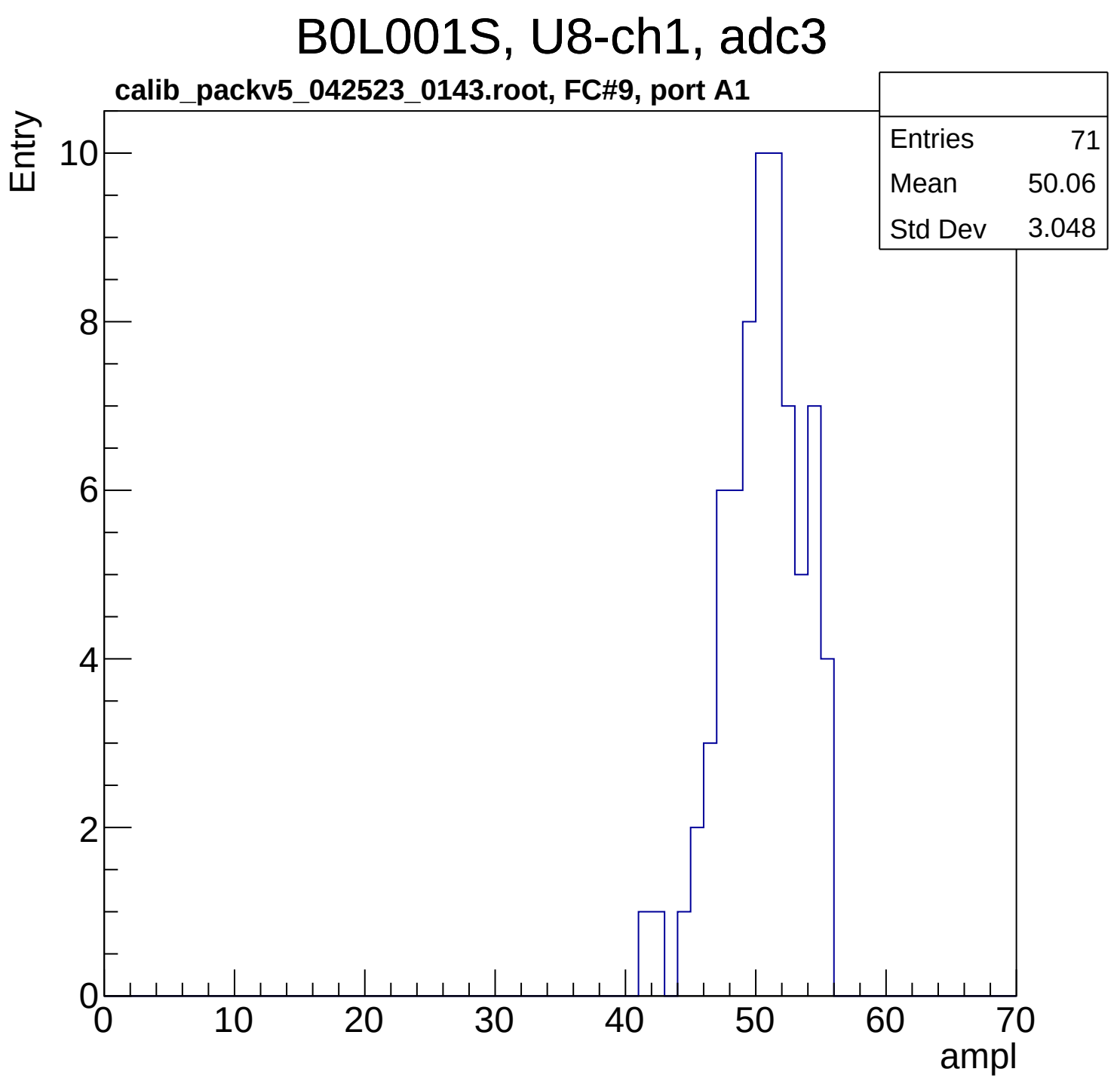
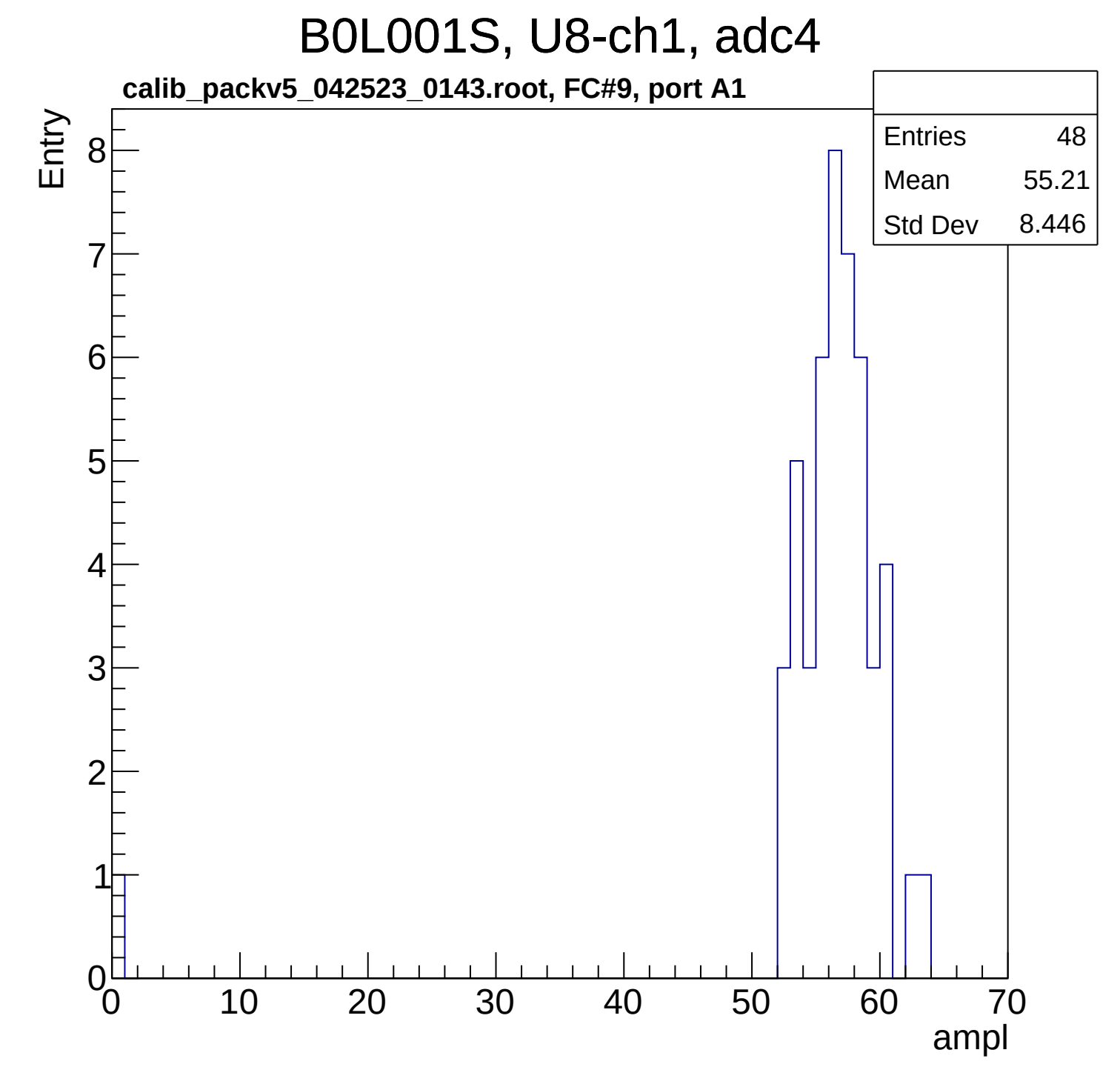

B0L001S, U8-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.06
Std Dev 3.048
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.21
Std Dev 8.446
Entry
ampl
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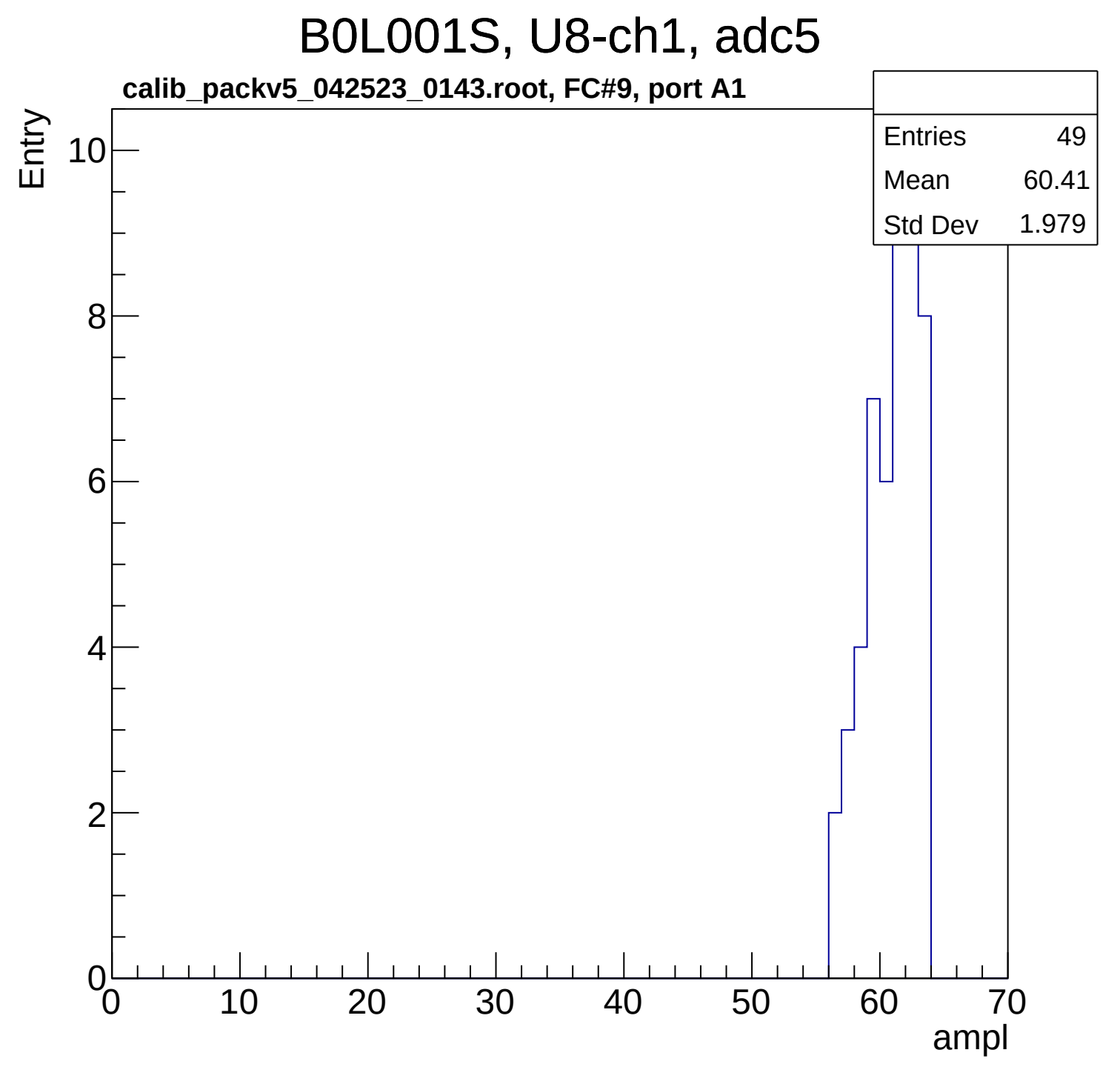
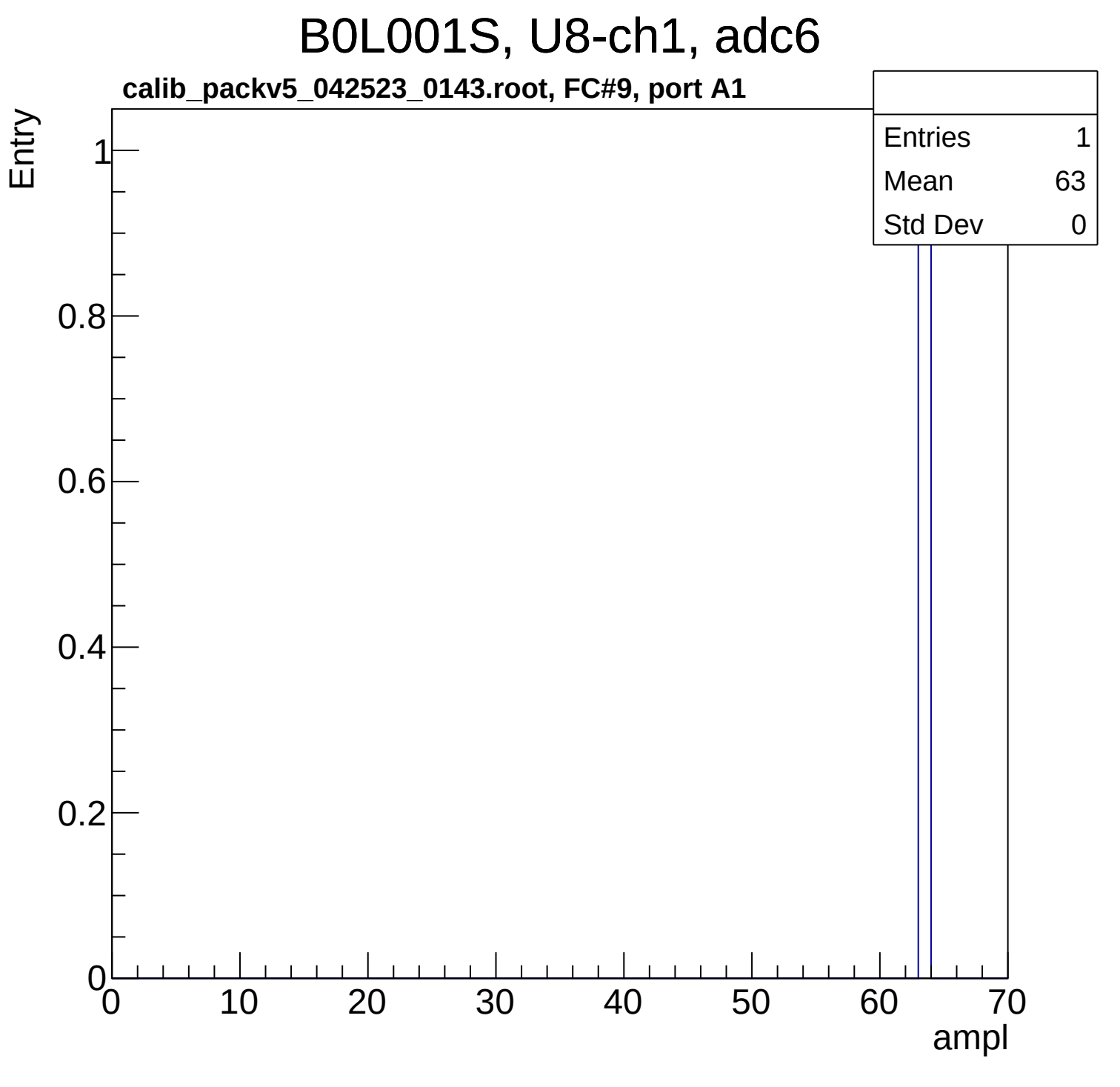
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 60.41
Std Dev 1.979
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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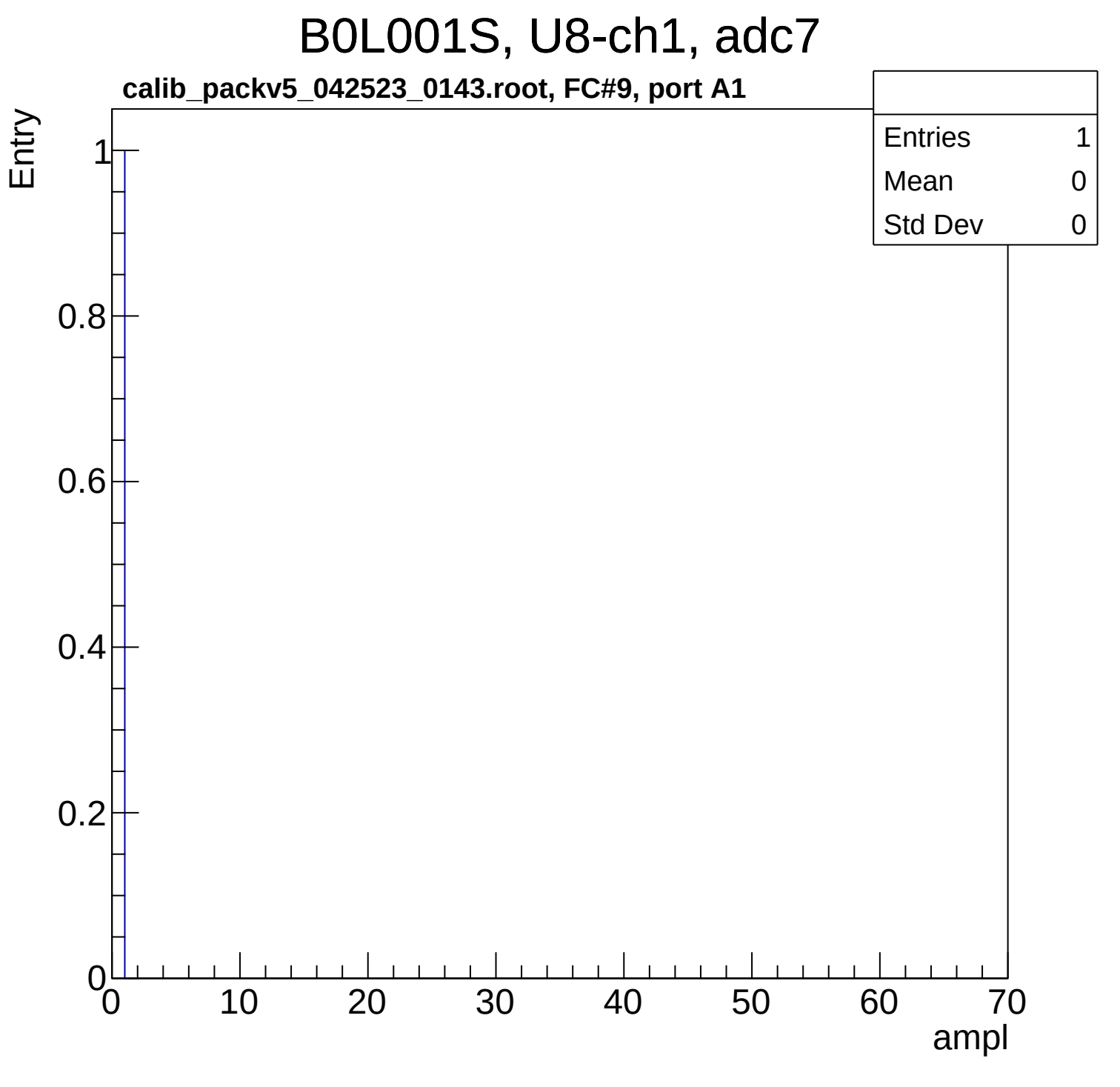
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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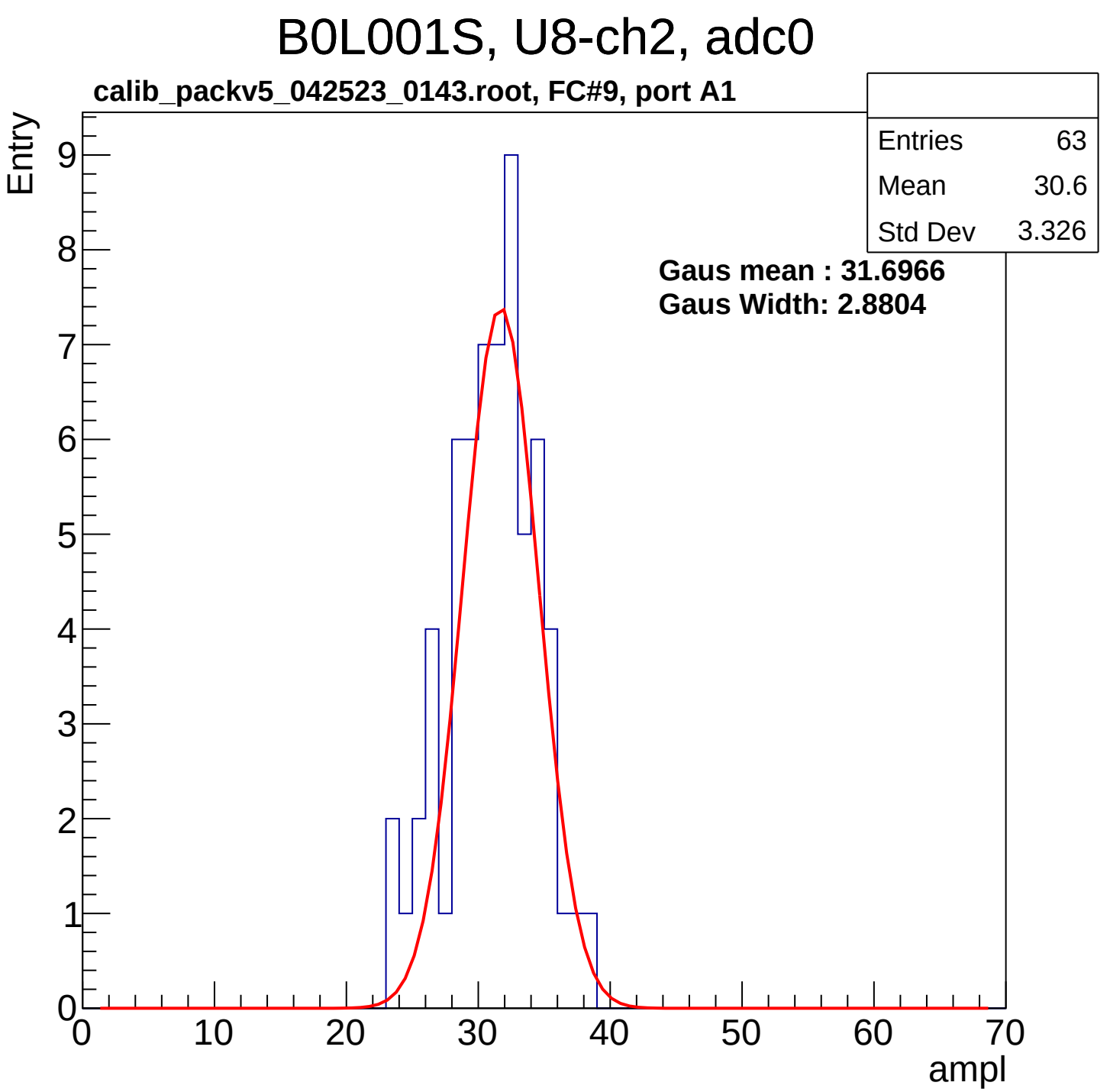
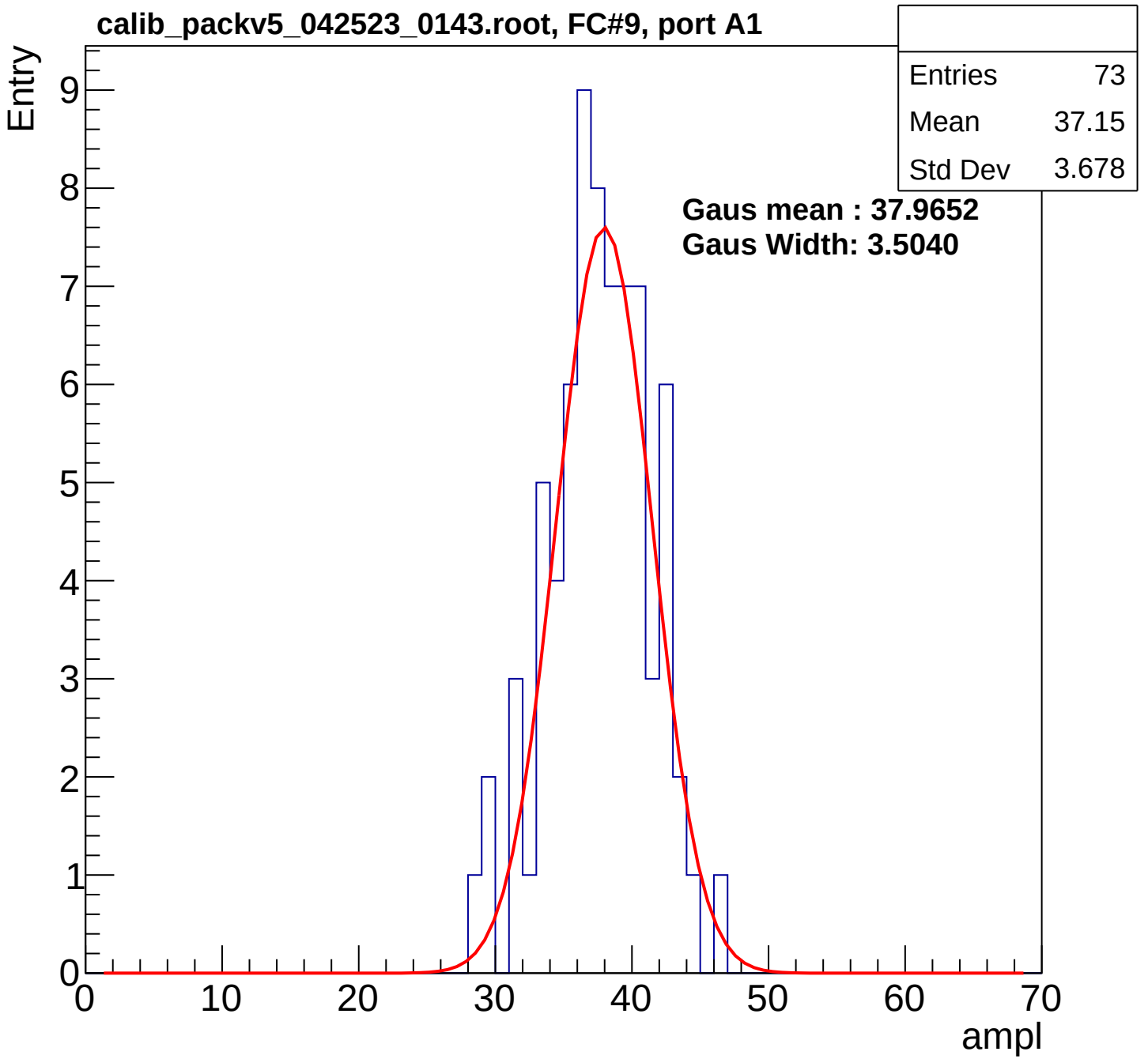
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.6
Std Dev 3.326
Gaus mean : 31.6966
Gaus Width: 2.8804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.15
Std Dev 3.678
Gaus mean : 37.9652
Gaus Width: 3.5040
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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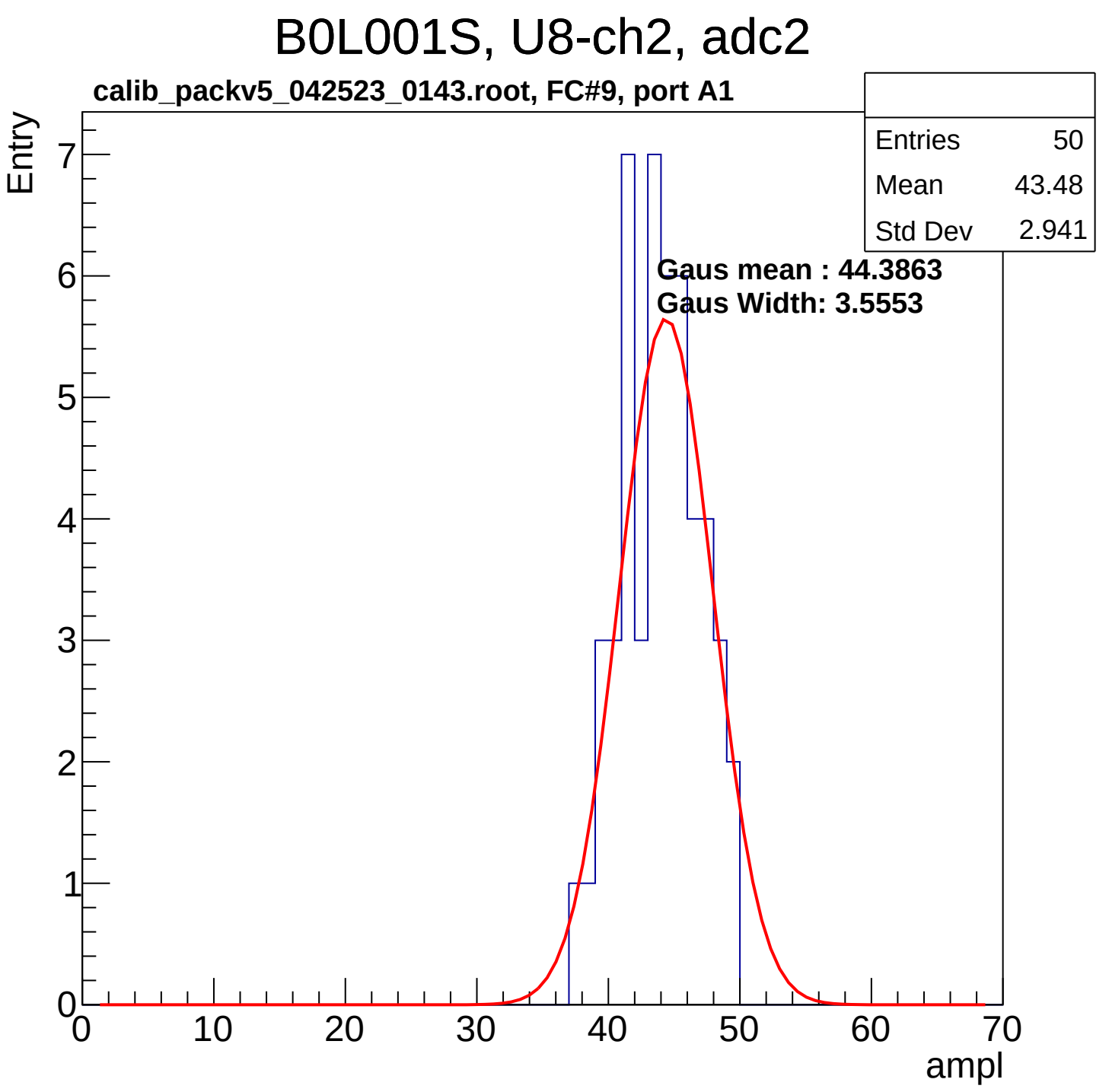
B0L001S, U8-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 43.48
Std Dev 2.941
Gaus mean : 44.3863
Gaus Width: 3.5553
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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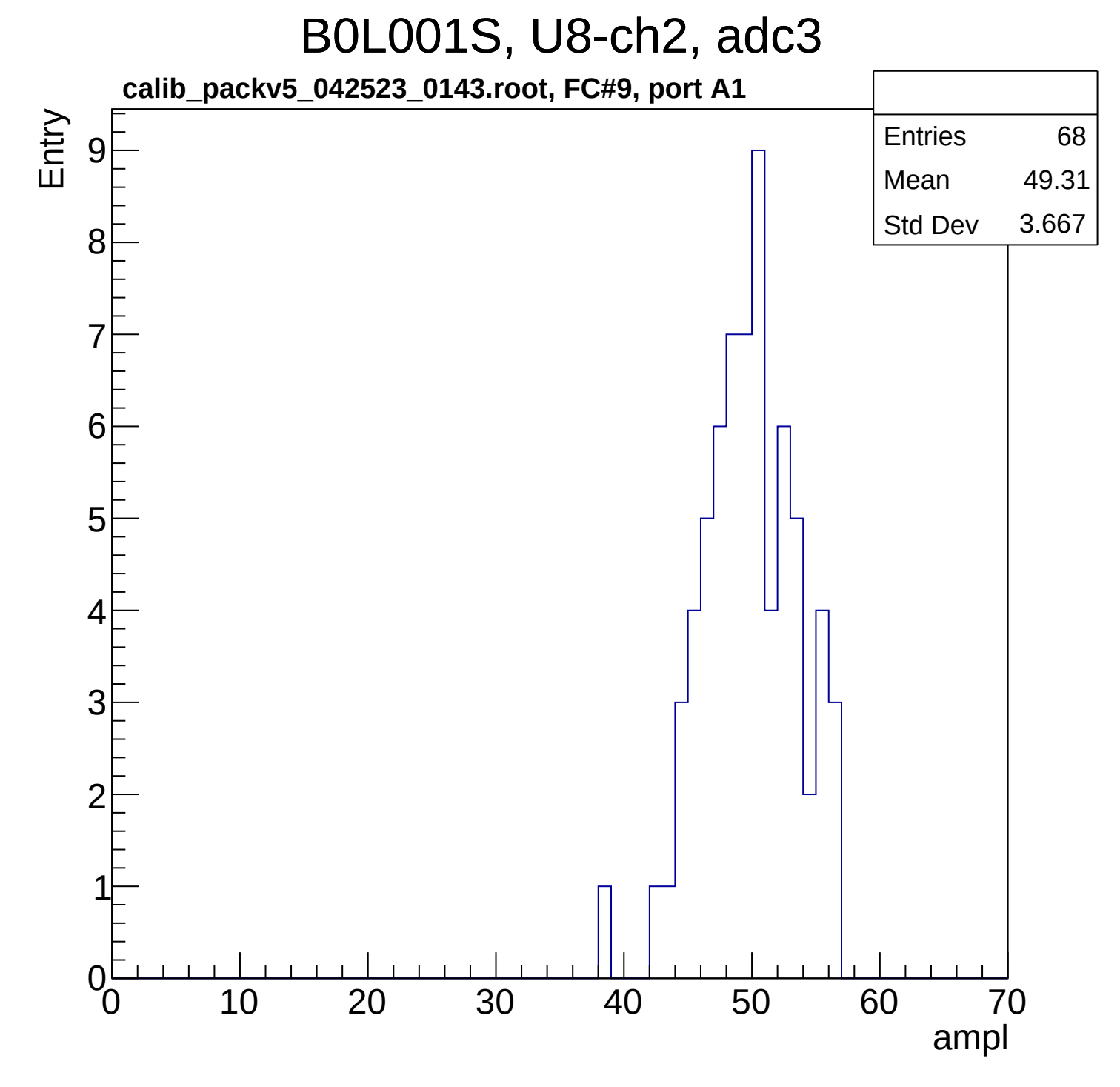
B0L001S, U8-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.31
Std Dev 3.667
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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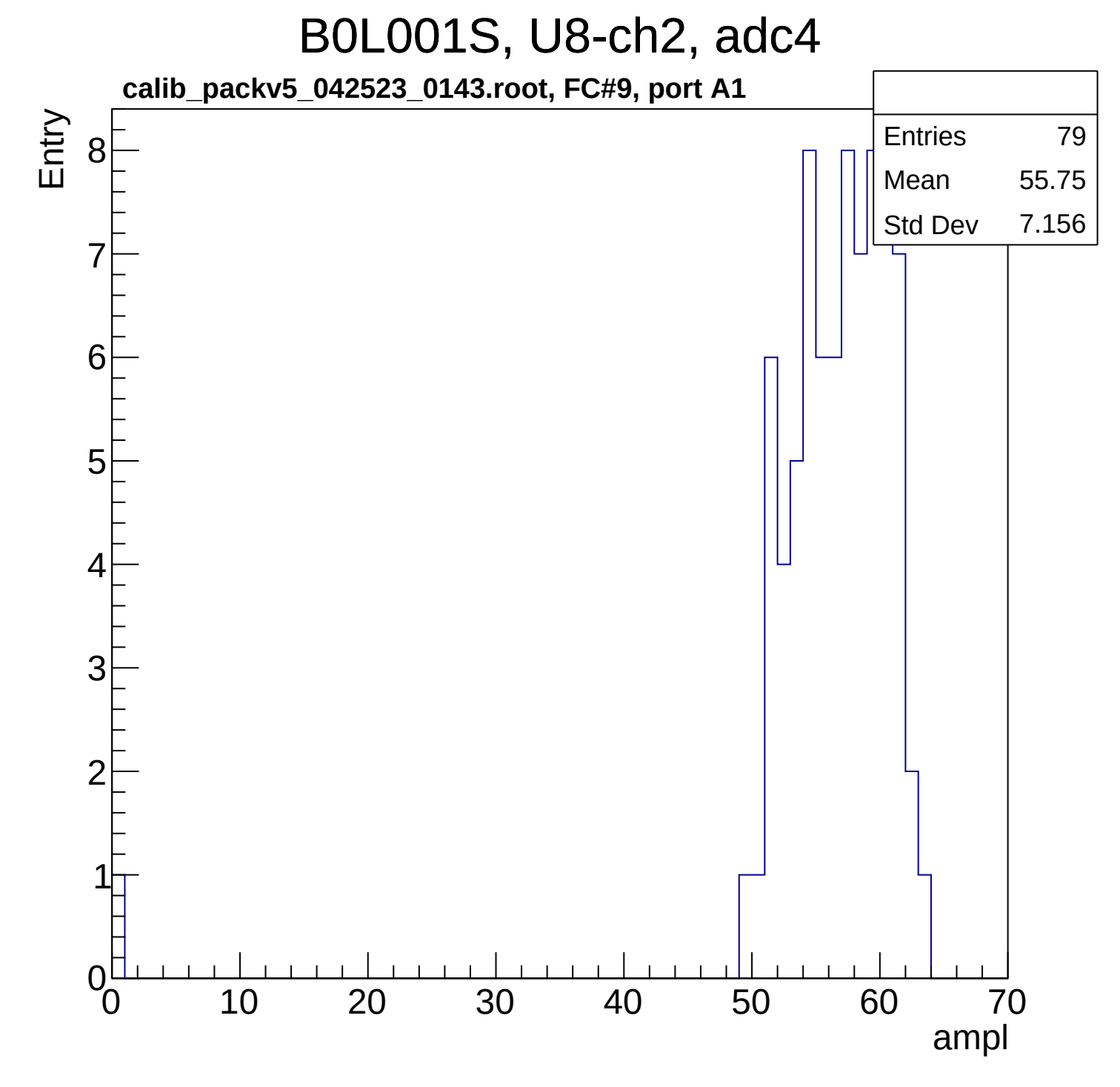
B0L001S, U8-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 55.75
Std Dev 7.156
Entry
ampl
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1
2
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4
5
6
7
8
0
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40
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60
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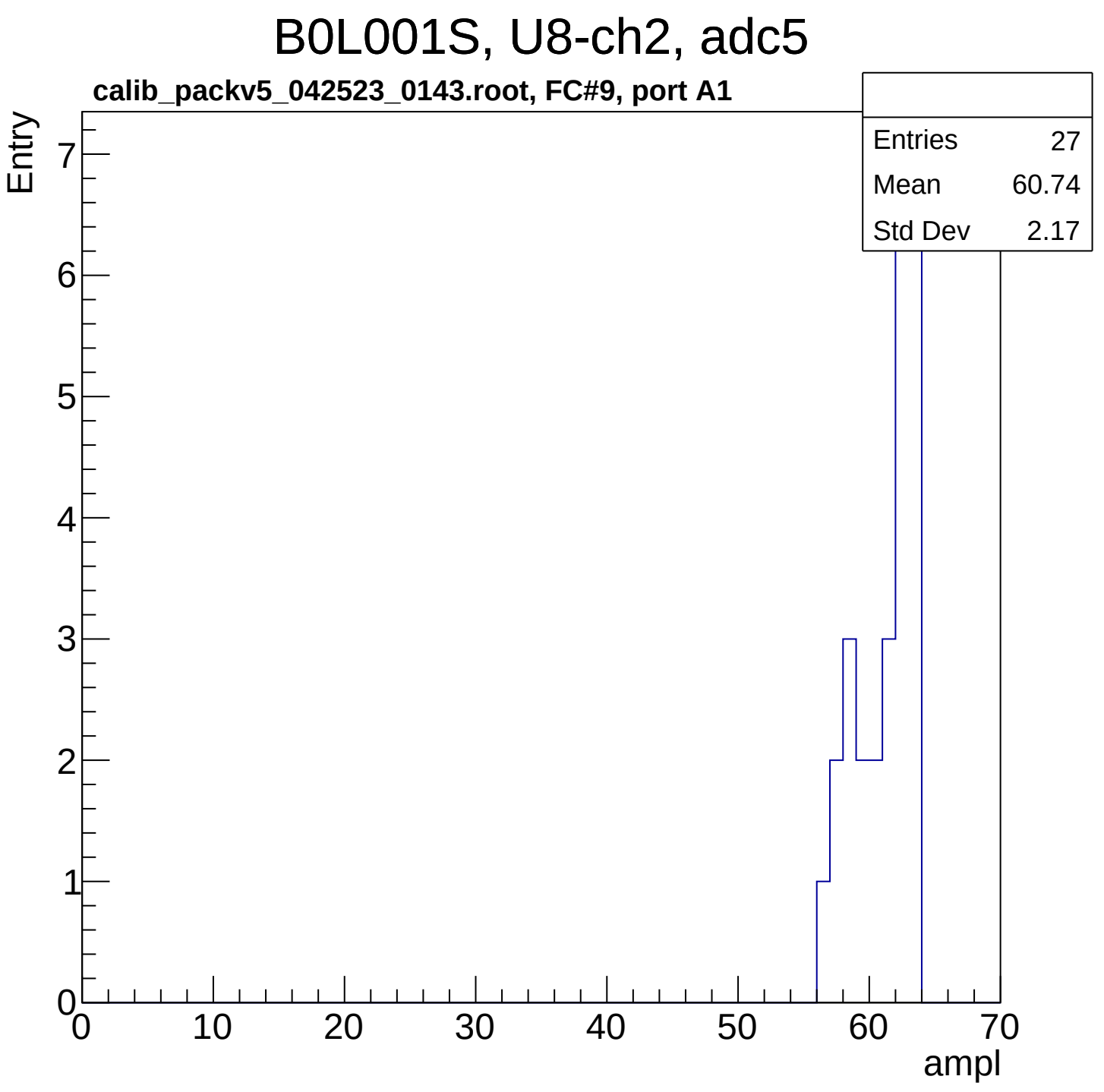
B0L001S, U8-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 60.74
Std Dev 2.17
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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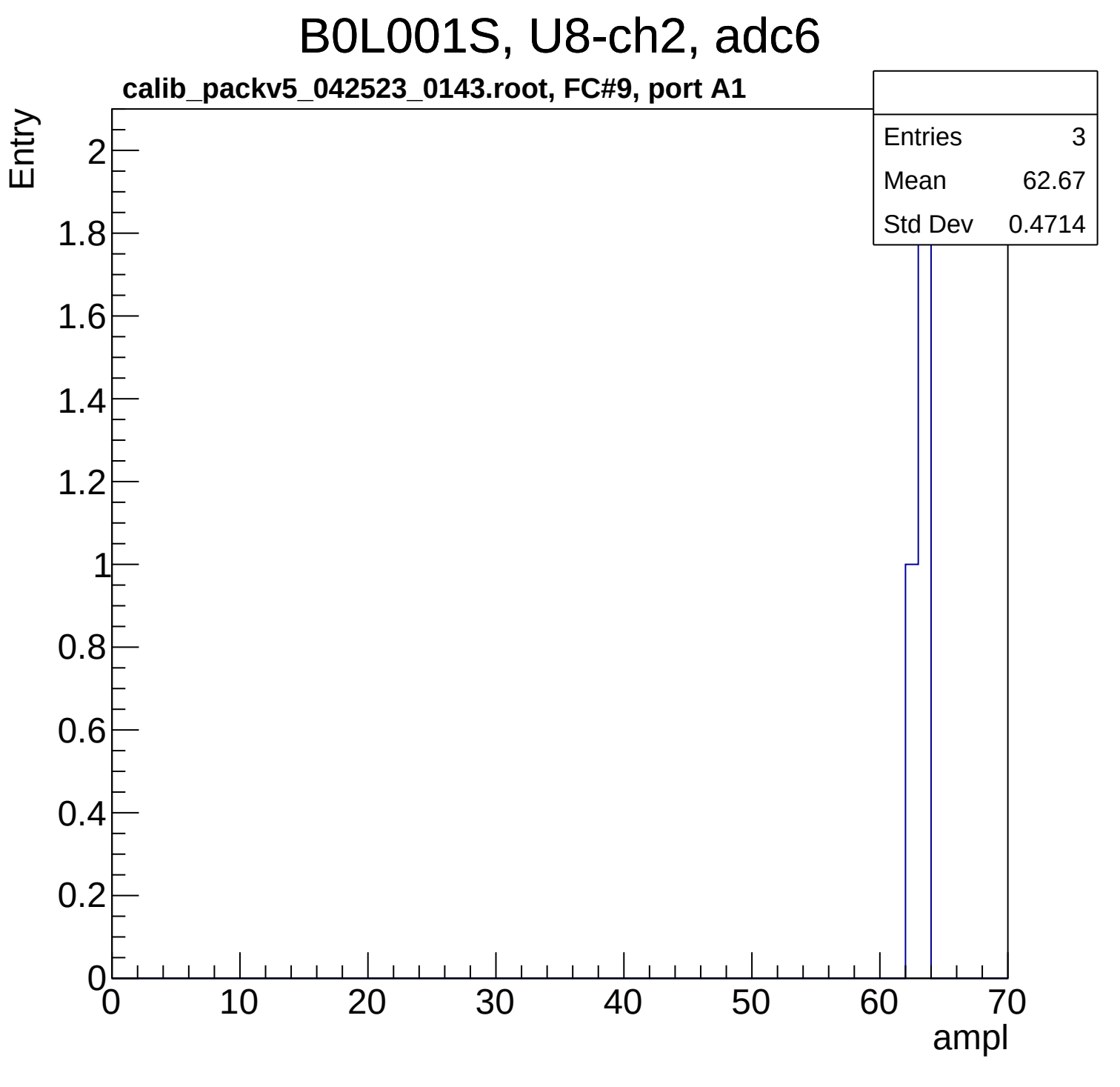
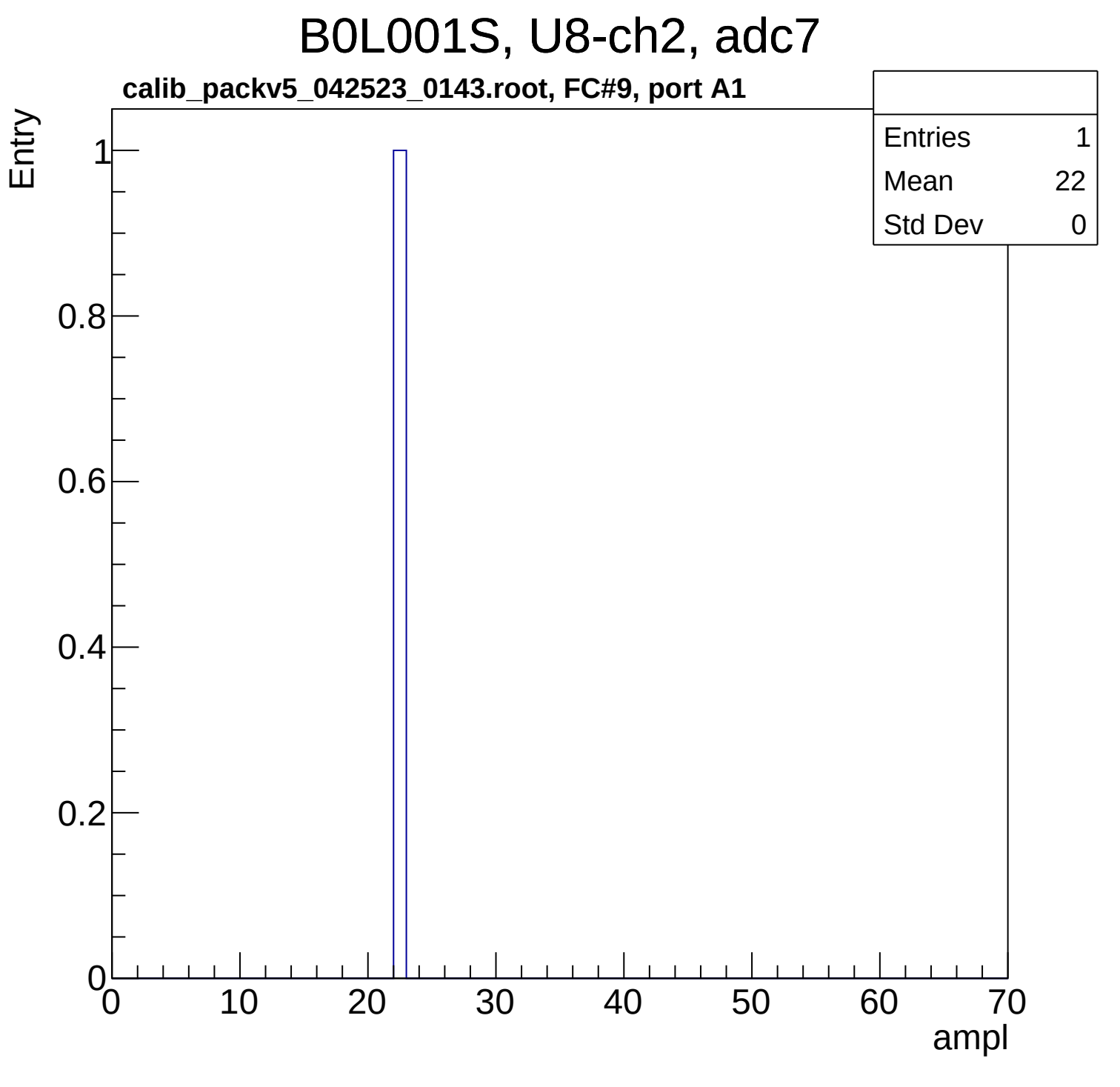

B0L001S, U8-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
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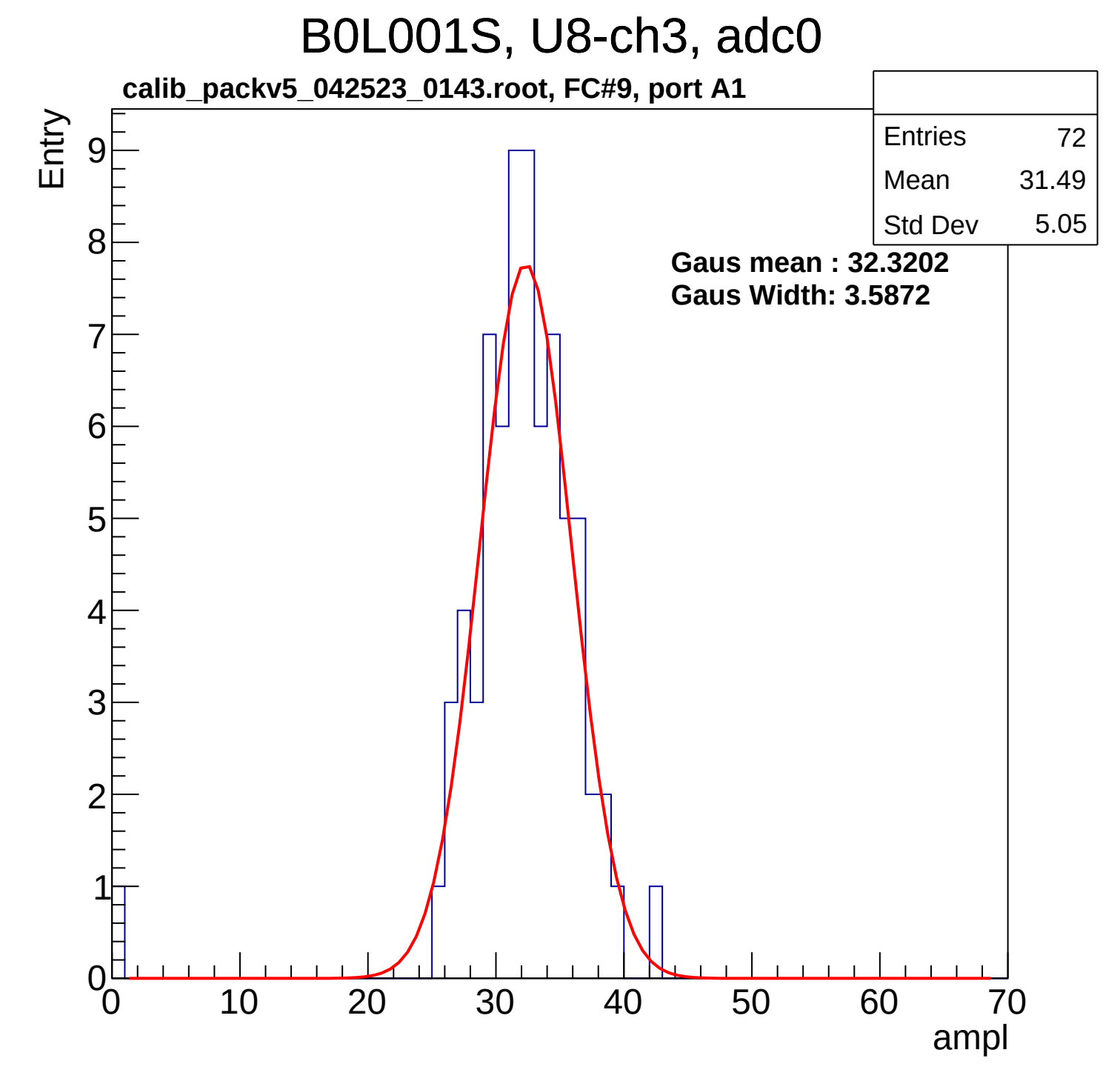
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 31.49
Std Dev 5.05
Gaus mean : 32.3202
Gaus Width: 3.5872
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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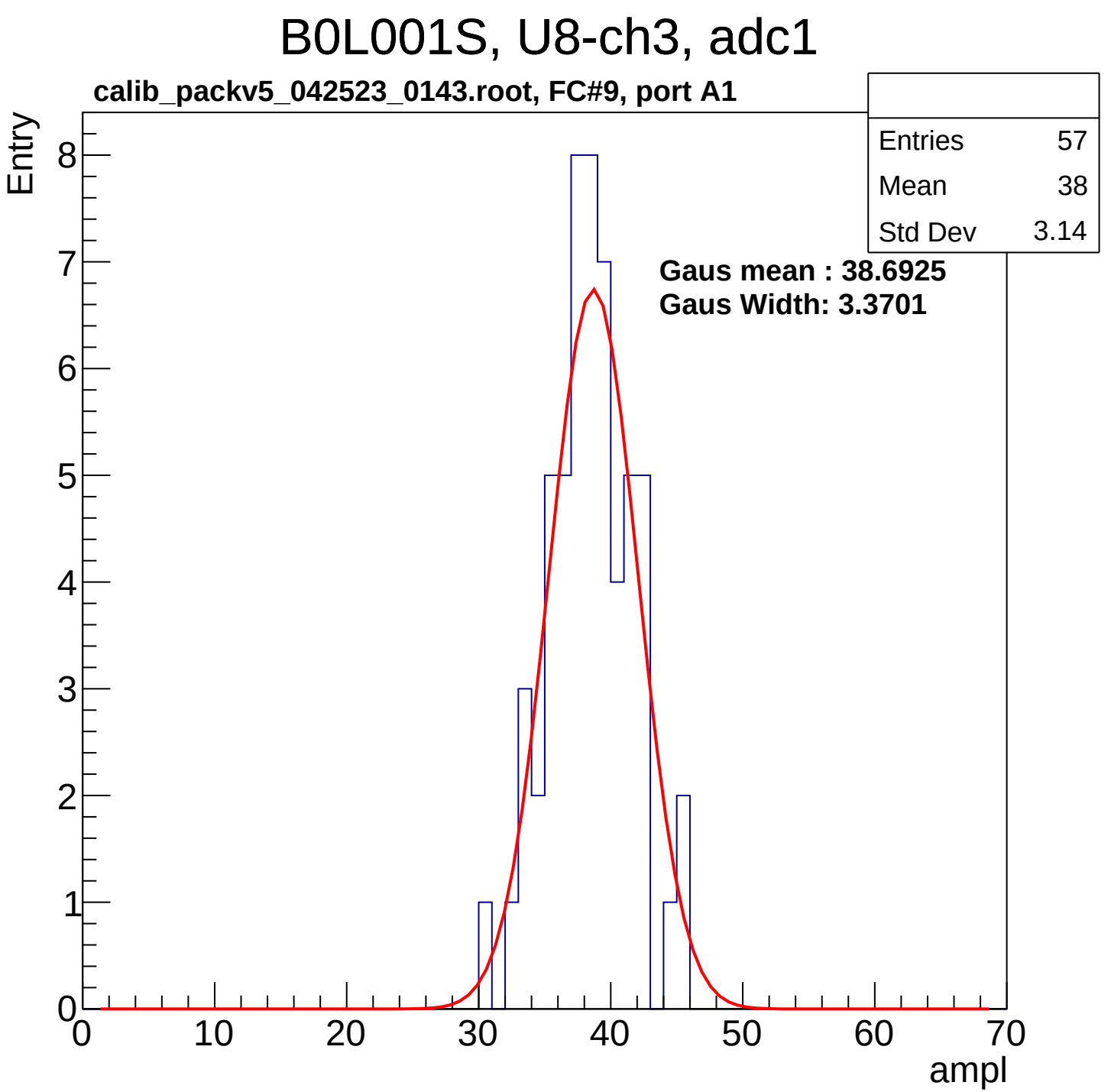
B0L001S, U8-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 38
Std Dev 3.14
Gaus mean : 38.6925
Gaus Width: 3.3701
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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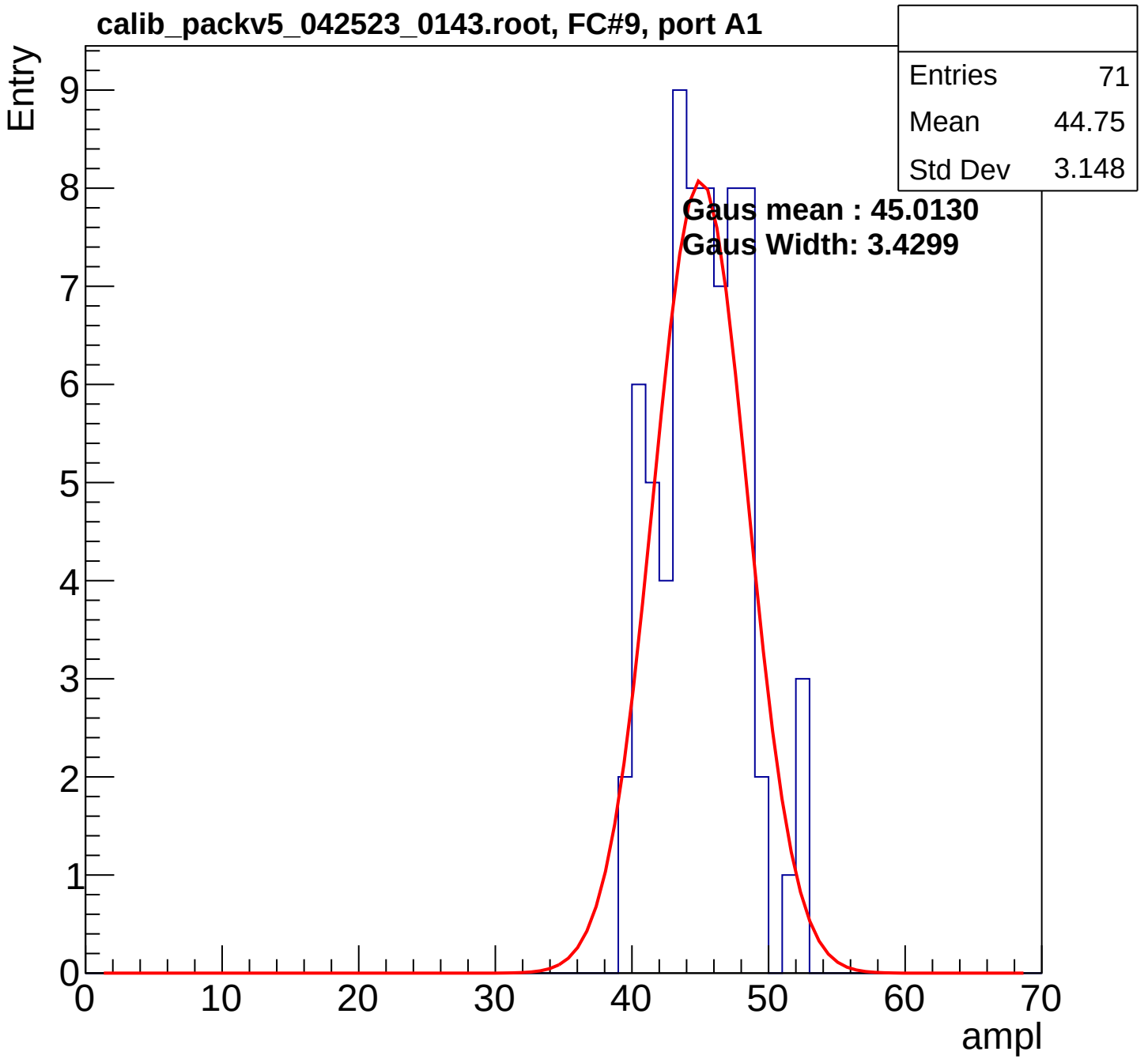
40
50
60
70


B0L001S, U8-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.75
Std Dev 3.148
Gaus mean : 45.0130
Gaus Width: 3.4299
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
50
60
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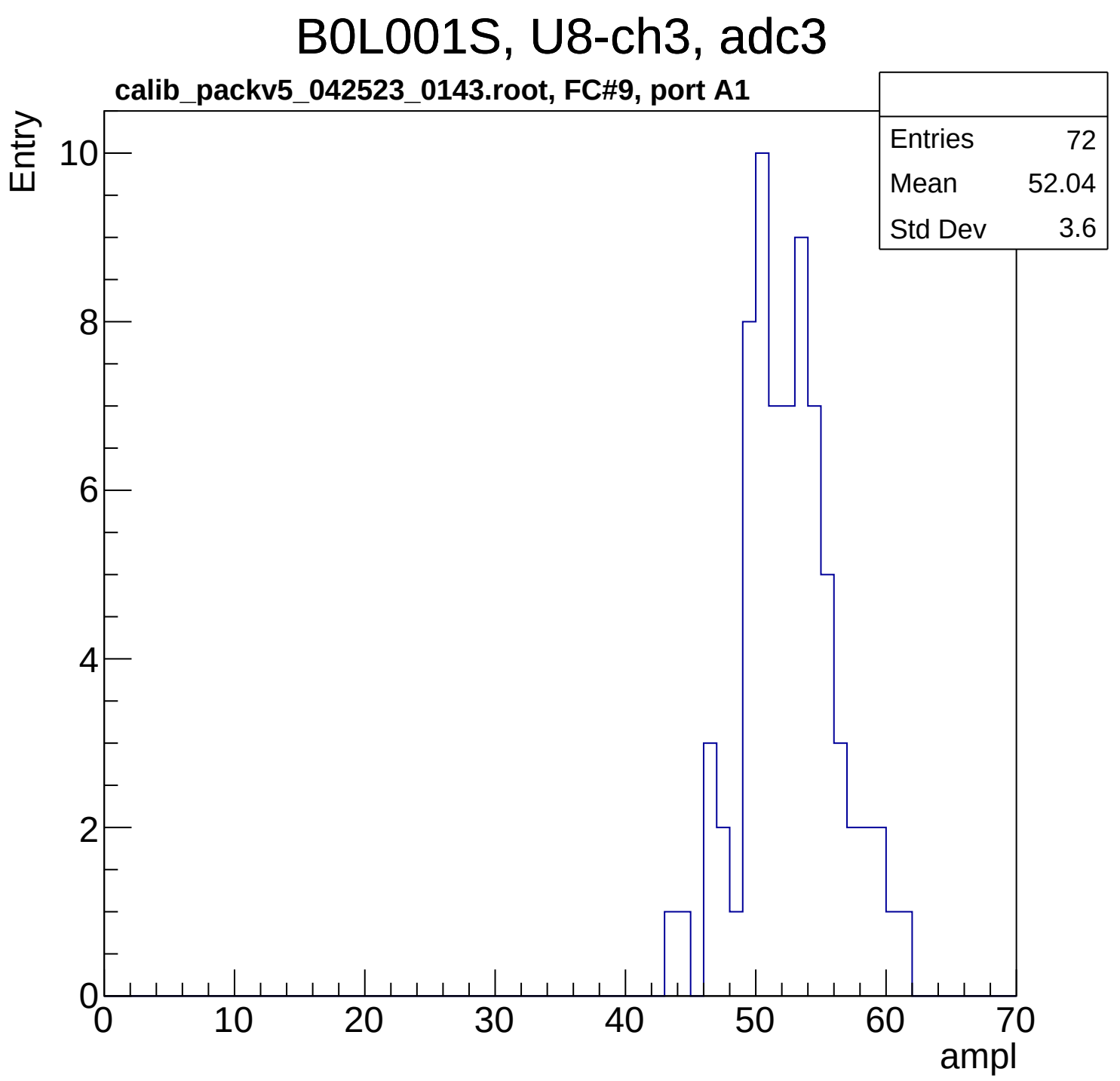
B0L001S, U8-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.04
Std Dev 3.6
Entry
ampl
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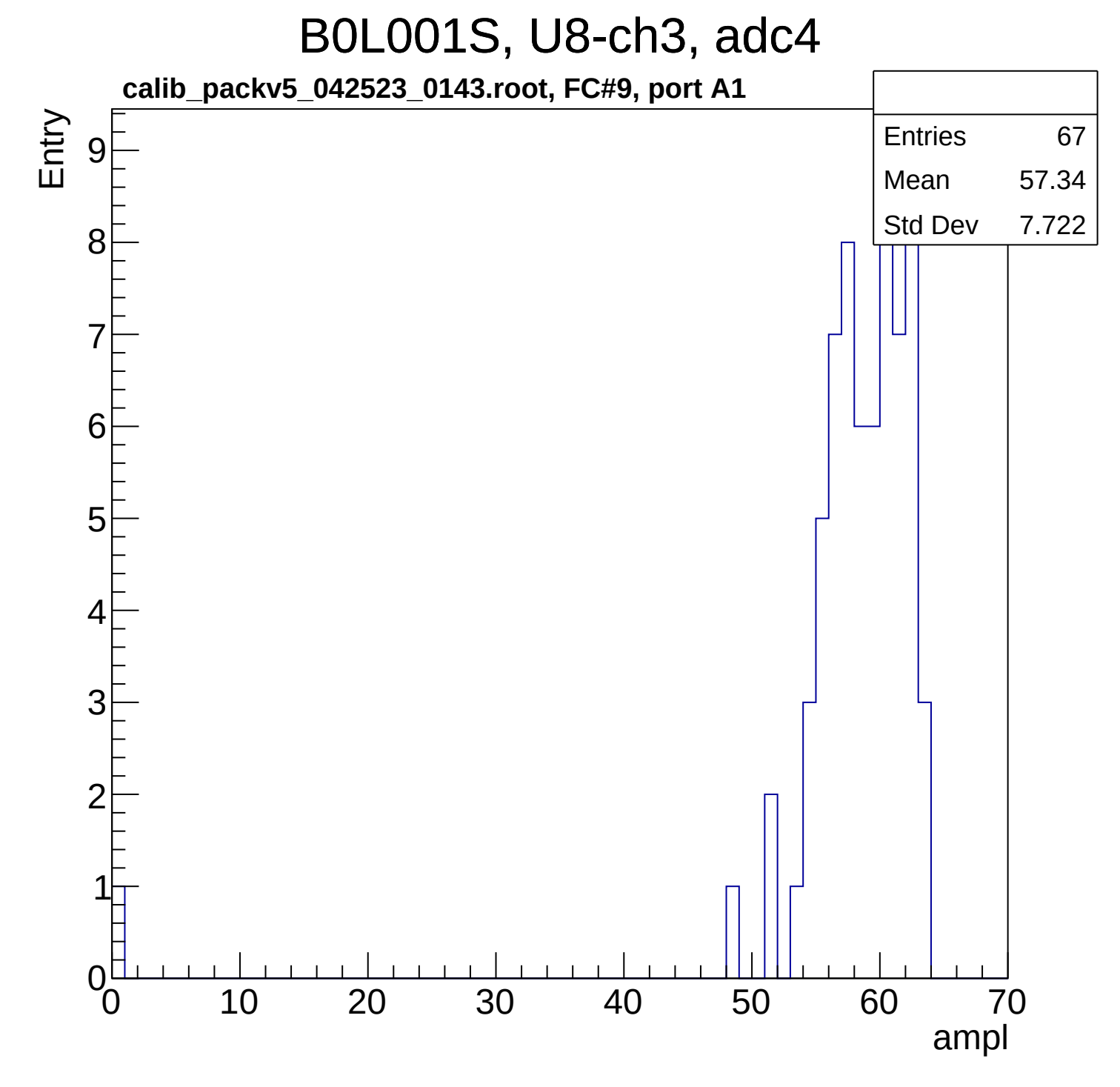
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57.34
Std Dev 7.722
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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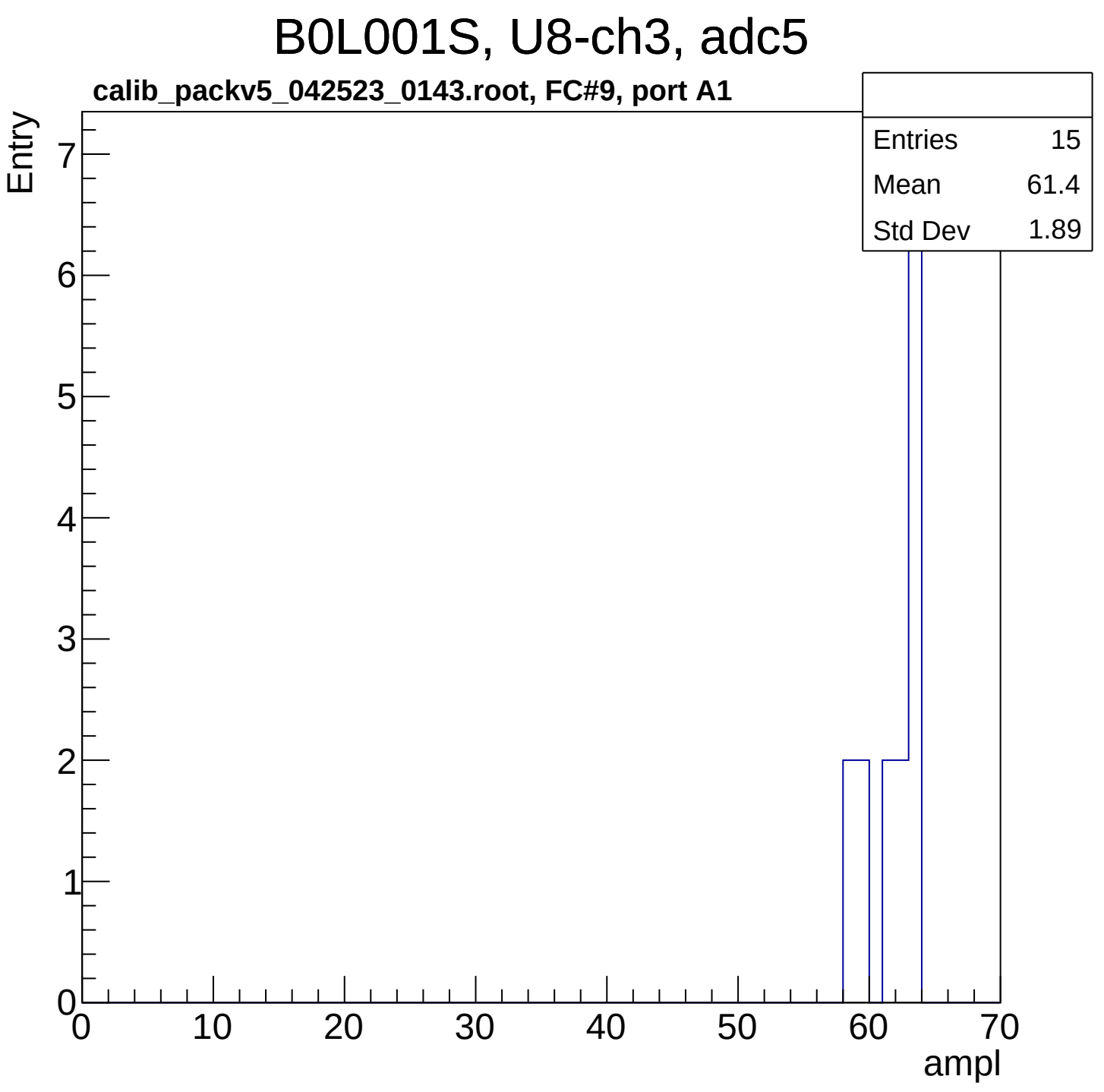
B0L001S, U8-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 61.4
Std Dev 1.89
Entry
ampl
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2
3
4
5
6
7
0
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40
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60
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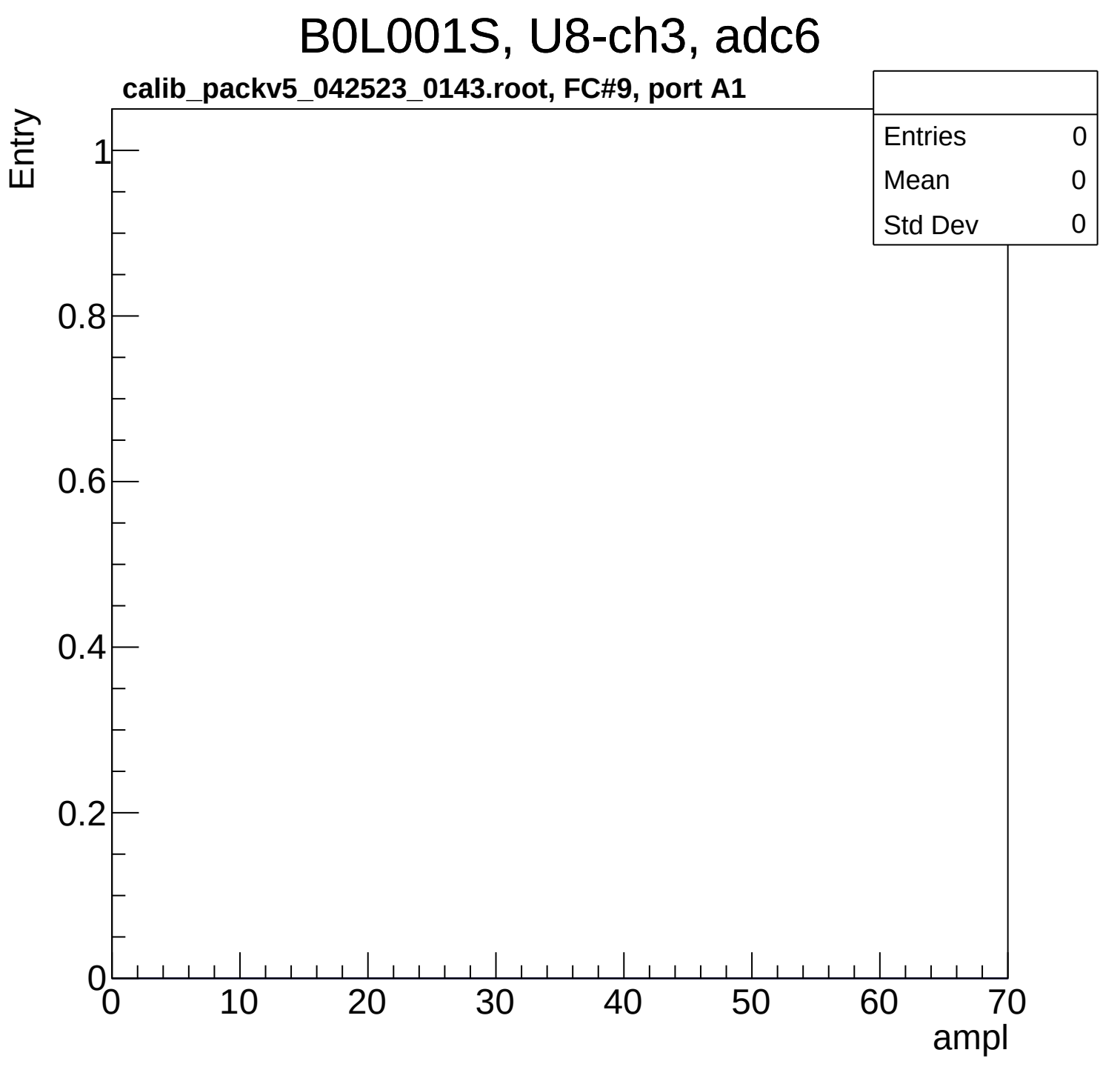
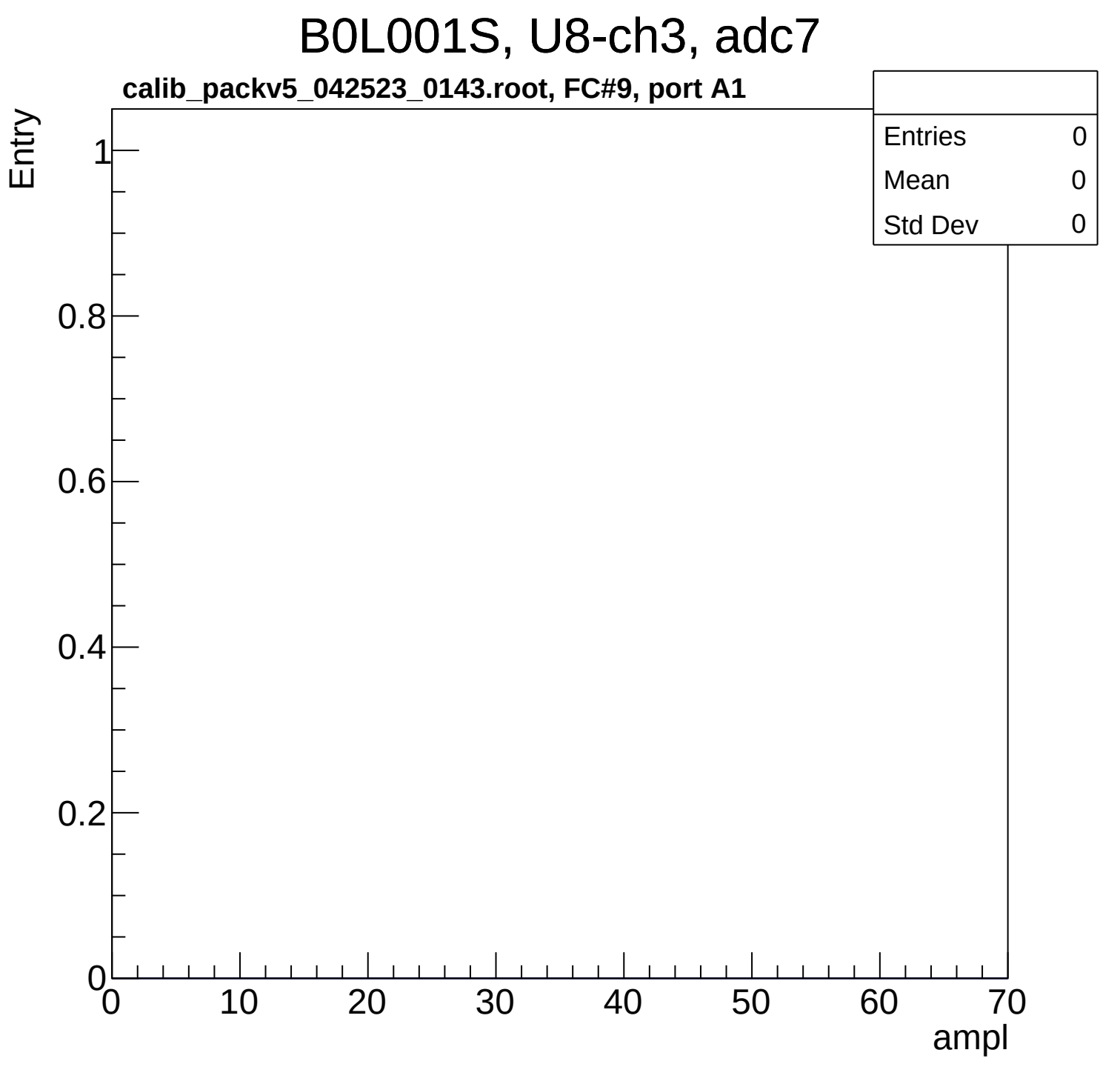
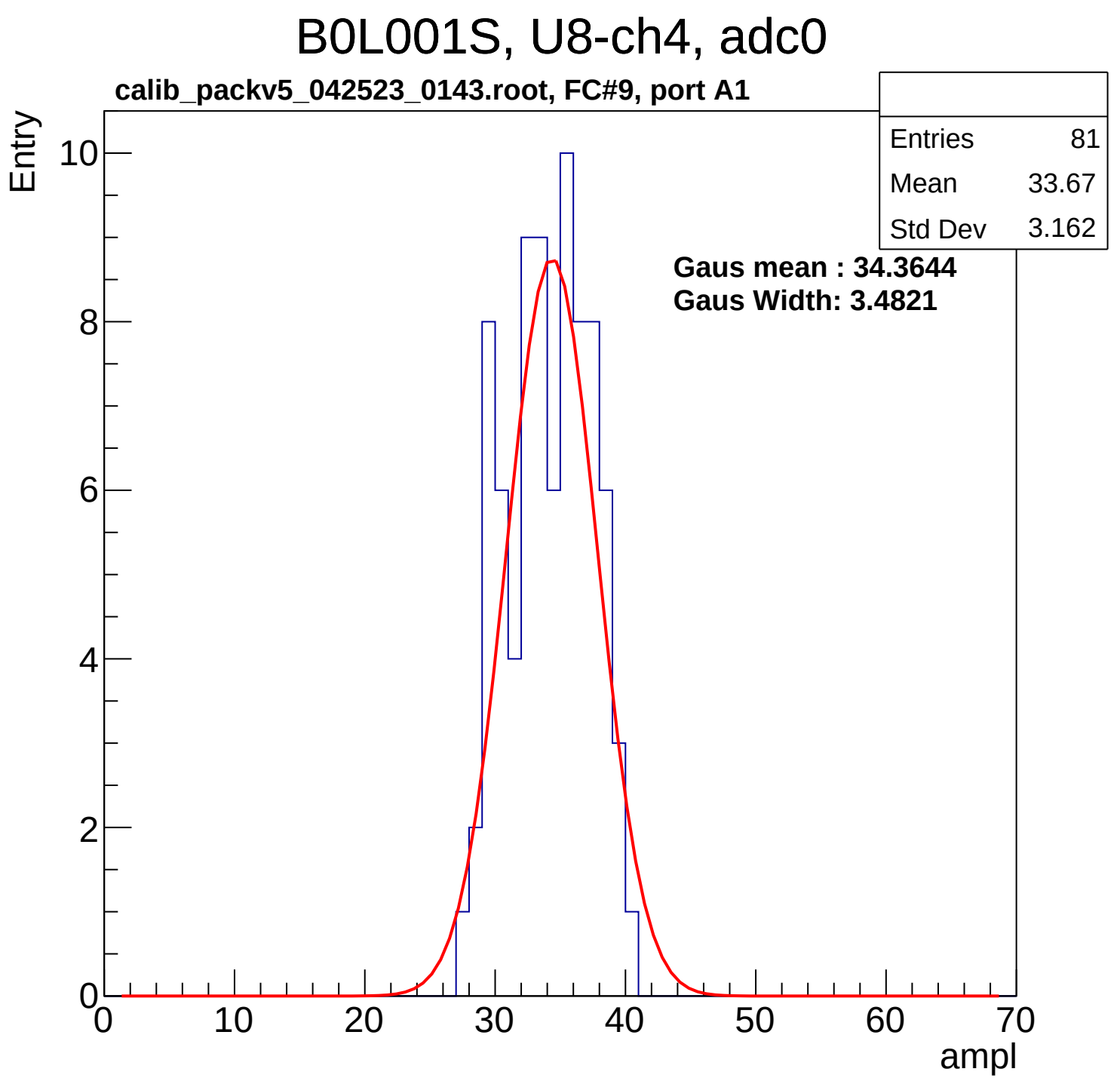
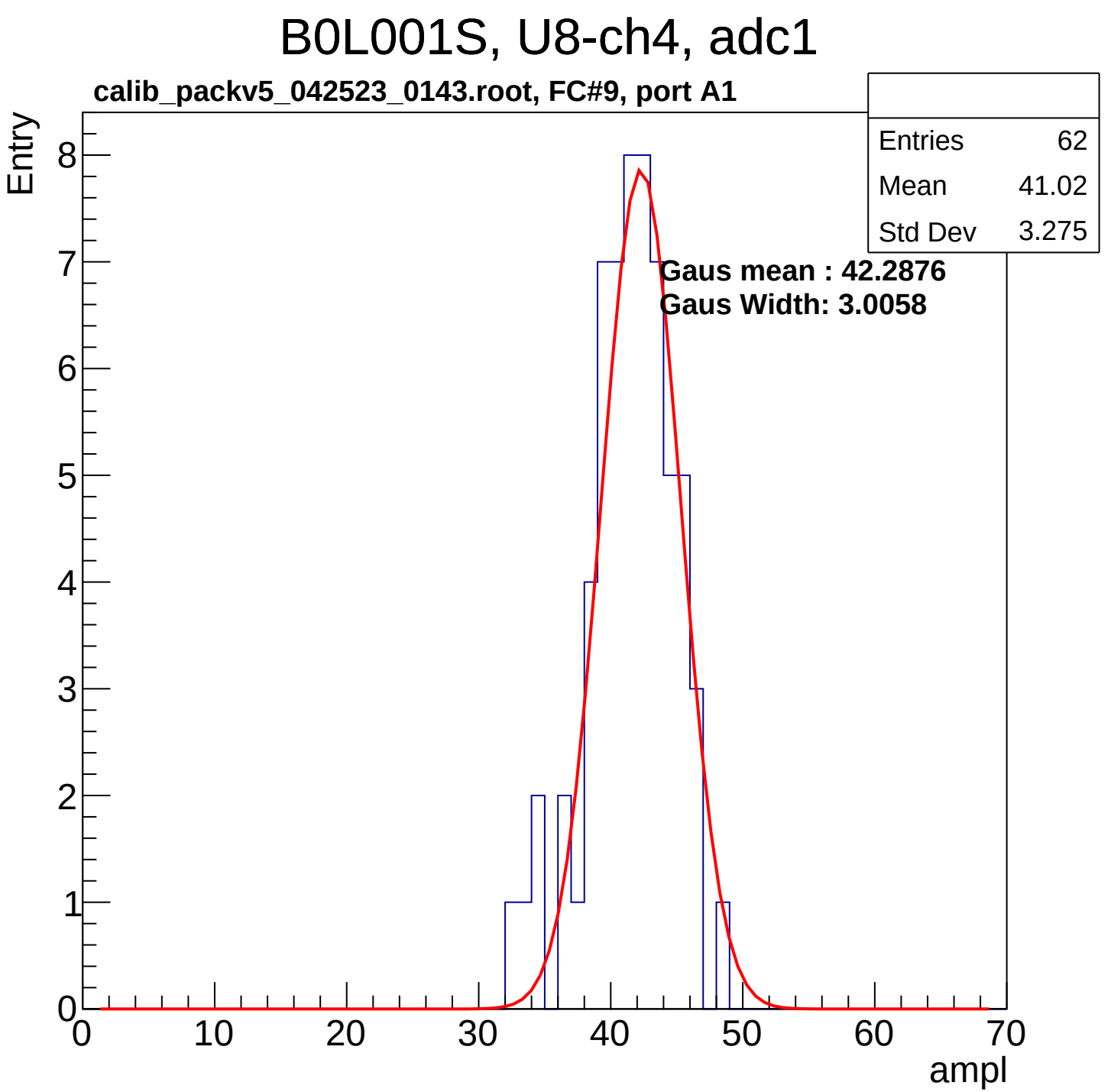

B0L001S, U8-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 33.67
Std Dev 3.162
Gaus mean : 34.3644
Gaus Width: 3.4821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 41.02
Std Dev 3.275
Gaus mean : 42.2876
Gaus Width: 3.0058
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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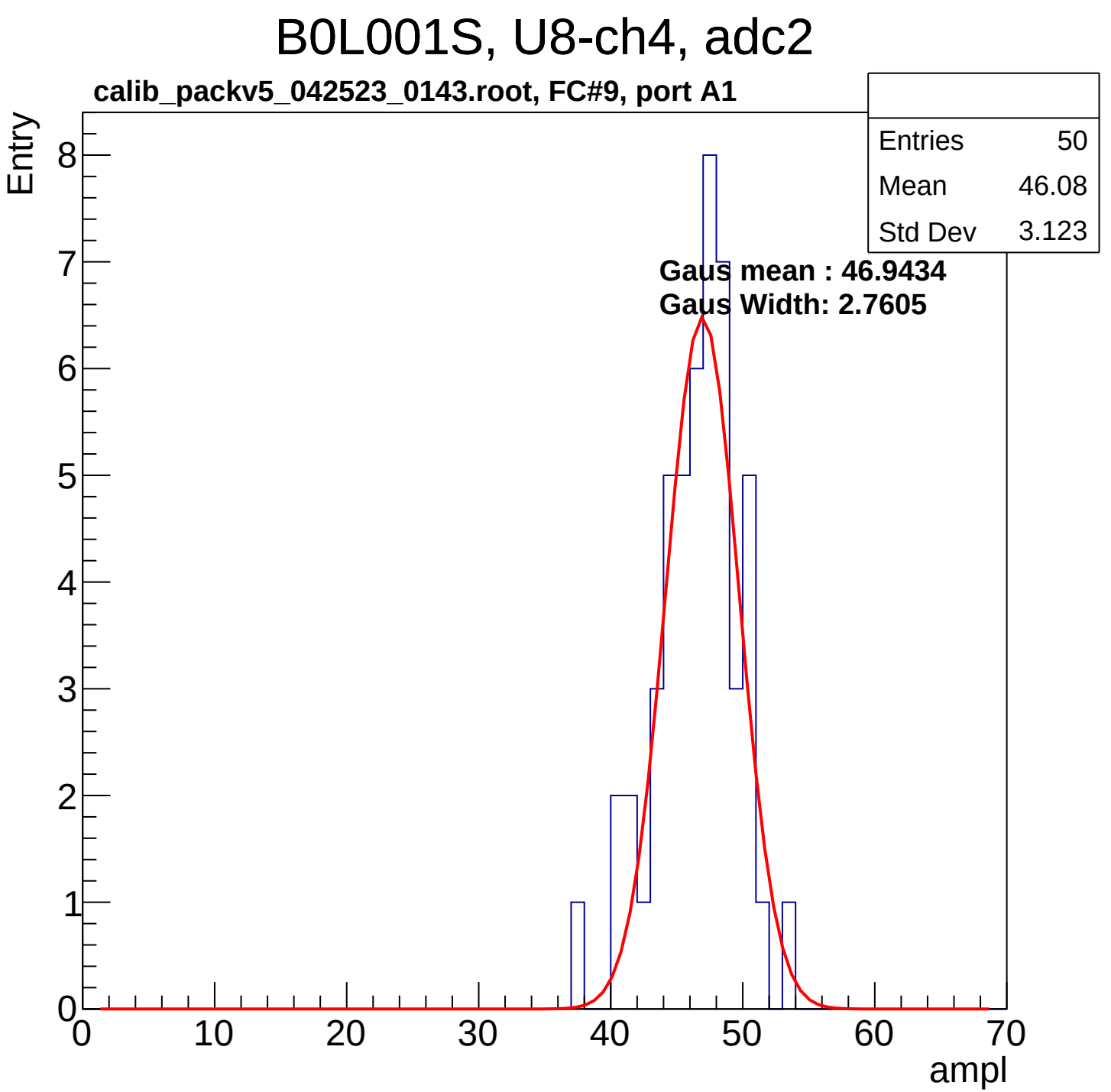
B0L001S, U8-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 46.08
Std Dev 3.123
Gaus mean : 46.9434
Gaus Width: 2.7605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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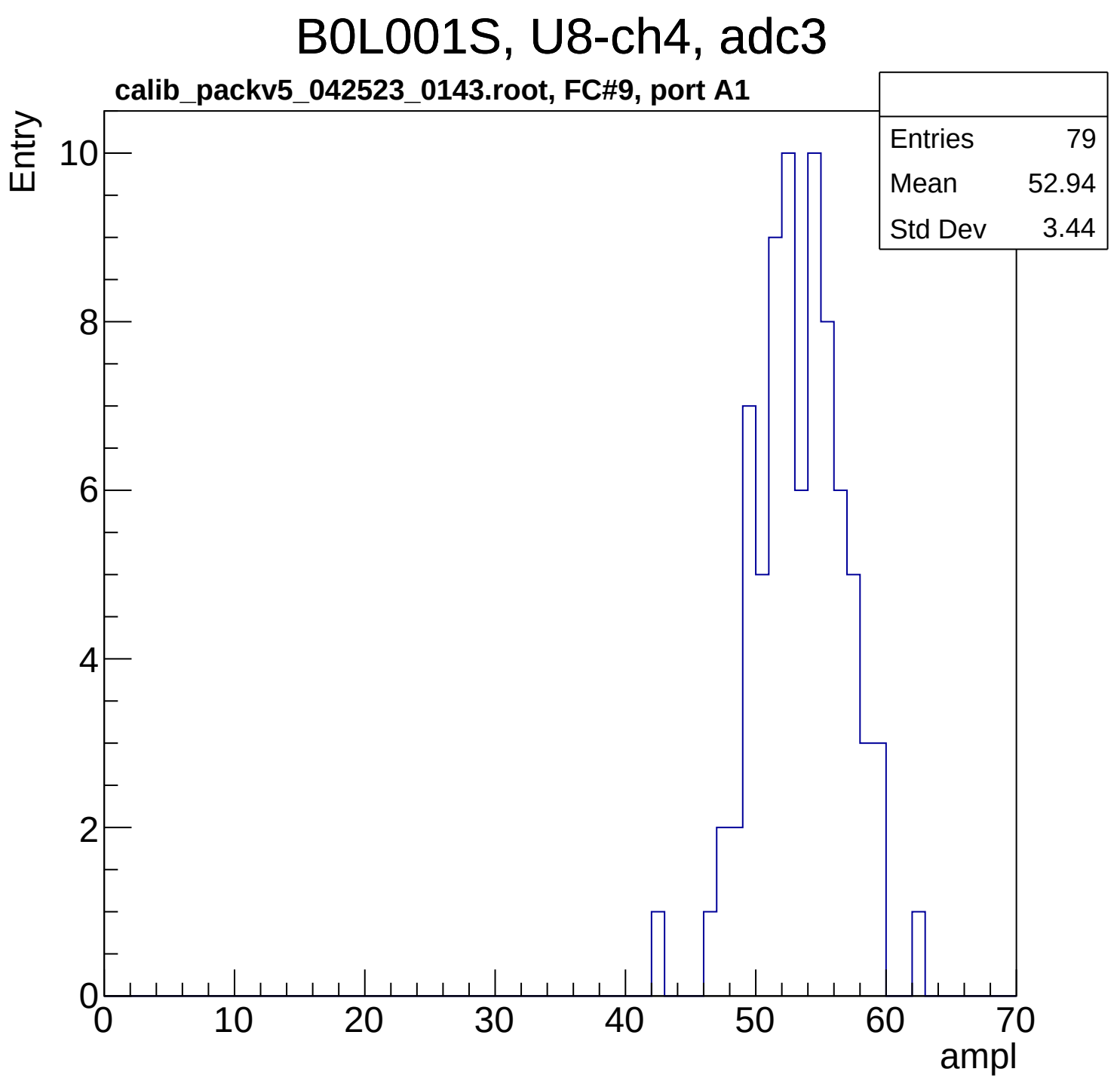
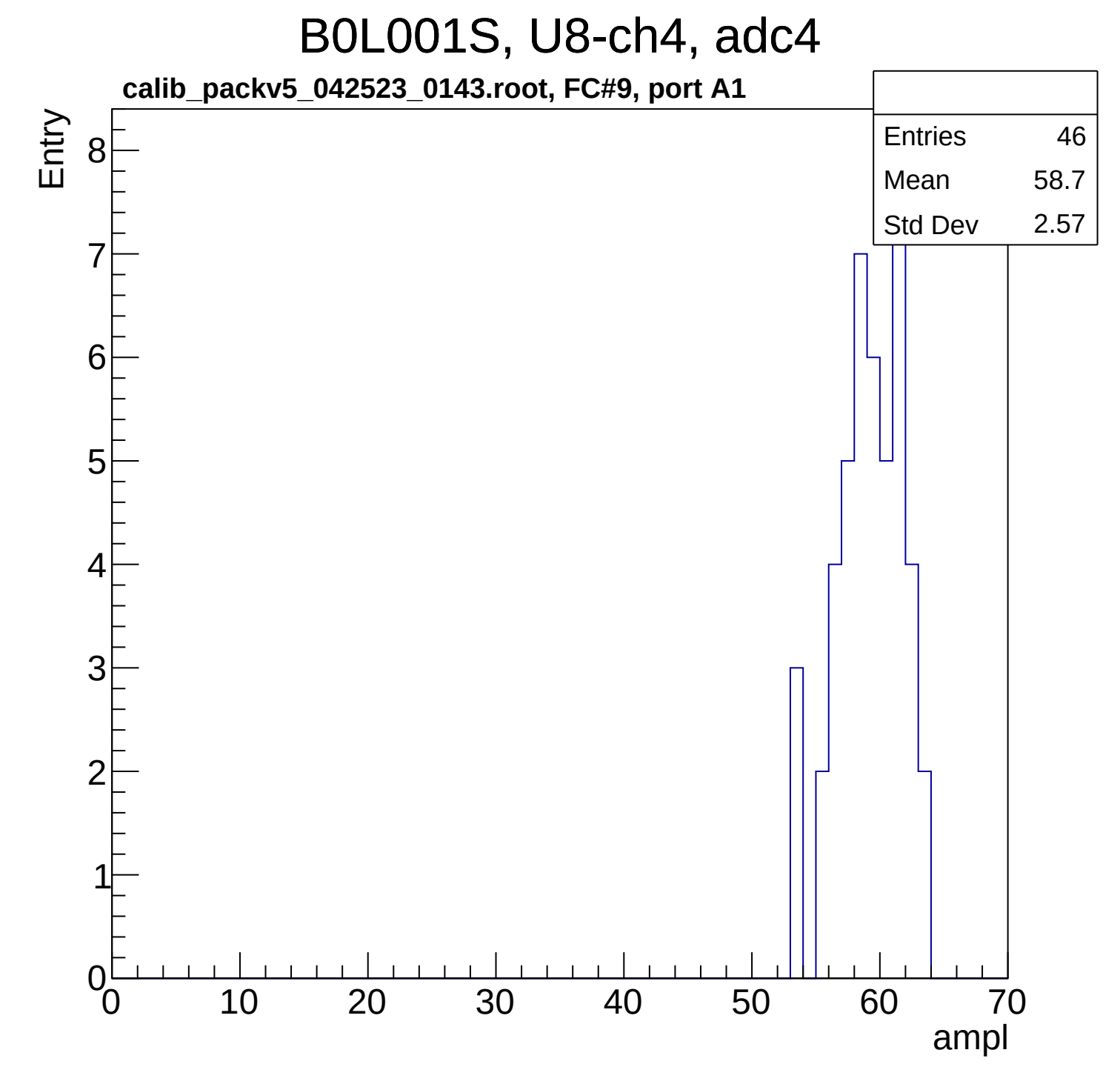

B0L001S, U8-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 52.94
Std Dev 3.44
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.7
Std Dev 2.57
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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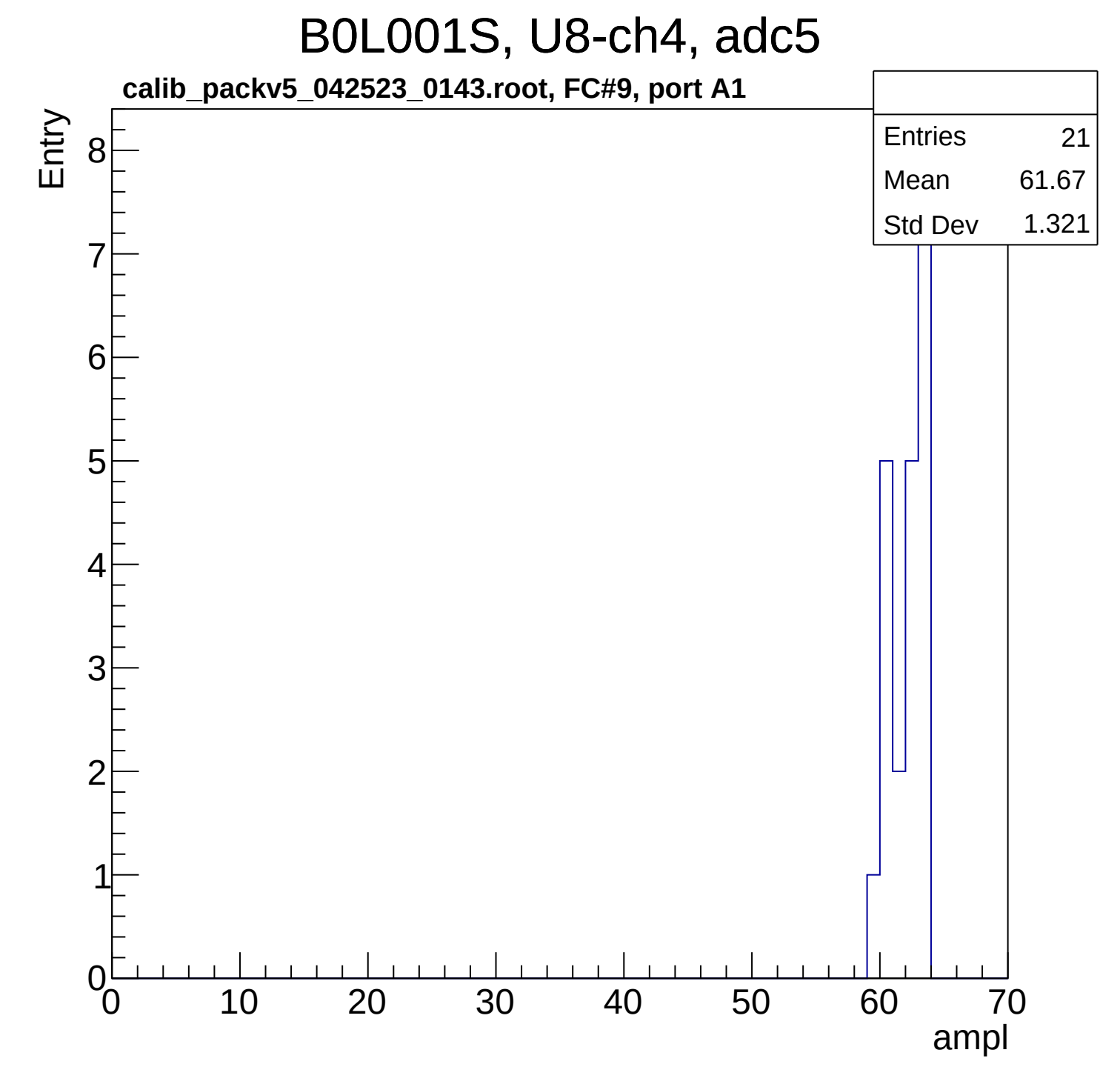
B0L001S, U8-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 61.67
Std Dev 1.321
Entry
ampl
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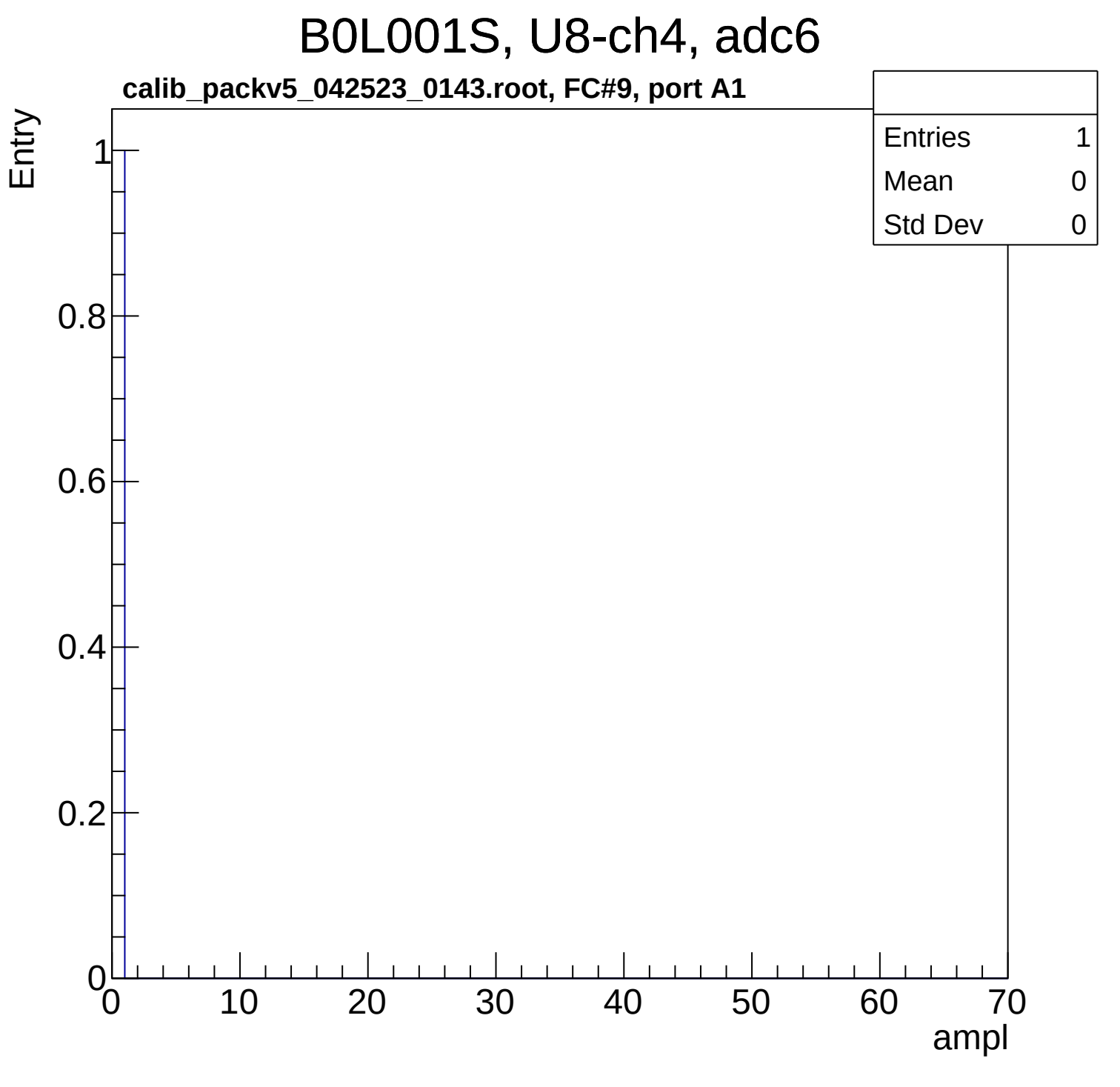
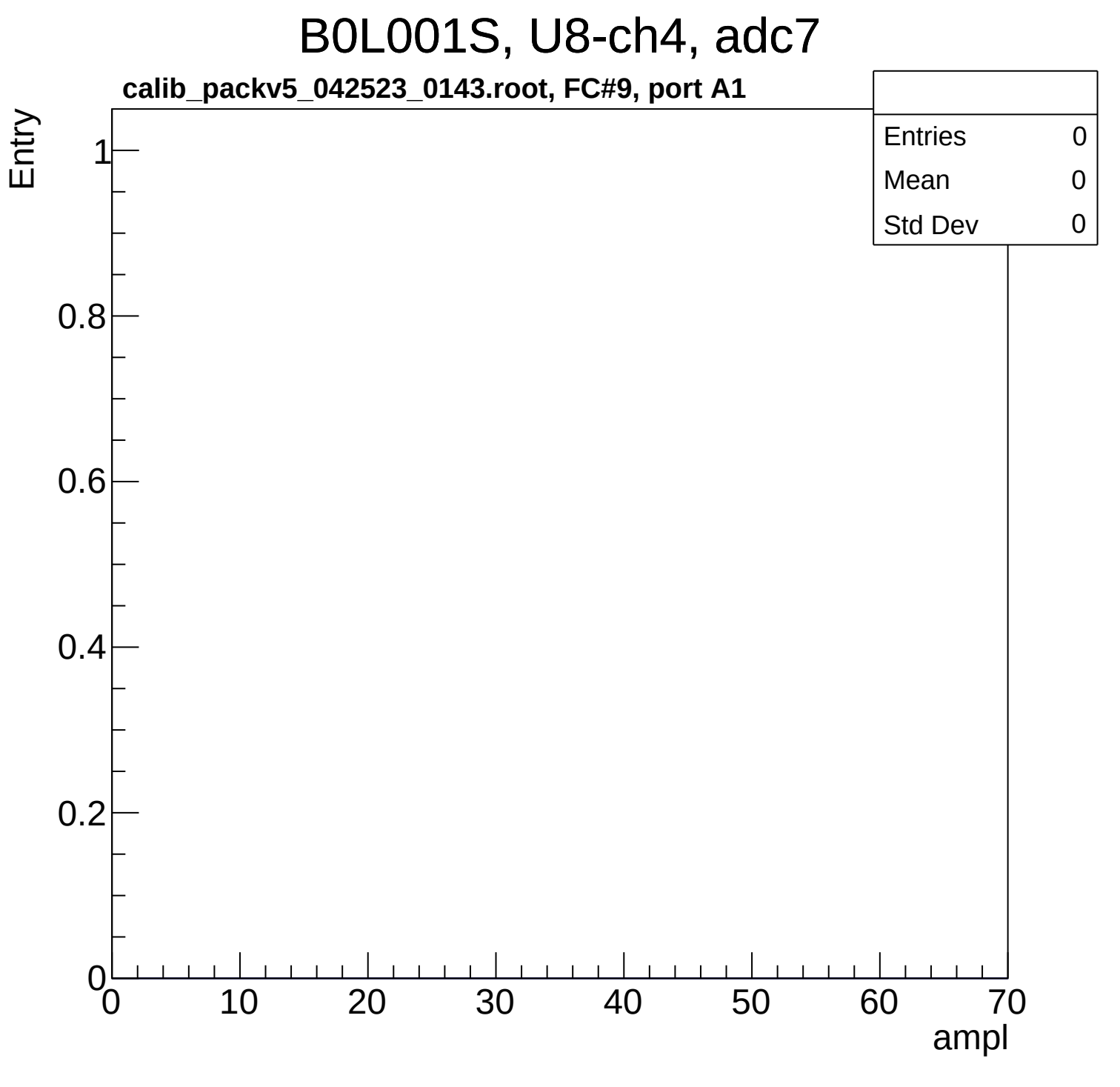
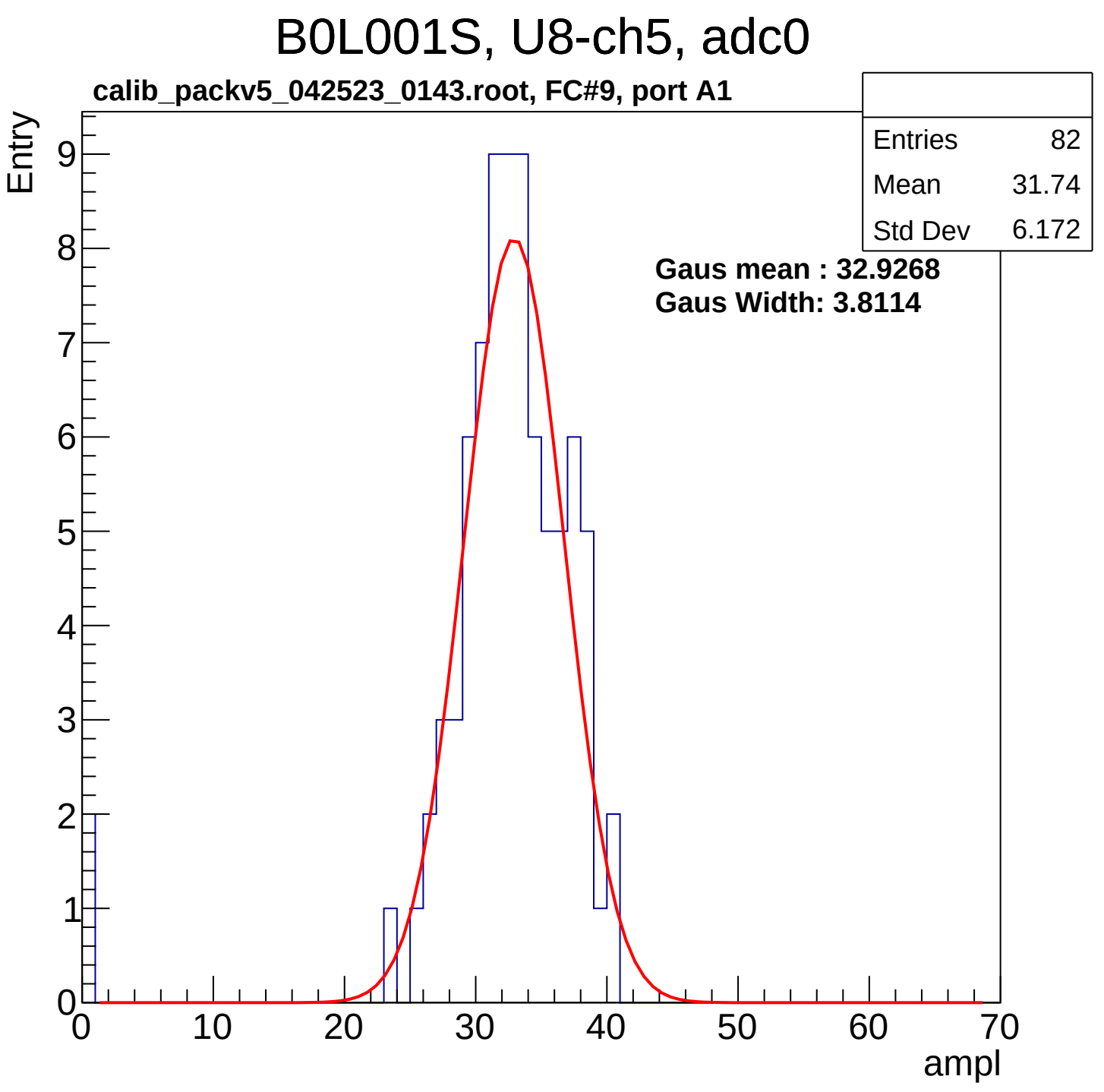
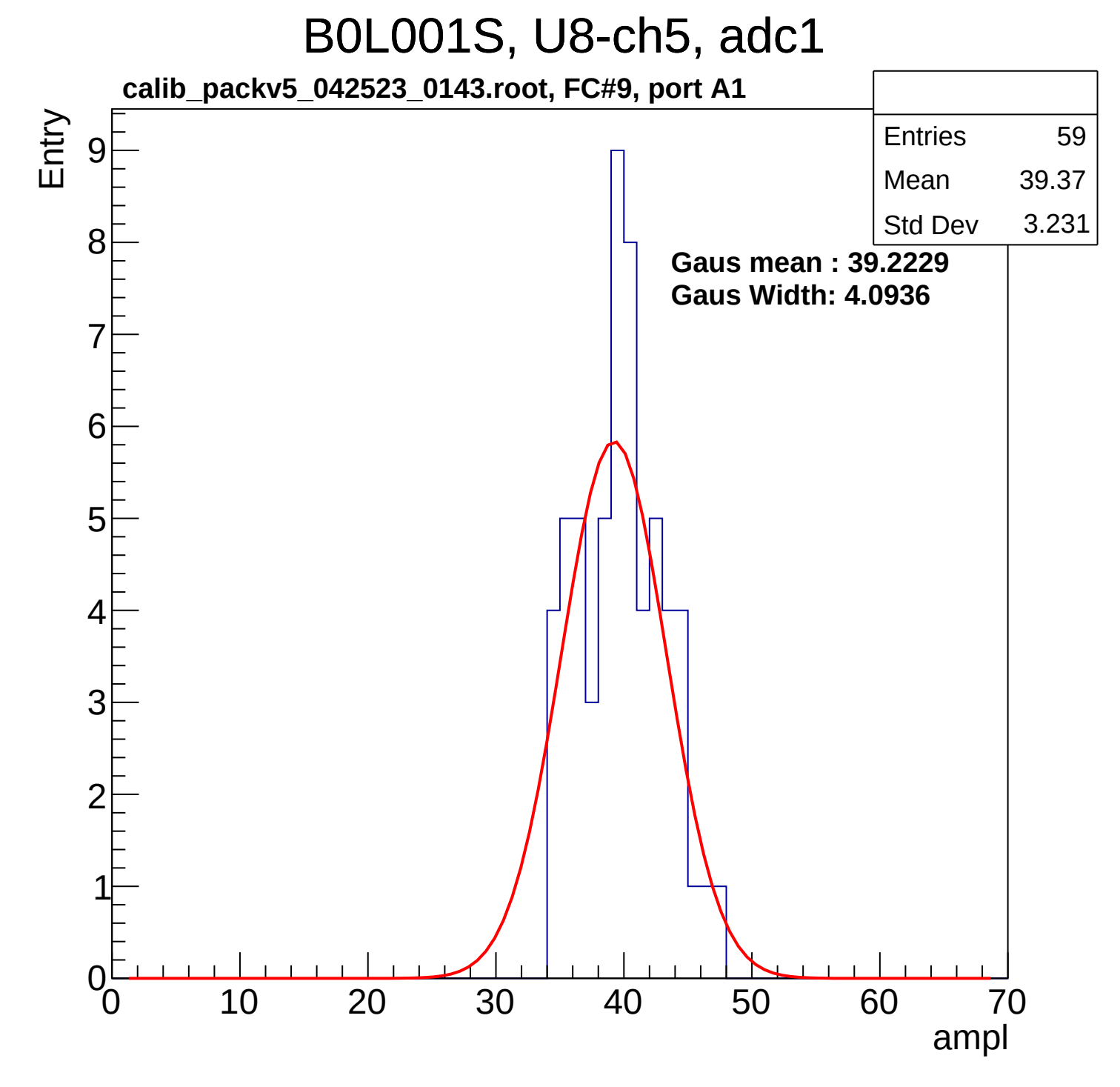
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 31.74
Std Dev 6.172
Gaus mean : 32.9268
Gaus Width: 3.8114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 39.37
Std Dev 3.231
Gaus mean : 39.2229
Gaus Width: 4.0936
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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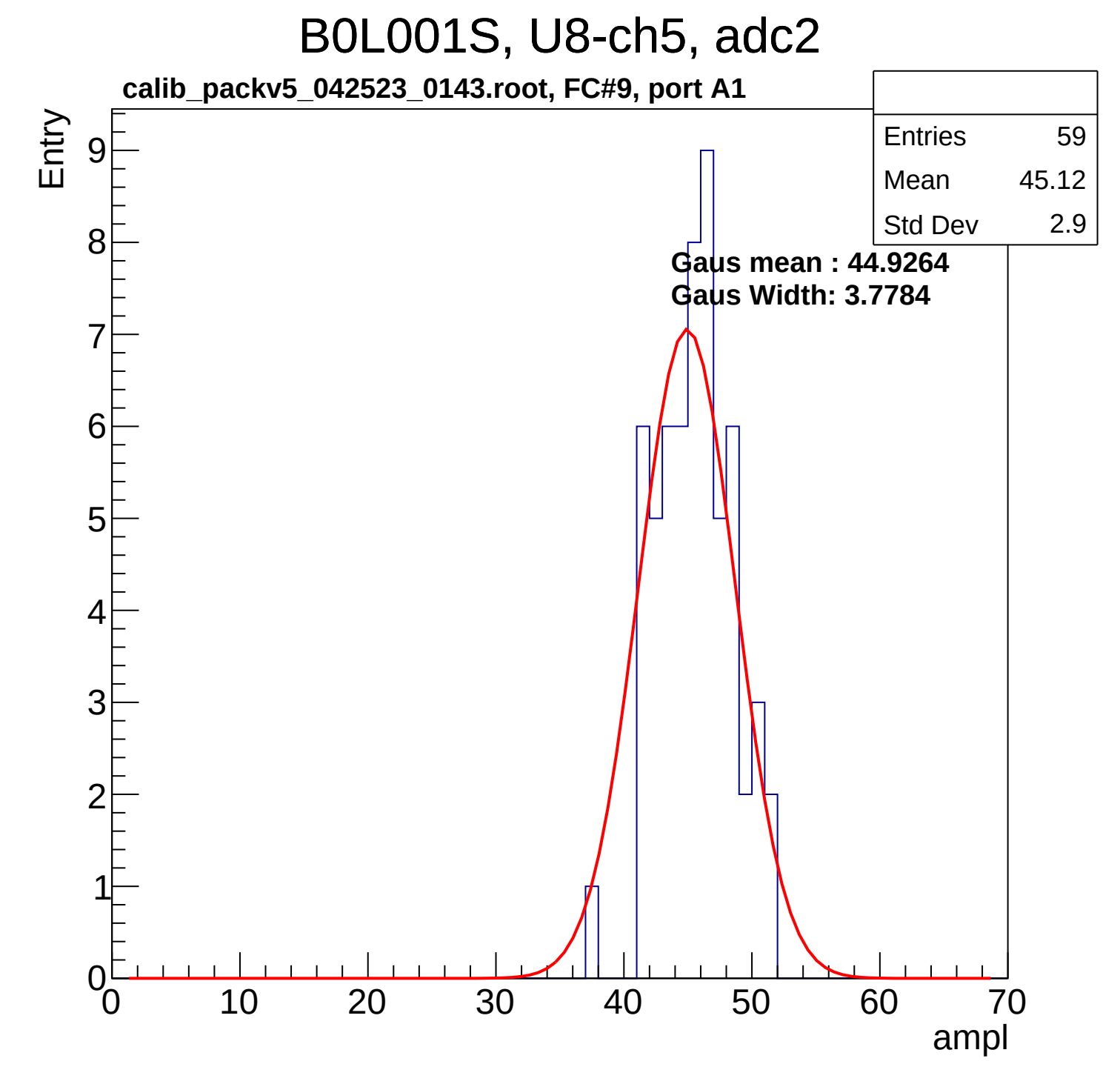
B0L001S, U8-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.12
Std Dev 2.9
Gaus mean : 44.9264
Gaus Width: 3.7784
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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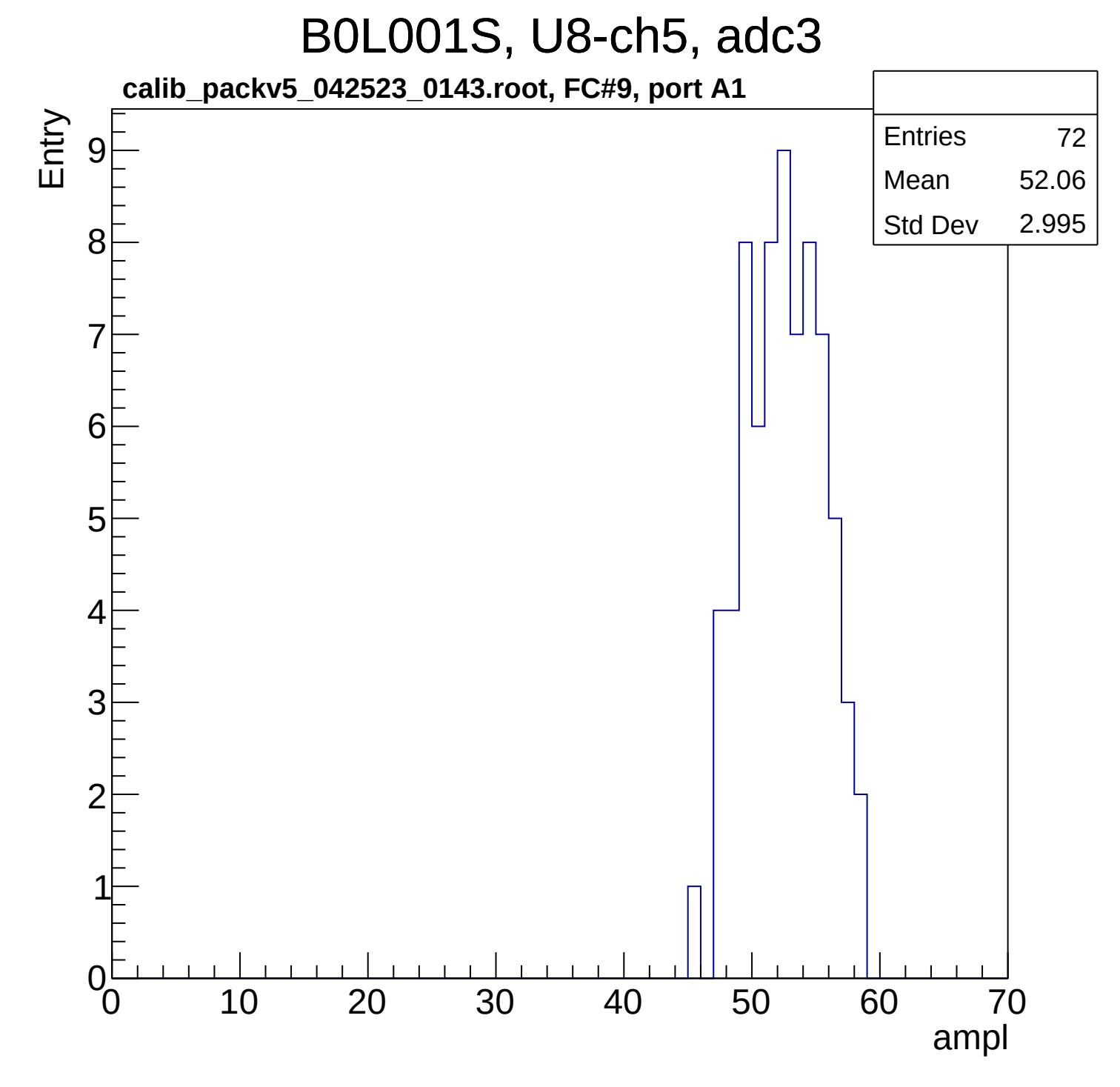
B0L001S, U8-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.06
Std Dev 2.995
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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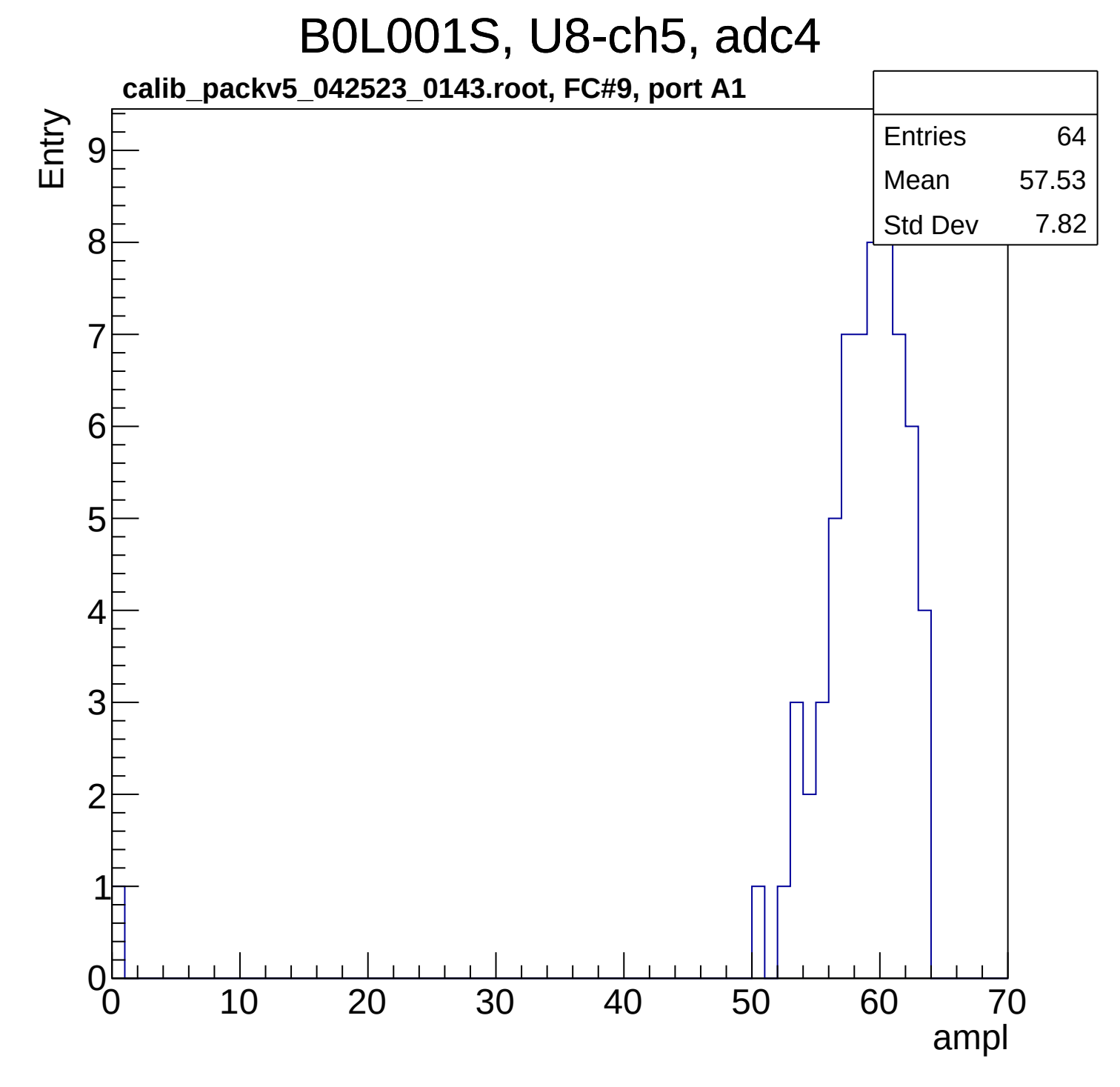
B0L001S, U8-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.53
Std Dev 7.82
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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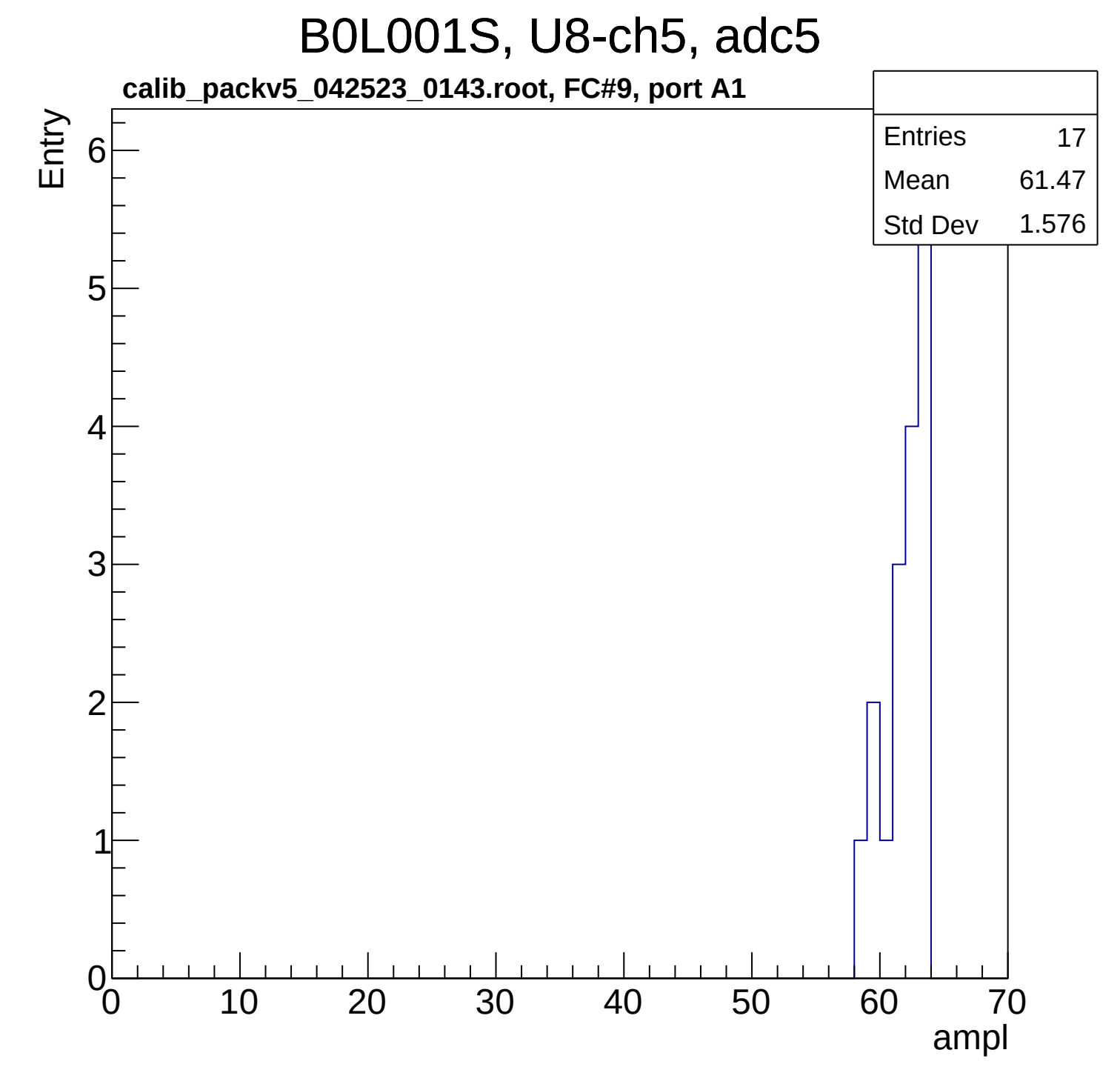
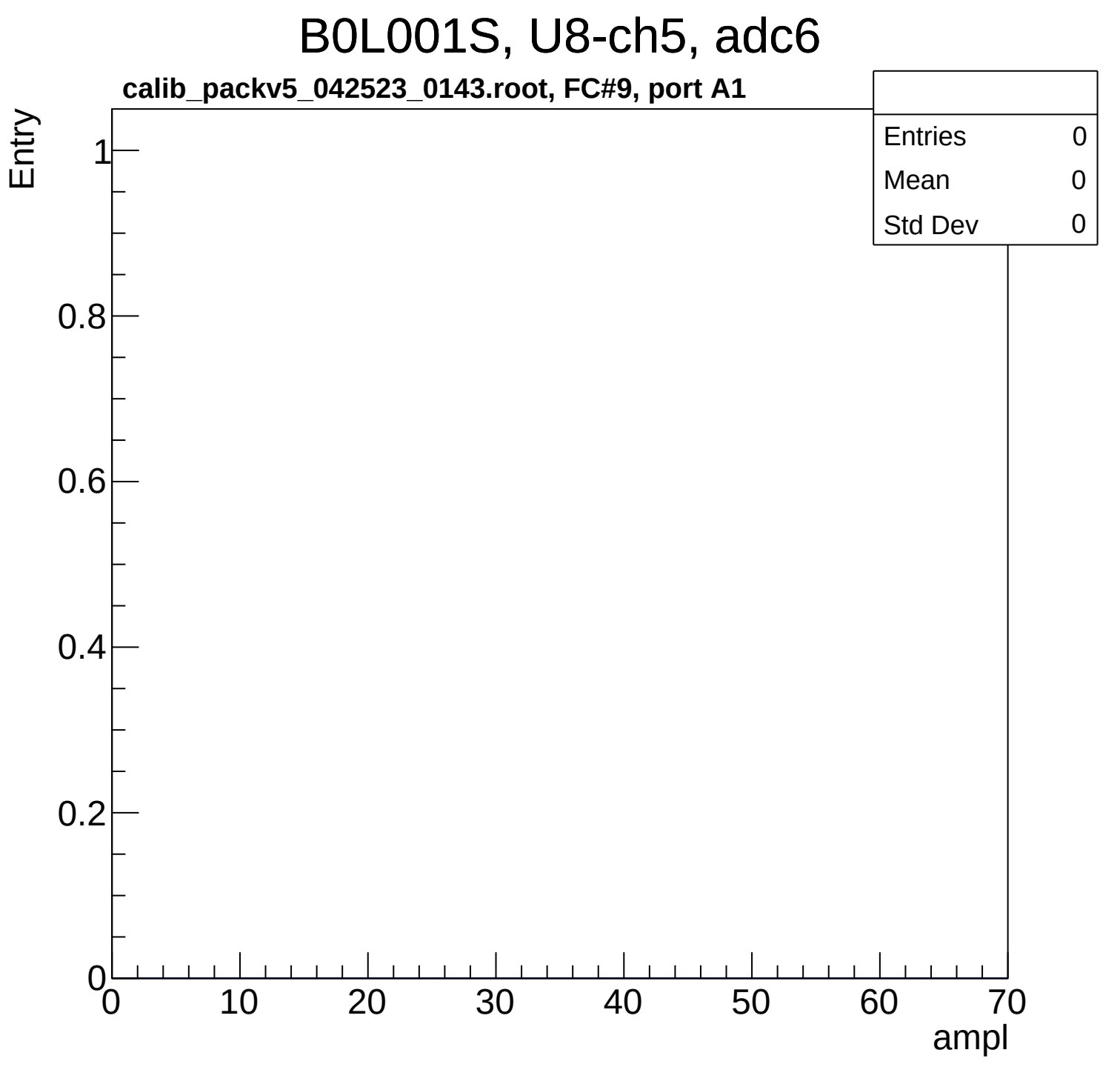
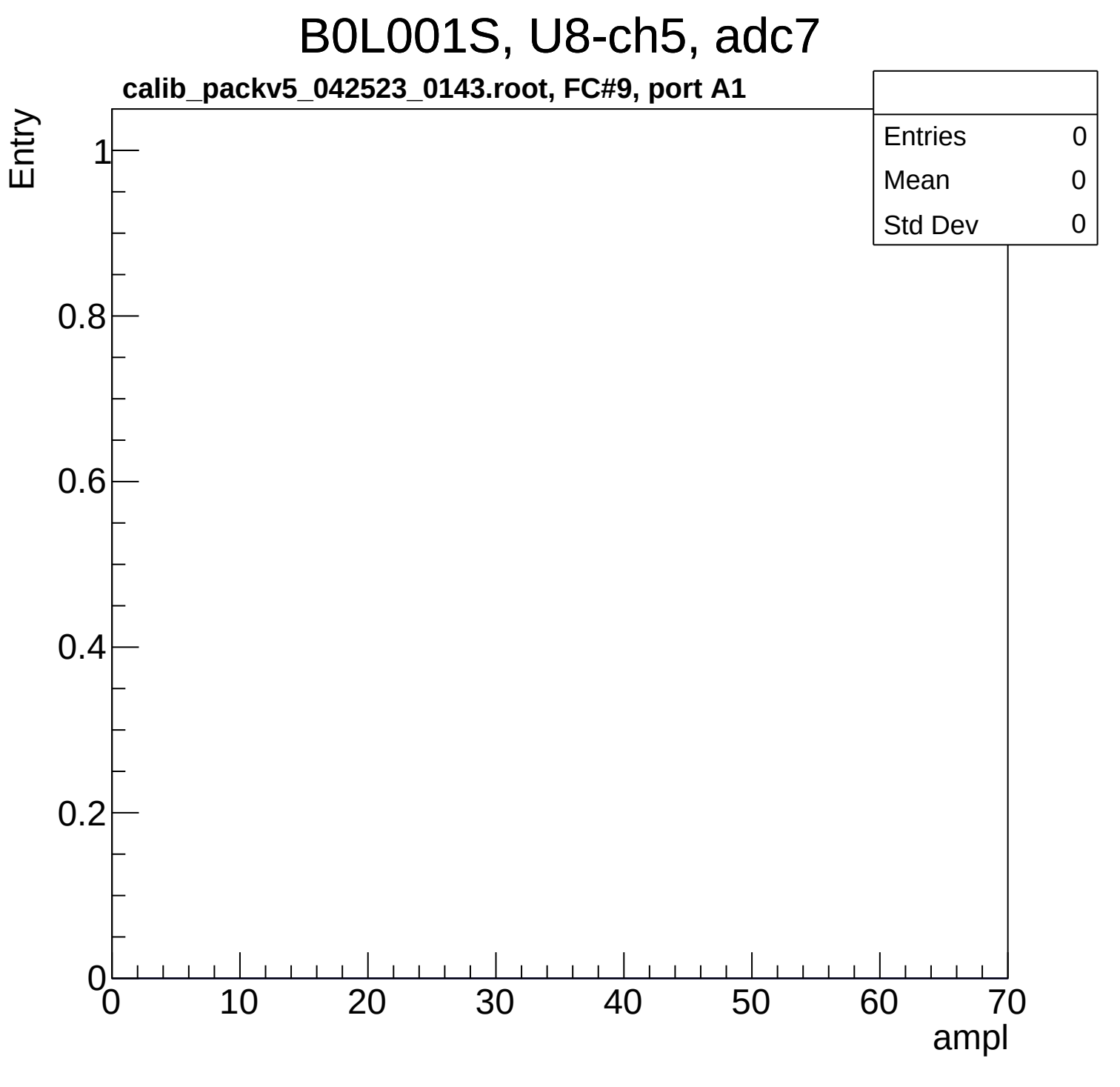

B0L001S, U8-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 61.47
Std Dev 1.576
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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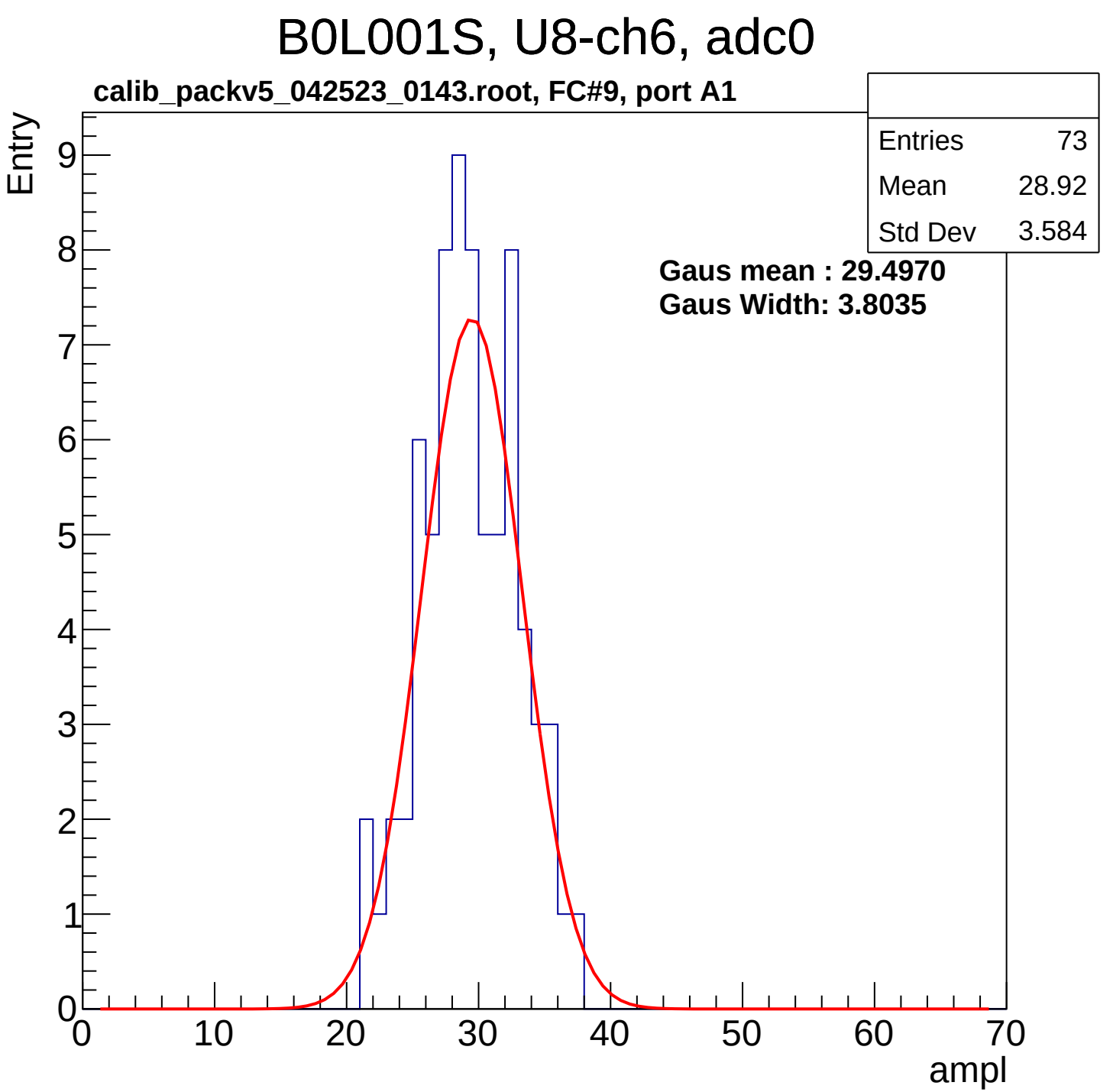
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 28.92
Std Dev 3.584
Gaus mean : 29.4970
Gaus Width: 3.8035
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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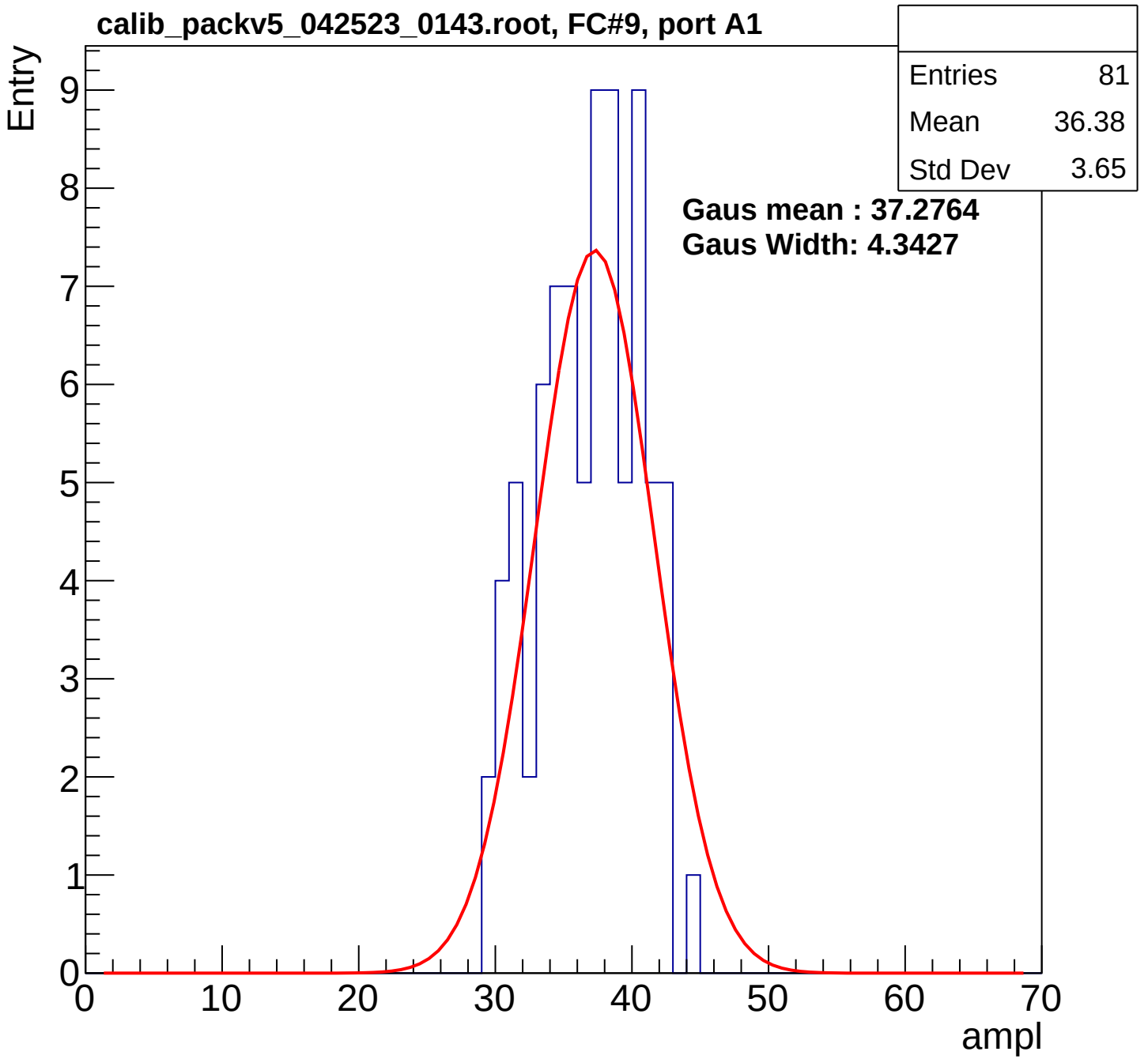
40
50
60
70


B0L001S, U8-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 36.38
Std Dev 3.65
Gaus mean : 37.2764
Gaus Width: 4.3427
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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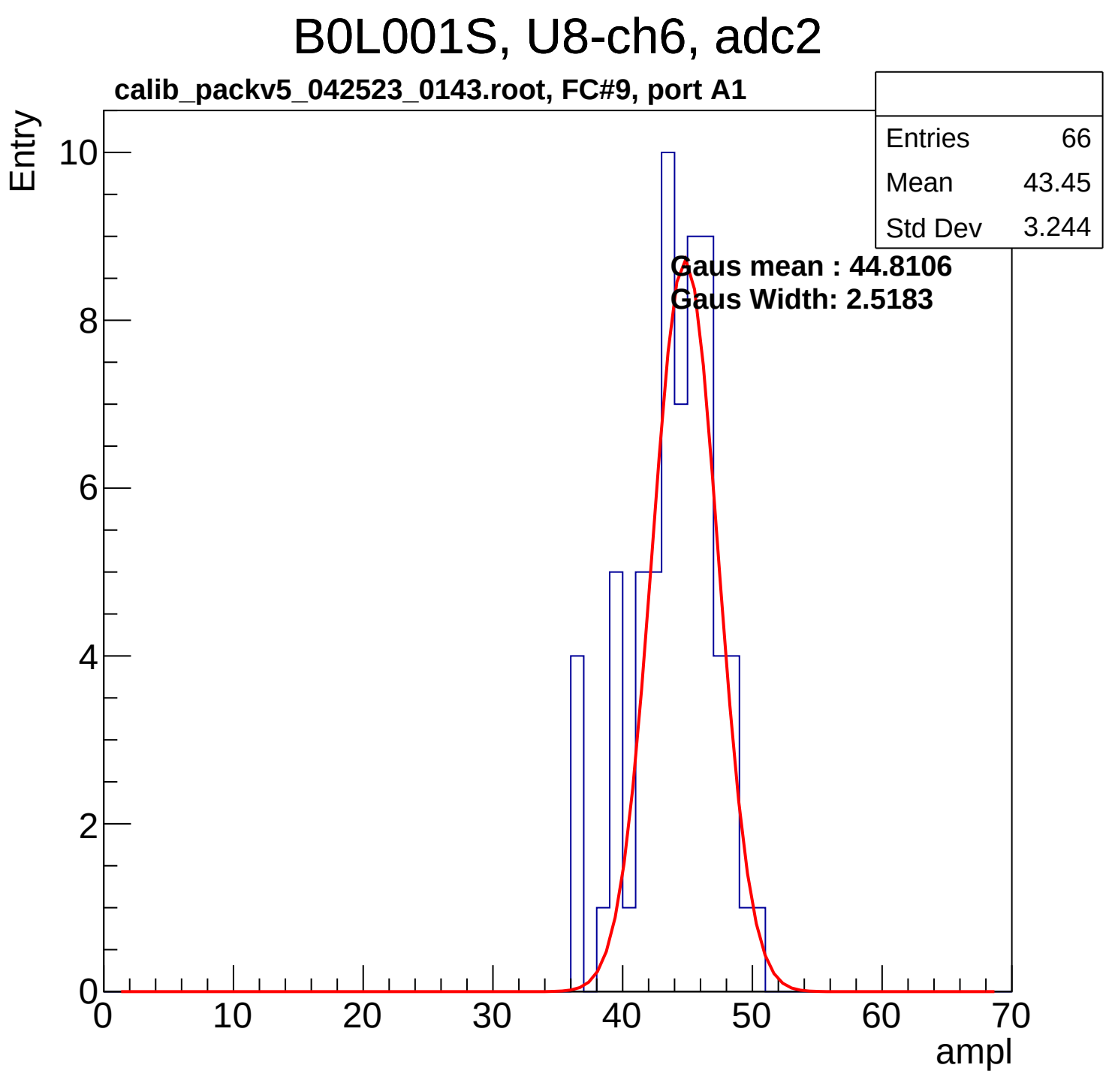
B0L001S, U8-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.45
Std Dev 3.244
Gaus mean : 44.8106
Gaus Width: 2.5183
Entry
ampl
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2
4
6
8
10
0
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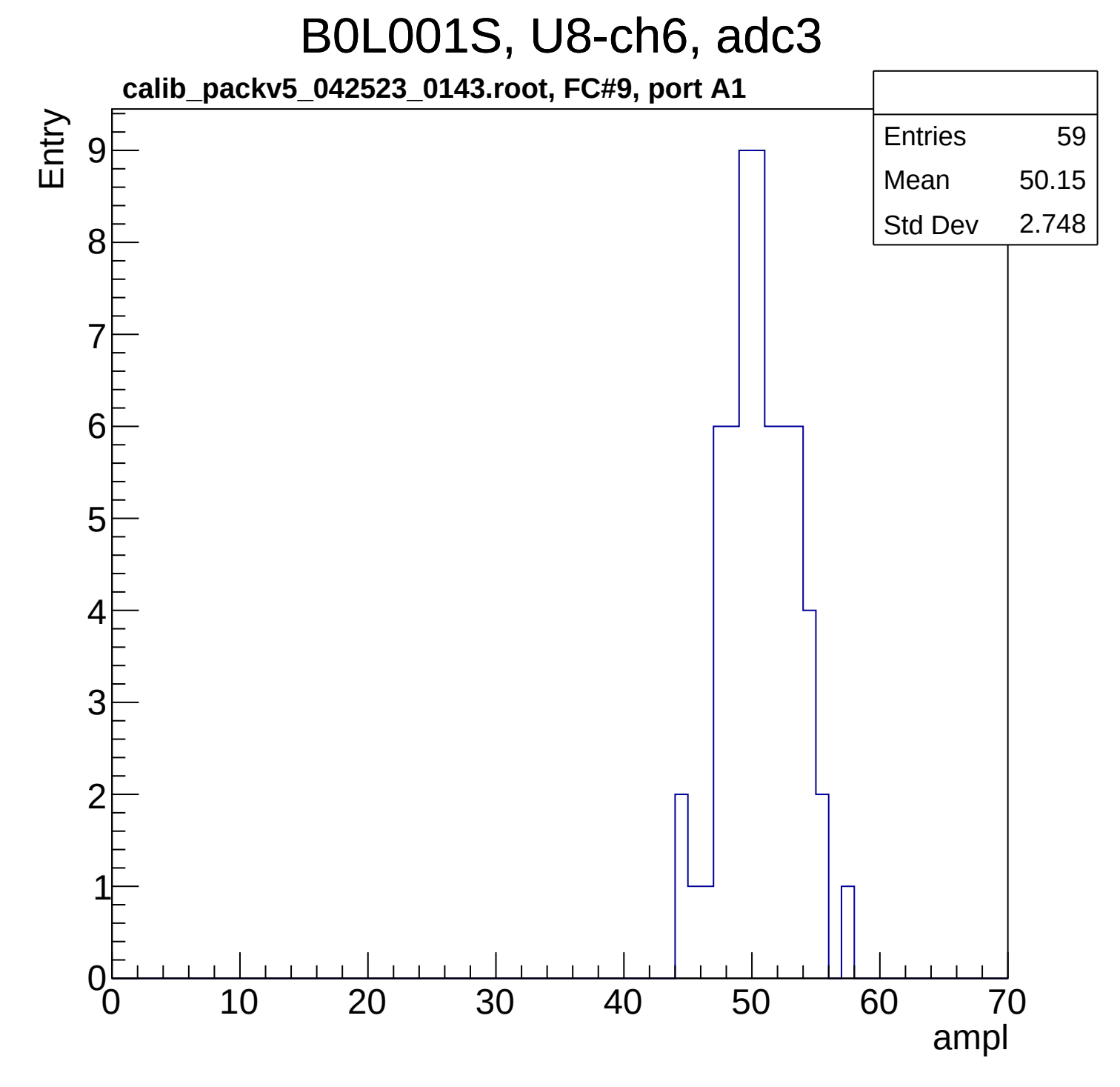
40
50
60
70


B0L001S, U8-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 50.15
Std Dev 2.748
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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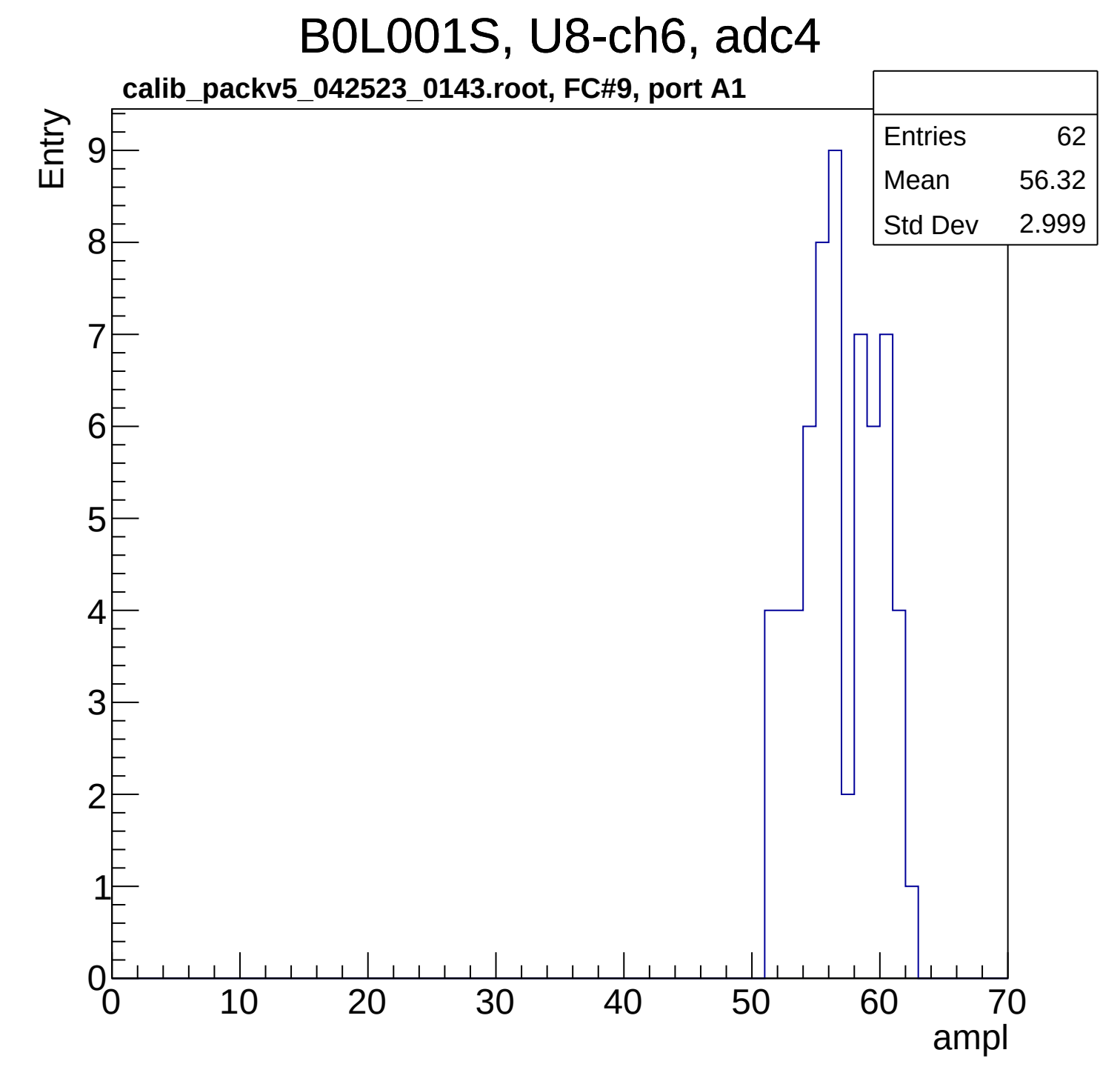
B0L001S, U8-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.32
Std Dev 2.999
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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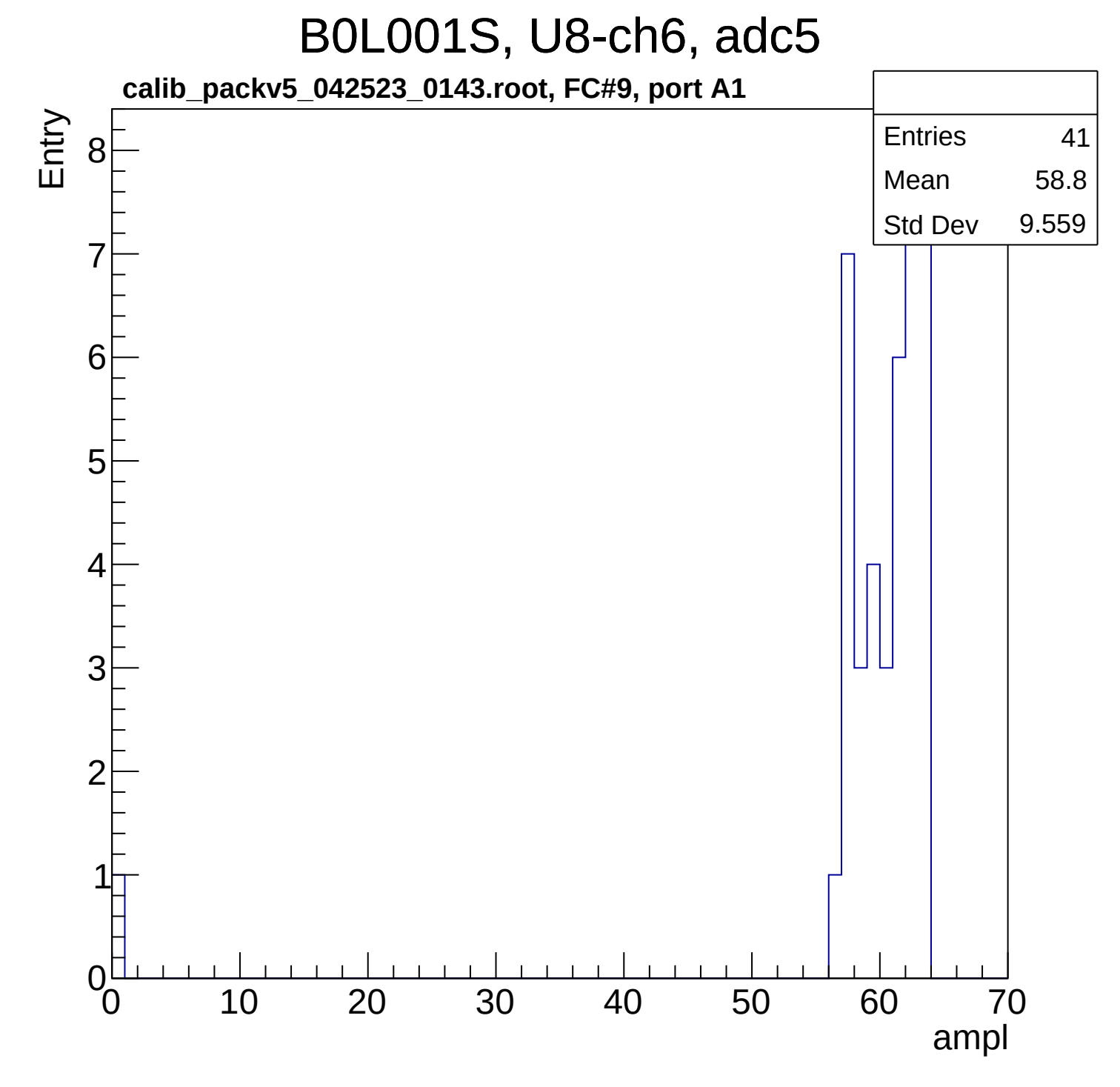
B0L001S, U8-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.8
Std Dev 9.559
Entry
ampl
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3
4
5
6
7
8
0
10
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40
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60
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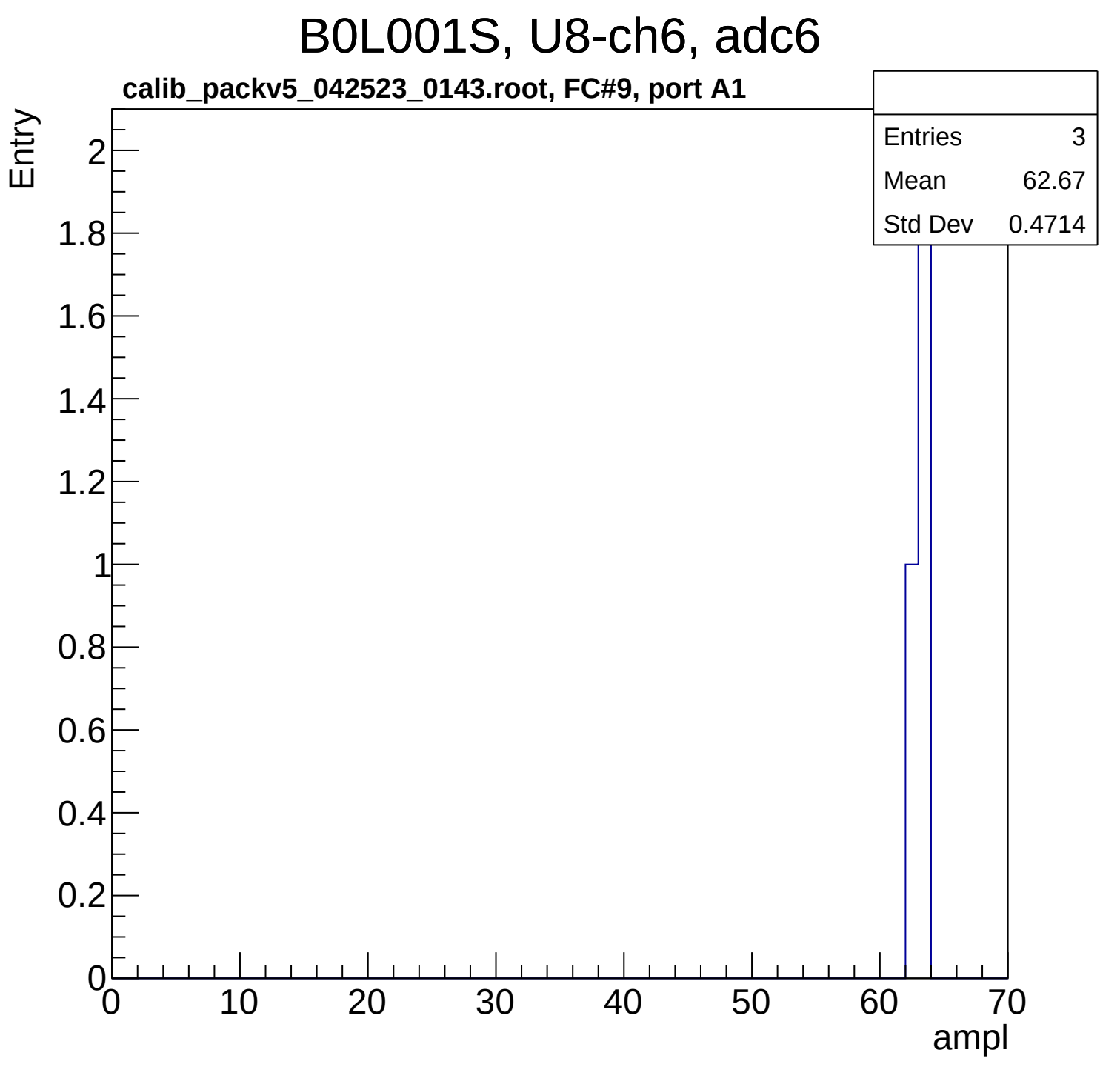
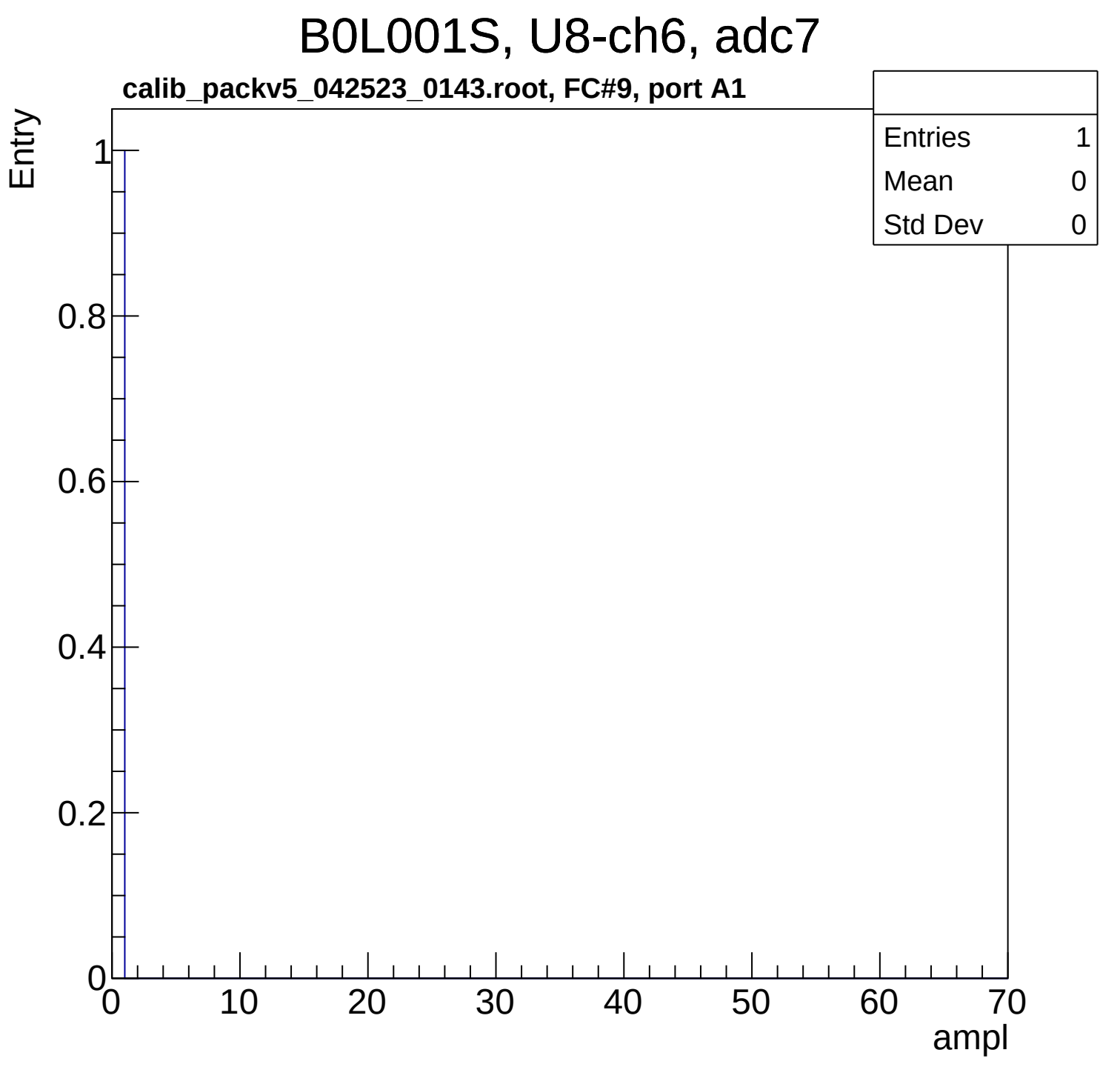

B0L001S, U8-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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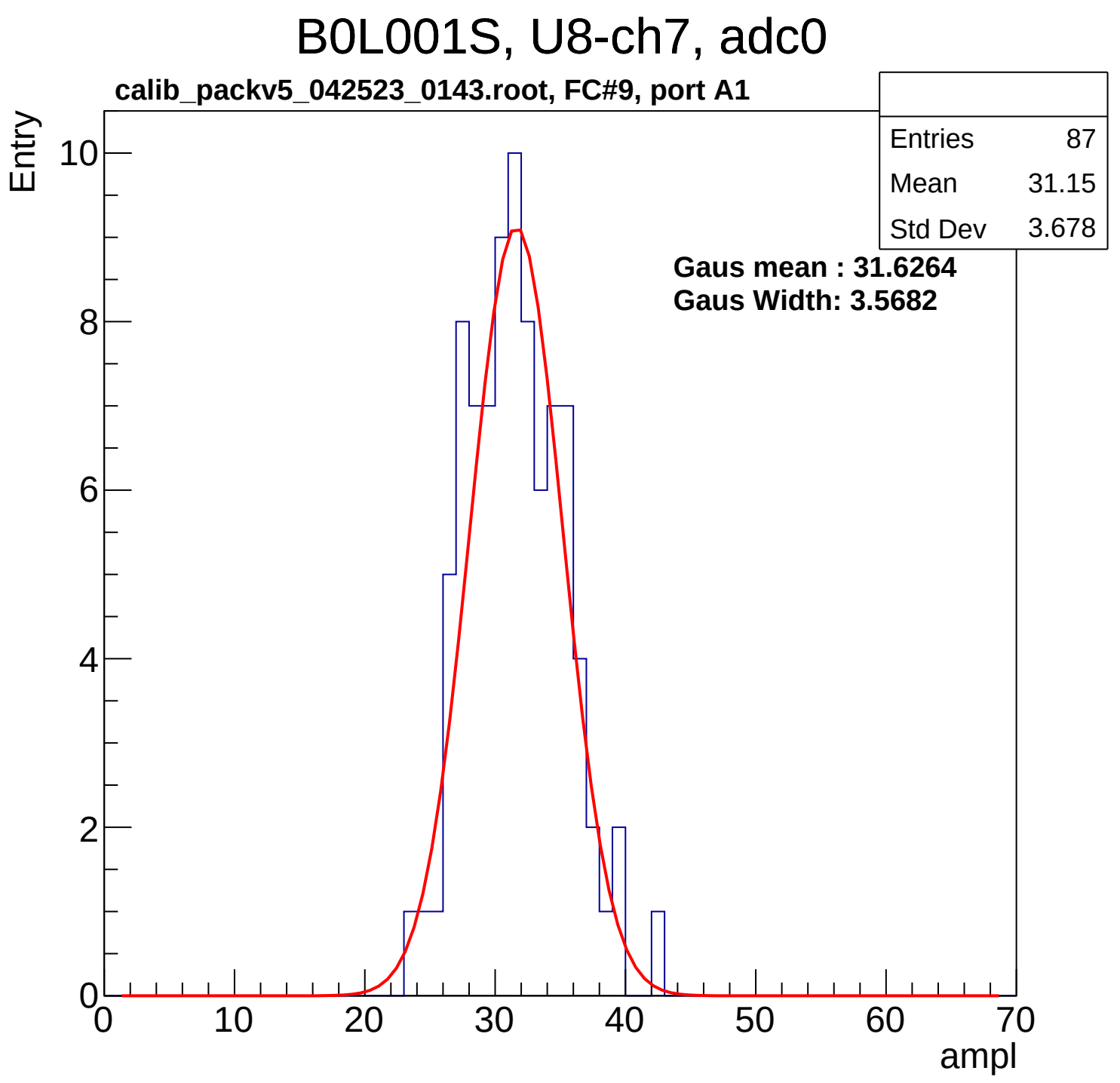
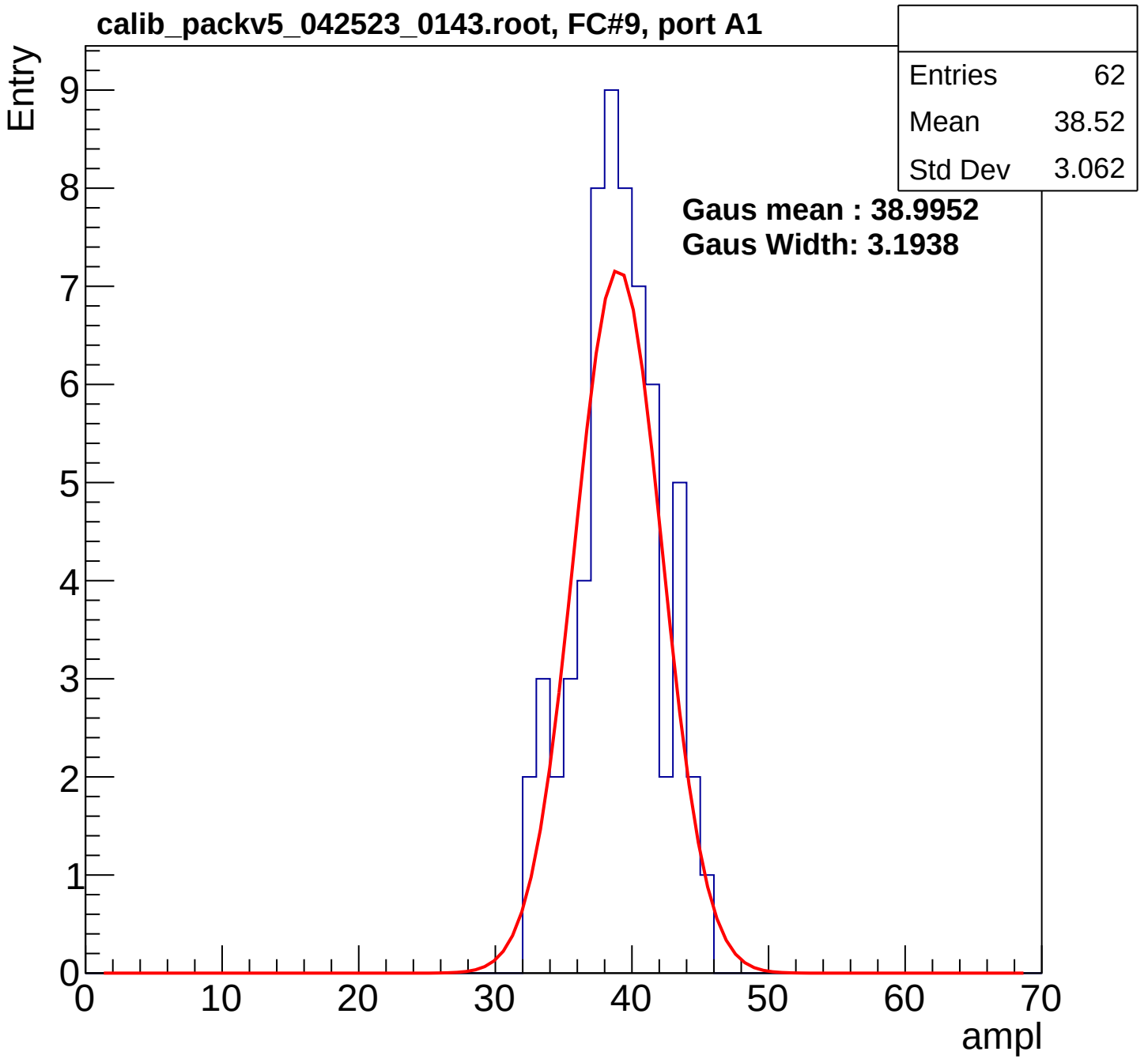
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 31.15
Std Dev 3.678
Gaus mean : 31.6264
Gaus Width: 3.5682
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.52
Std Dev 3.062
Gaus mean : 38.9952
Gaus Width: 3.1938
Entry
ampl
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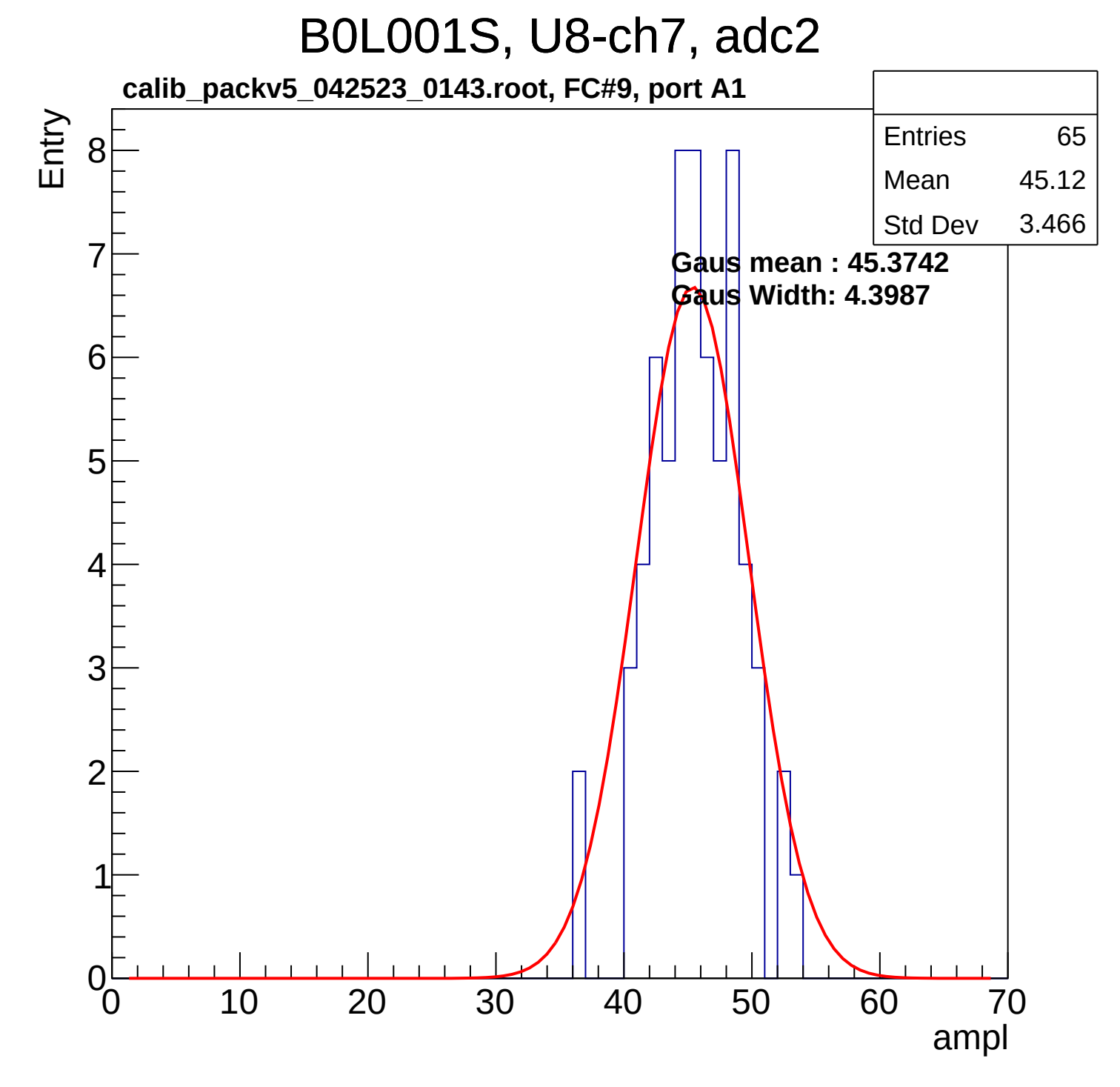
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.12
Std Dev 3.466
Gaus mean : 45.3742
Gaus Width: 4.3987
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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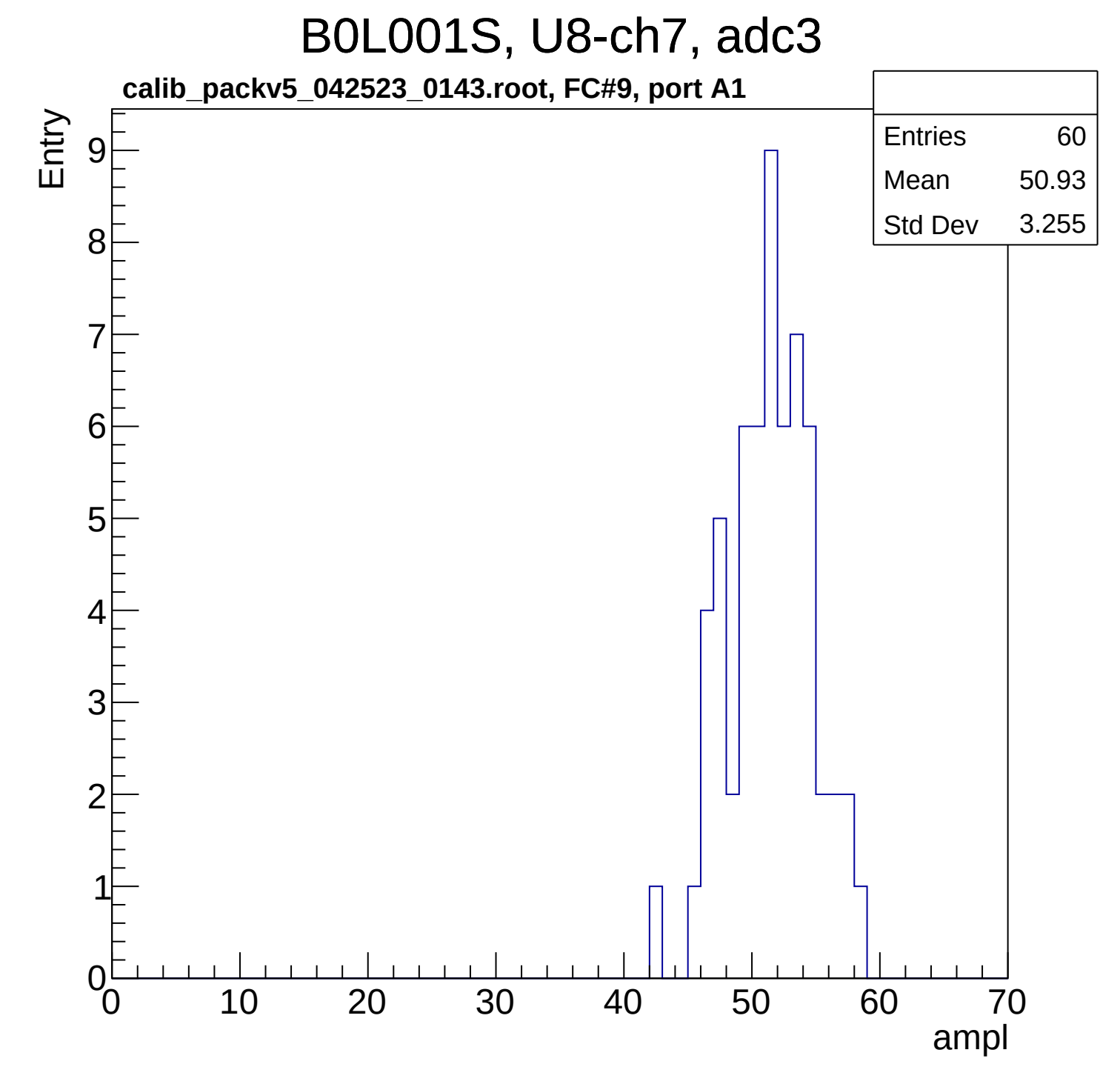
B0L001S, U8-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.93
Std Dev 3.255
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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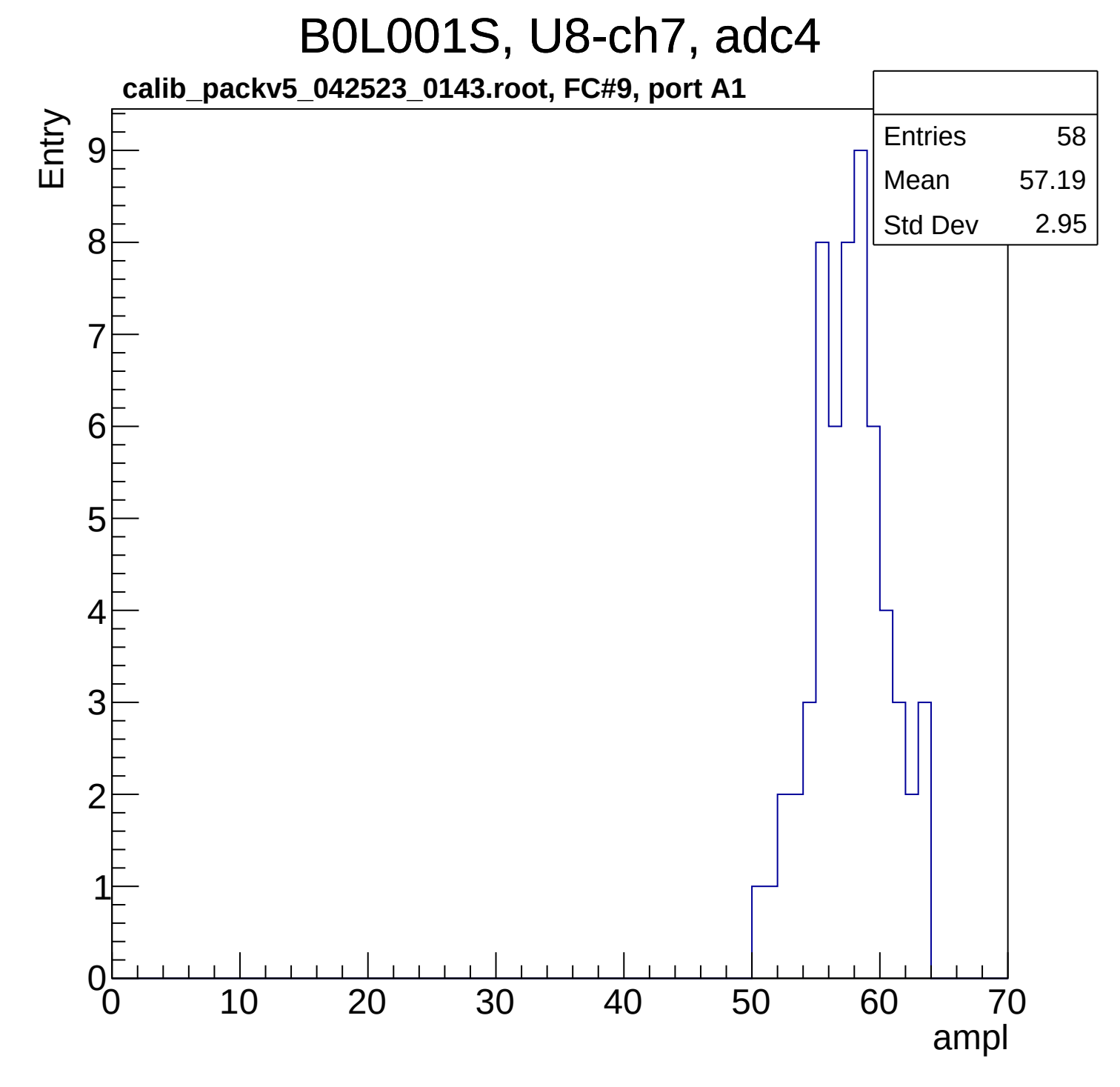
B0L001S, U8-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.19
Std Dev 2.95
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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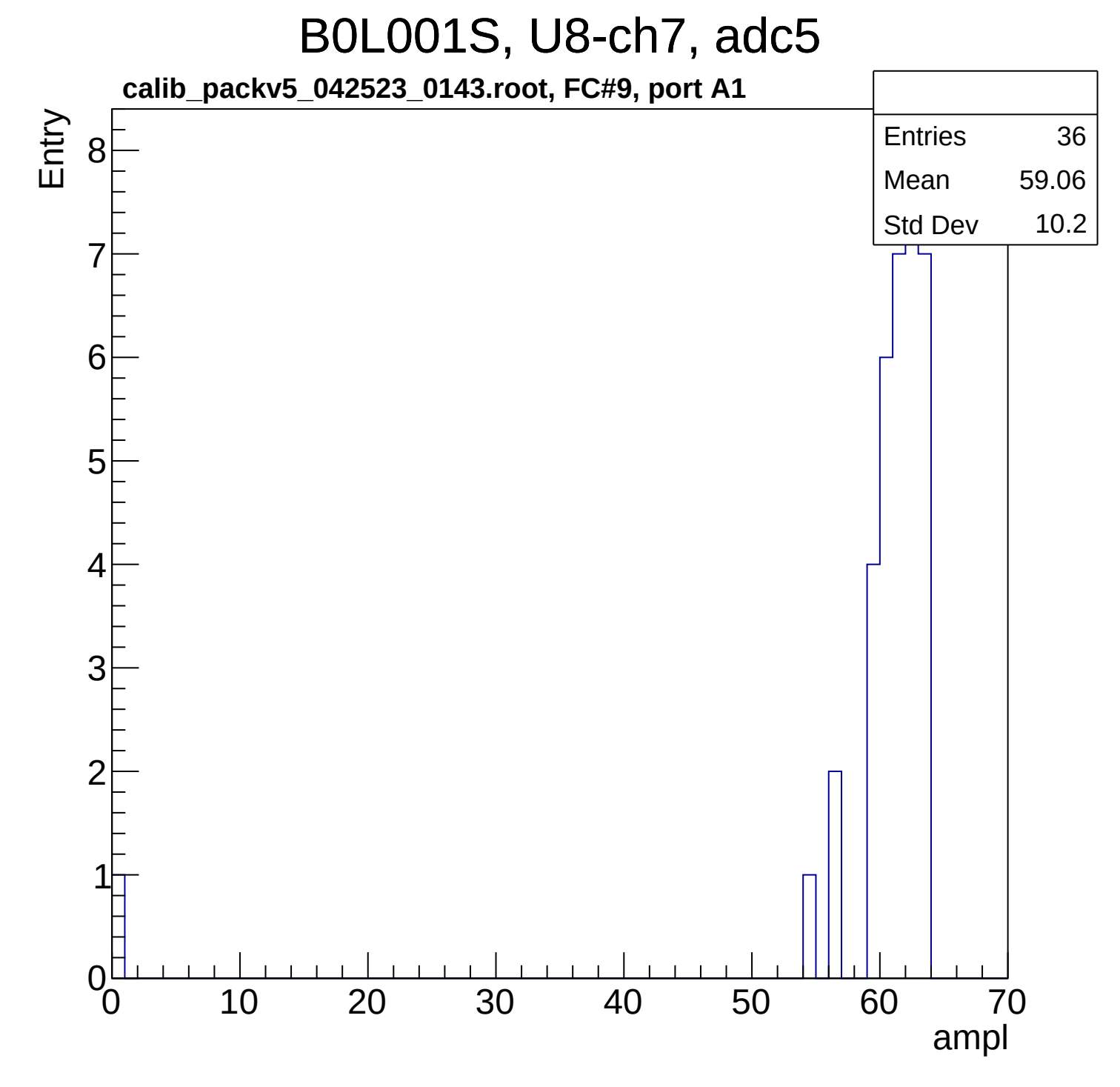
40
50
60
70


B0L001S, U8-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.06
Std Dev 10.2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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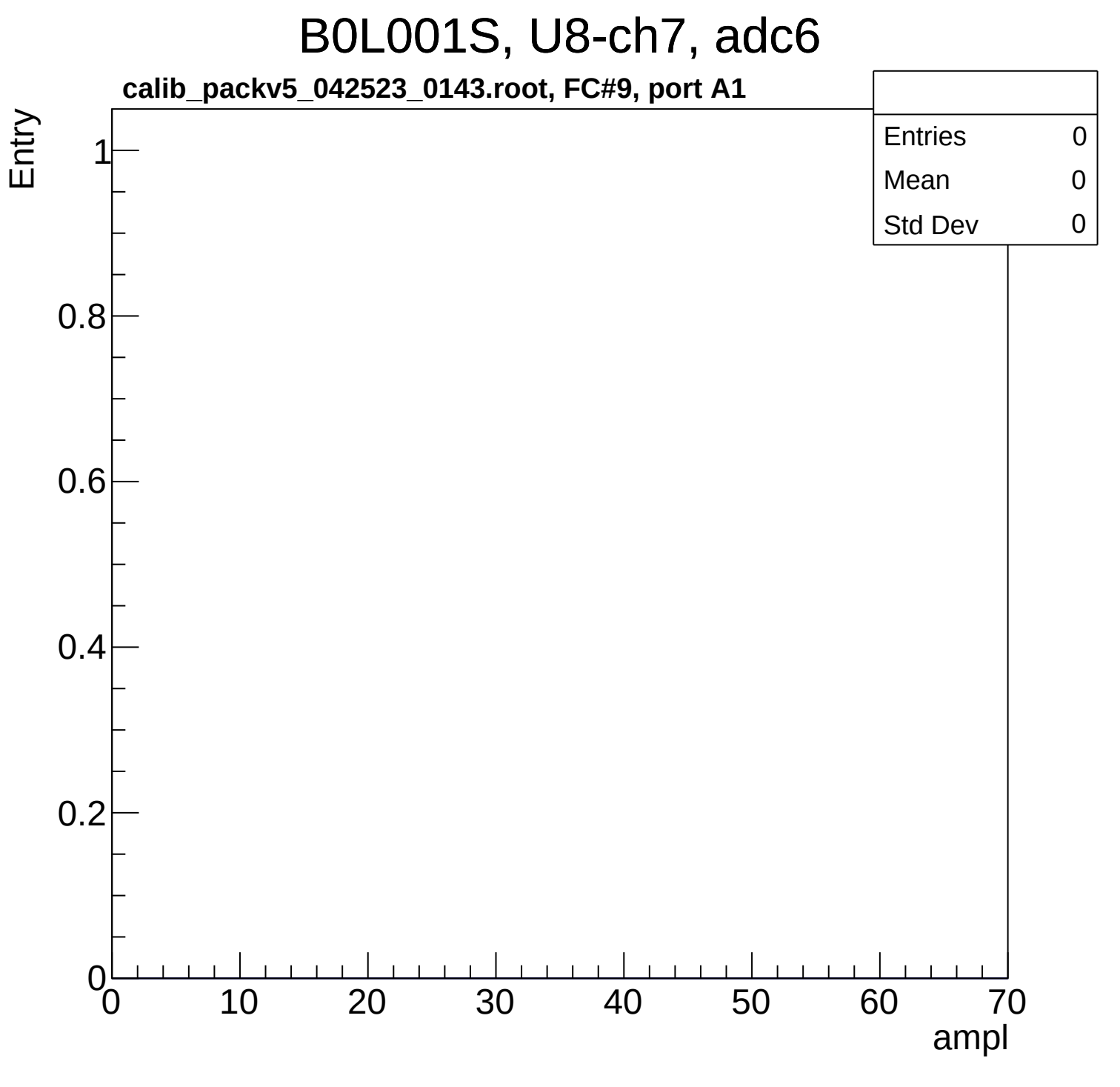
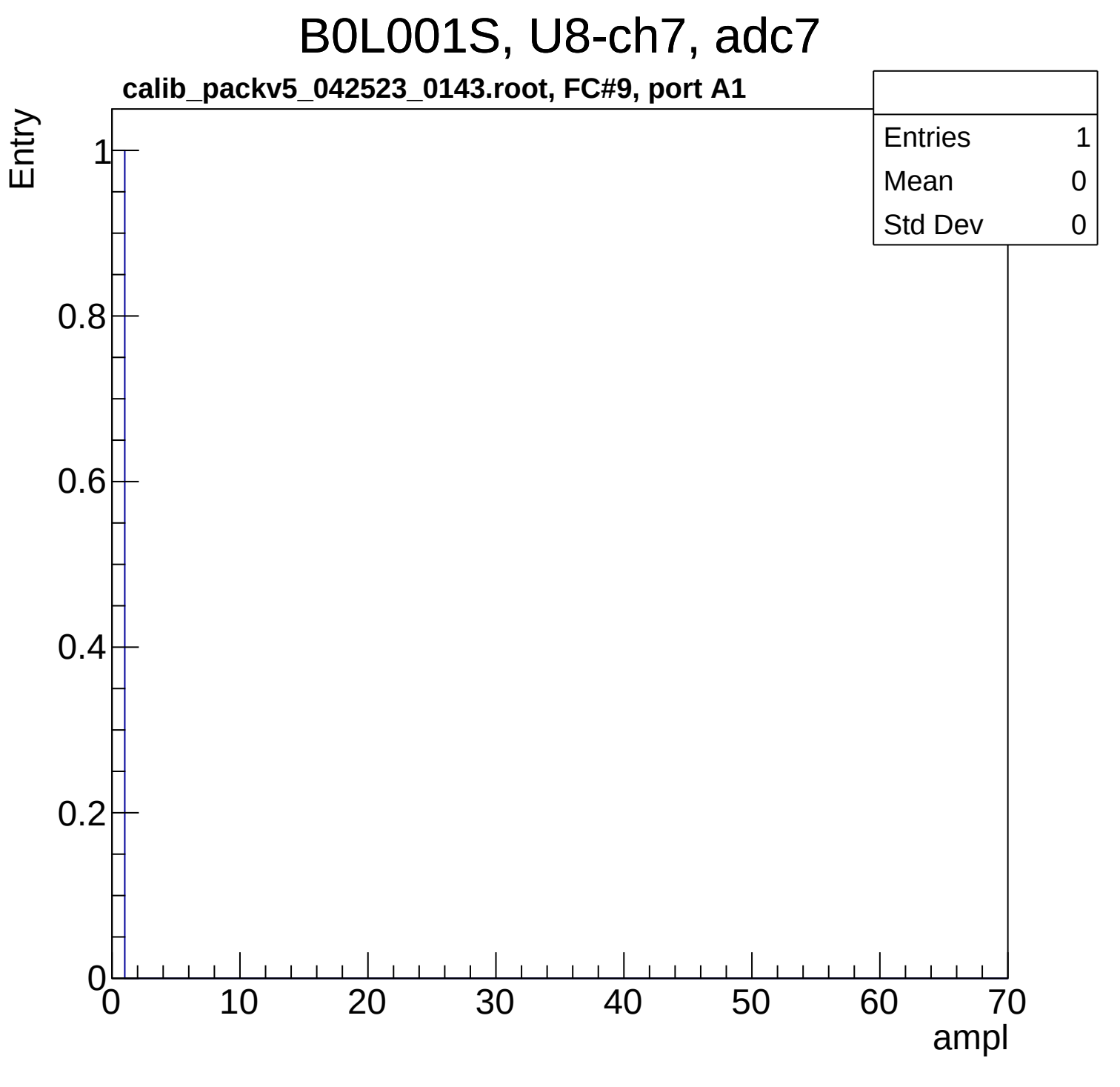

B0L001S, U8-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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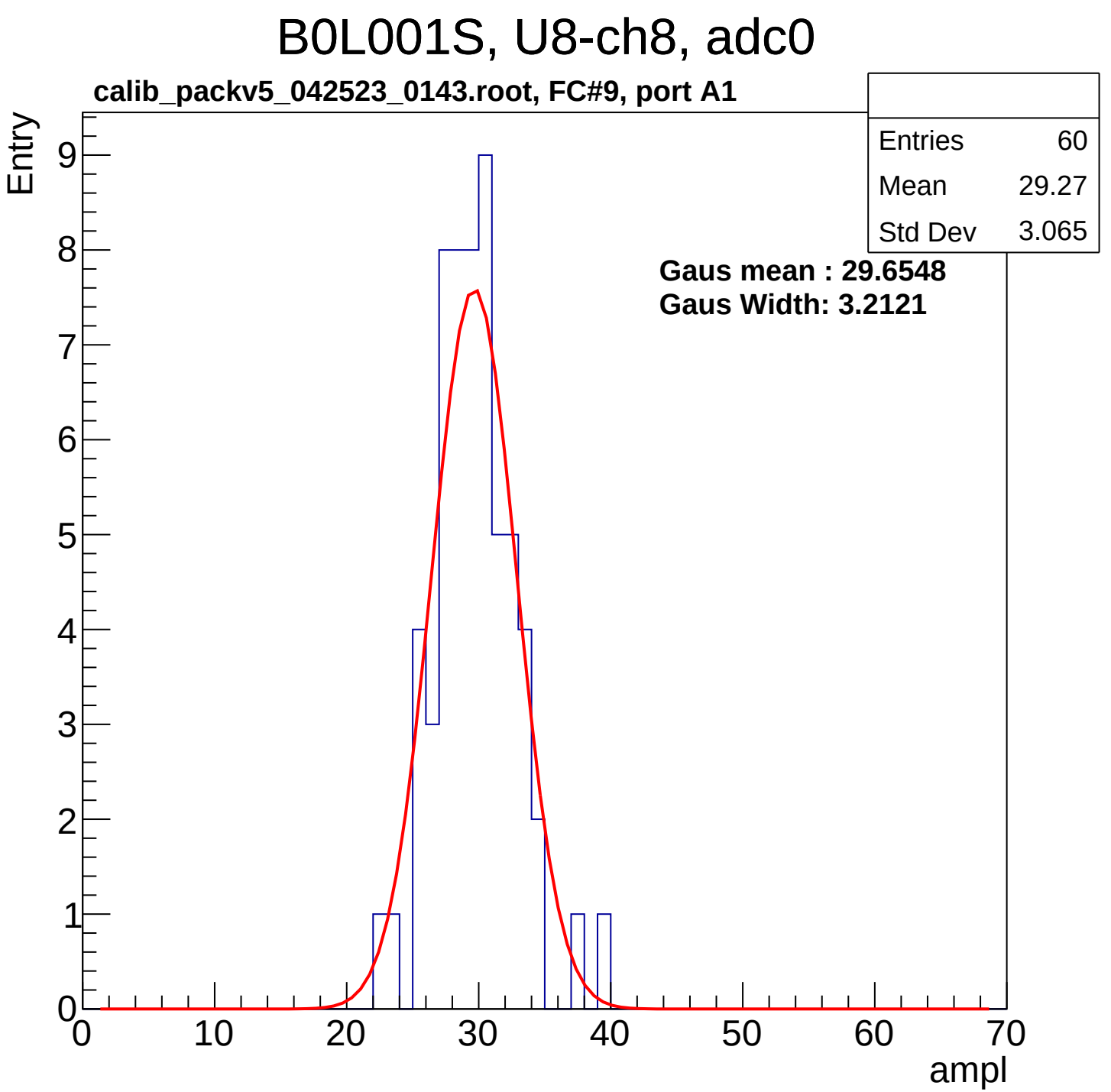
50
60
70
ampl


B0L001S, U8-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.27
Std Dev 3.065
Gaus mean : 29.6548
Gaus Width: 3.2121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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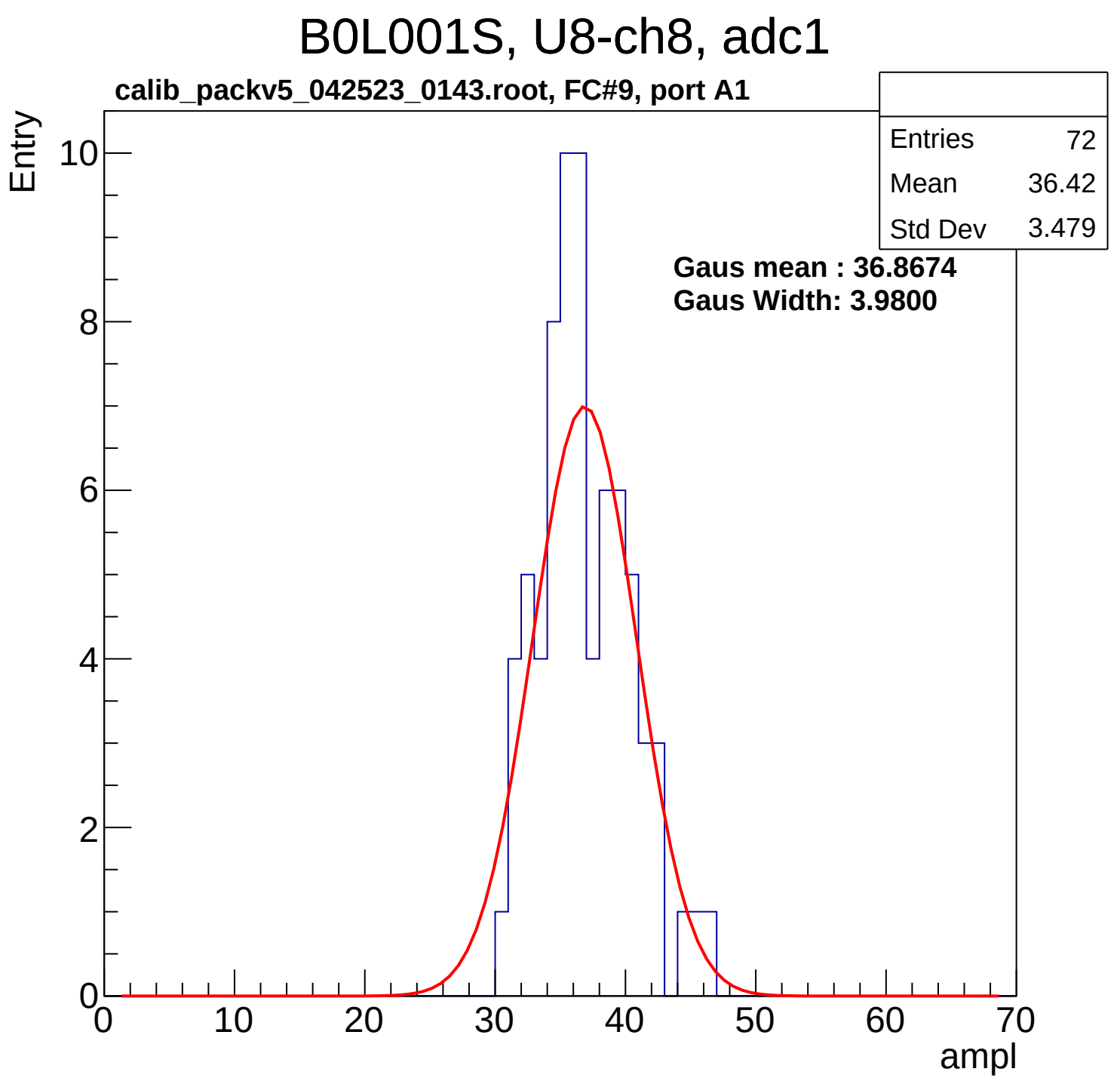
B0L001S, U8-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.42
Std Dev 3.479
Gaus mean : 36.8674
Gaus Width: 3.9800
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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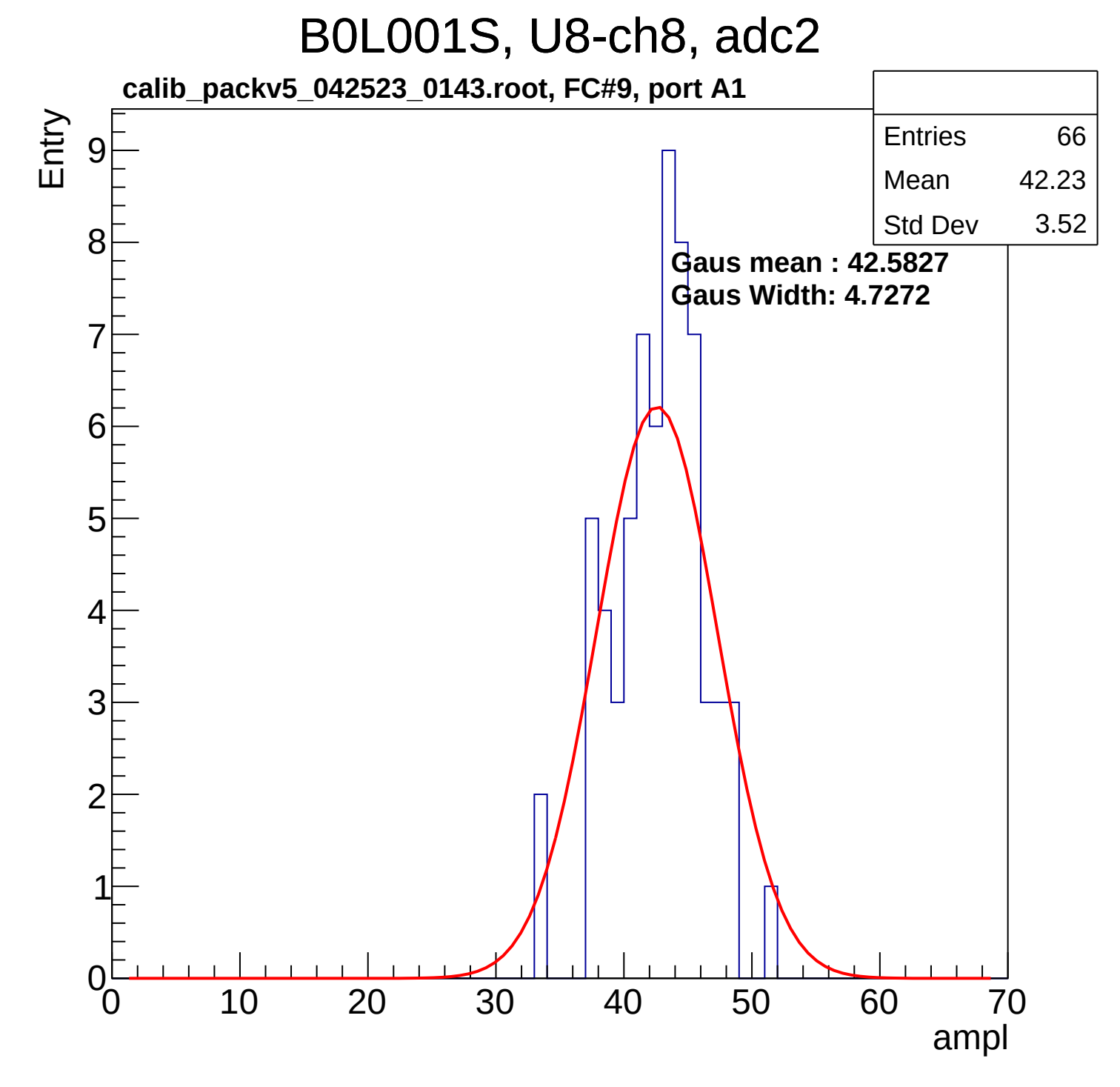
B0L001S, U8-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.23
Std Dev 3.52
Gaus mean : 42.5827
Gaus Width: 4.7272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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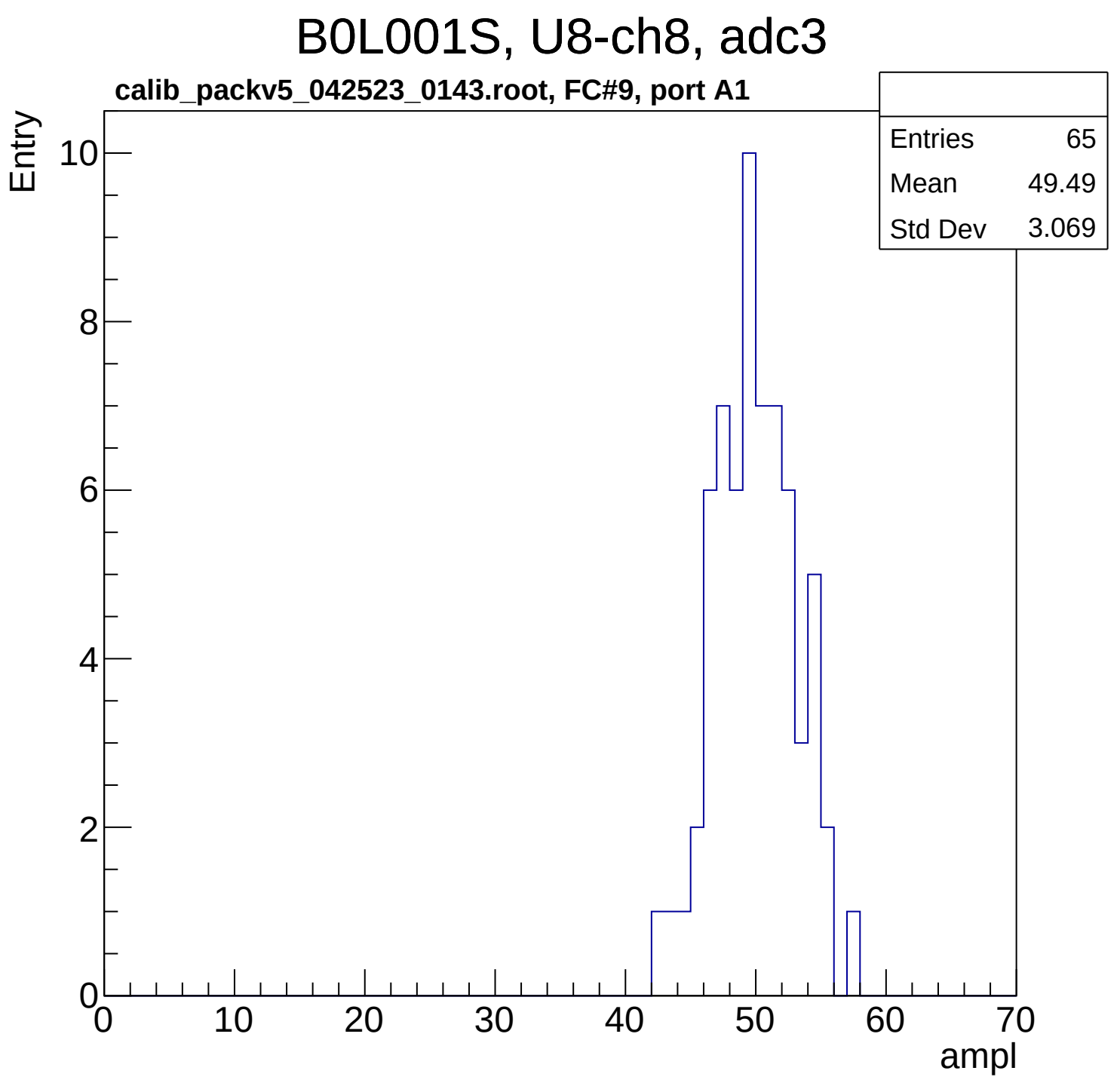
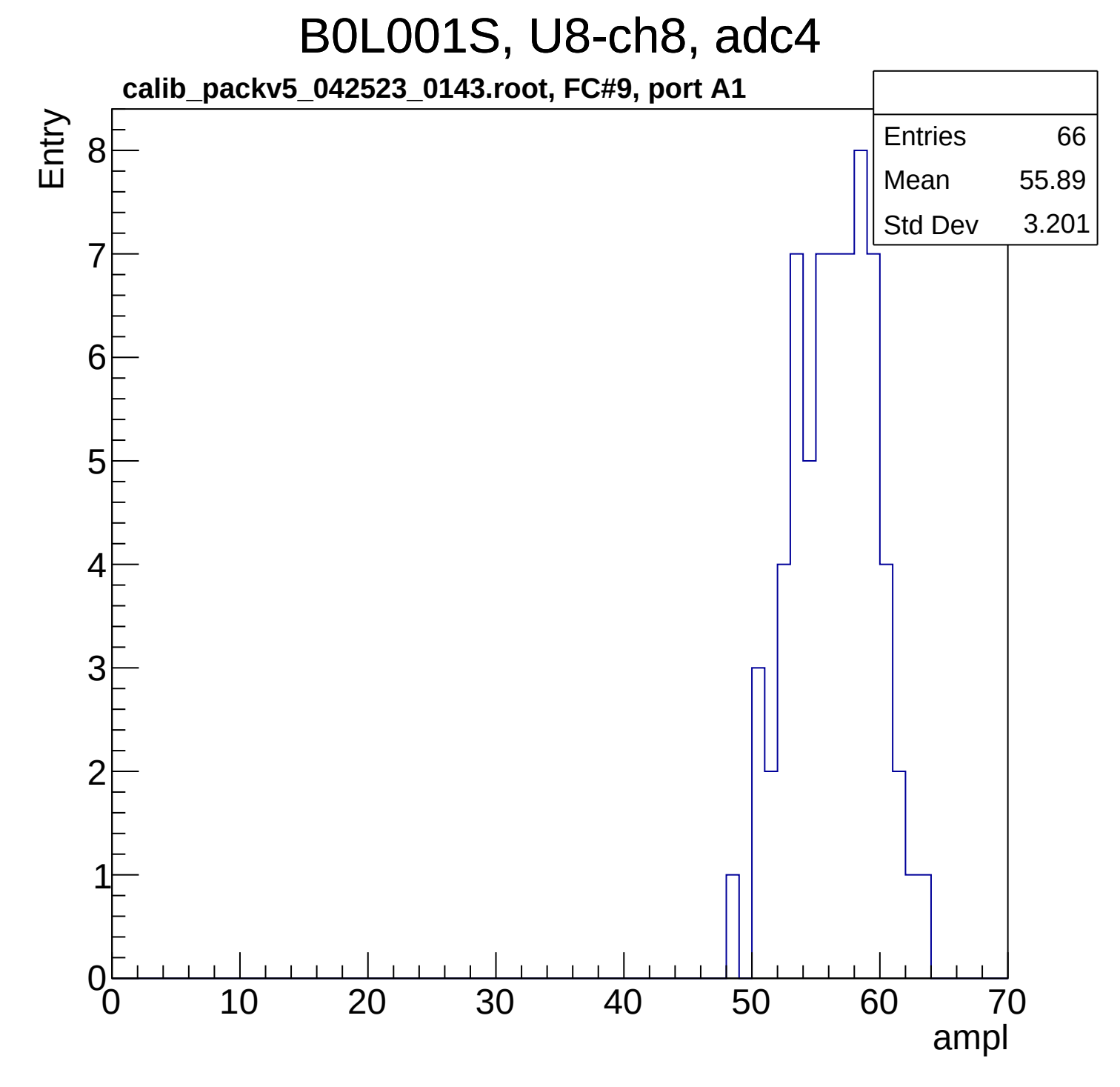

B0L001S, U8-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.49
Std Dev 3.069
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.89
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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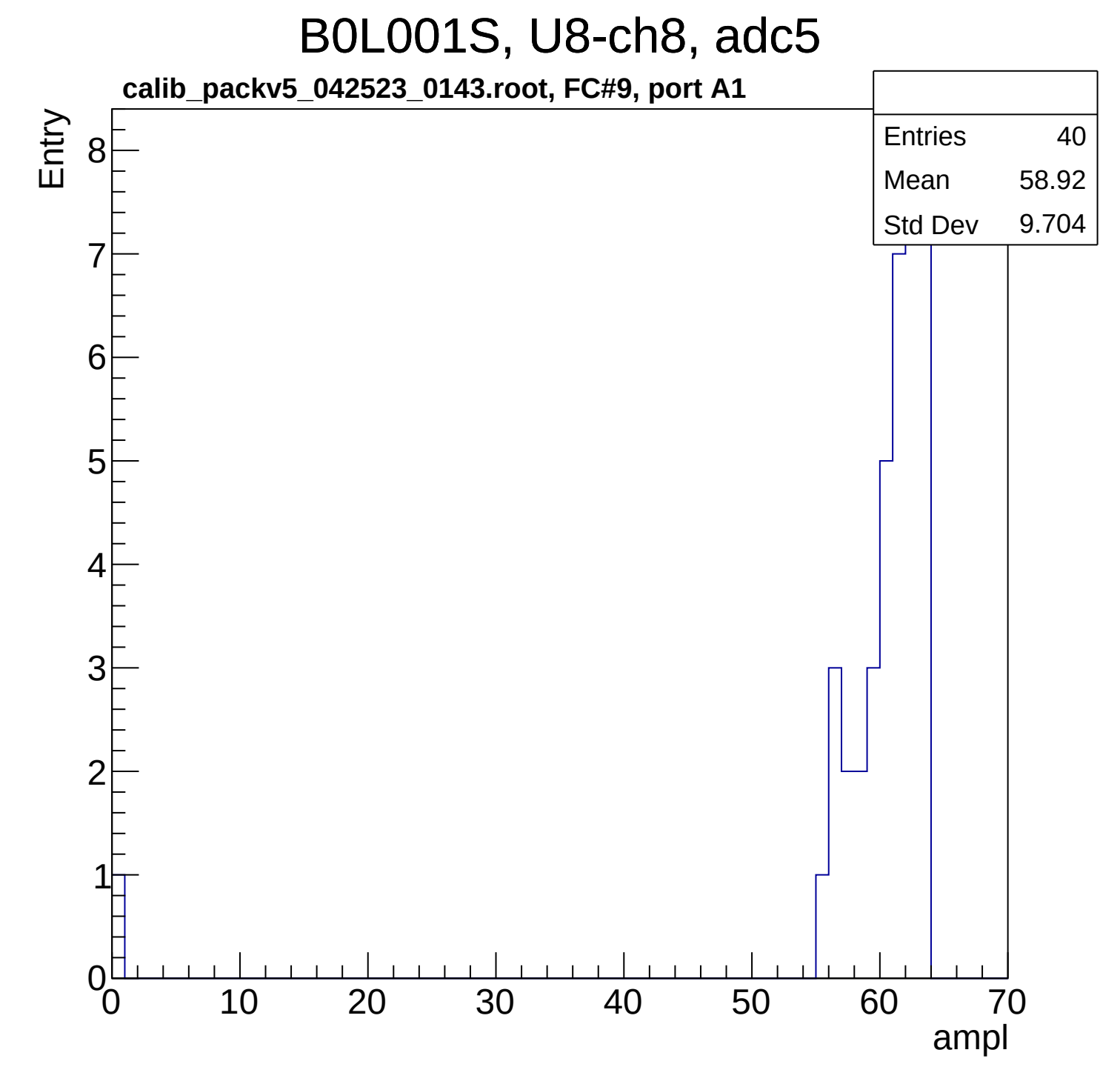
B0L001S, U8-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.92
Std Dev 9.704
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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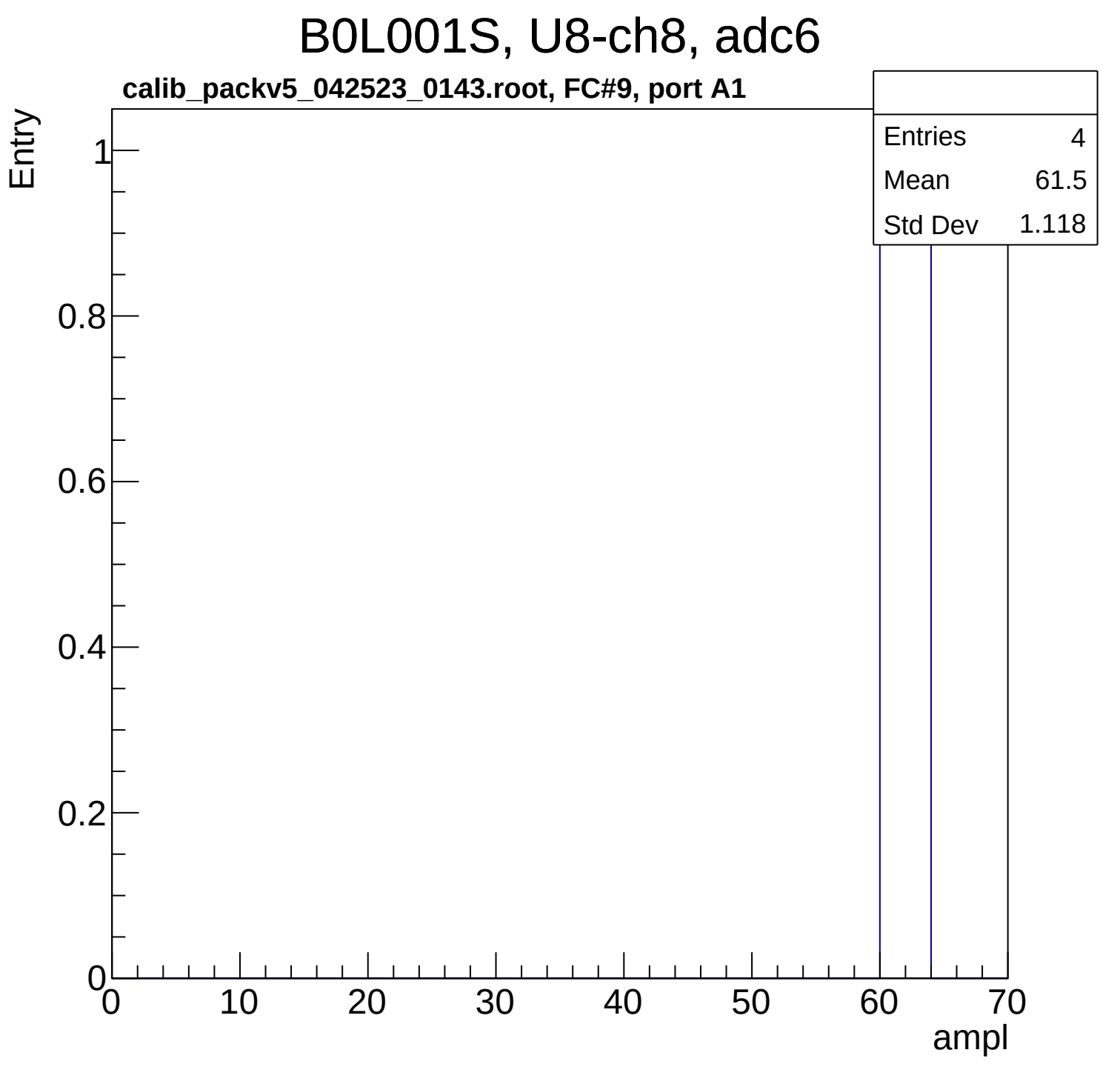
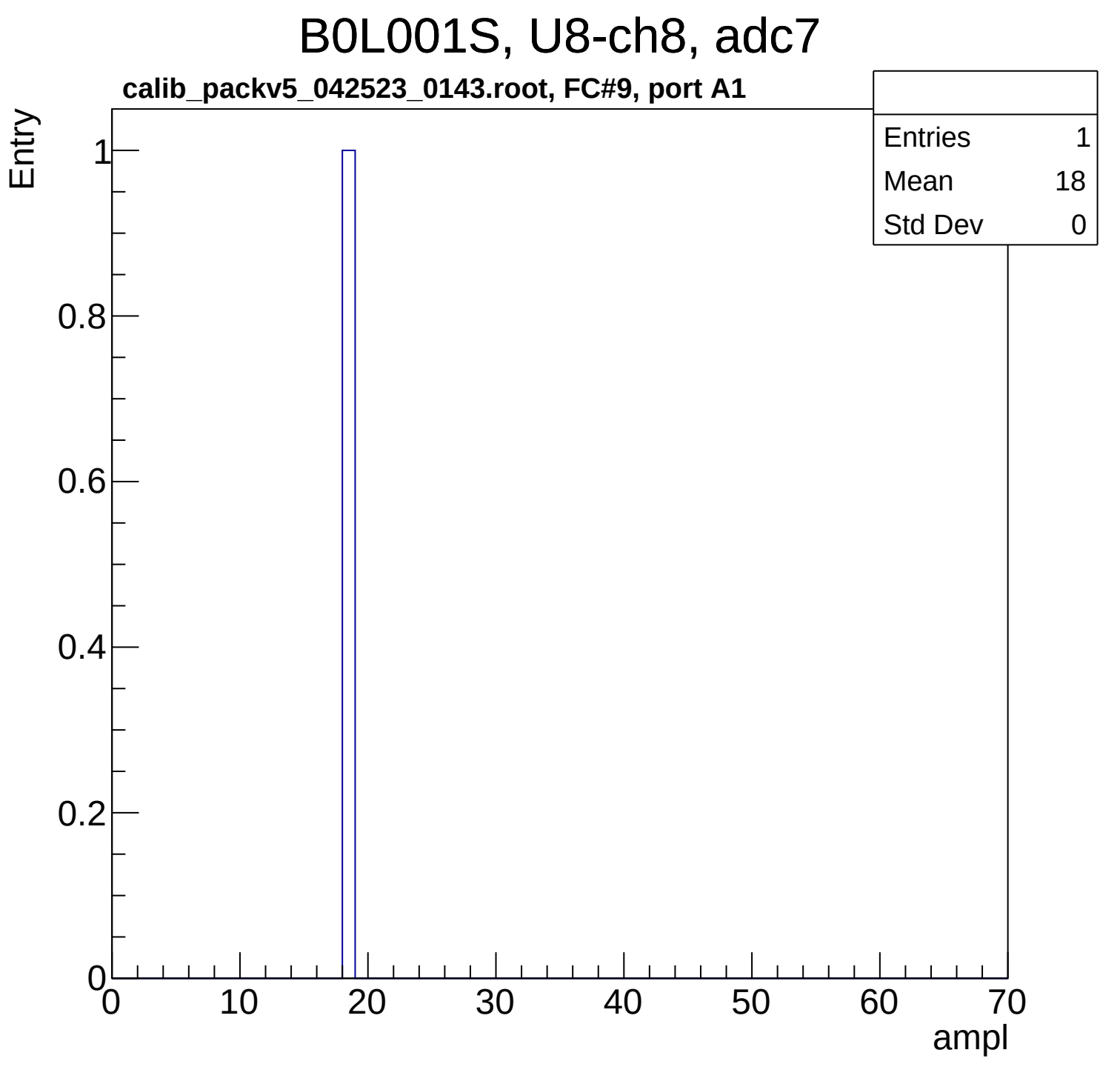
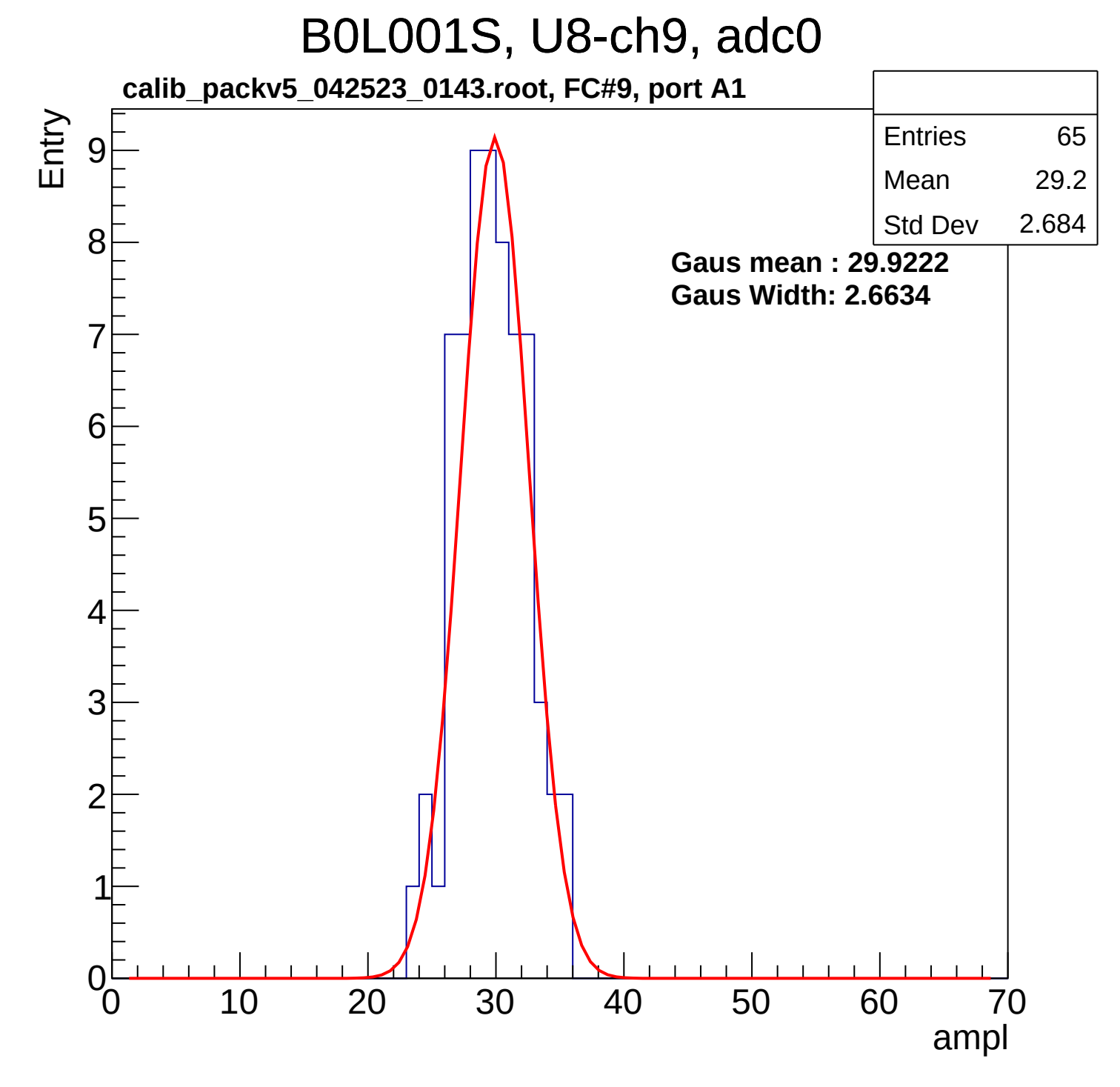
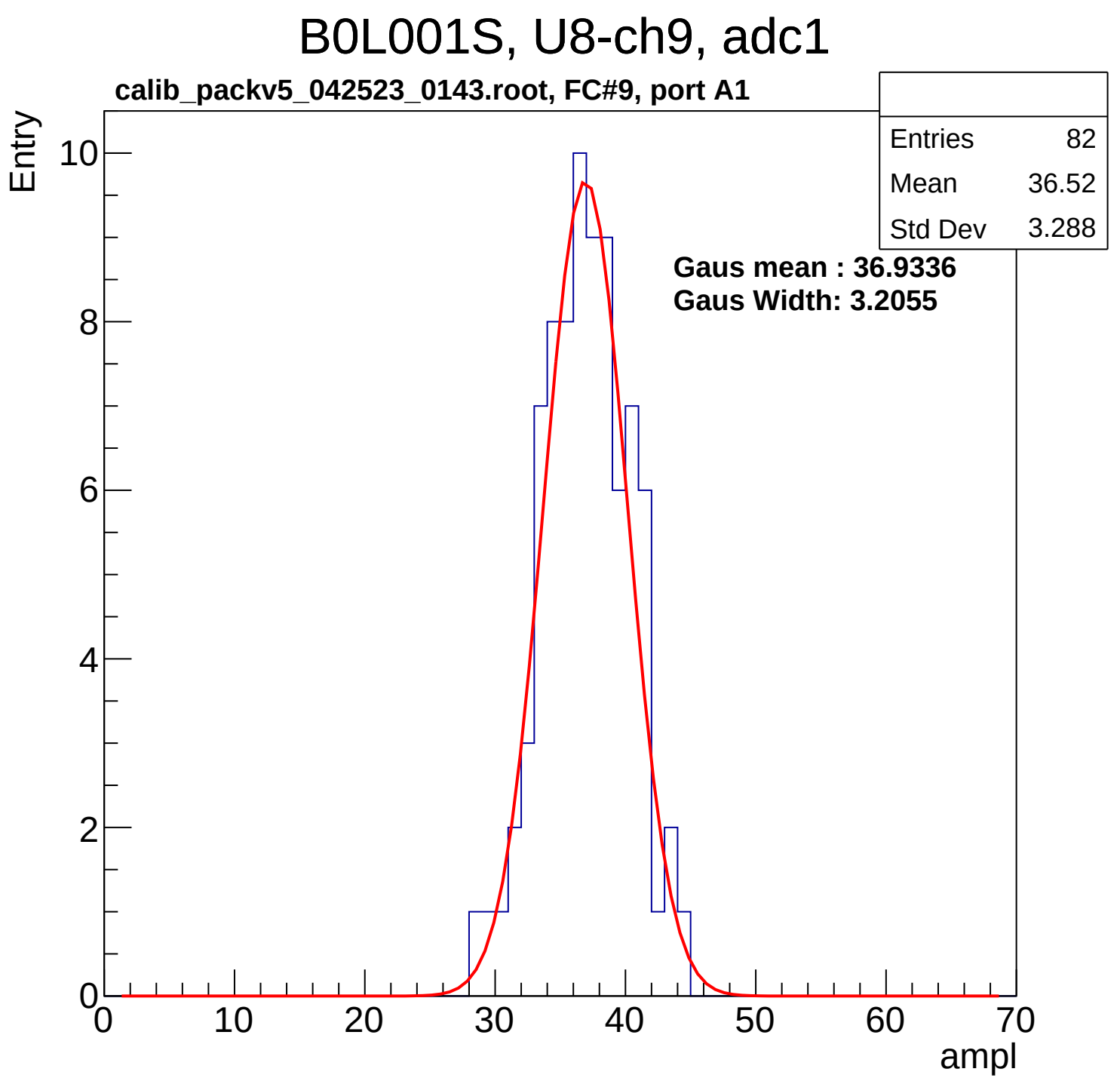
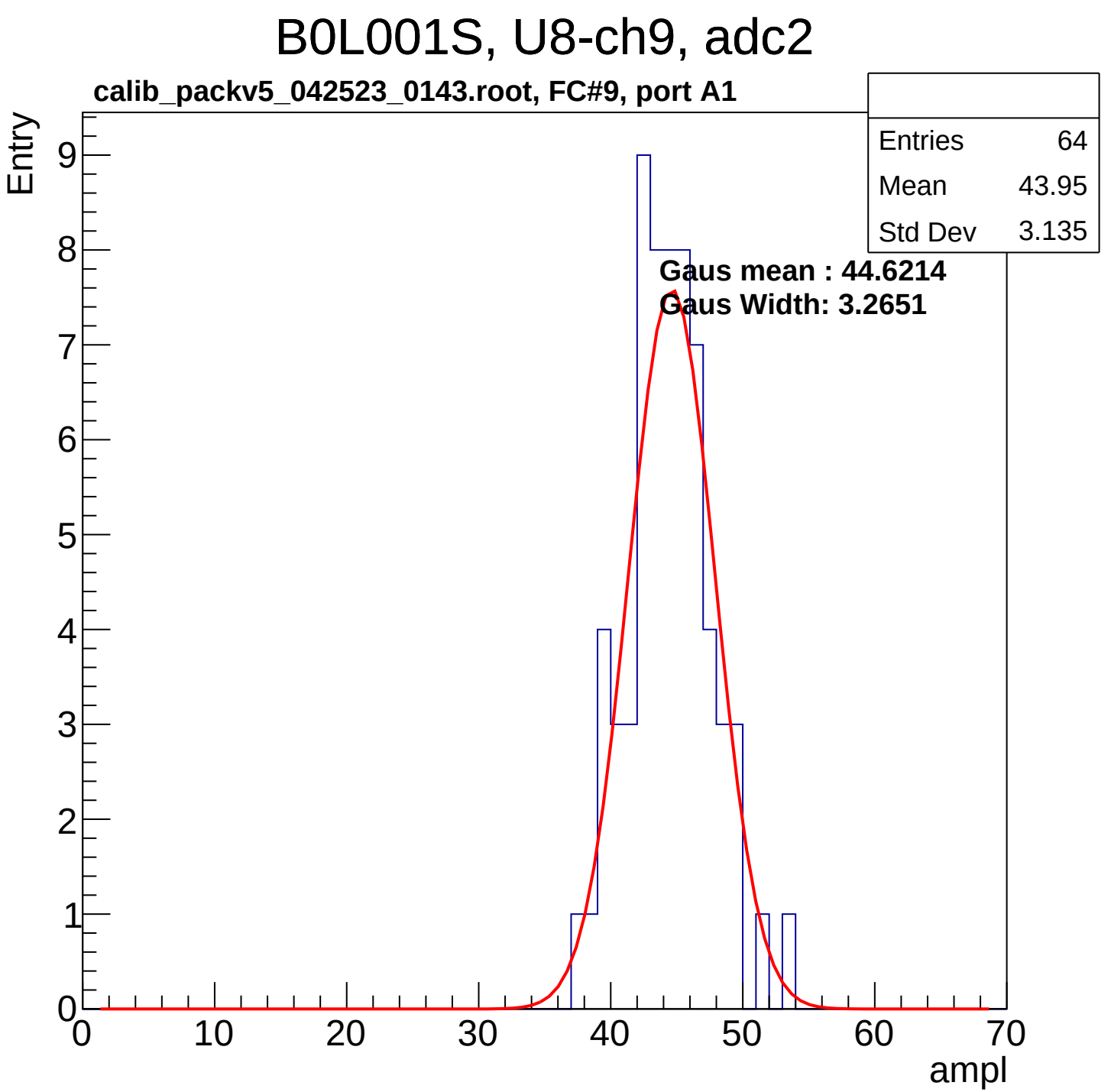

B0L001S, U8-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.2
Std Dev 2.684
Gaus mean : 29.9222
Gaus Width: 2.6634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 36.52
Std Dev 3.288
Gaus mean : 36.9336
Gaus Width: 3.2055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.95
Std Dev 3.135
Gaus mean : 44.6214
Gaus Width: 3.2651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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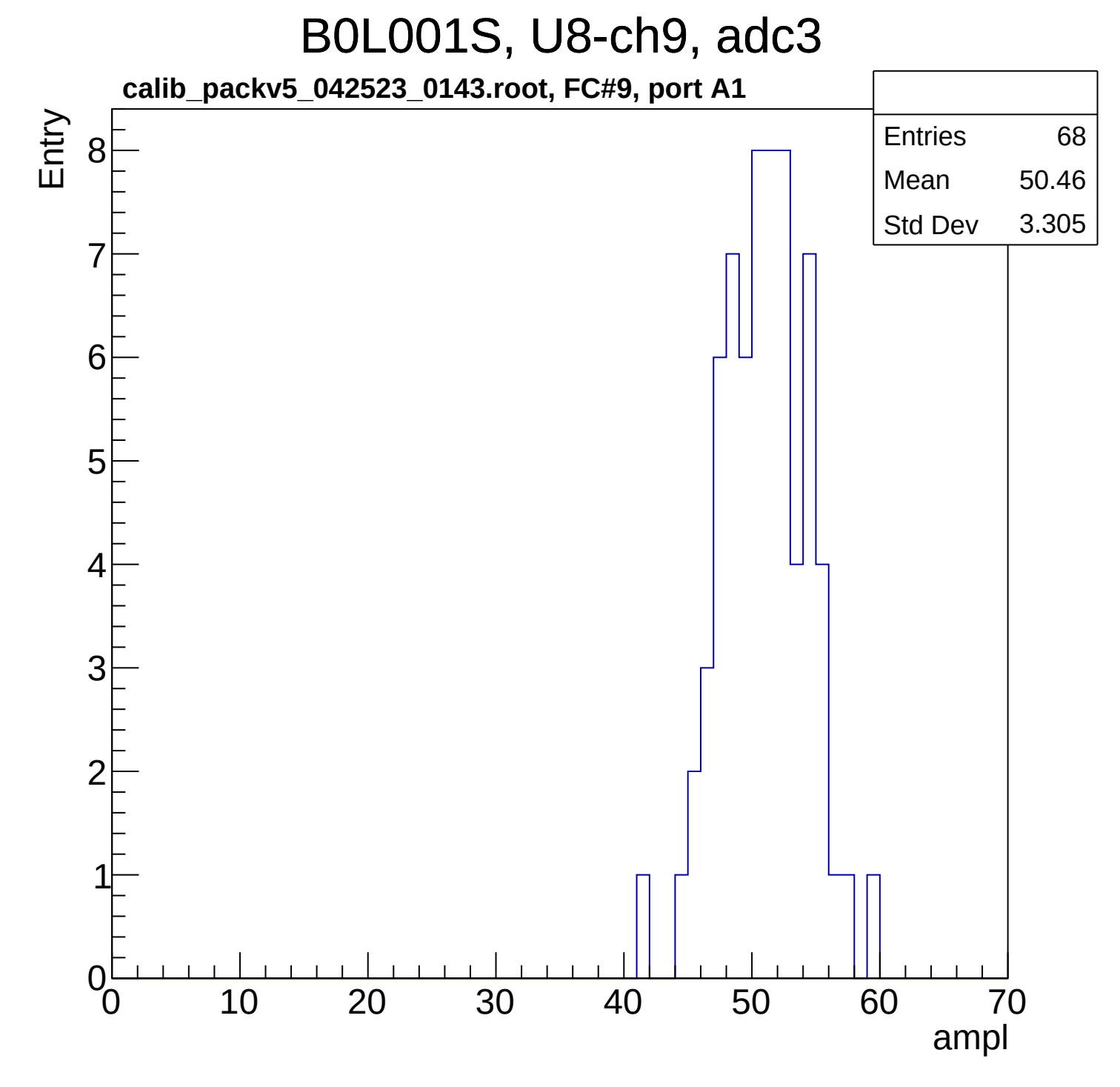
B0L001S, U8-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50.46
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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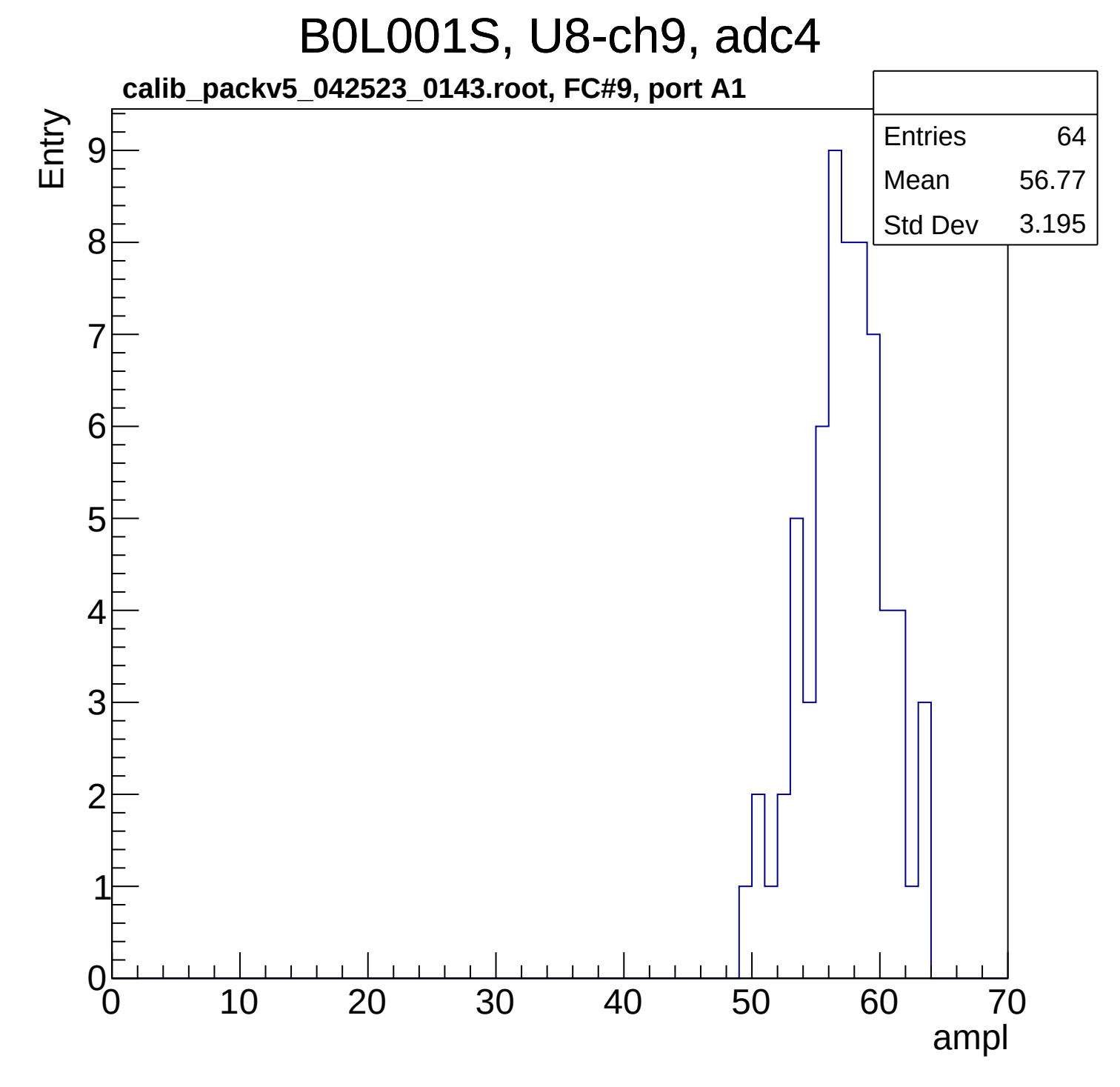
B0L001S, U8-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.77
Std Dev 3.195
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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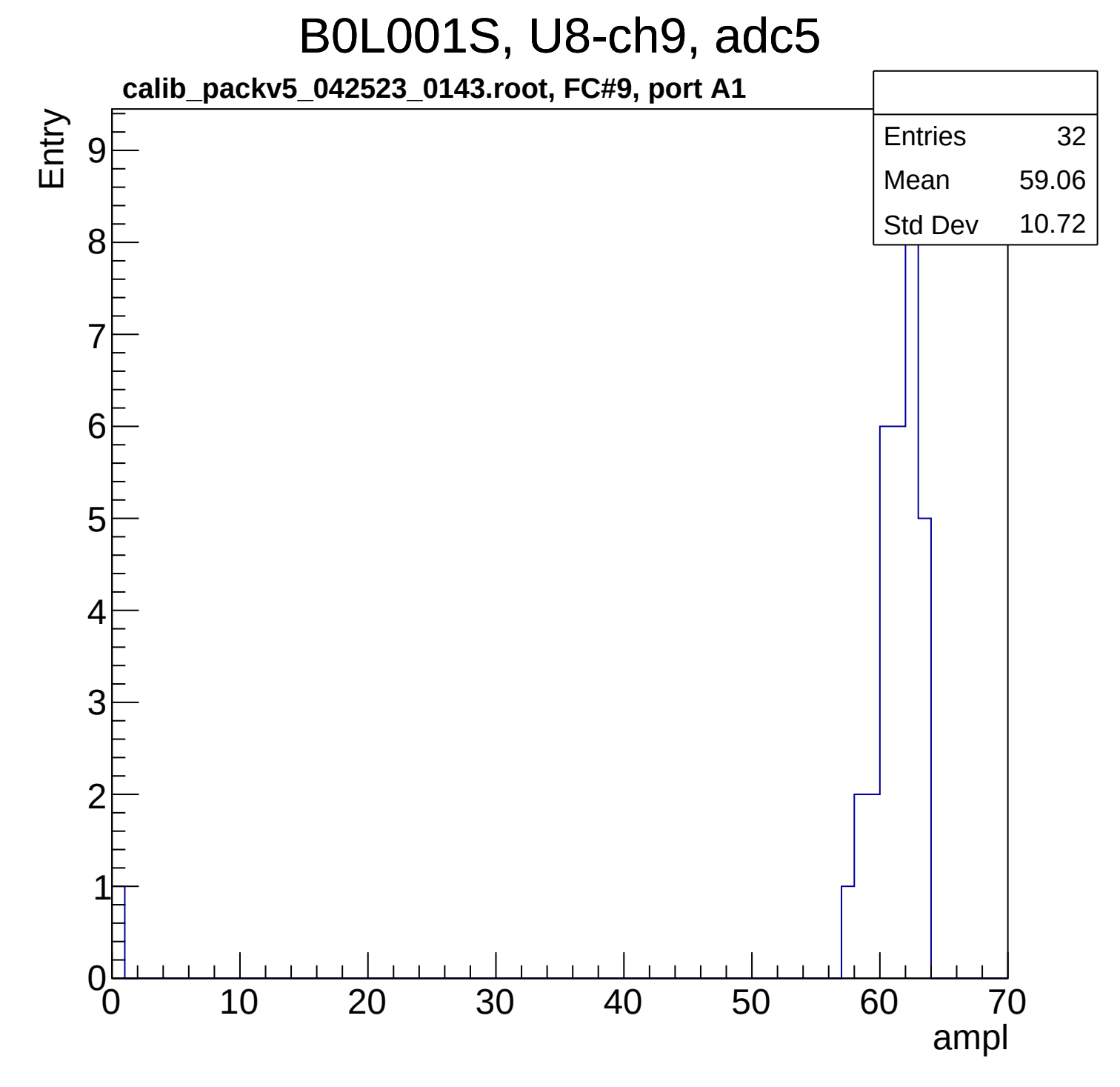
B0L001S, U8-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.06
Std Dev 10.72
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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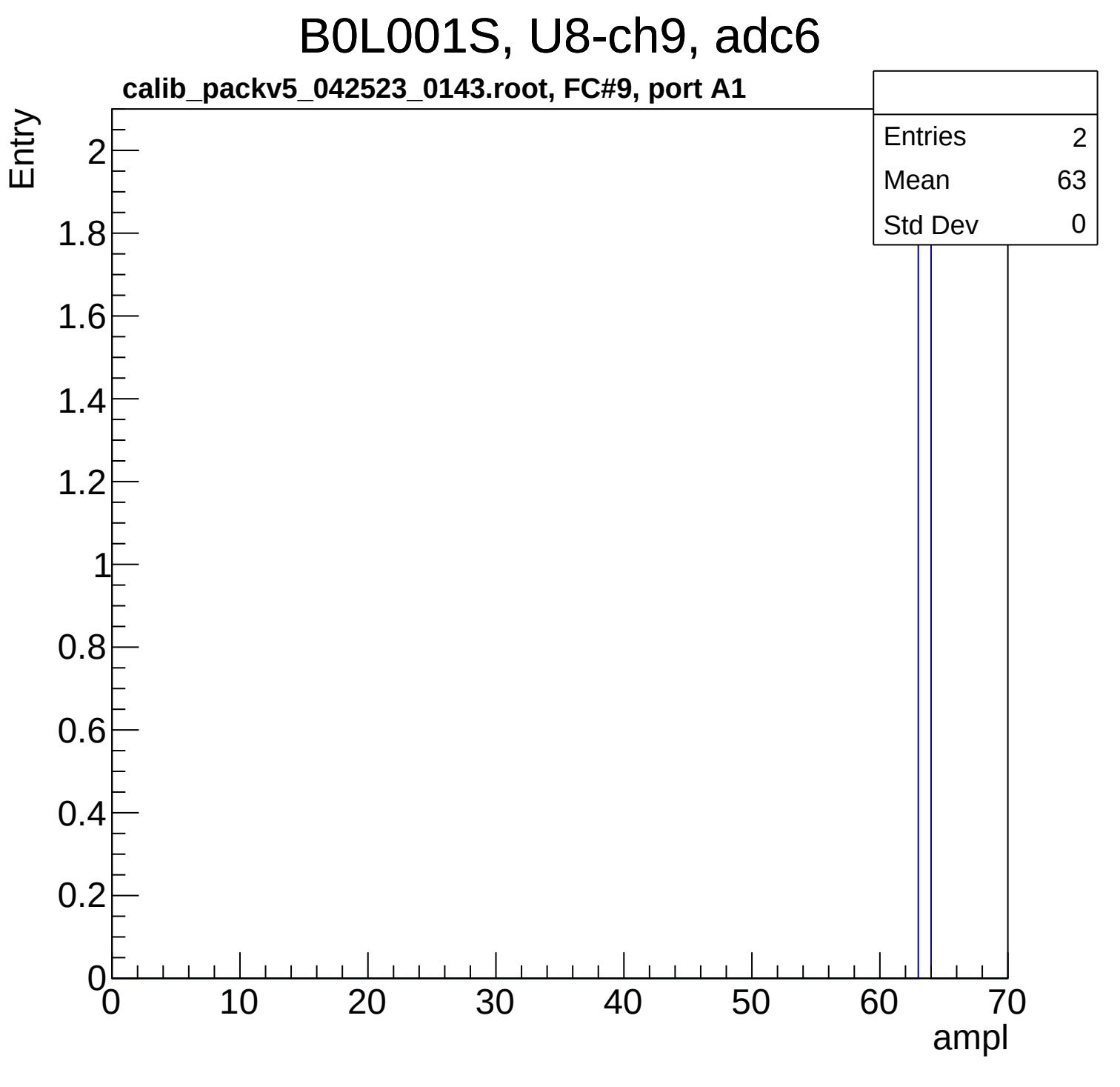
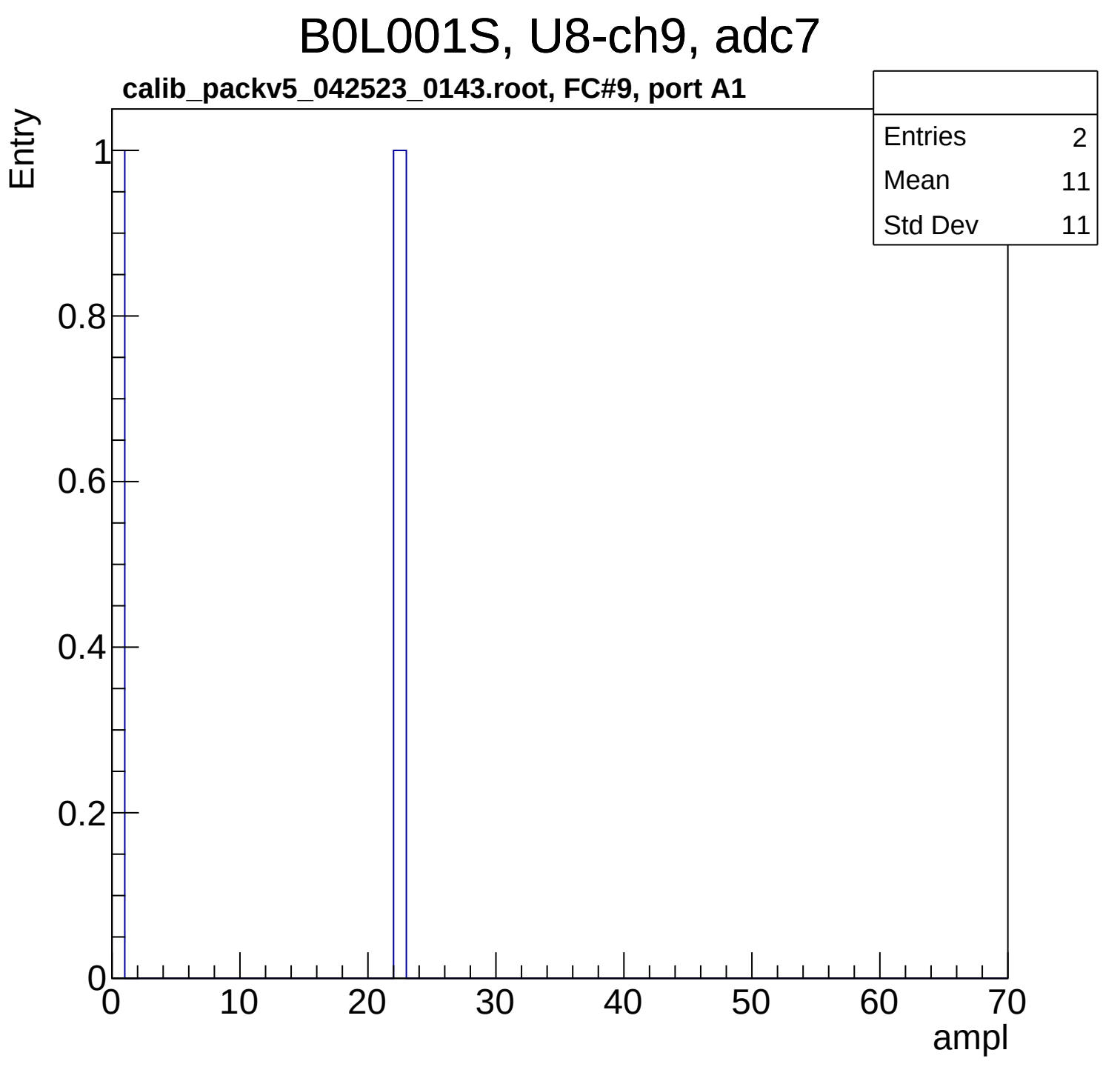

B0L001S, U8-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
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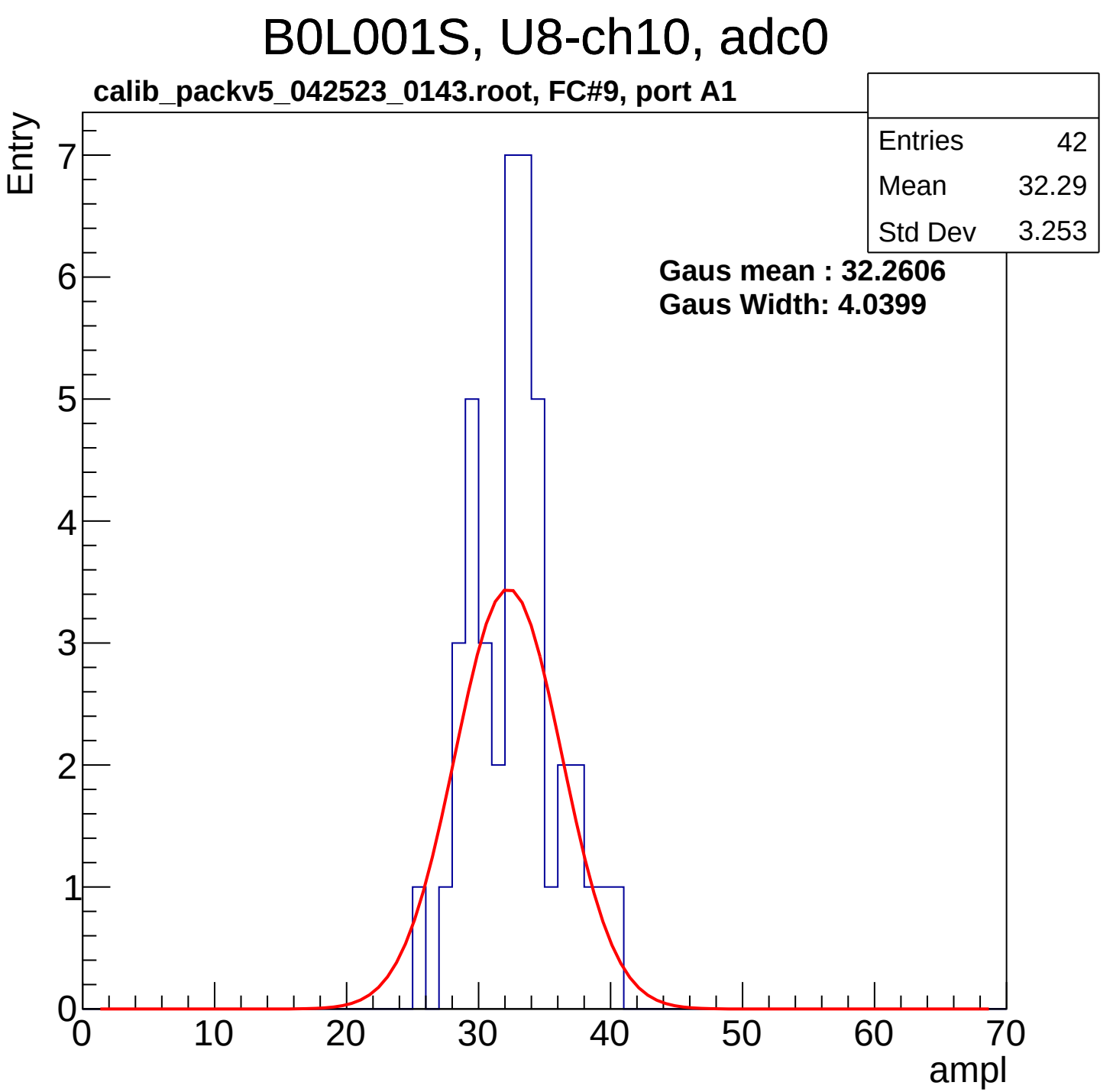
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 32.29
Std Dev 3.253
Gaus mean : 32.2606
Gaus Width: 4.0399
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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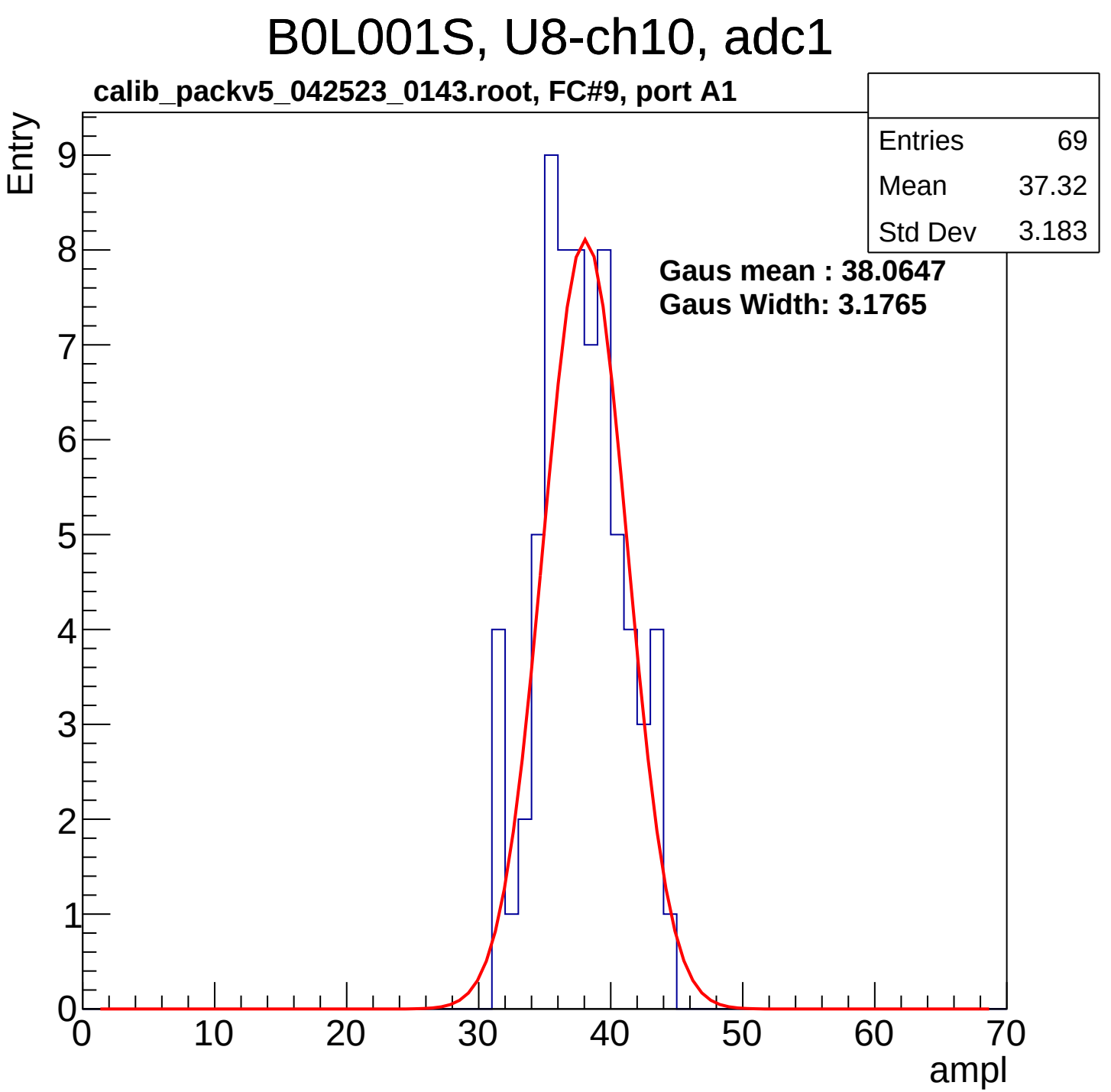
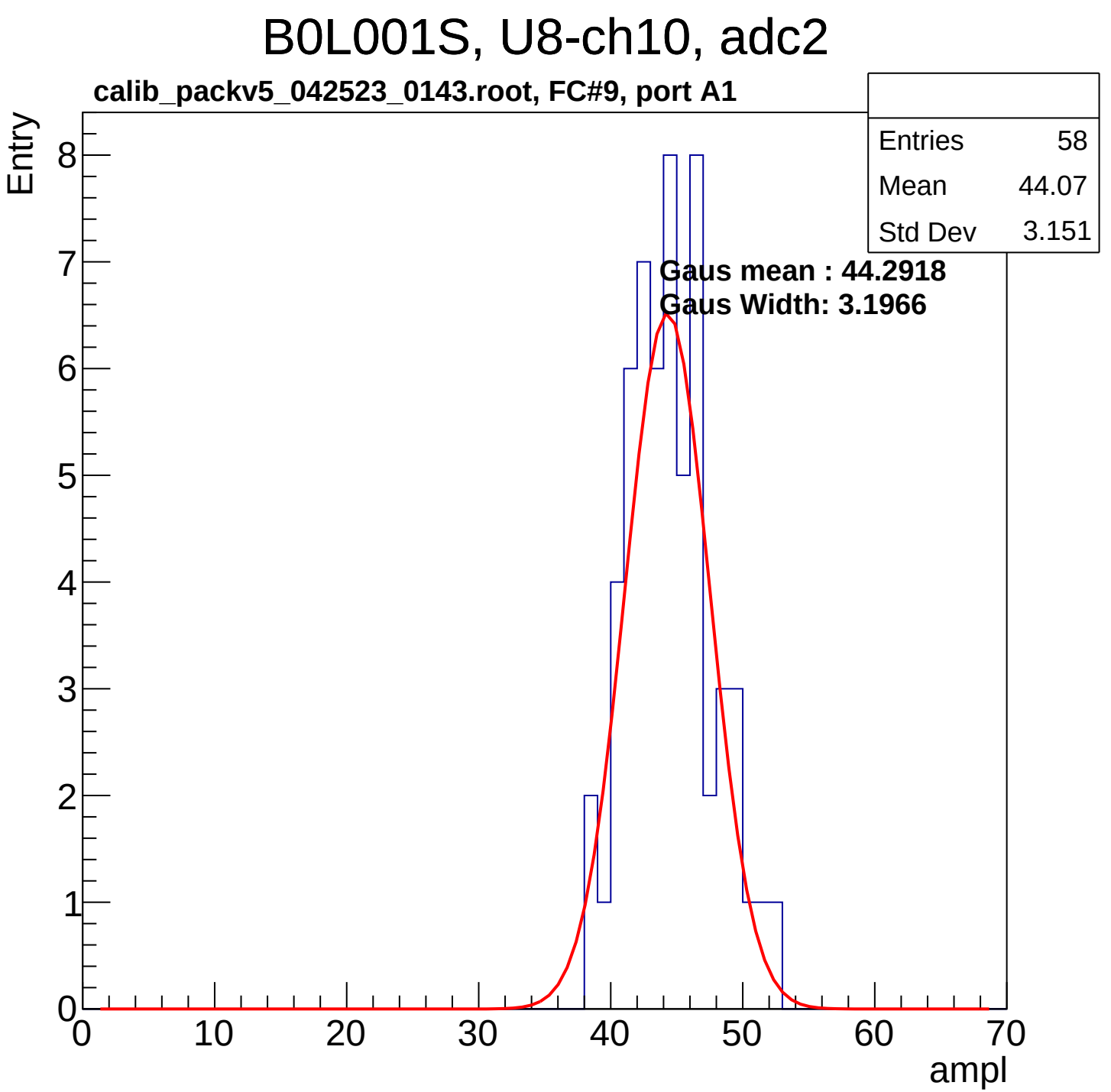
40
50
60
70


B0L001S, U8-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.32
Std Dev 3.183
Gaus mean : 38.0647
Gaus Width: 3.1765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.07
Std Dev 3.151
Gaus mean : 44.2918
Gaus Width: 3.1966
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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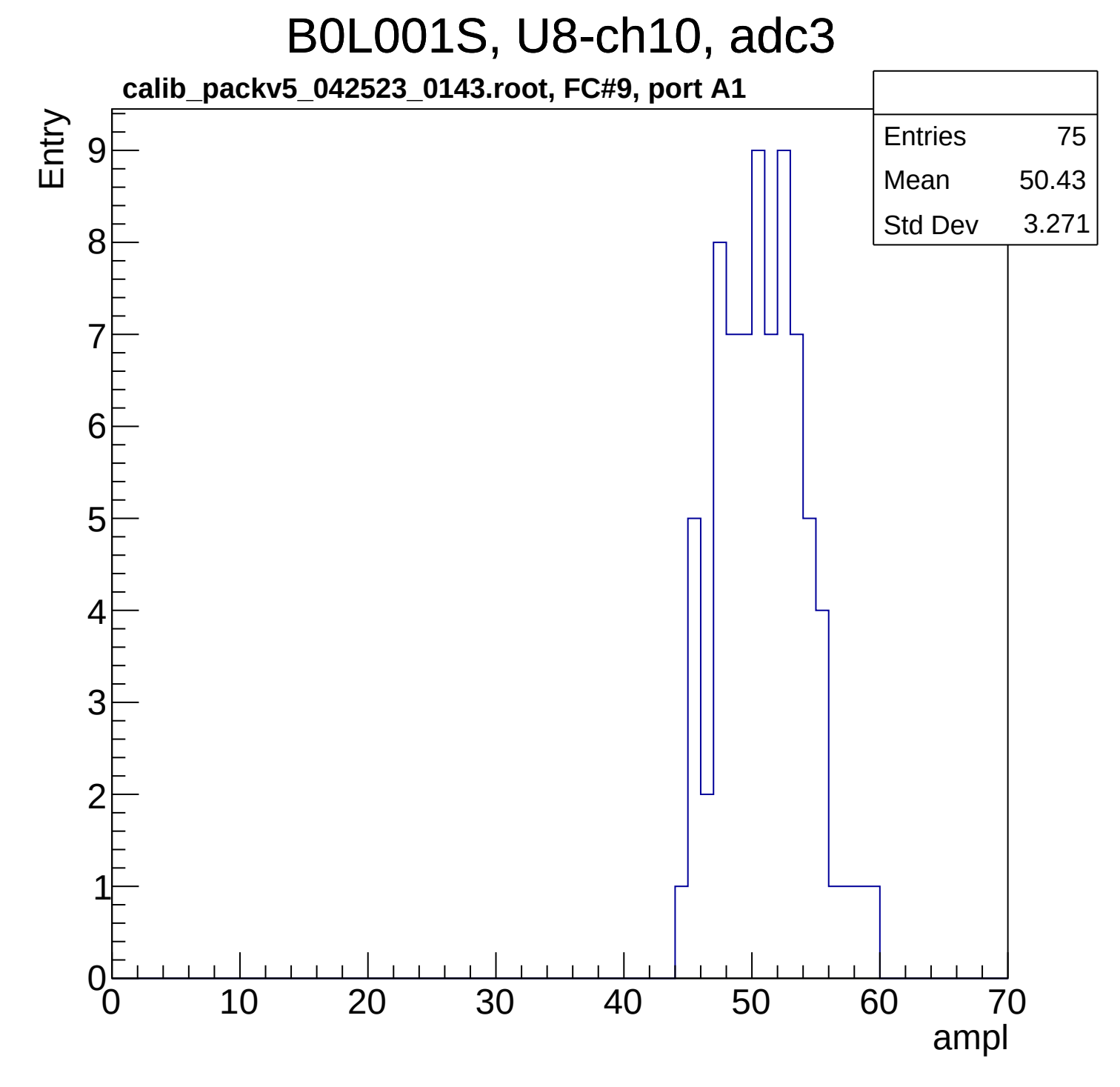
60
70


B0L001S, U8-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.43
Std Dev 3.271
Entry
ampl
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3
4
5
6
7
8
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0
10
20
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40
50
60
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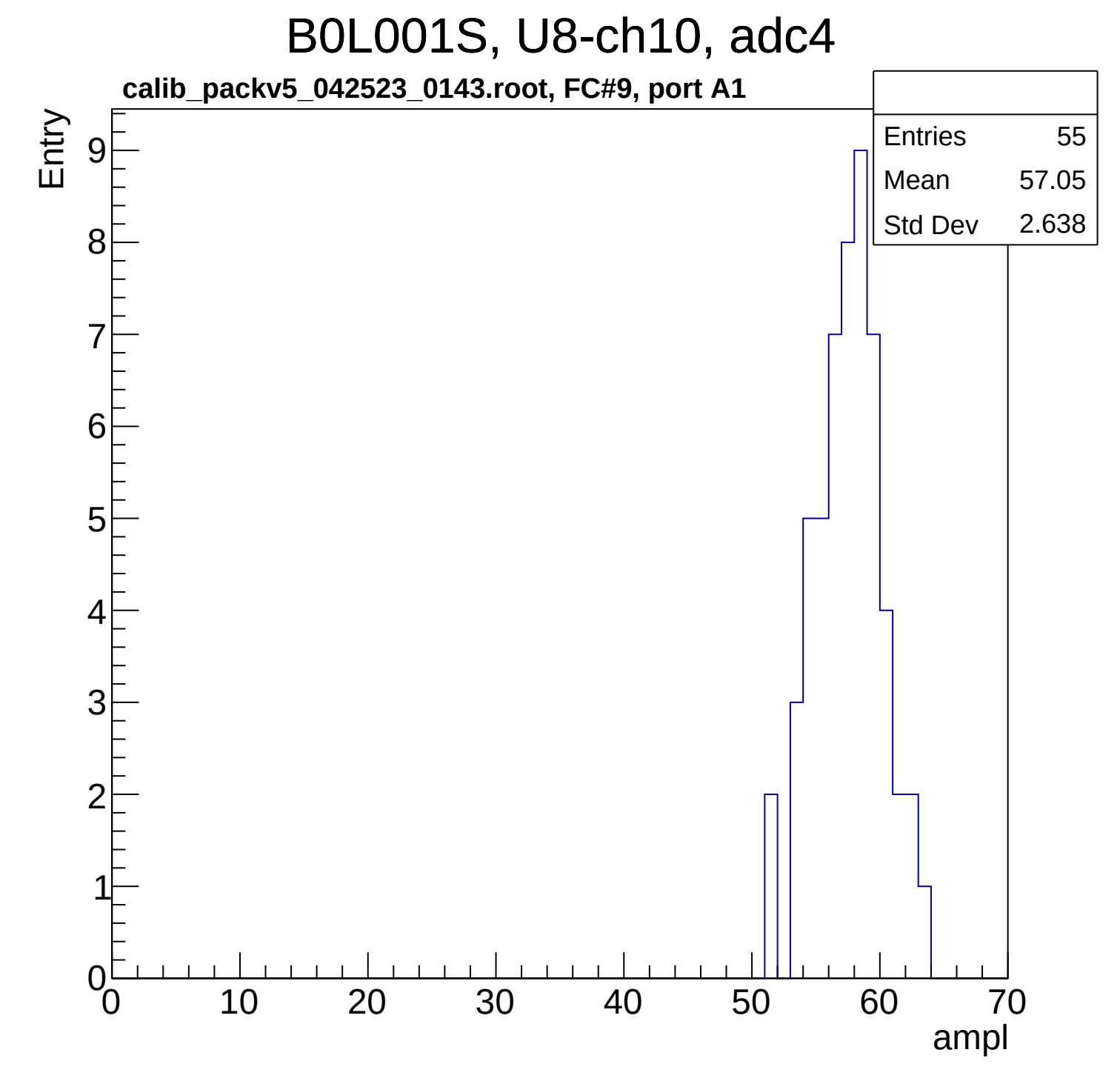
B0L001S, U8-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.05
Std Dev 2.638
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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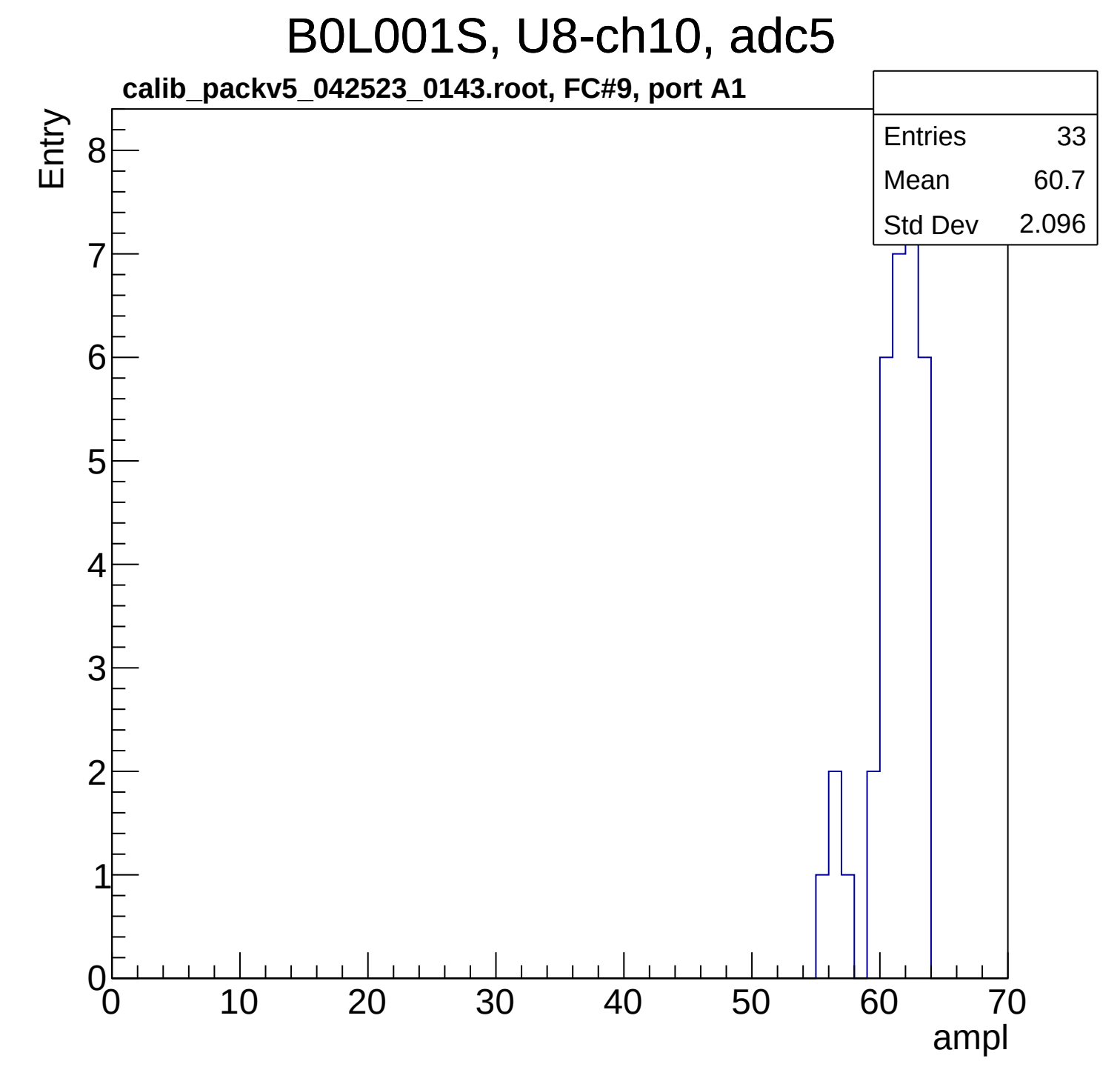
B0L001S, U8-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 60.7
Std Dev 2.096
Entry
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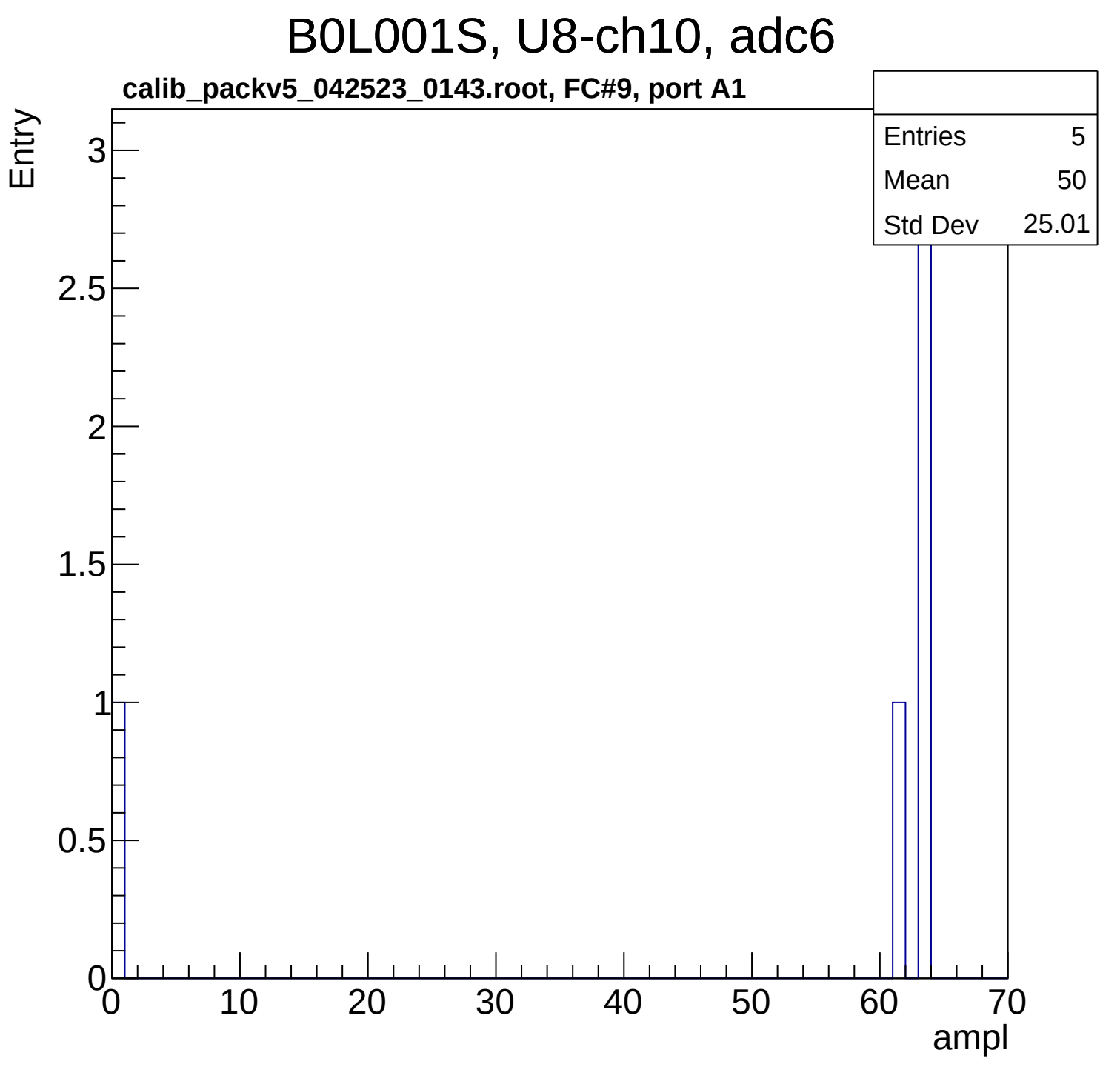
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
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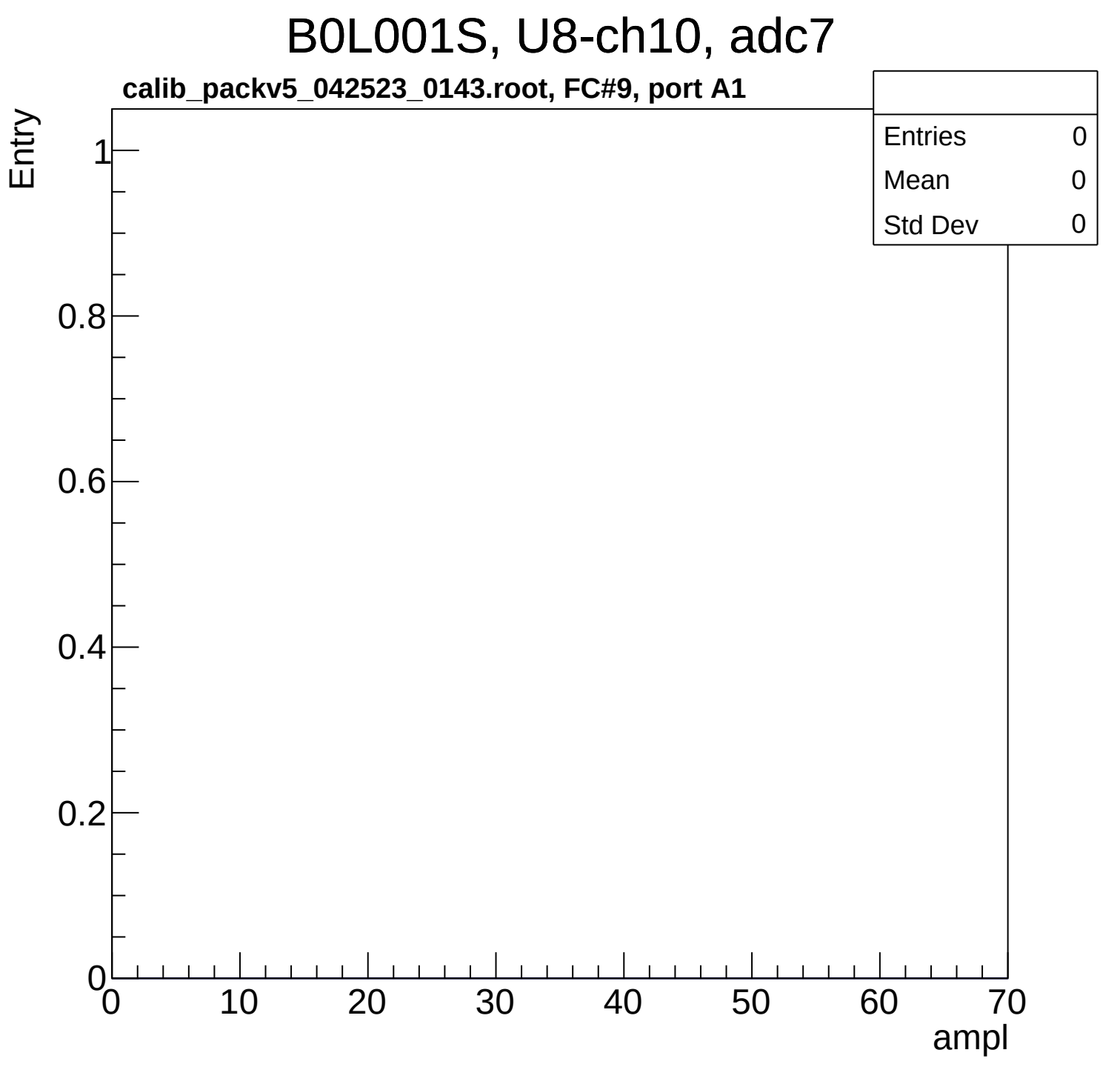
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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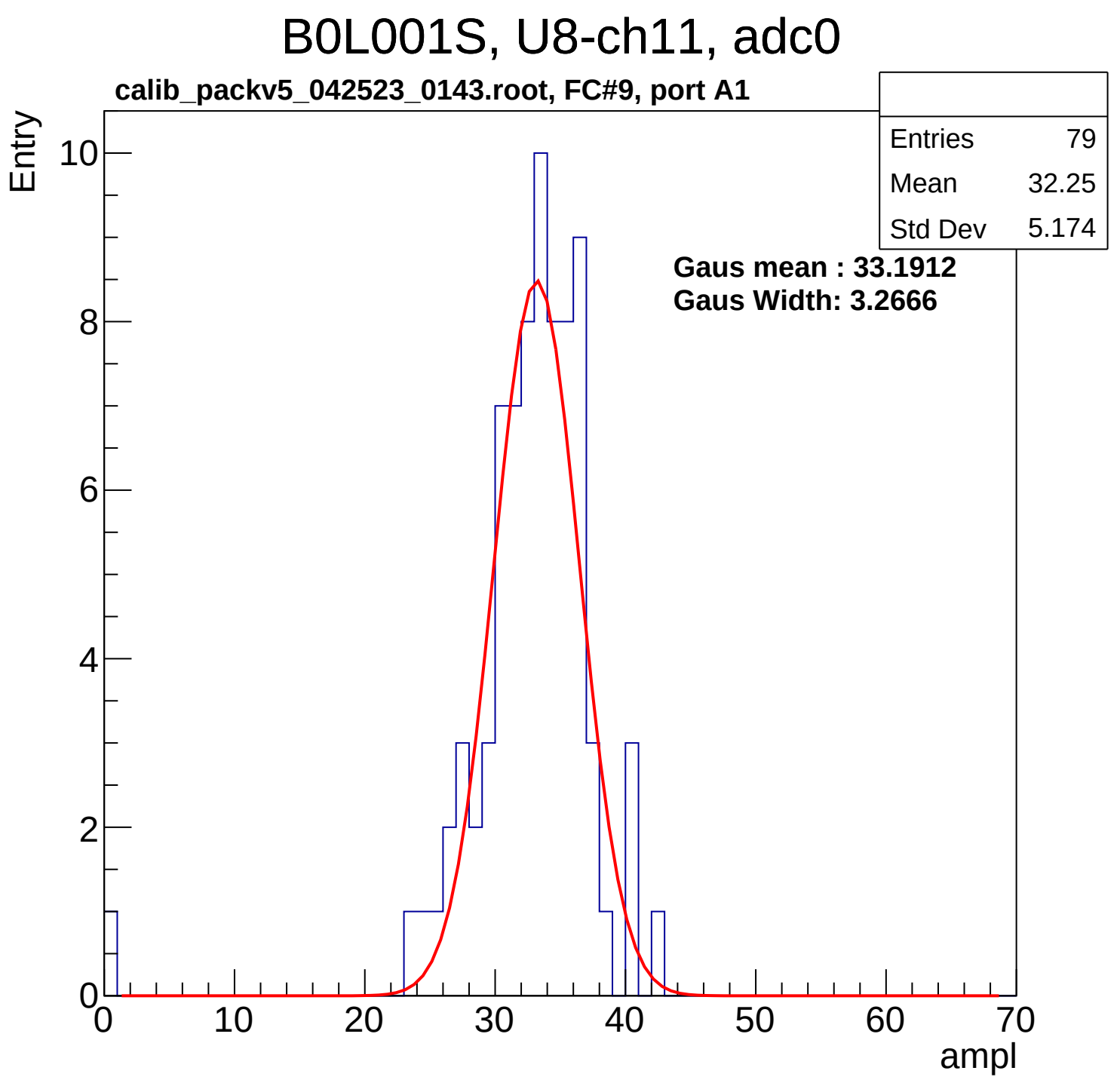
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 32.25
Std Dev 5.174
Gaus mean : 33.1912
Gaus Width: 3.2666
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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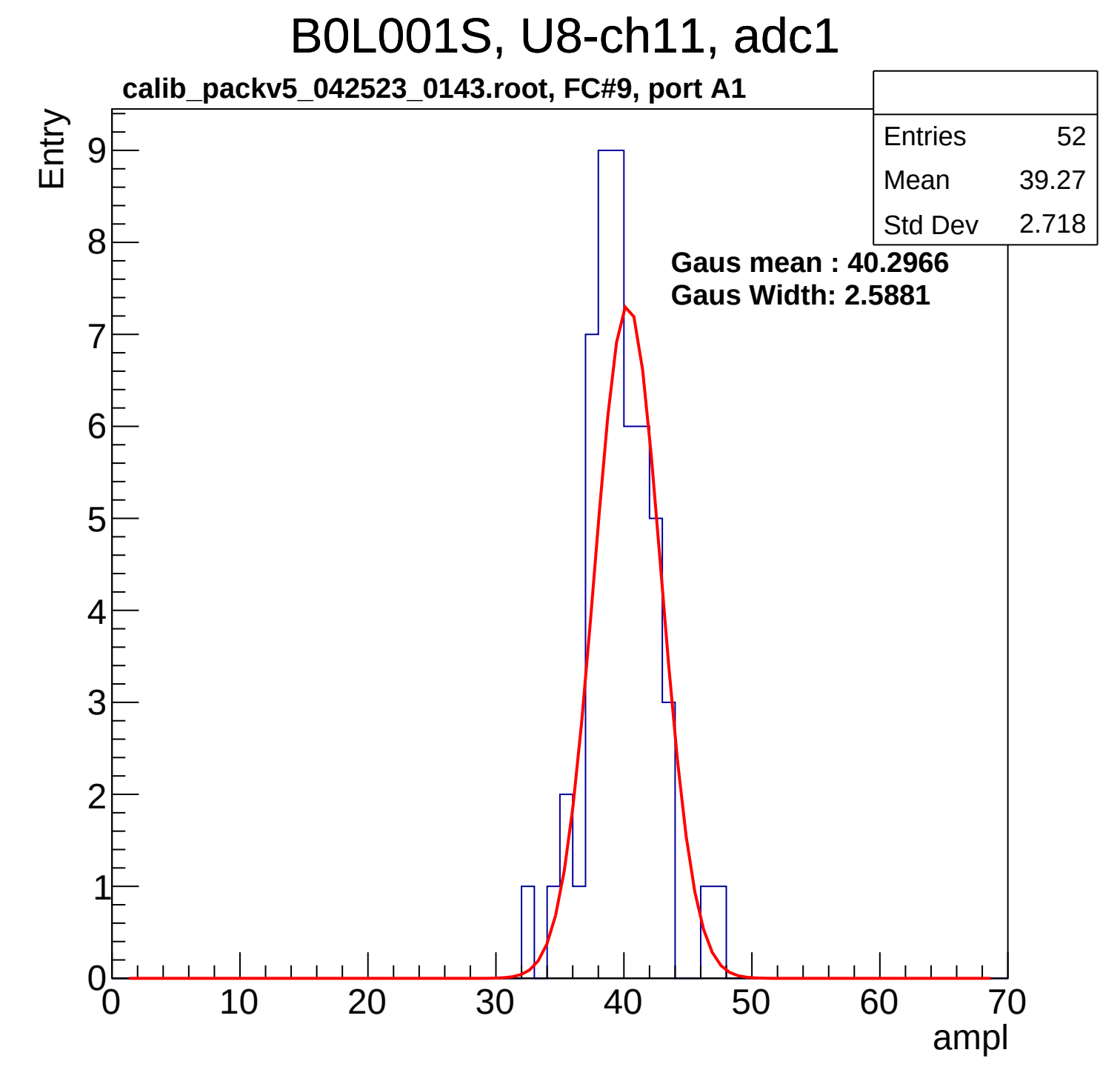
B0L001S, U8-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 39.27
Std Dev 2.718
Gaus mean : 40.2966
Gaus Width: 2.5881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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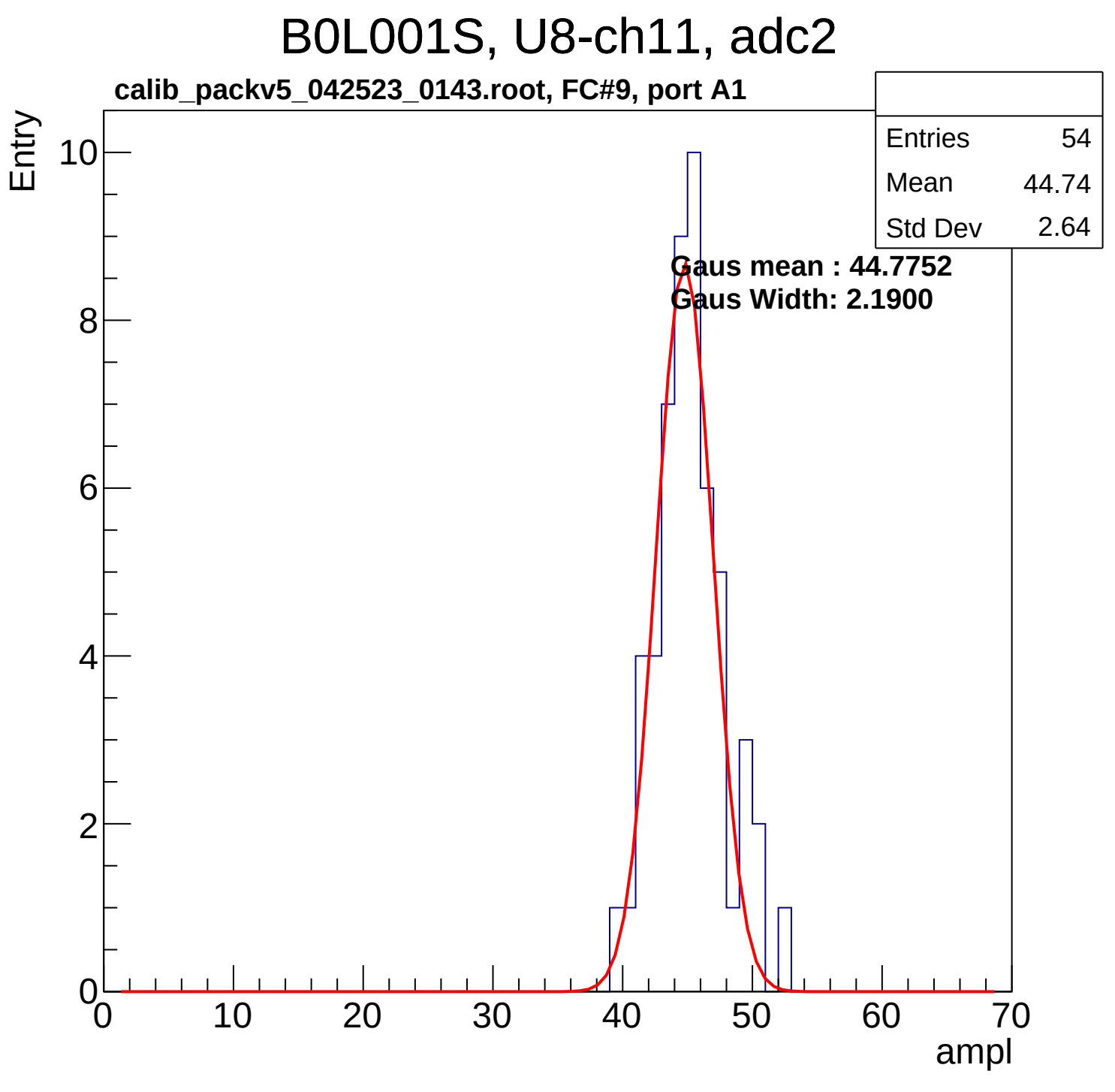
B0L001S, U8-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.74
Std Dev 2.64
Gaus mean : 44.7752
Gaus Width: 2.1900
Entry
ampl
0
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4
6
8
10
0
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40
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60
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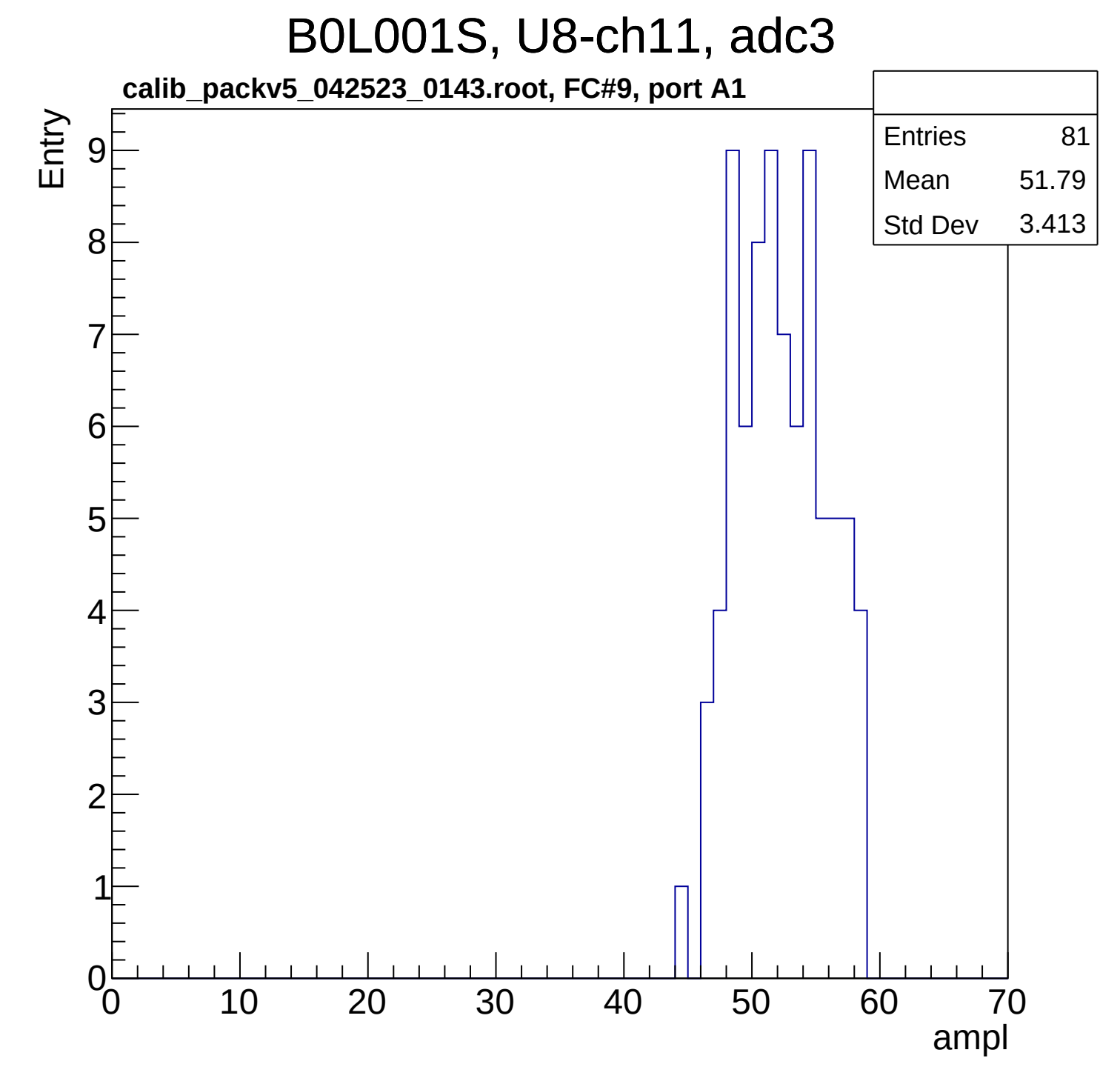
B0L001S, U8-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 51.79
Std Dev 3.413
Entry
ampl
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3
4
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6
7
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0
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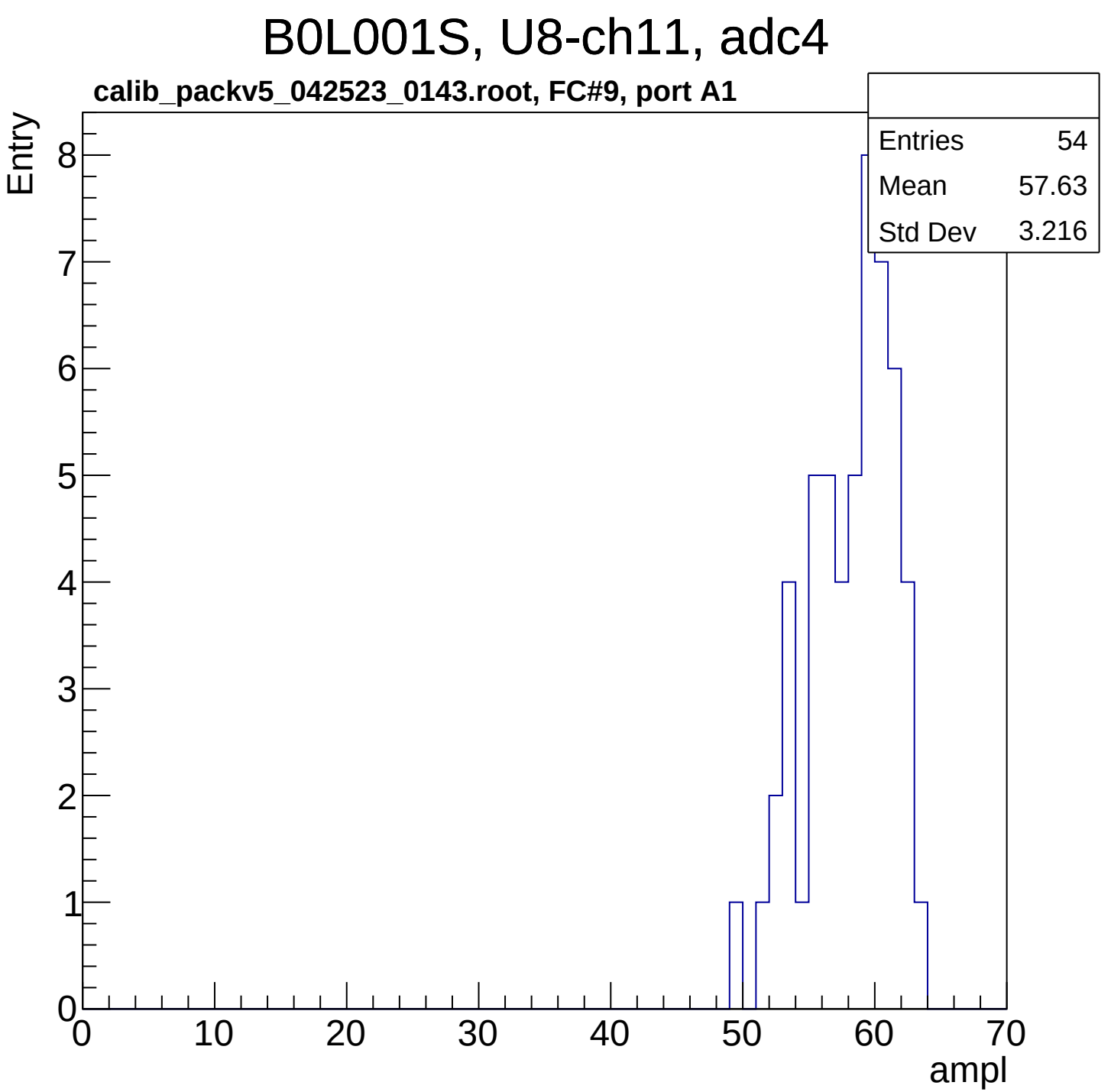
B0L001S, U8-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.63
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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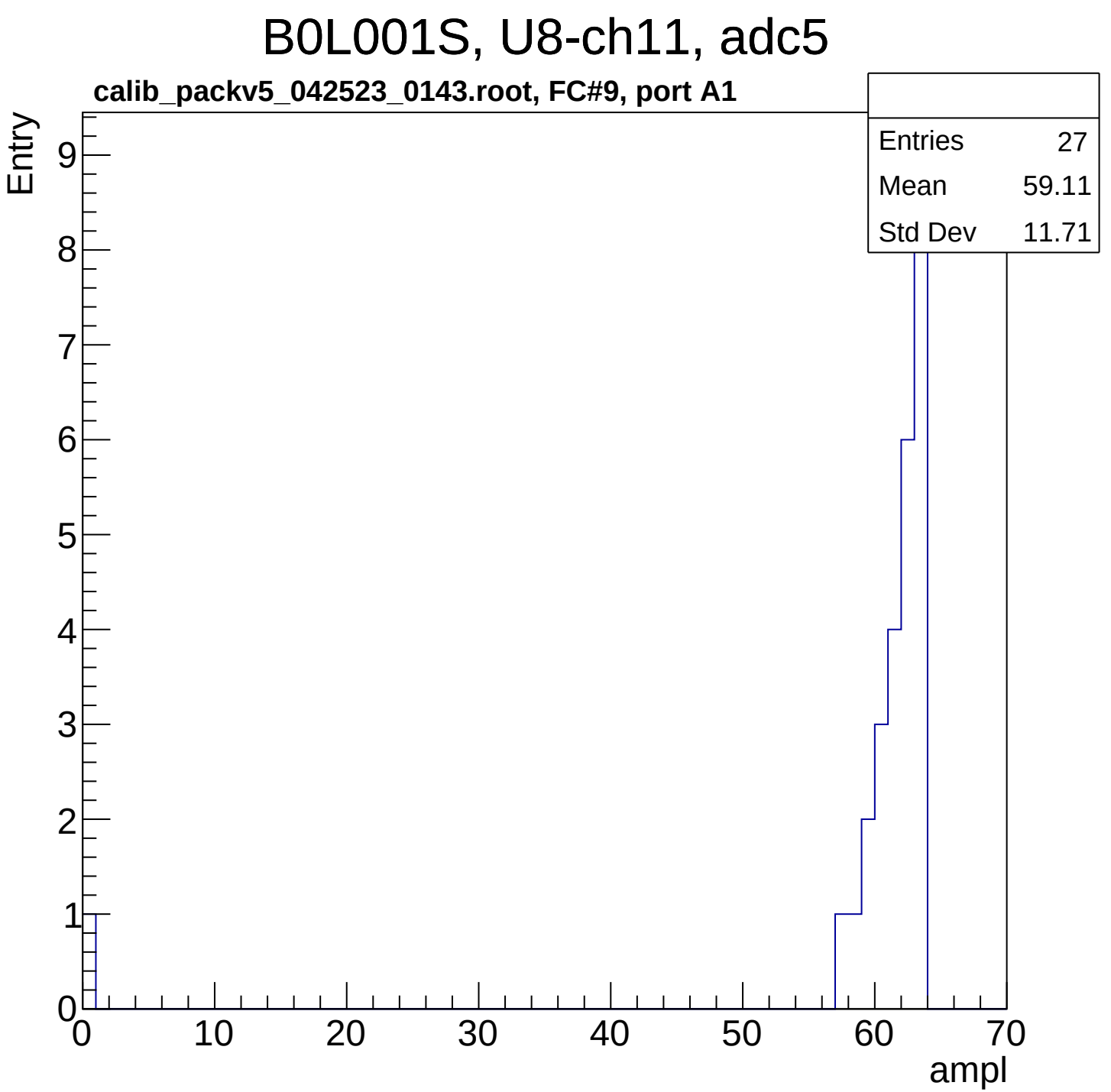
B0L001S, U8-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 59.11
Std Dev 11.71
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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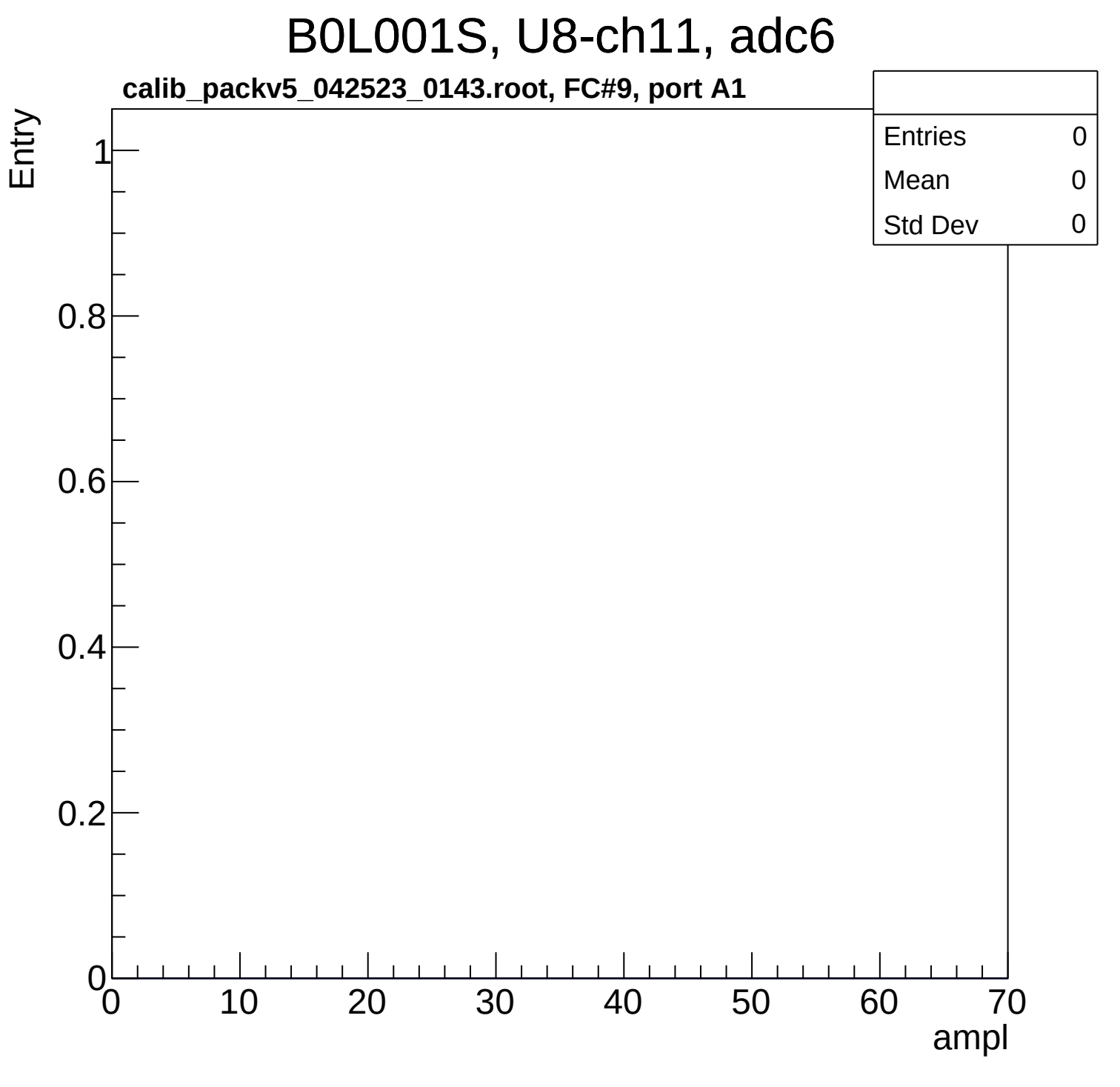
B0L001S, U8-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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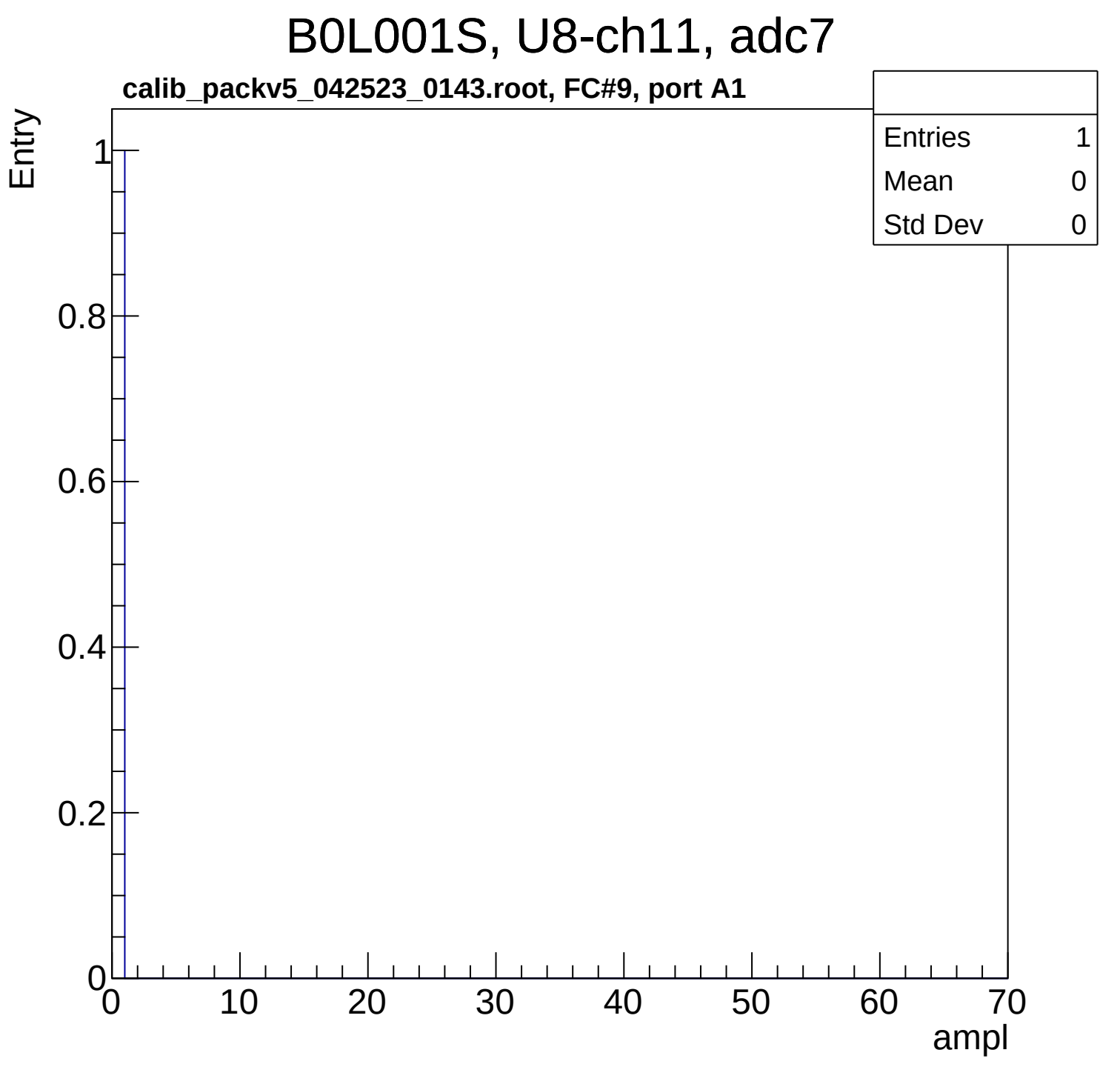
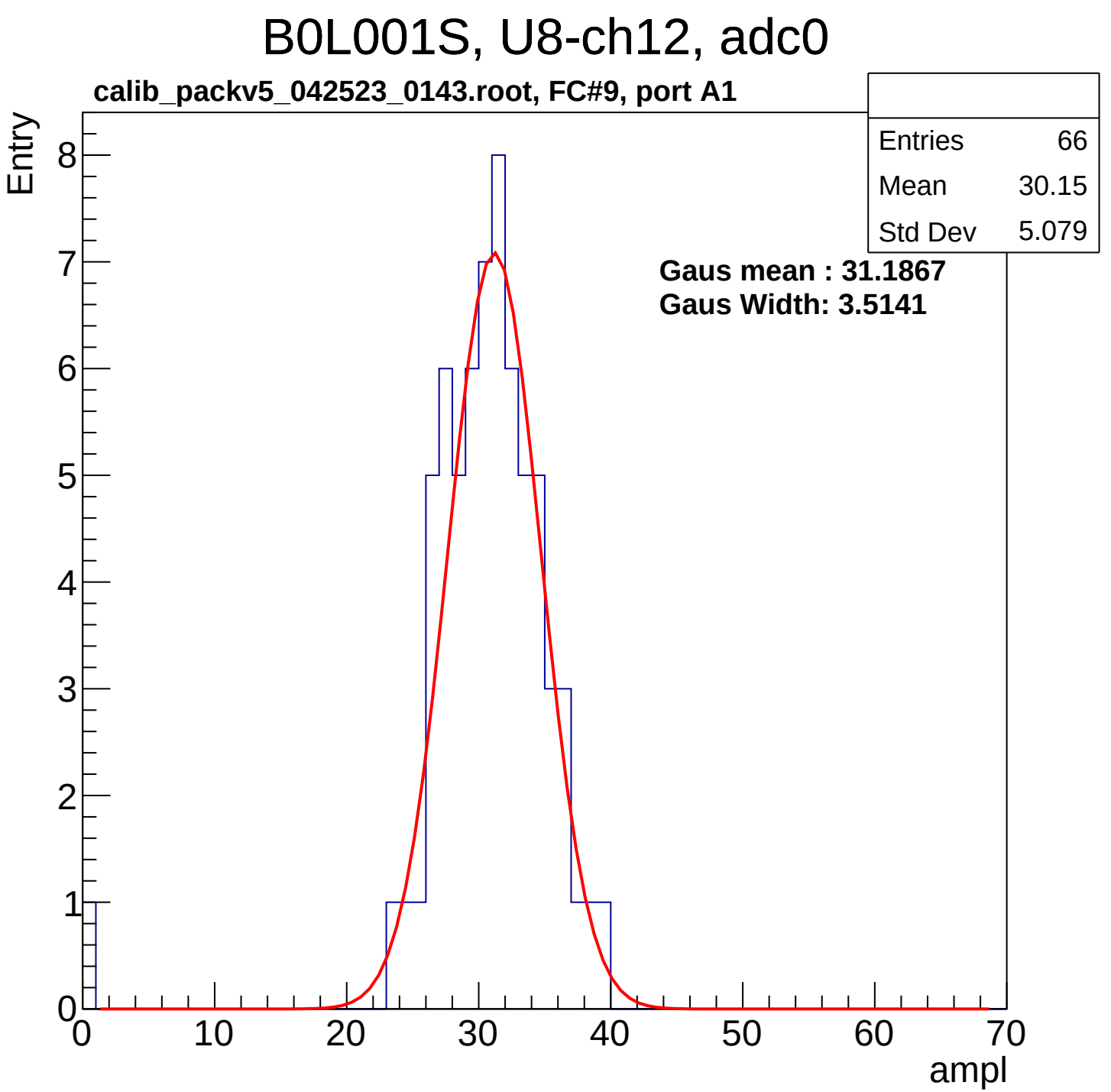
40
50
60
70
ampl


B0L001S, U8-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.15
Std Dev 5.079
Gaus mean : 31.1867
Gaus Width: 3.5141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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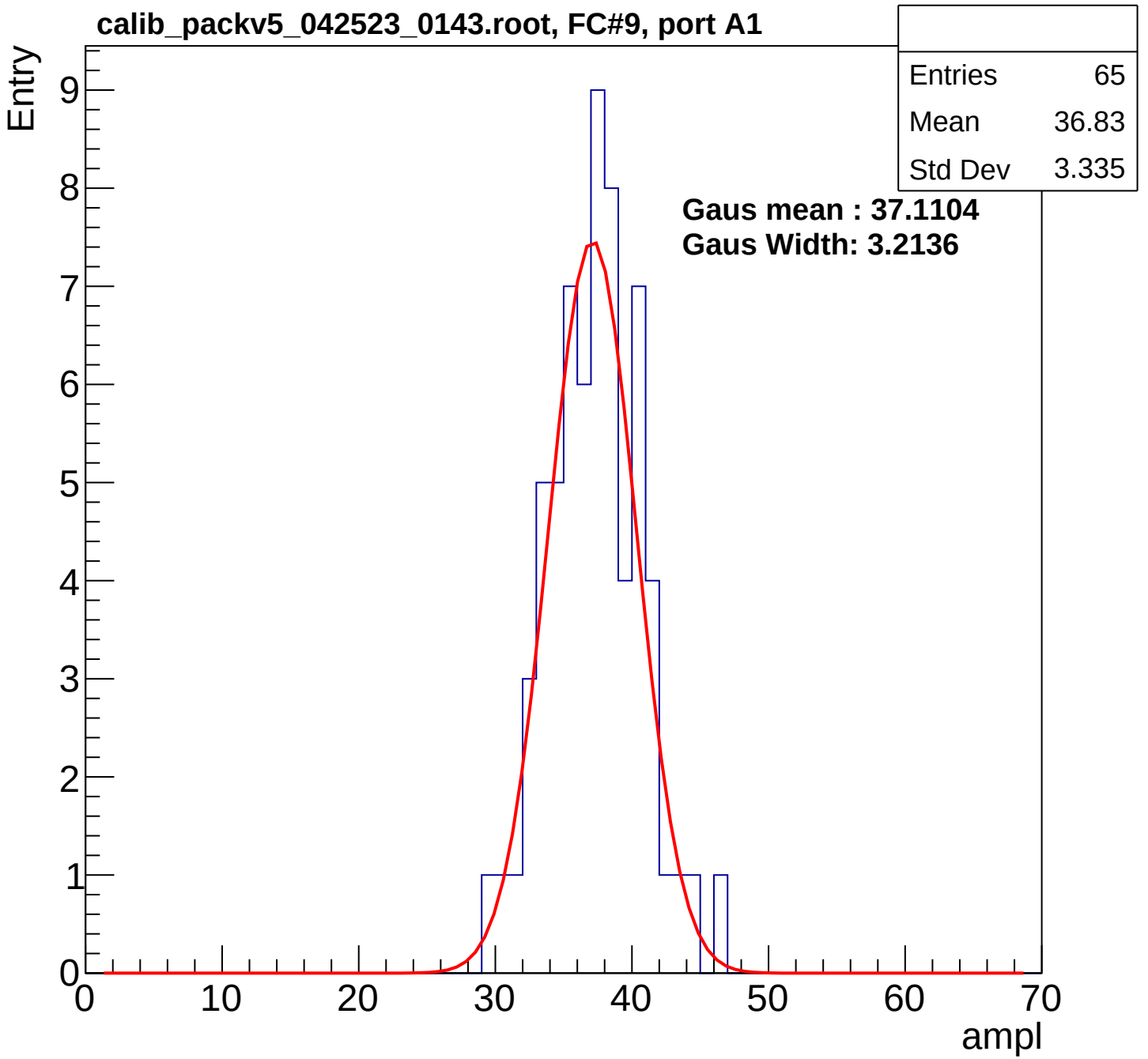
B0L001S, U8-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.83
Std Dev 3.335
Gaus mean : 37.1104
Gaus Width: 3.2136
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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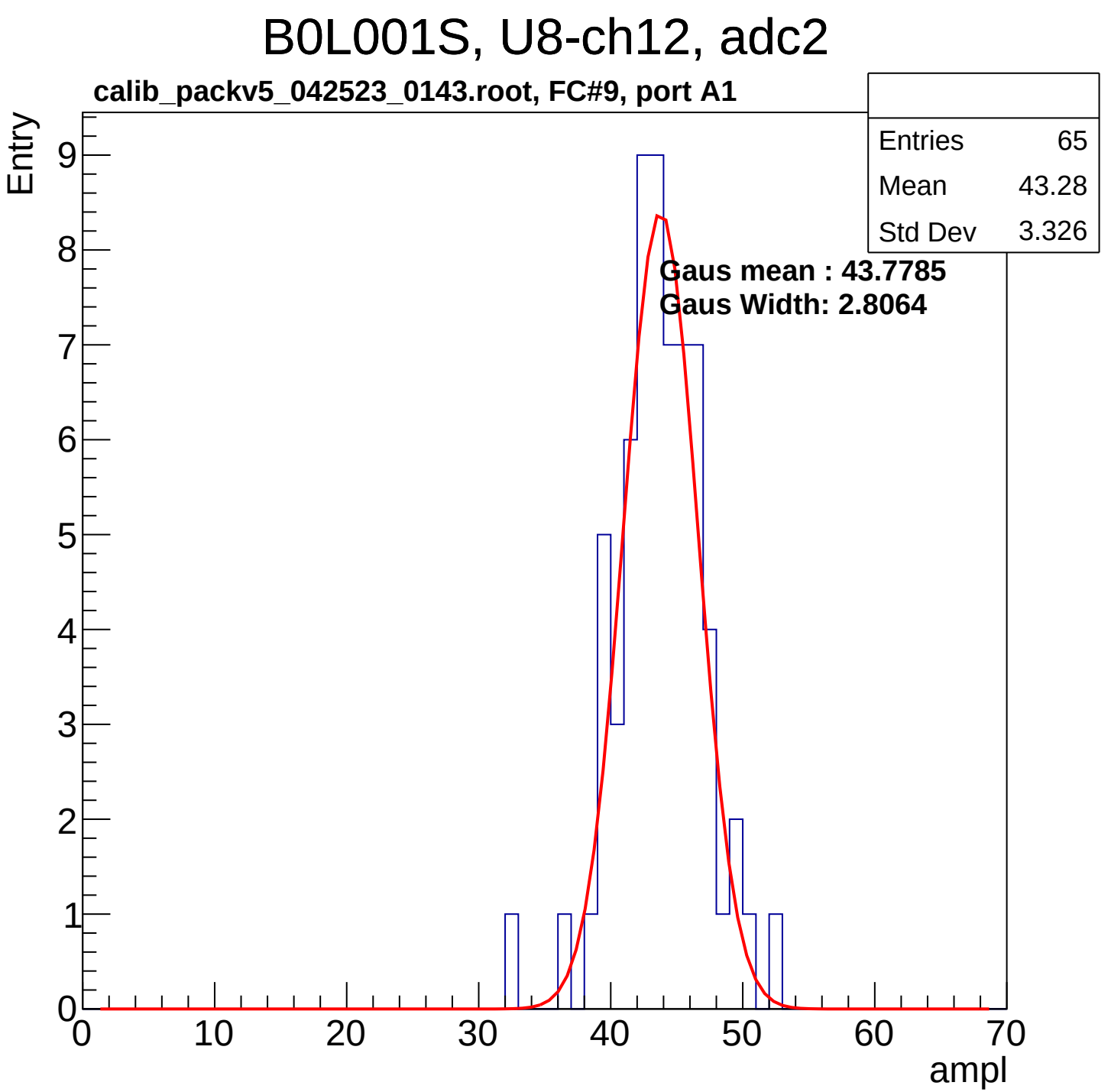
B0L001S, U8-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.28
Std Dev 3.326
Gaus mean : 43.7785
Gaus Width: 2.8064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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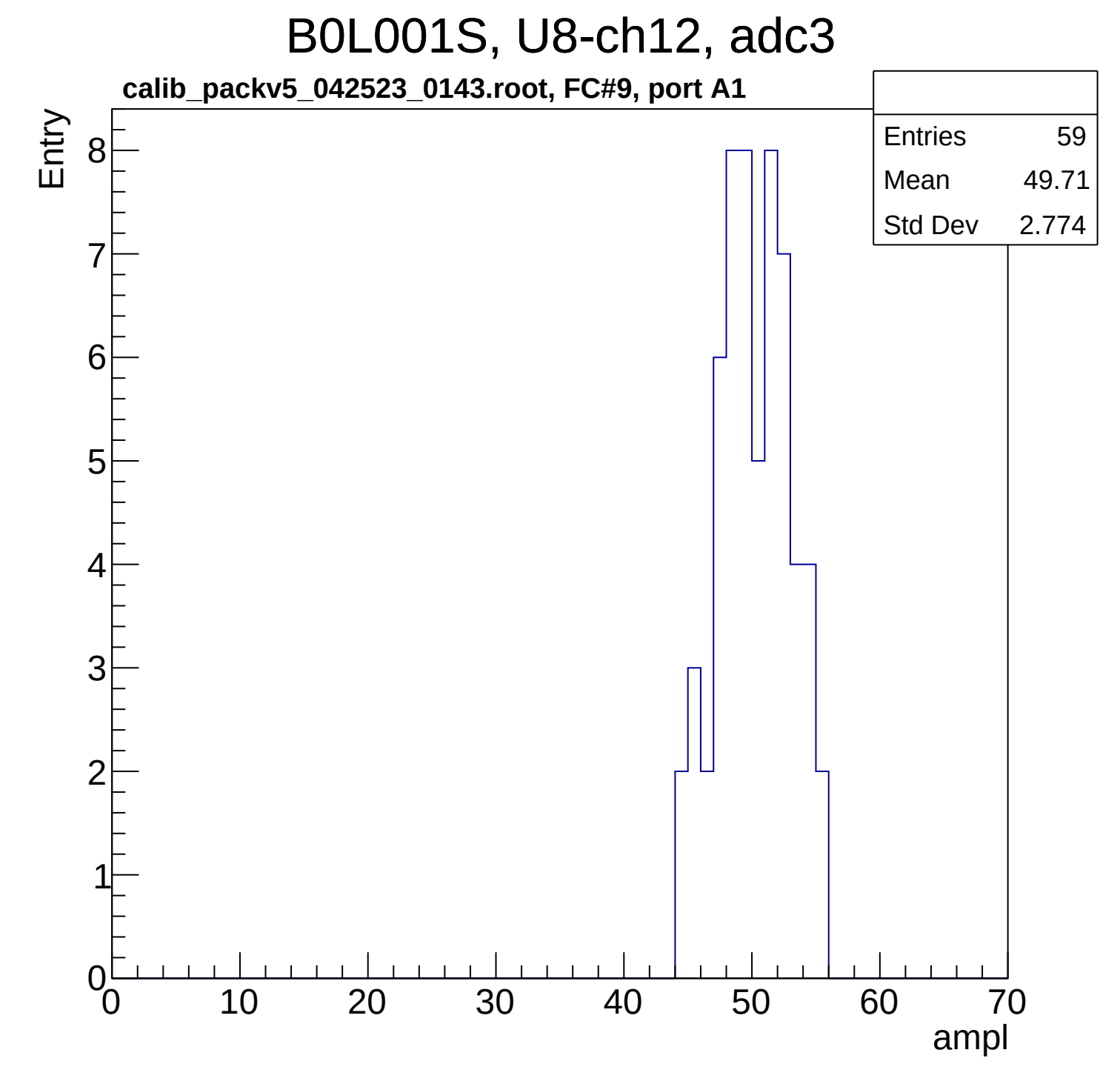
B0L001S, U8-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 49.71
Std Dev 2.774
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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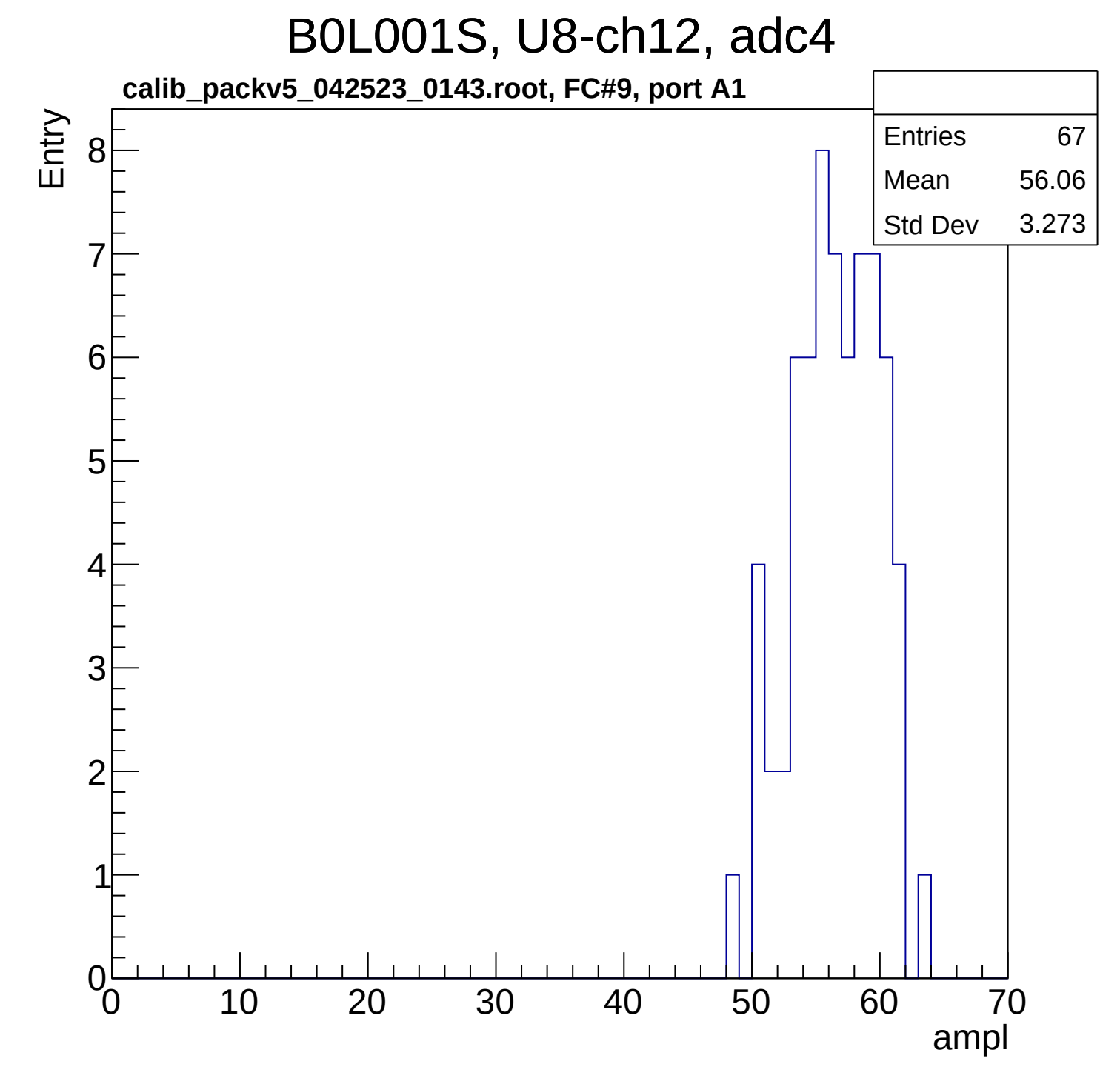
B0L001S, U8-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.06
Std Dev 3.273
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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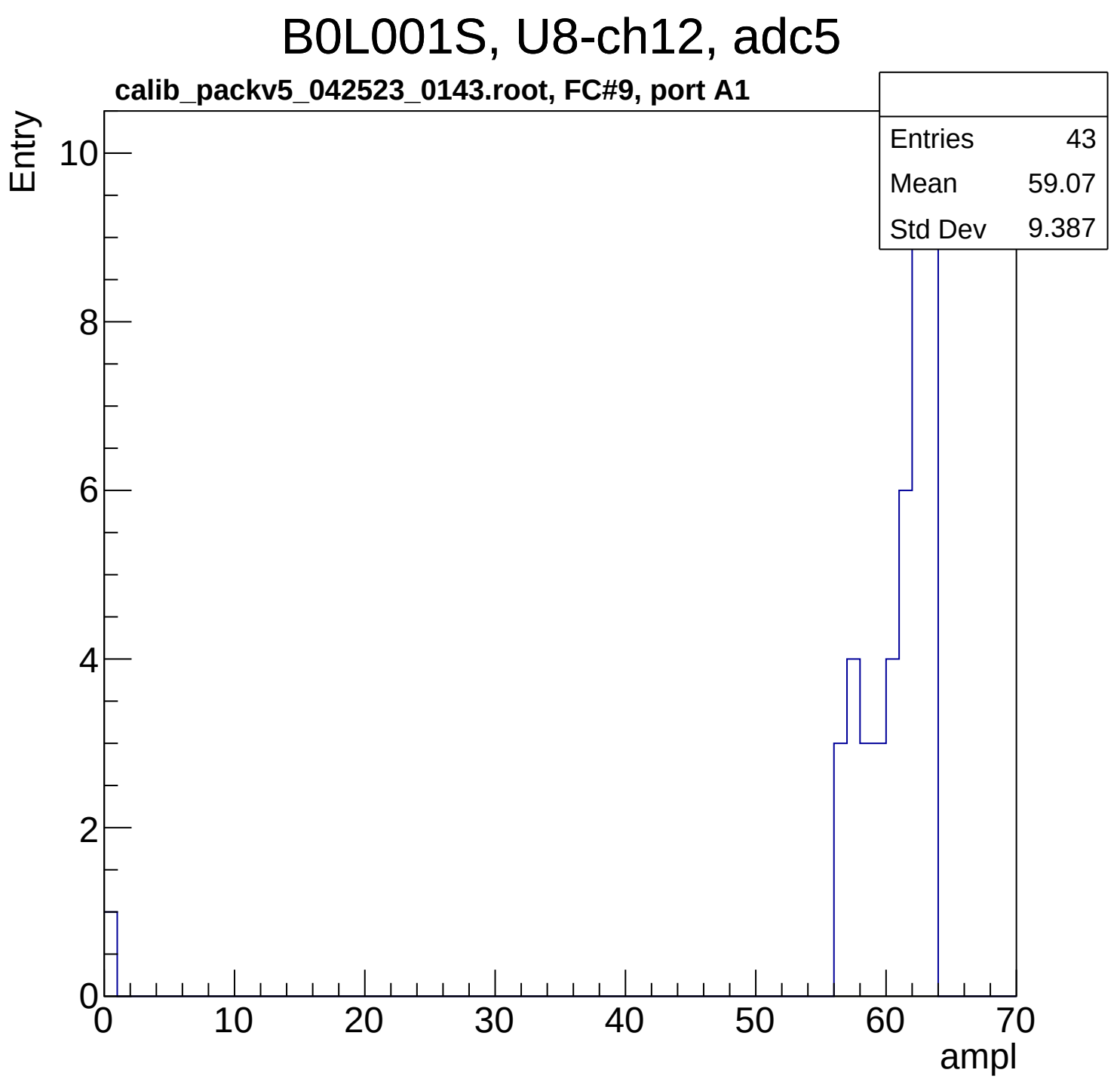
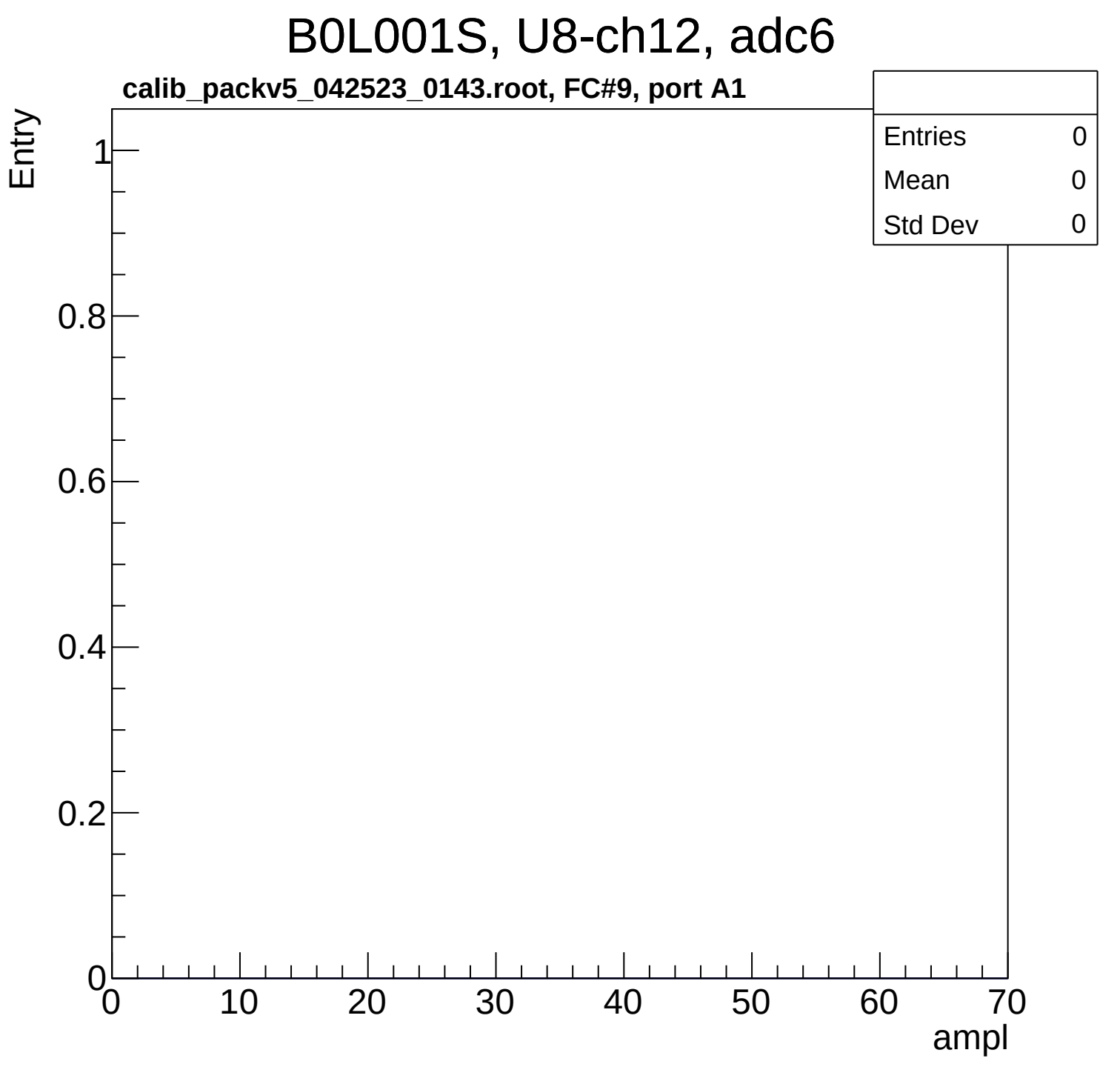
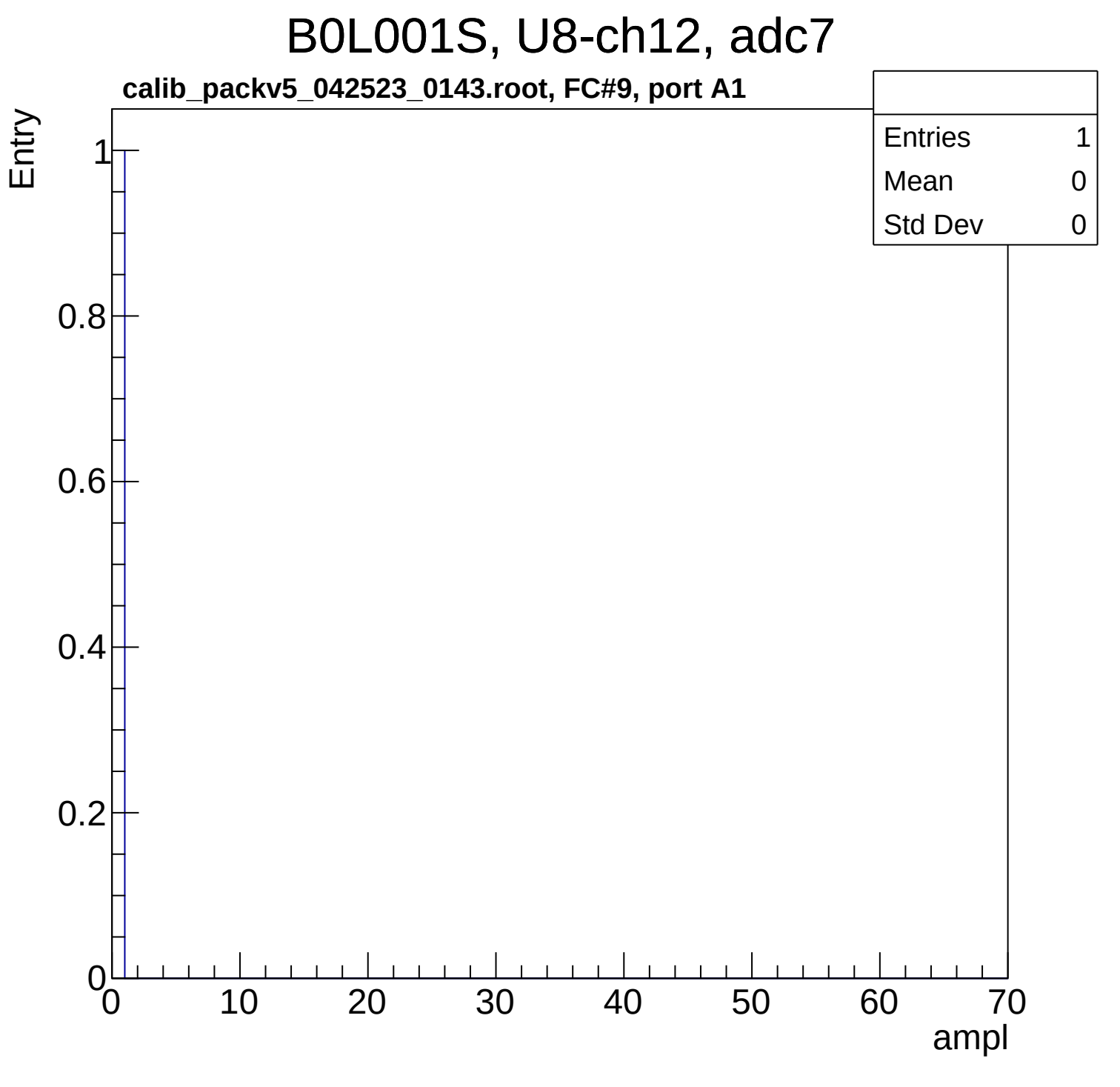
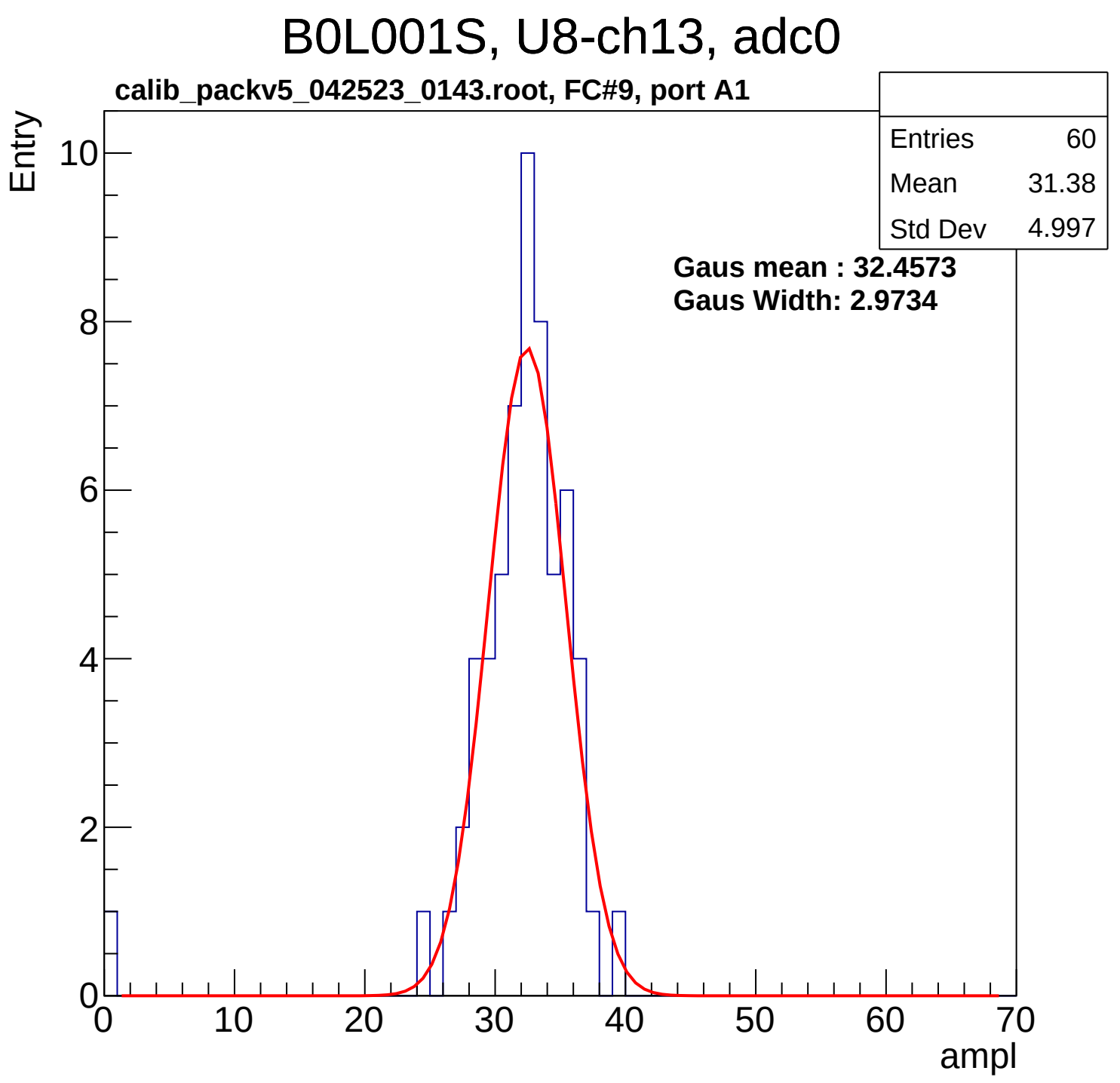

B0L001S, U8-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.07
Std Dev 9.387
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.38
Std Dev 4.997
Gaus mean : 32.4573
Gaus Width: 2.9734
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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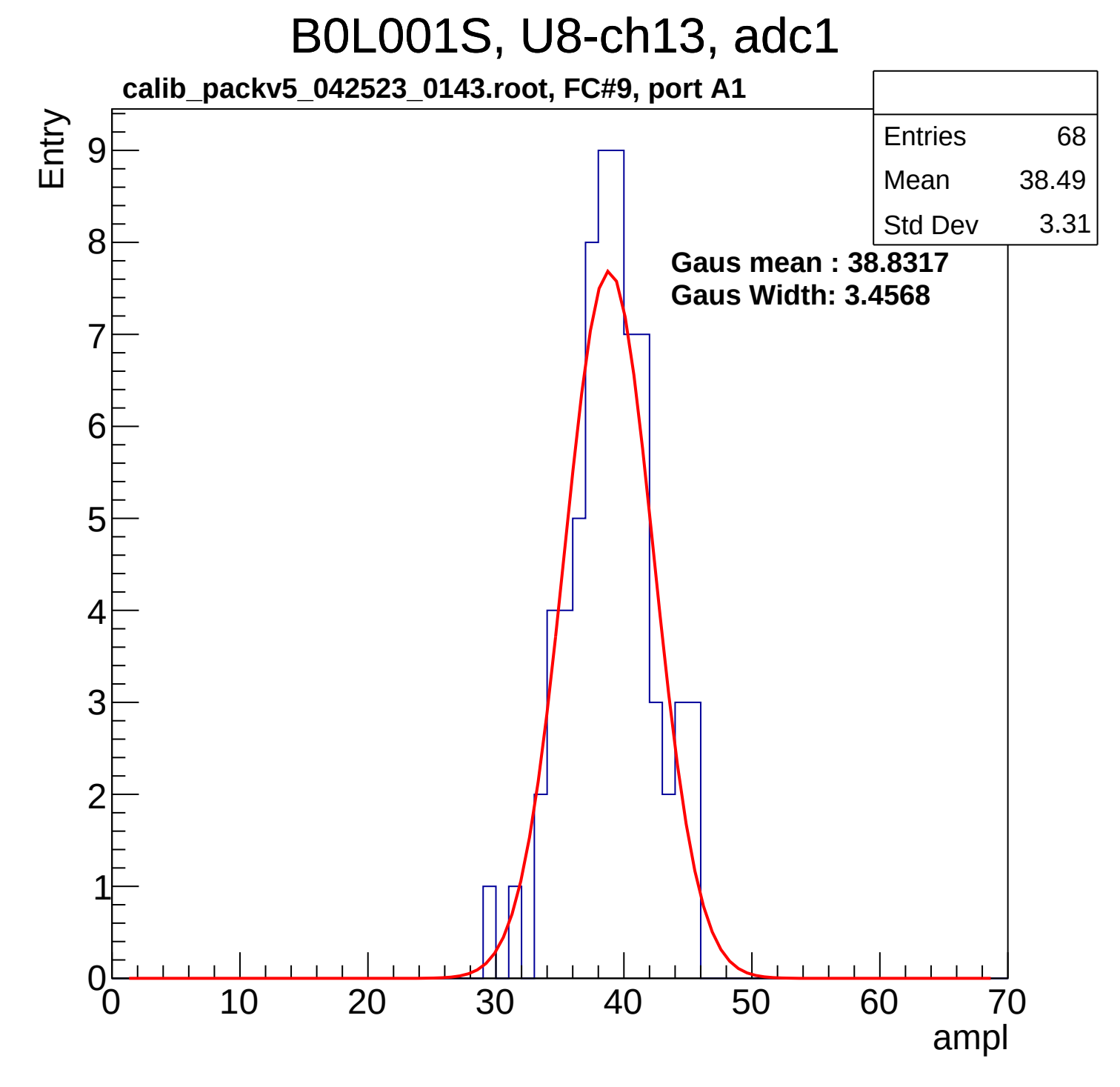
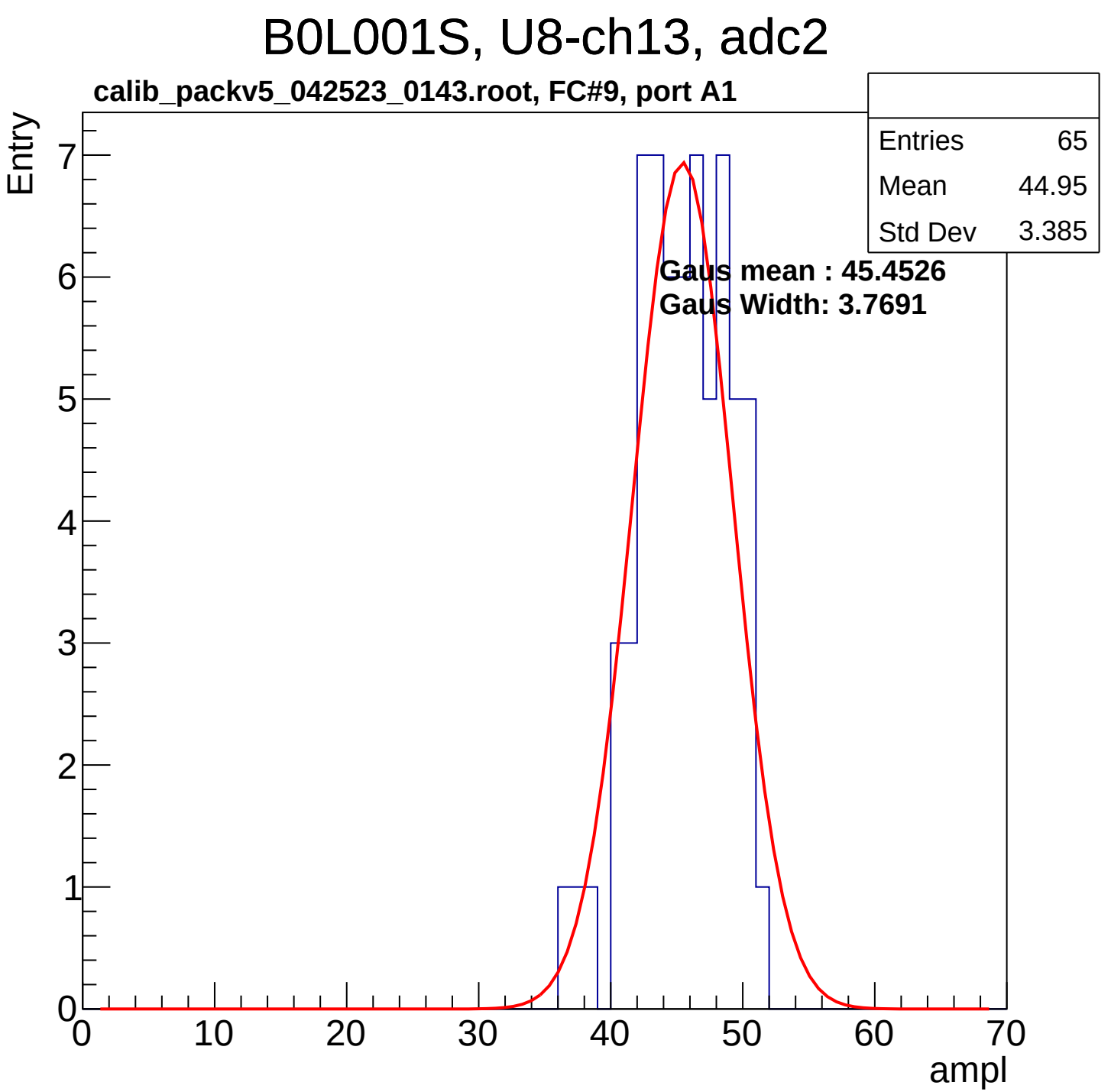
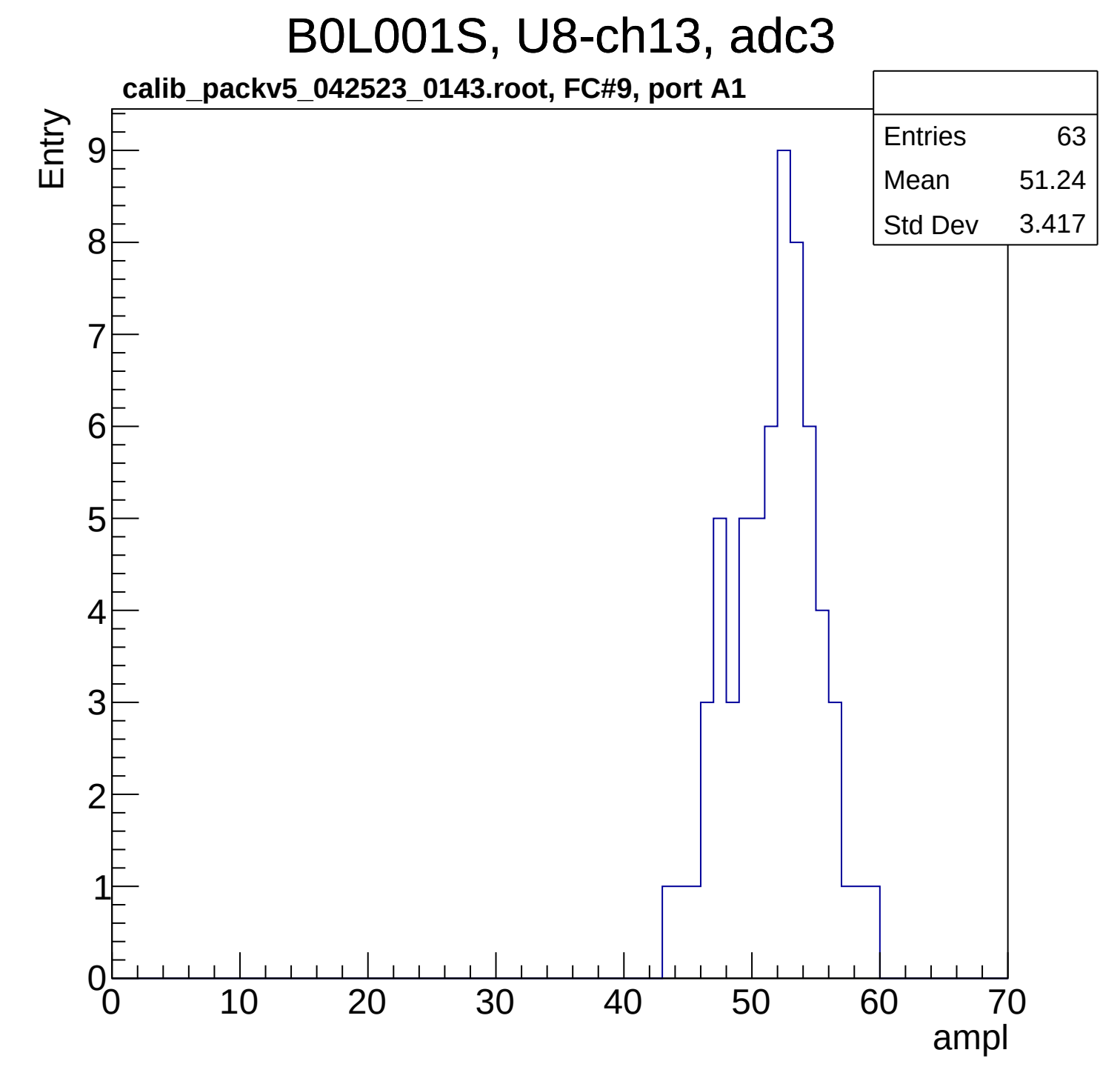

B0L001S, U8-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 38.49
Std Dev 3.31
Gaus mean : 38.8317
Gaus Width: 3.4568
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.95
Std Dev 3.385
Gaus mean : 45.4526
Gaus Width: 3.7691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.24
Std Dev 3.417
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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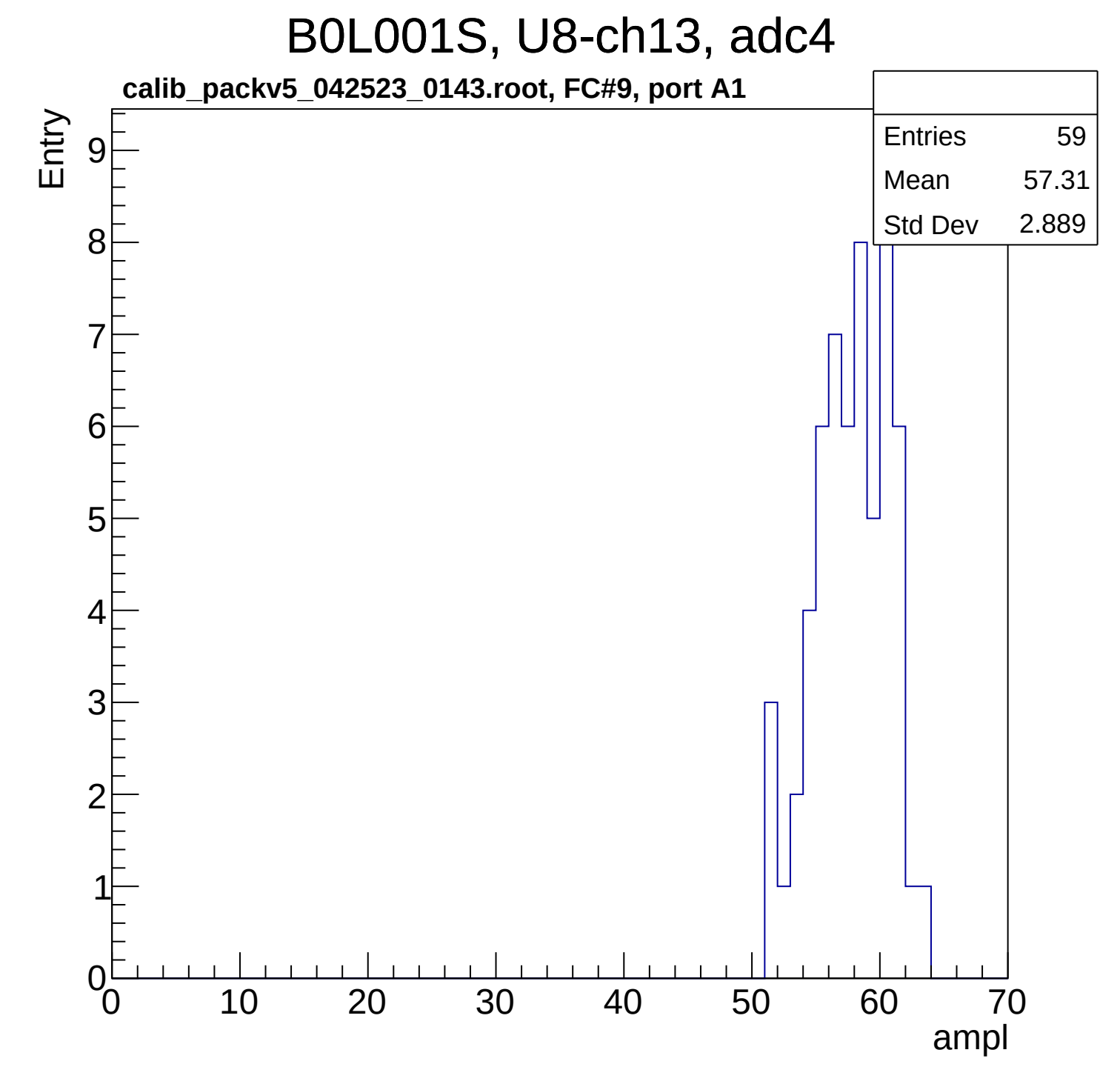
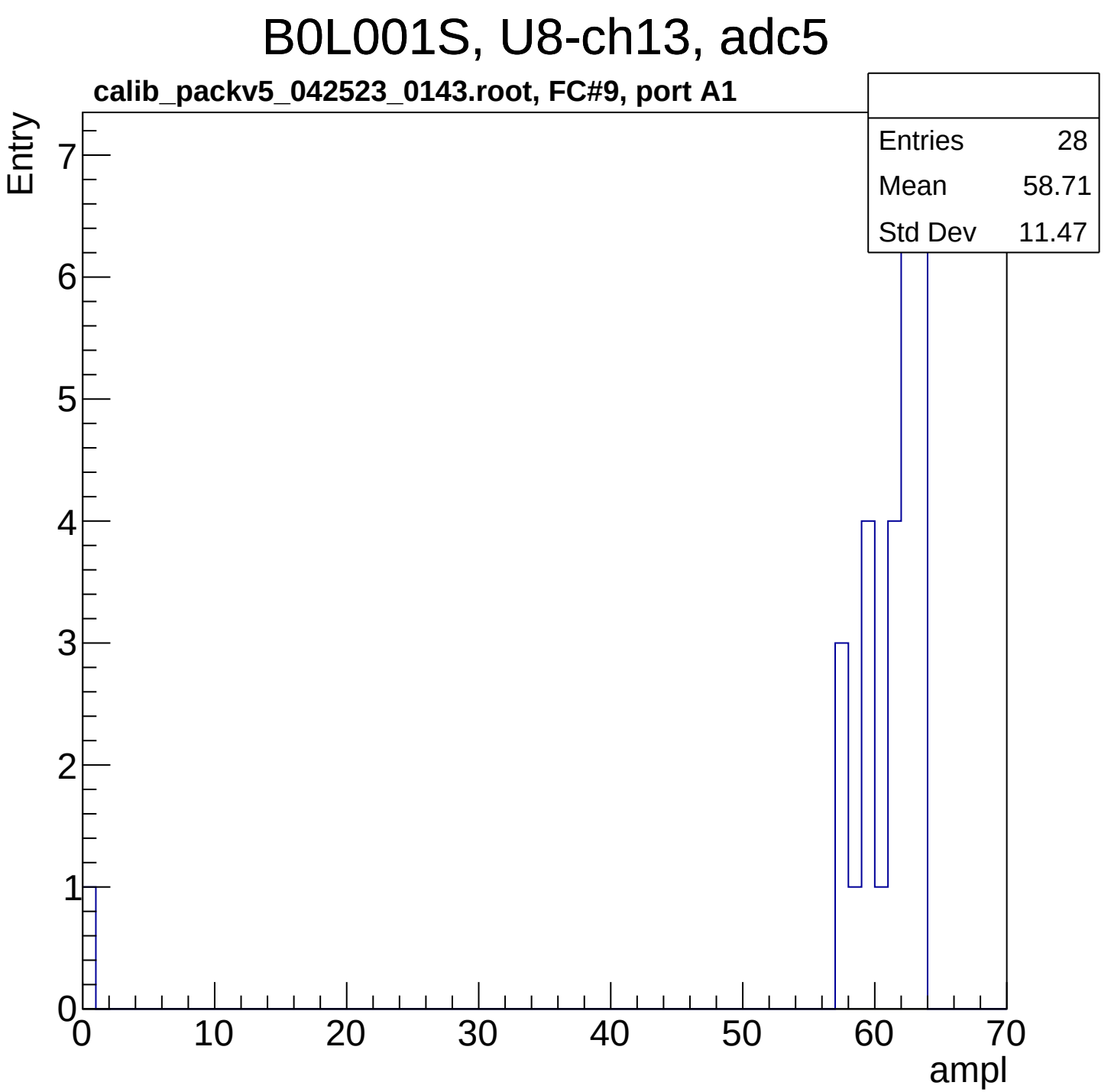

B0L001S, U8-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.31
Std Dev 2.889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.71
Std Dev 11.47
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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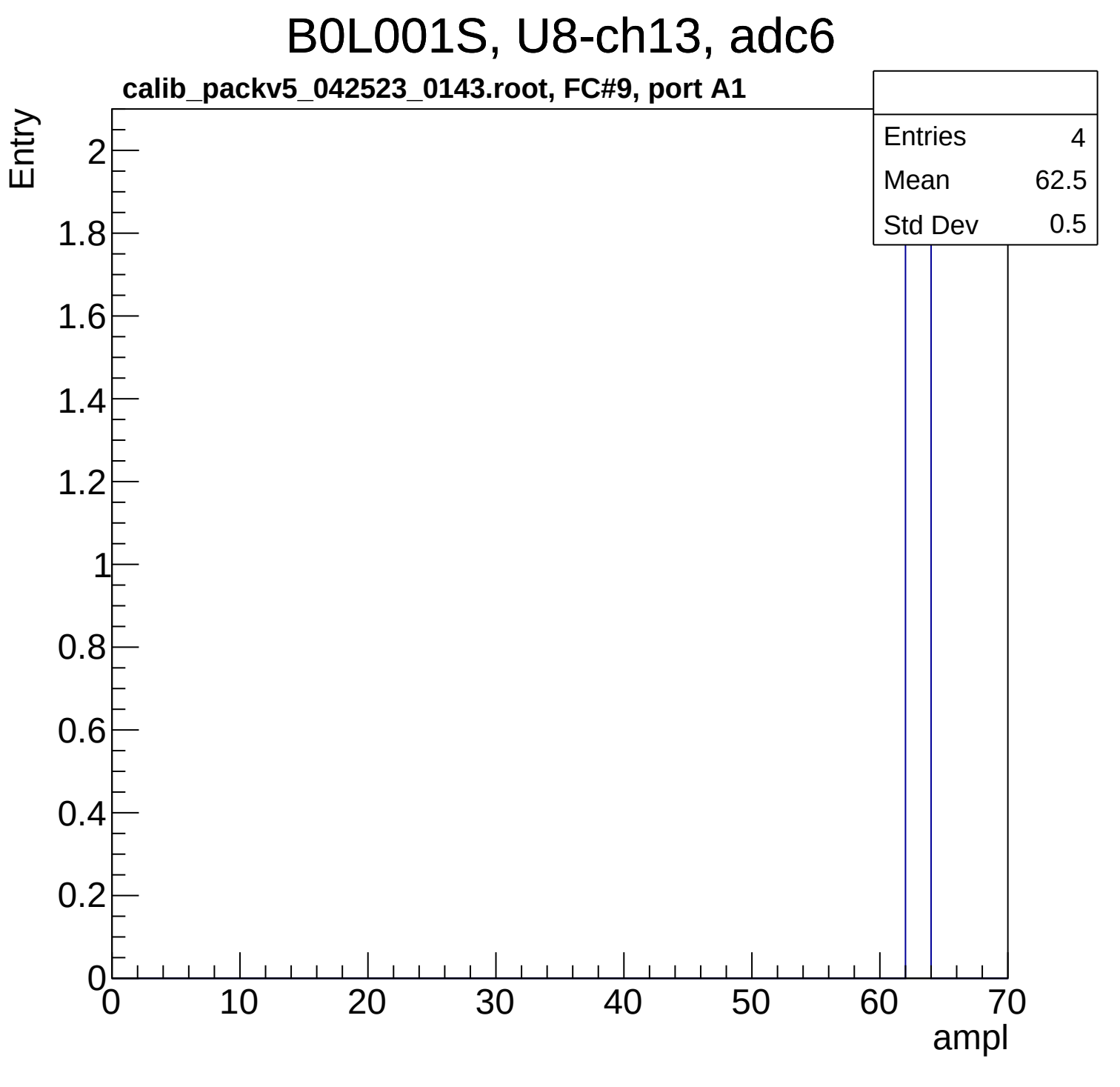
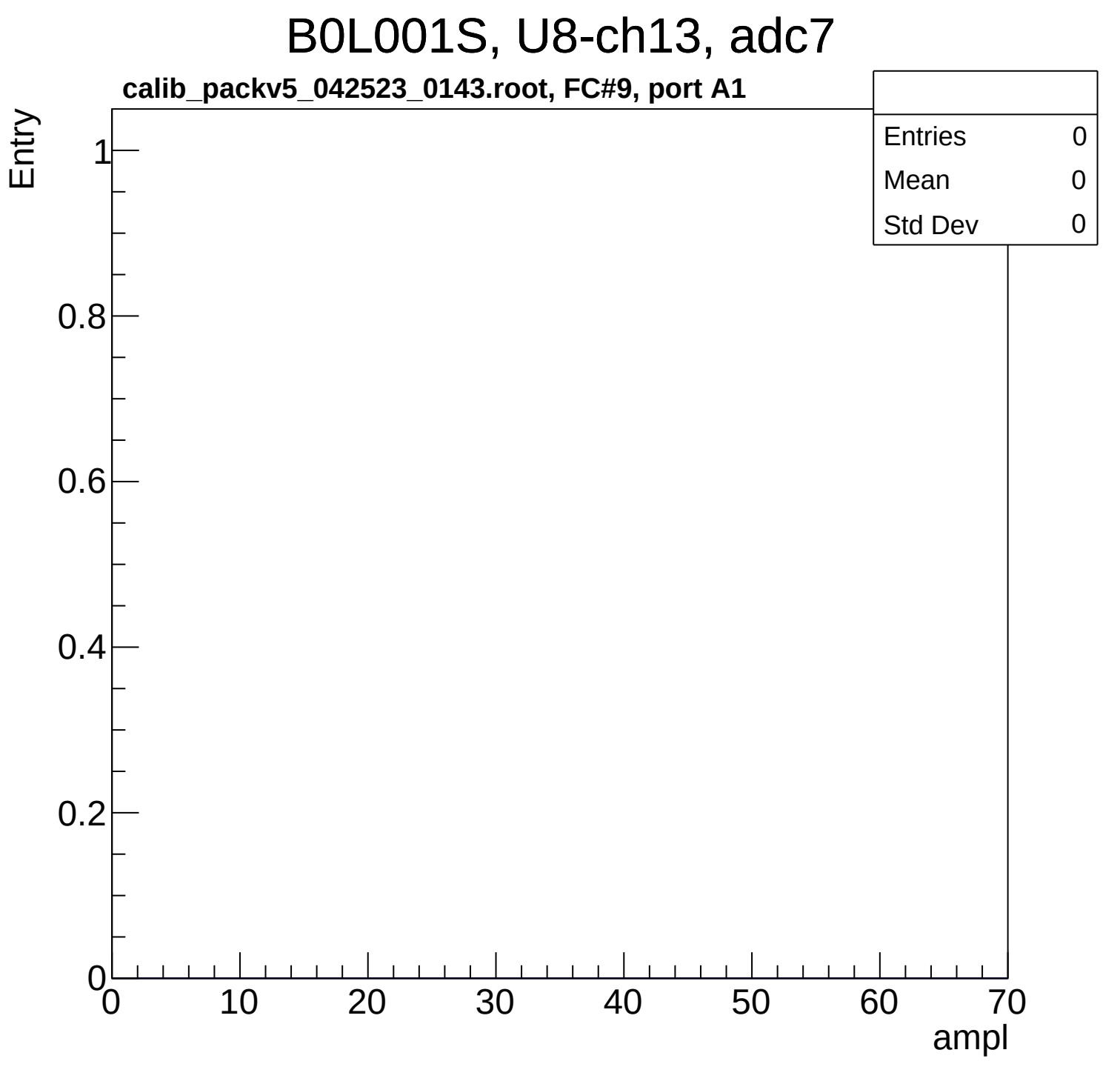
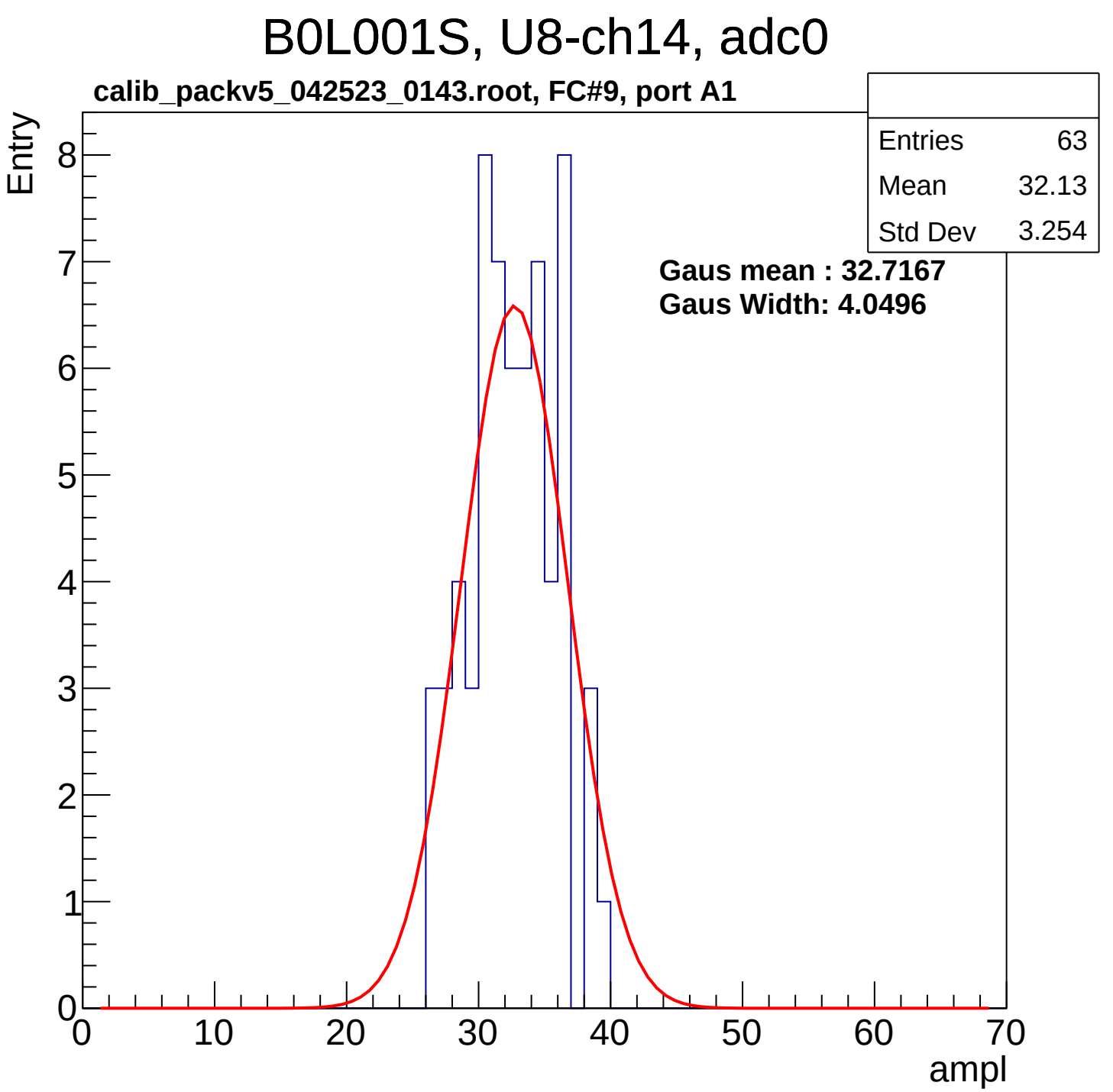

B0L001S, U8-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 32.13
Std Dev 3.254
Gaus mean : 32.7167
Gaus Width: 4.0496
Entry
ampl
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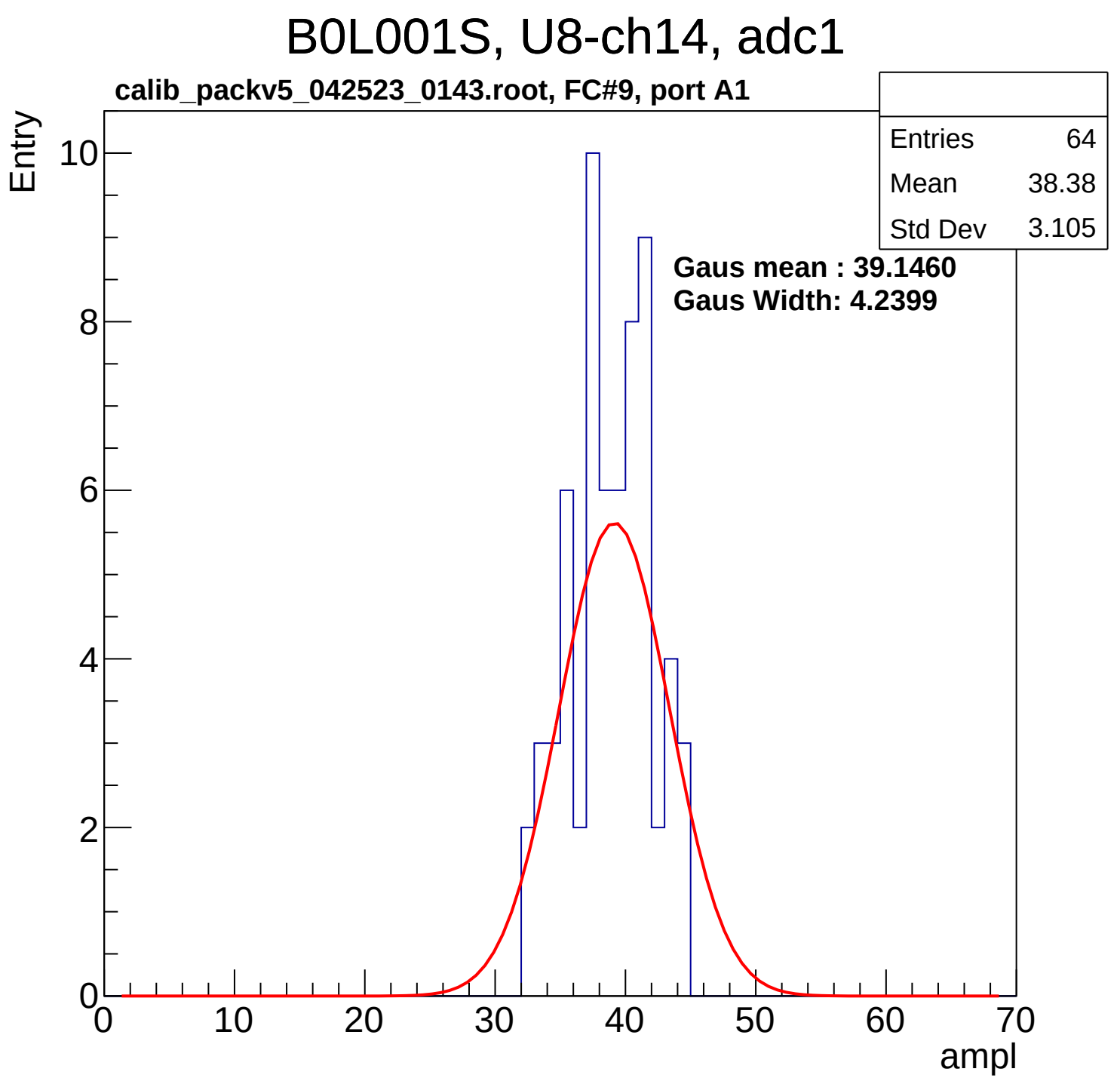
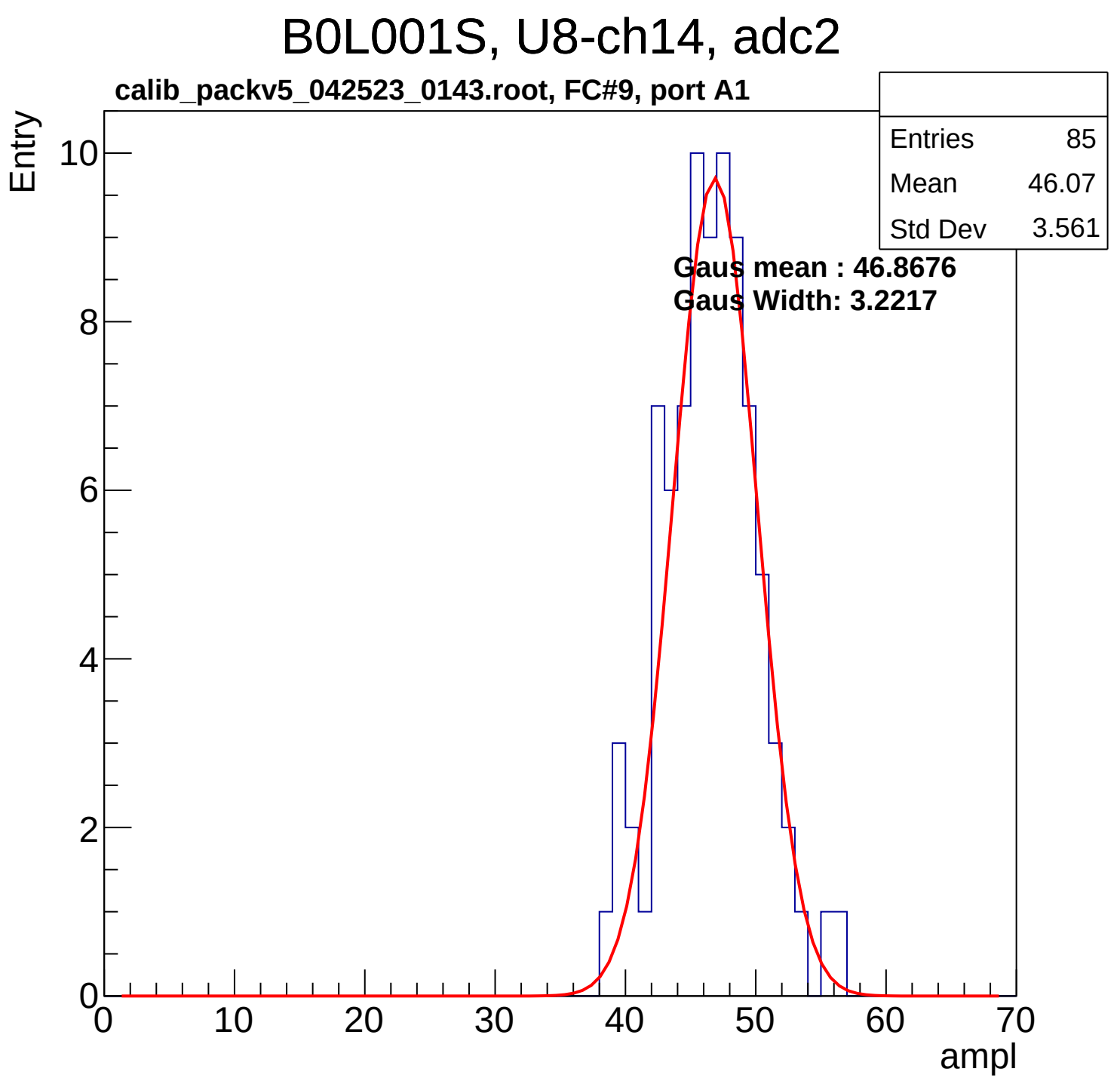
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.38
Std Dev 3.105
Gaus mean : 39.1460
Gaus Width: 4.2399
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 46.07
Std Dev 3.561
Gaus mean : 46.8676
Gaus Width: 3.2217
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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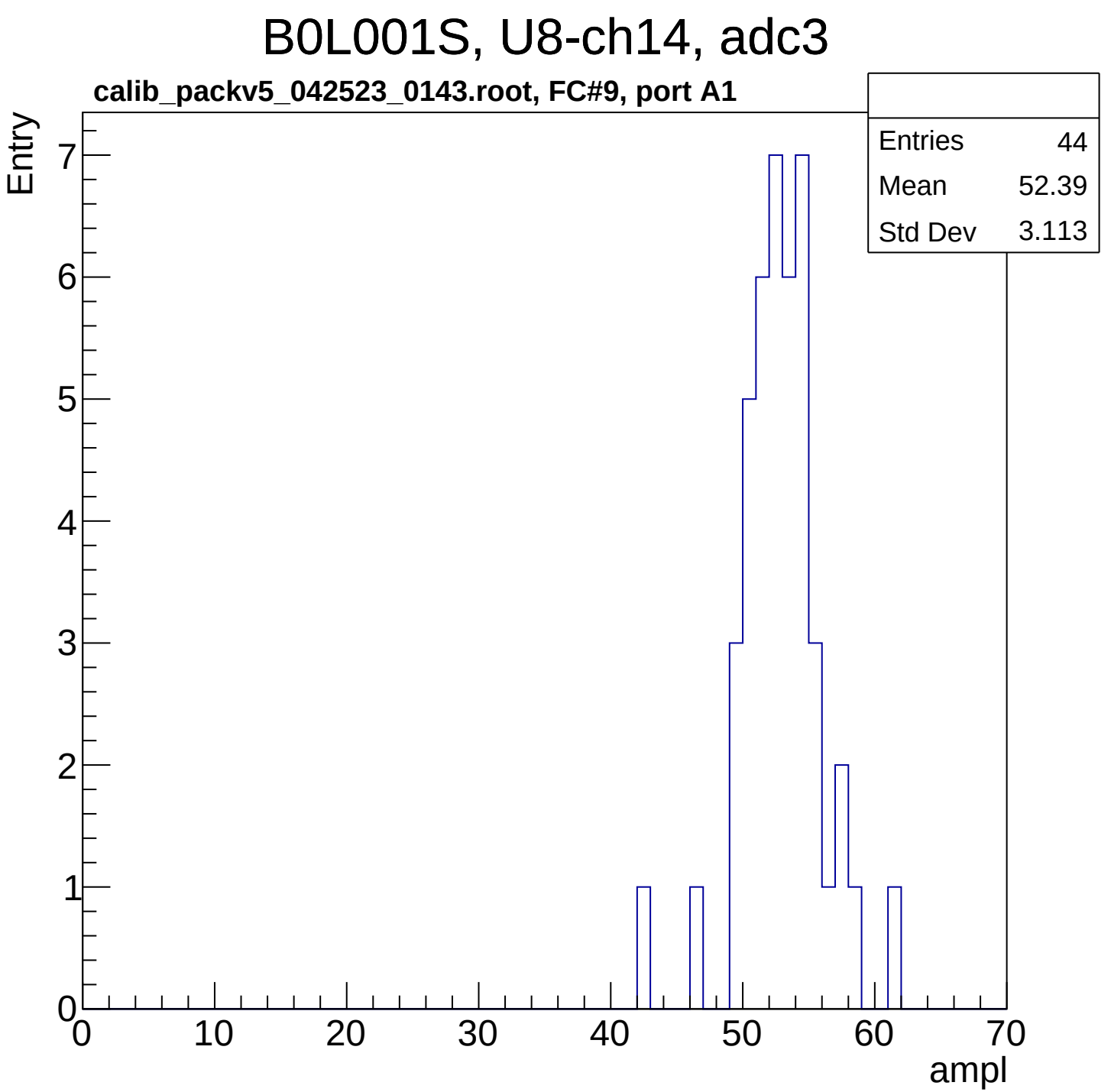
B0L001S, U8-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 52.39
Std Dev 3.113
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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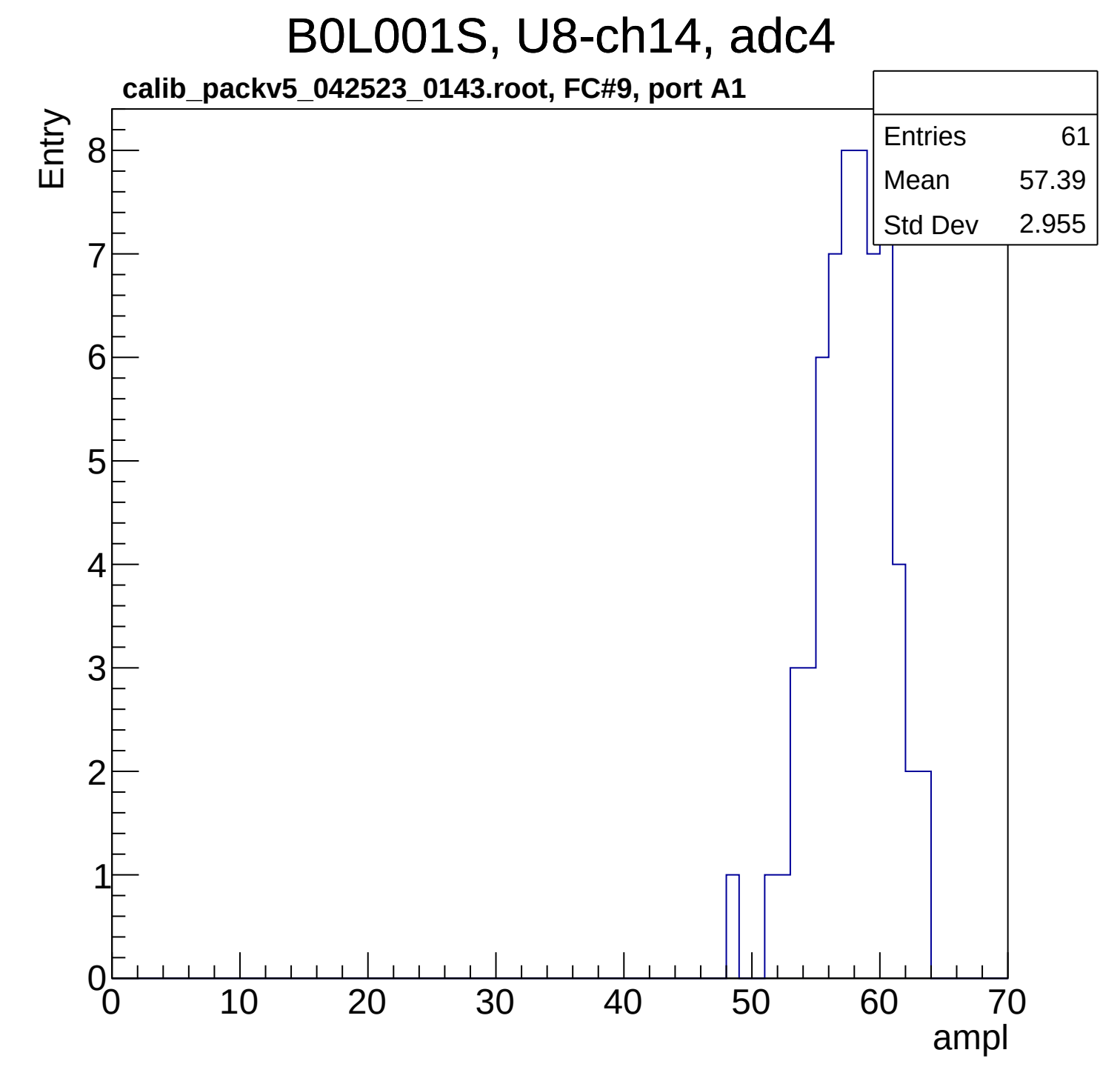
B0L001S, U8-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.39
Std Dev 2.955
Entry
ampl
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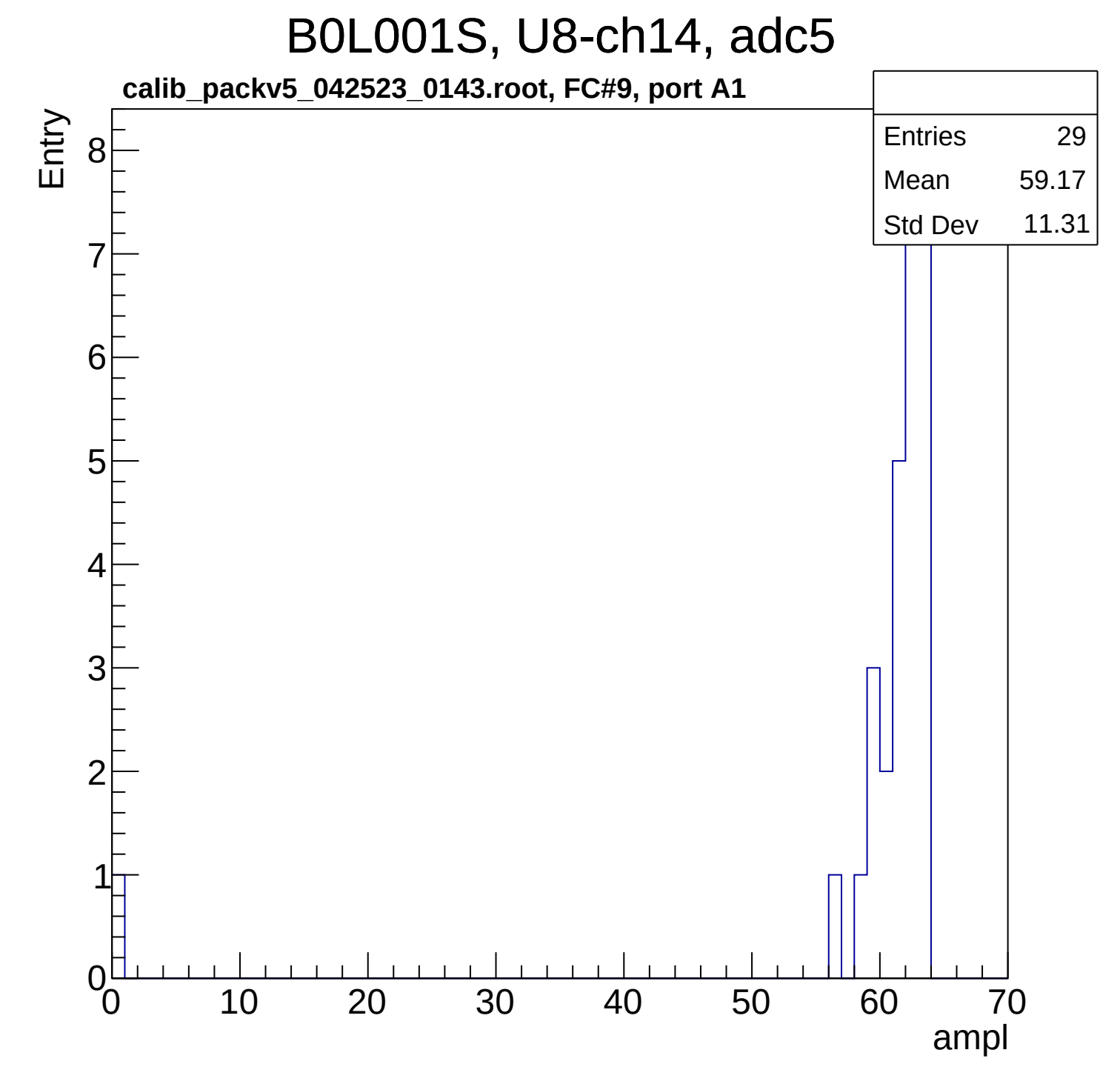
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59.17
Std Dev 11.31
Entry
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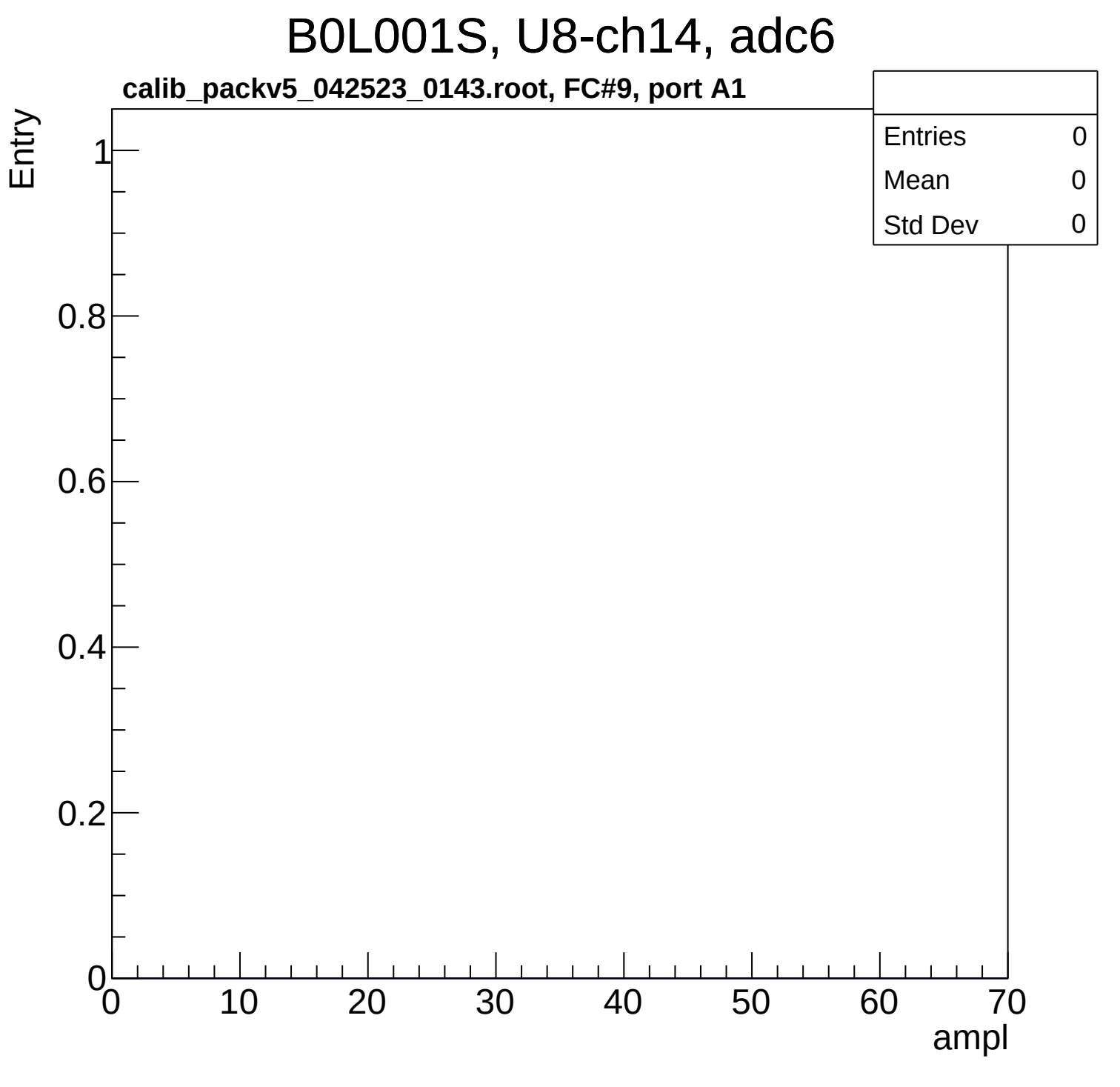
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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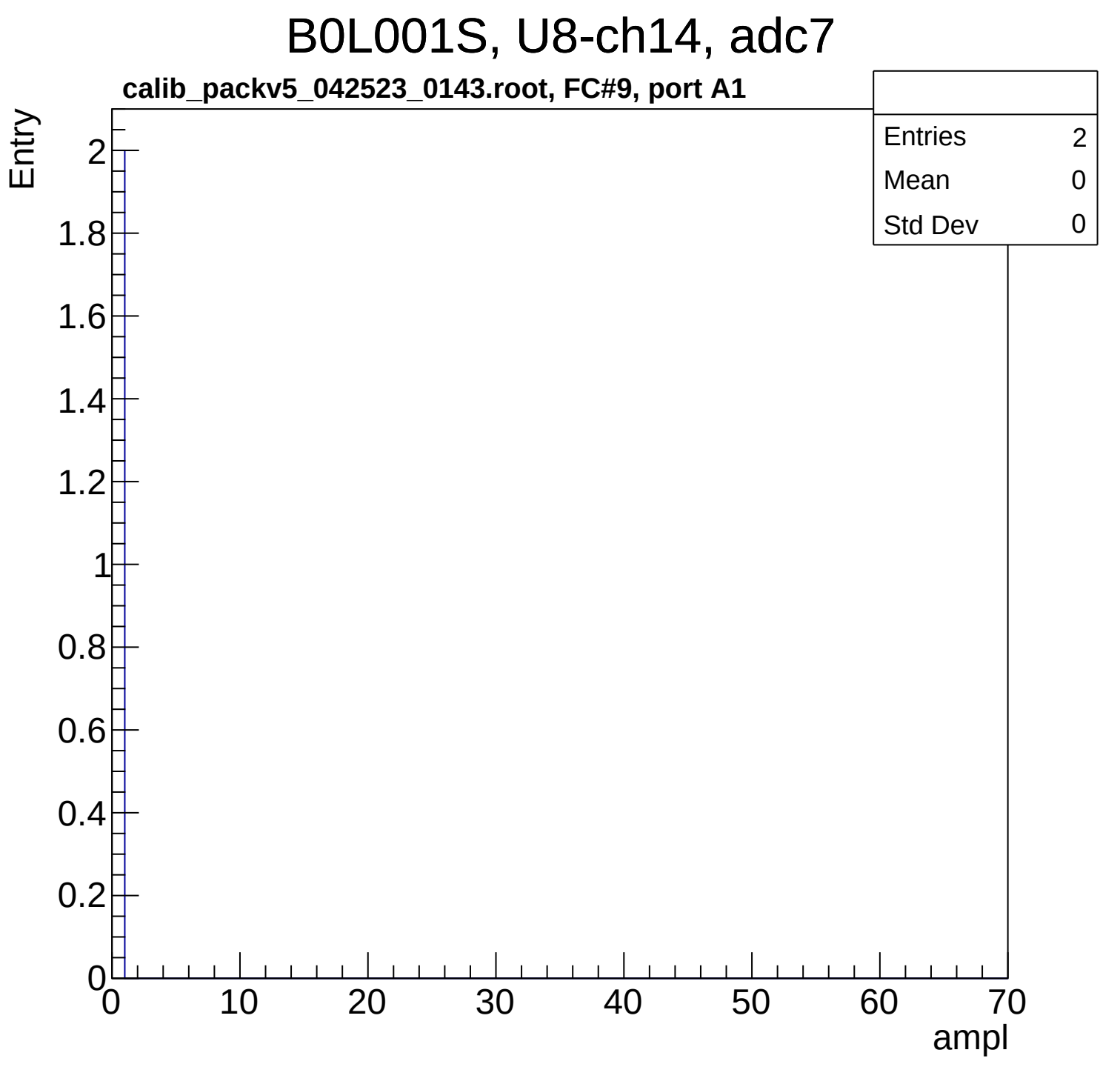
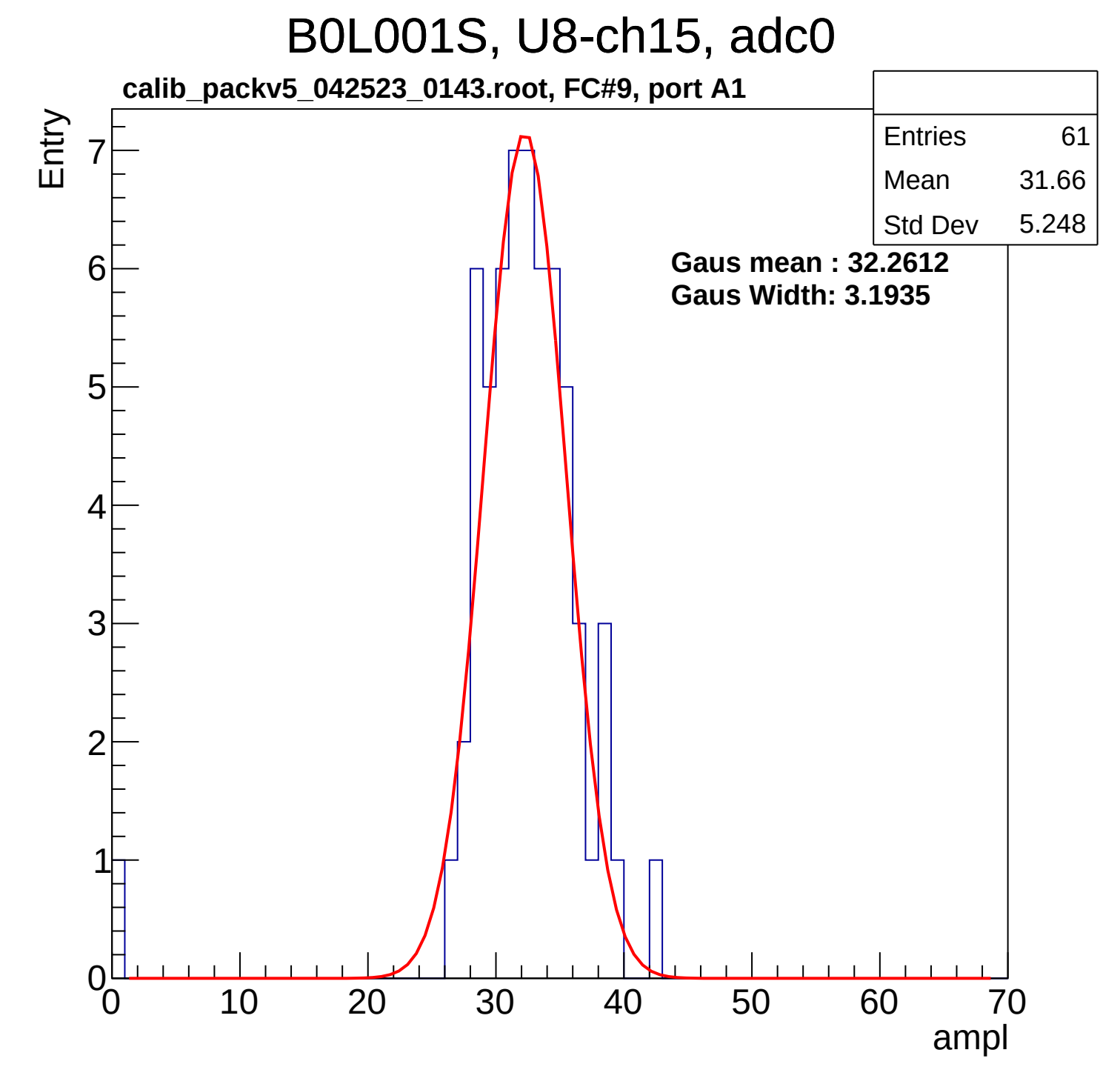
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.66
Std Dev 5.248
Gaus mean : 32.2612
Gaus Width: 3.1935
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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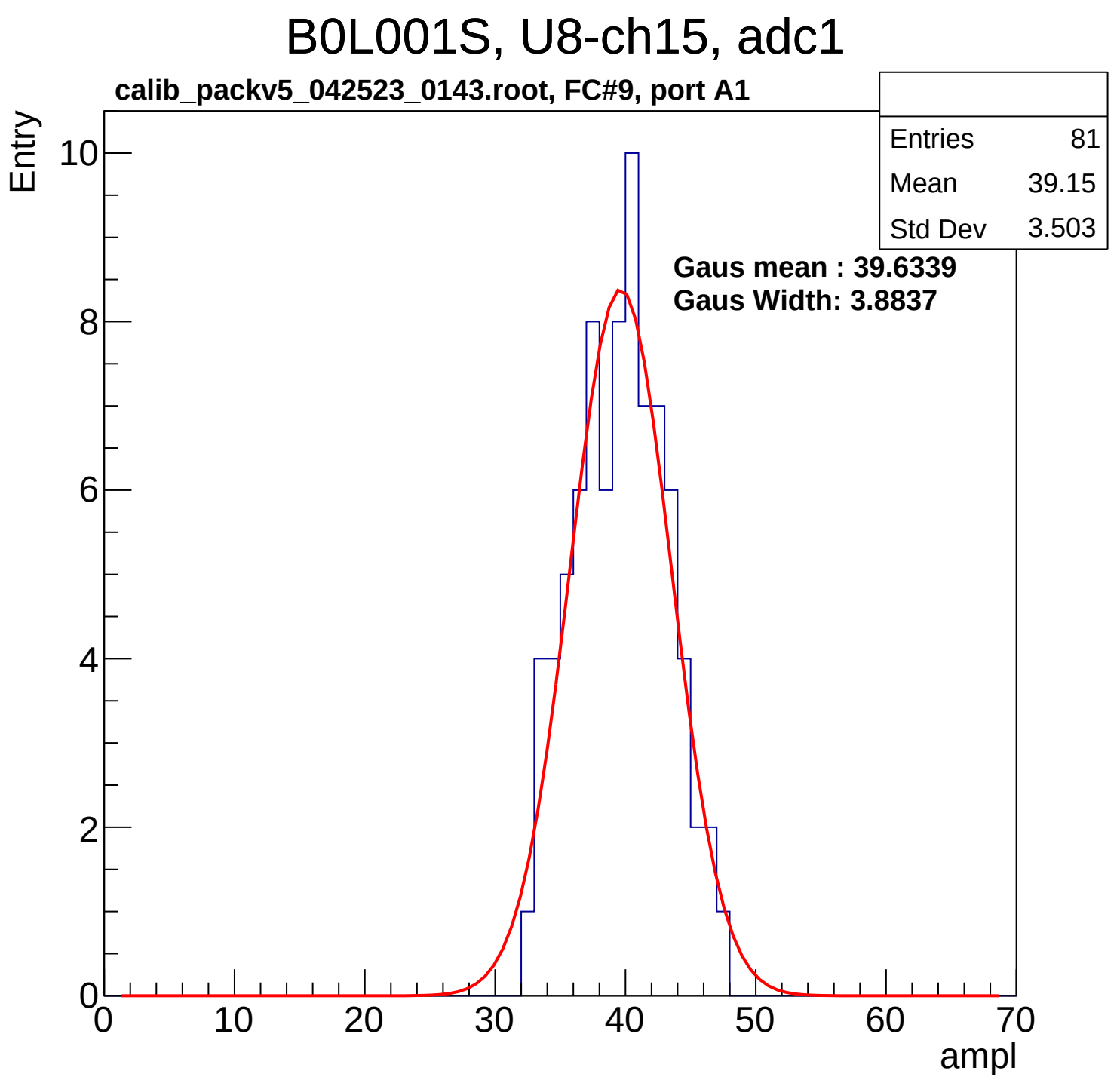
40
50
60
70


B0L001S, U8-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 39.15
Std Dev 3.503
Gaus mean : 39.6339
Gaus Width: 3.8837
Entry
ampl
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6
8
10
0
10
20
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50
60
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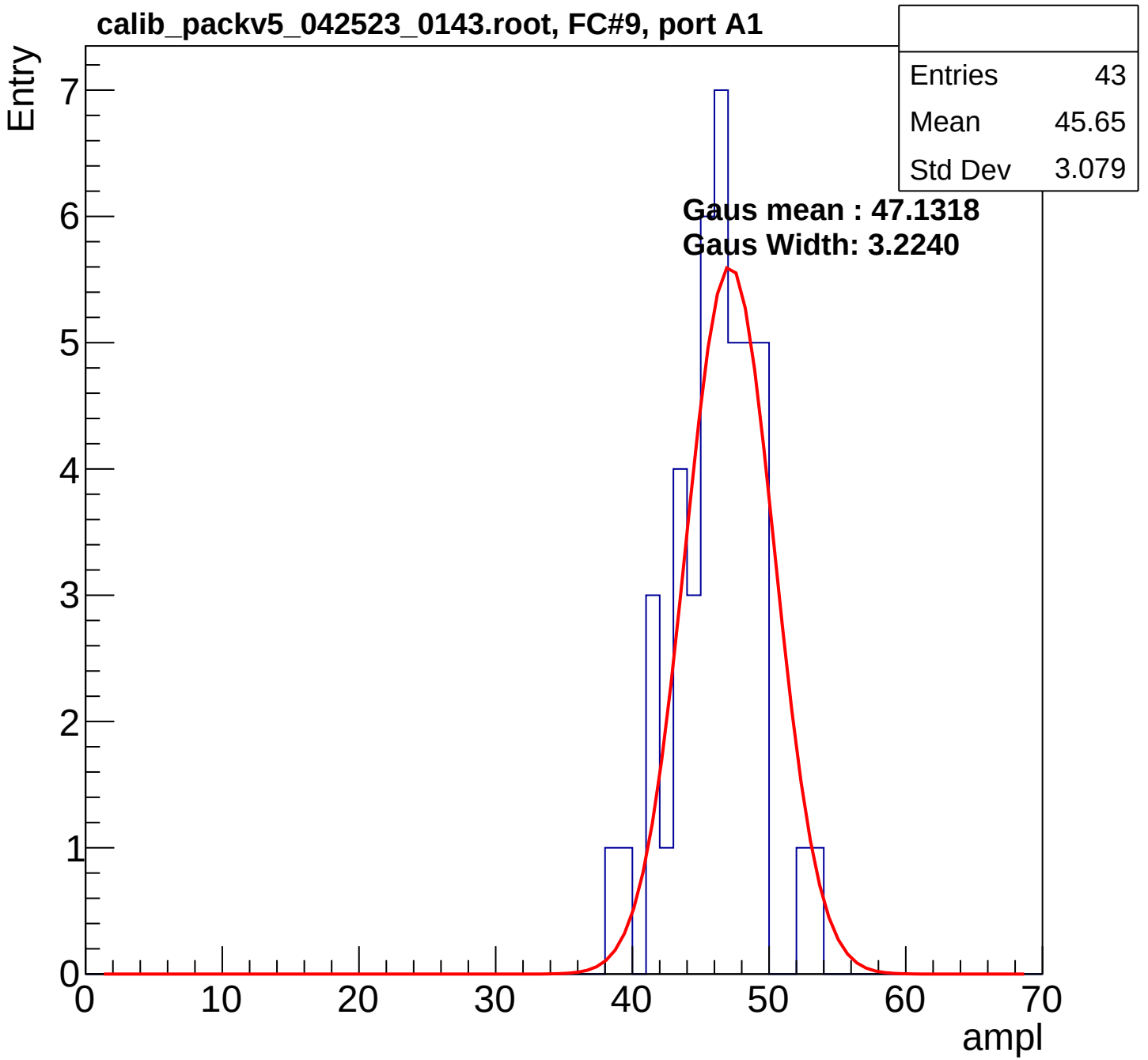
B0L001S, U8-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 45.65
Std Dev 3.079
Gaus mean : 47.1318
Gaus Width: 3.2240
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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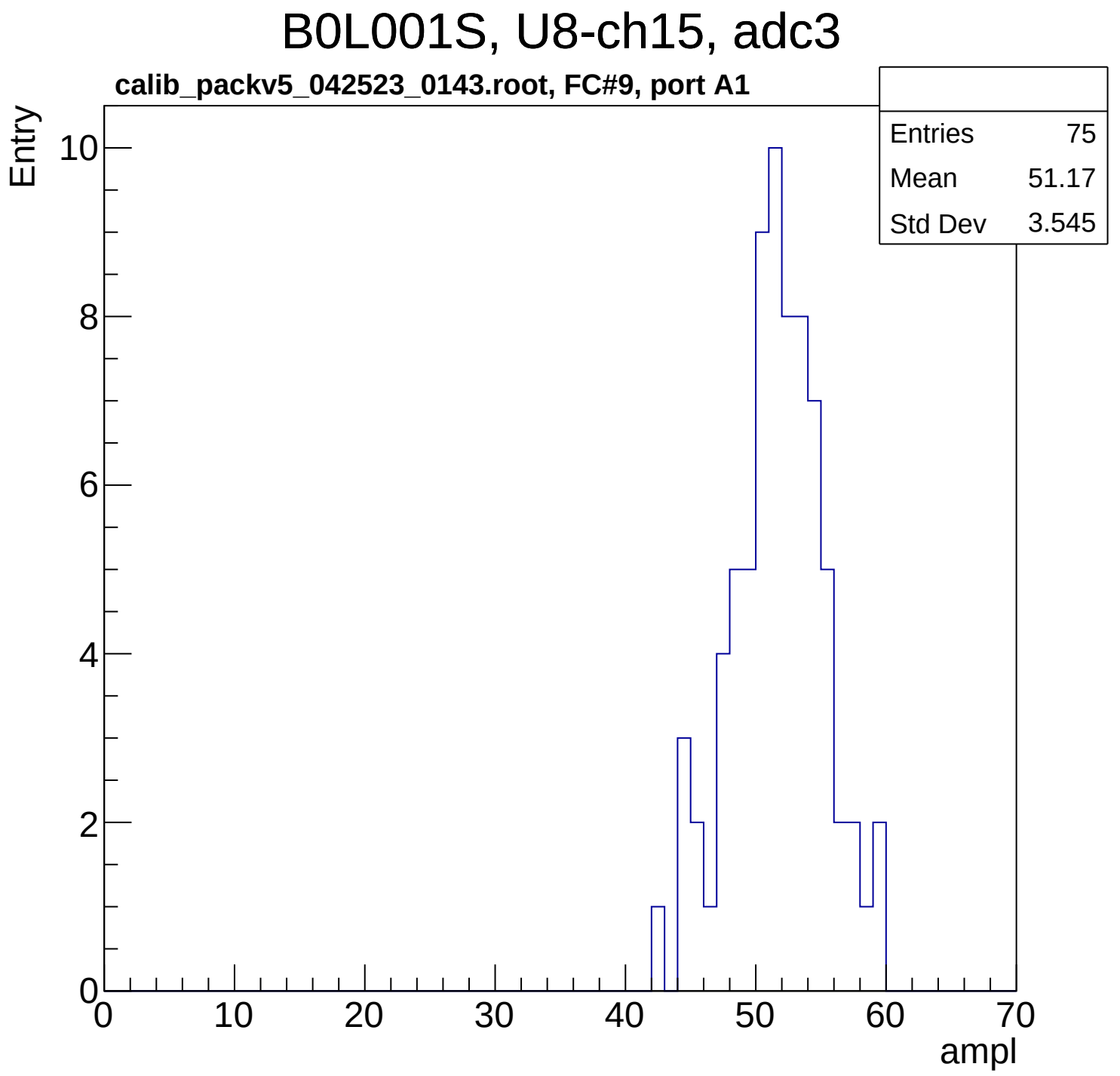
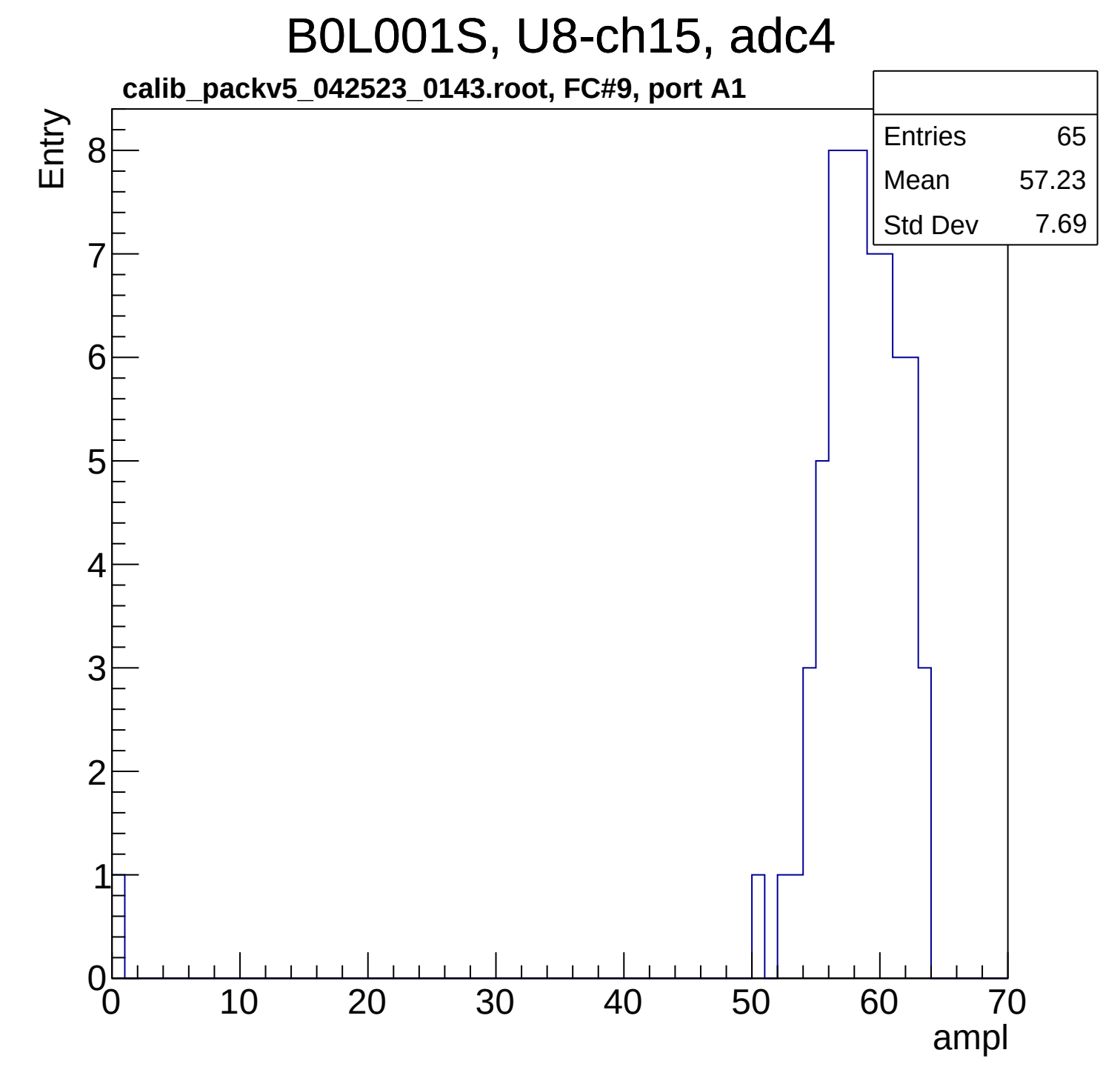

B0L001S, U8-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.17
Std Dev 3.545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.23
Std Dev 7.69
Entry
ampl
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1
2
3
4
5
6
7
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40
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60
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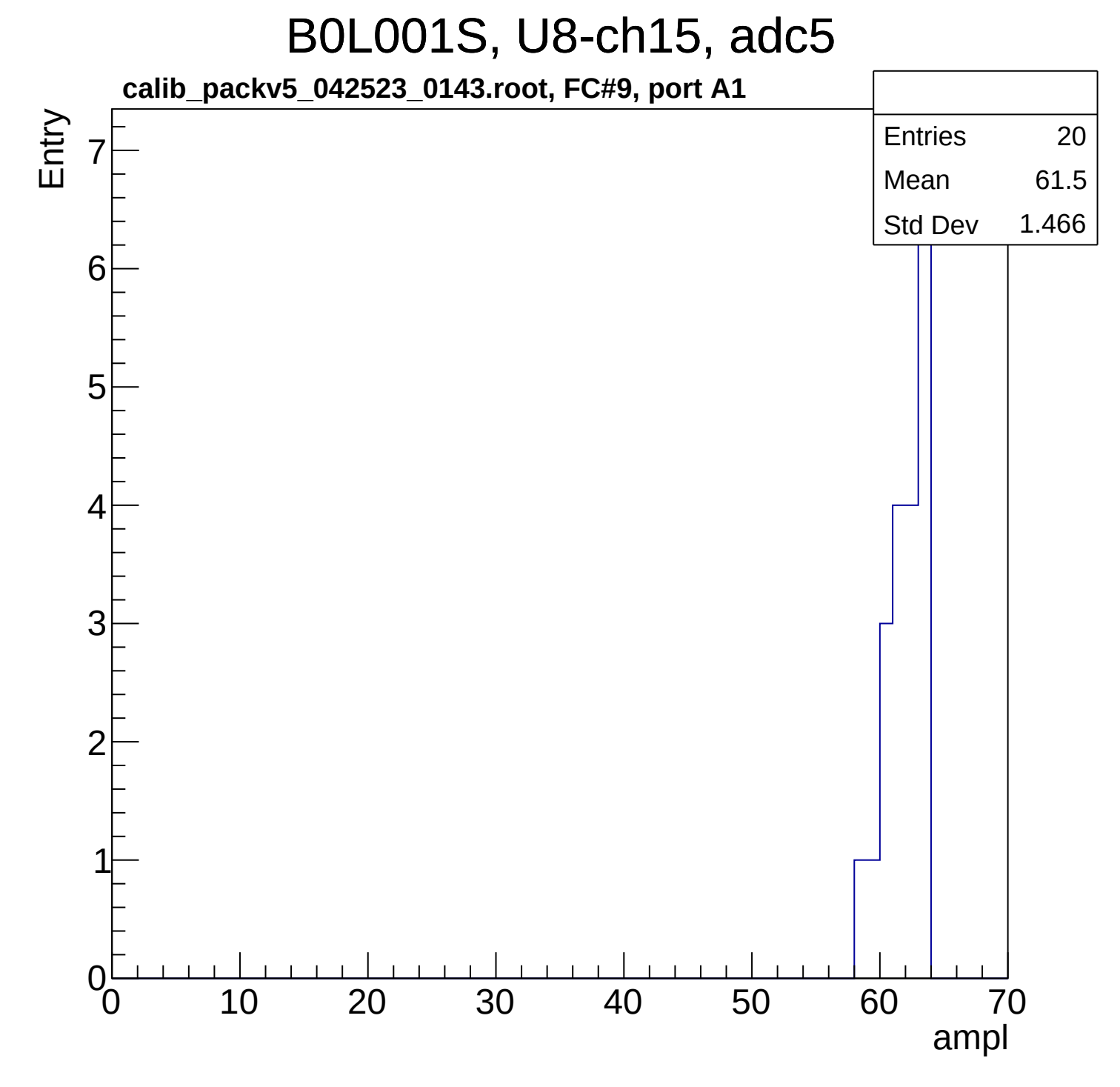
B0L001S, U8-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 61.5
Std Dev 1.466
Entry
ampl
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4
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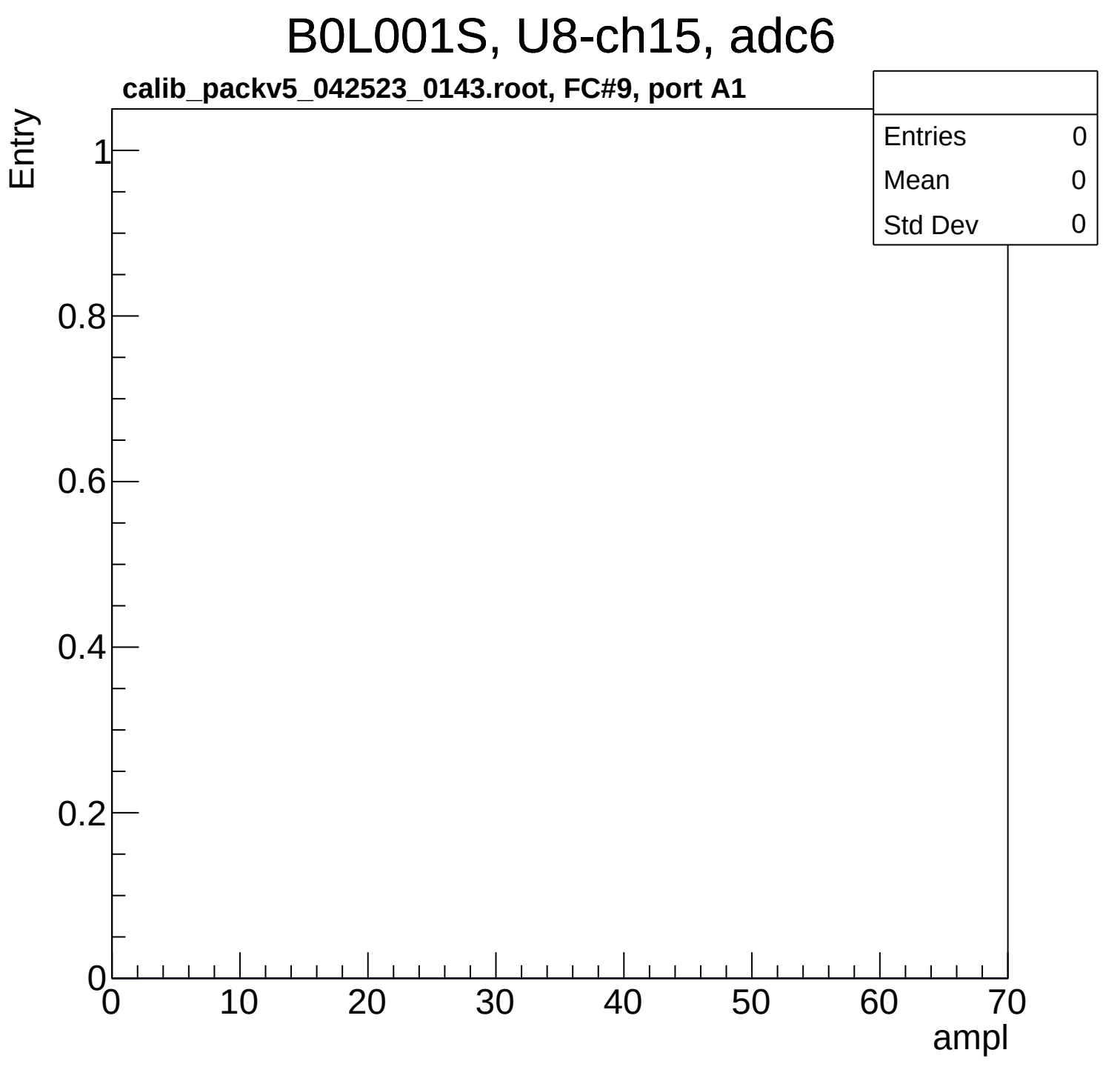
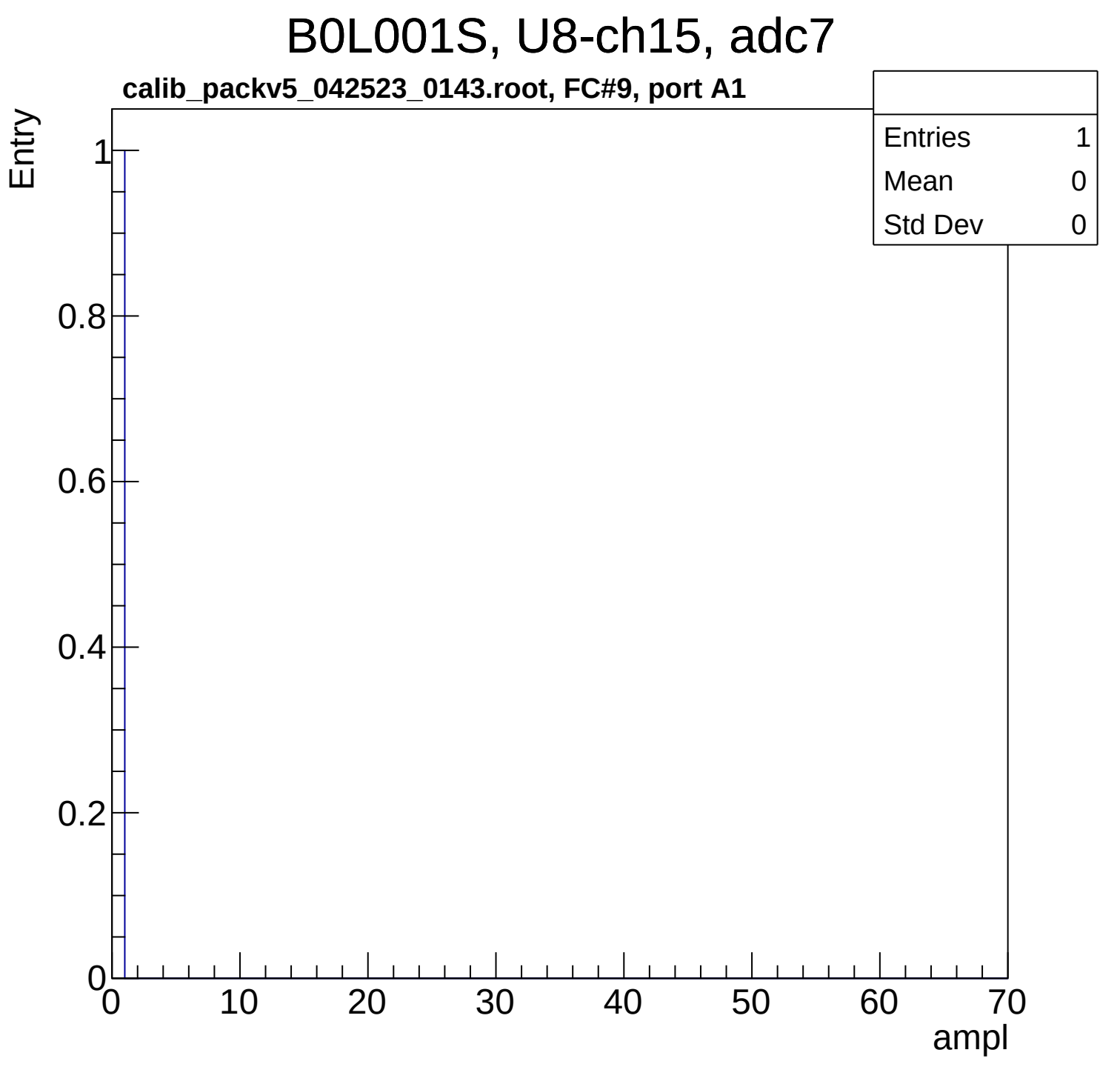
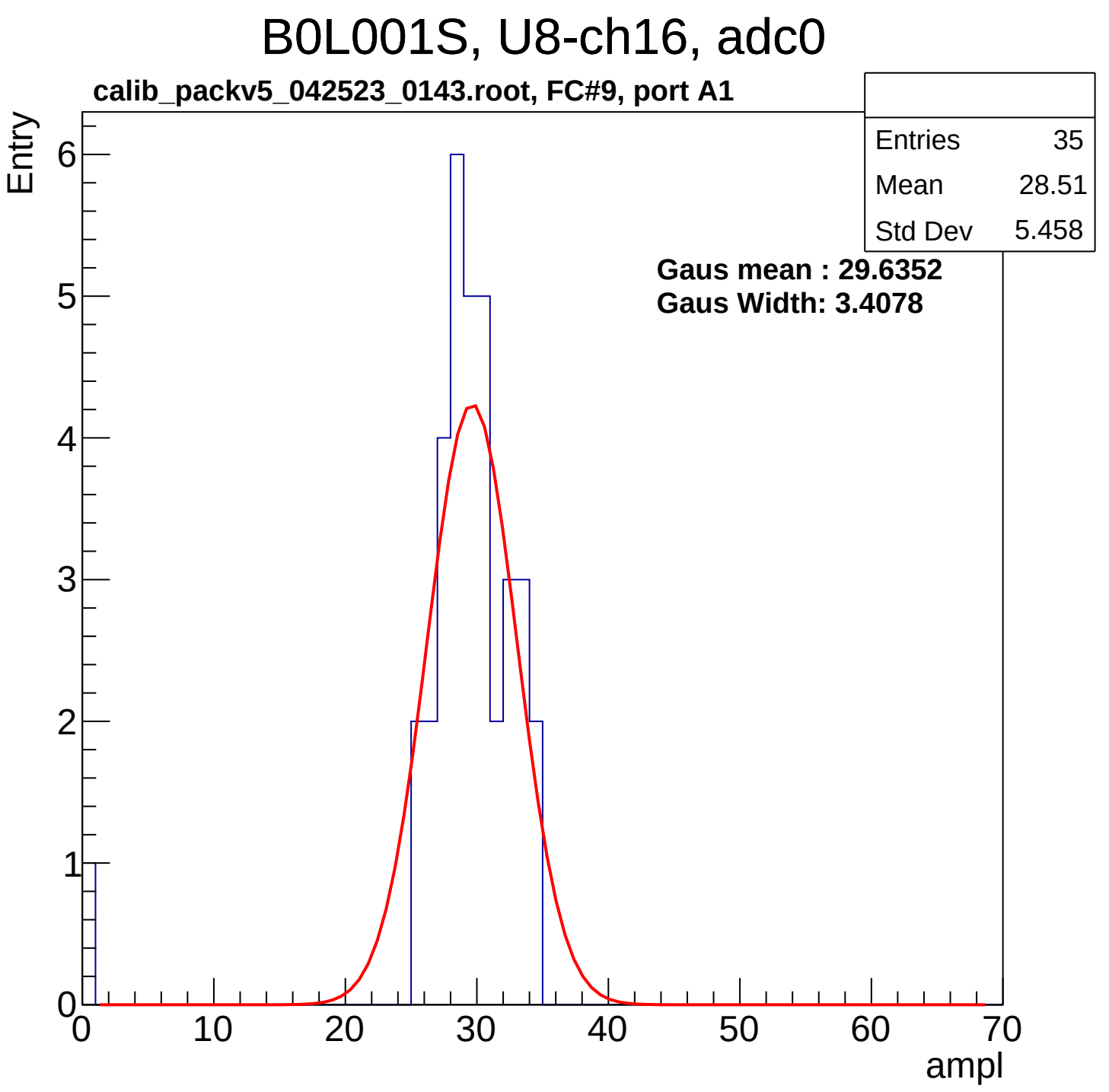

B0L001S, U8-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 28.51
Std Dev 5.458
Gaus mean : 29.6352
Gaus Width: 3.4078
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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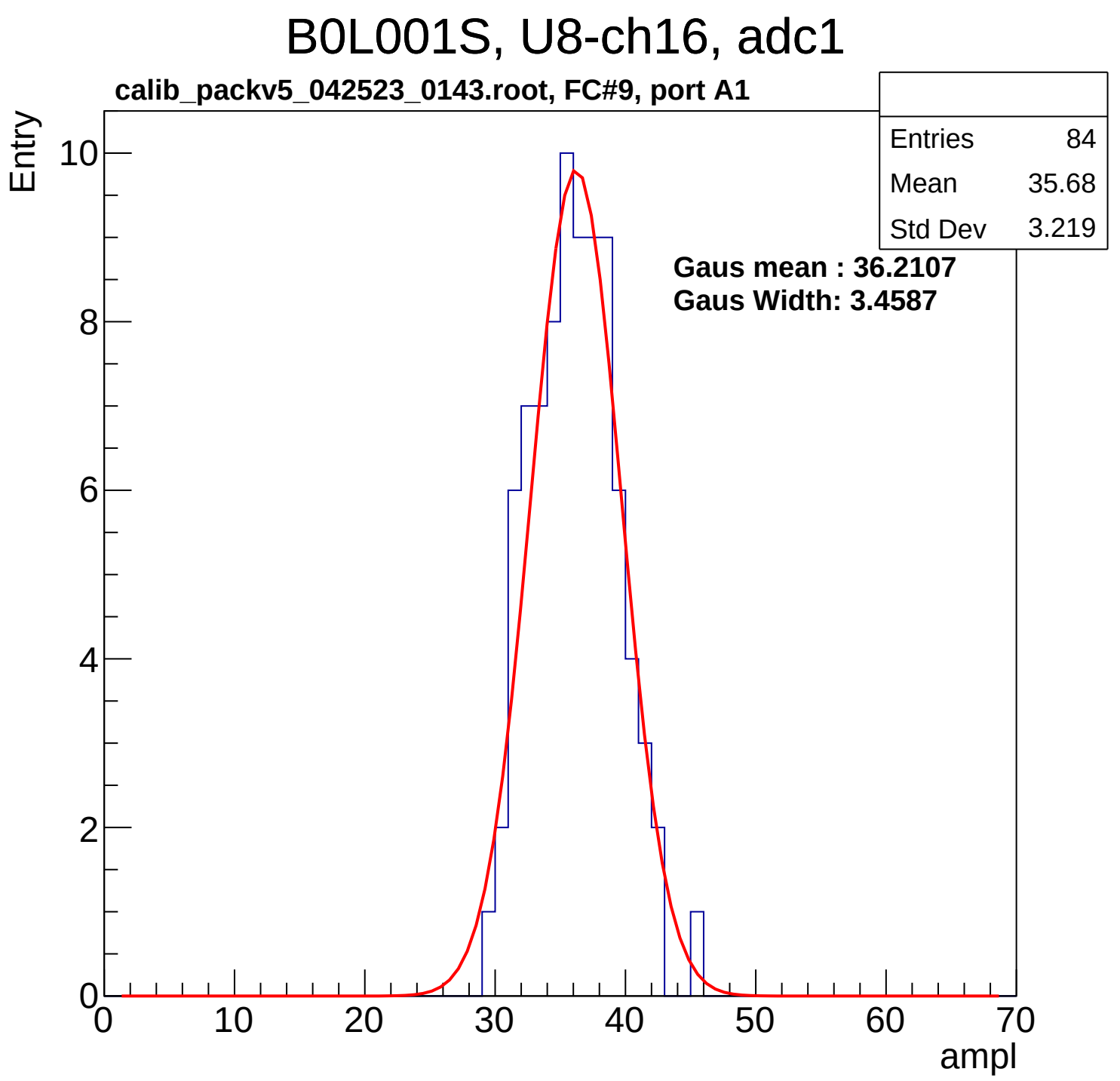
B0L001S, U8-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 35.68
Std Dev 3.219
Gaus mean : 36.2107
Gaus Width: 3.4587
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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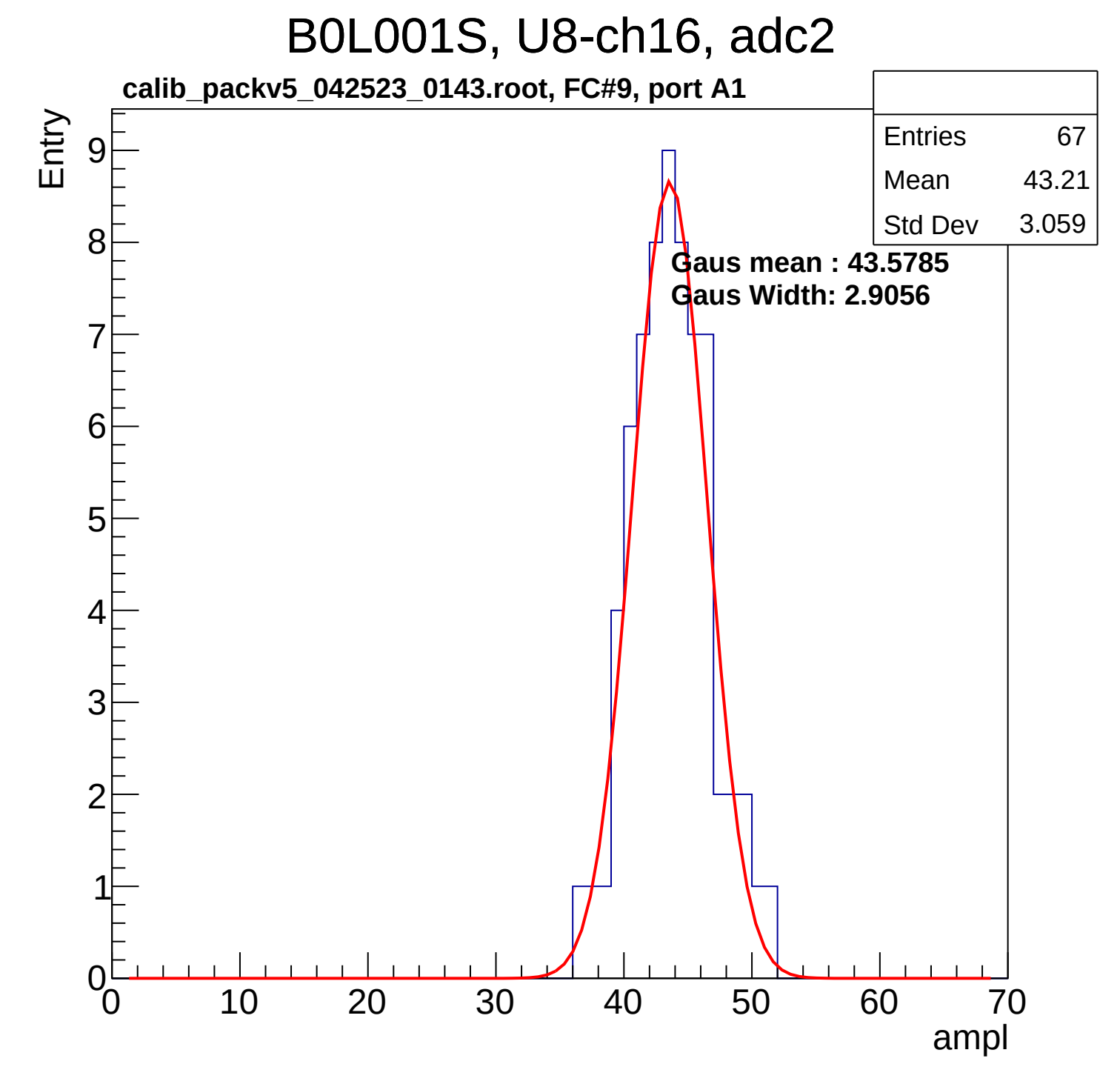
B0L001S, U8-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.21
Std Dev 3.059
Gaus mean : 43.5785
Gaus Width: 2.9056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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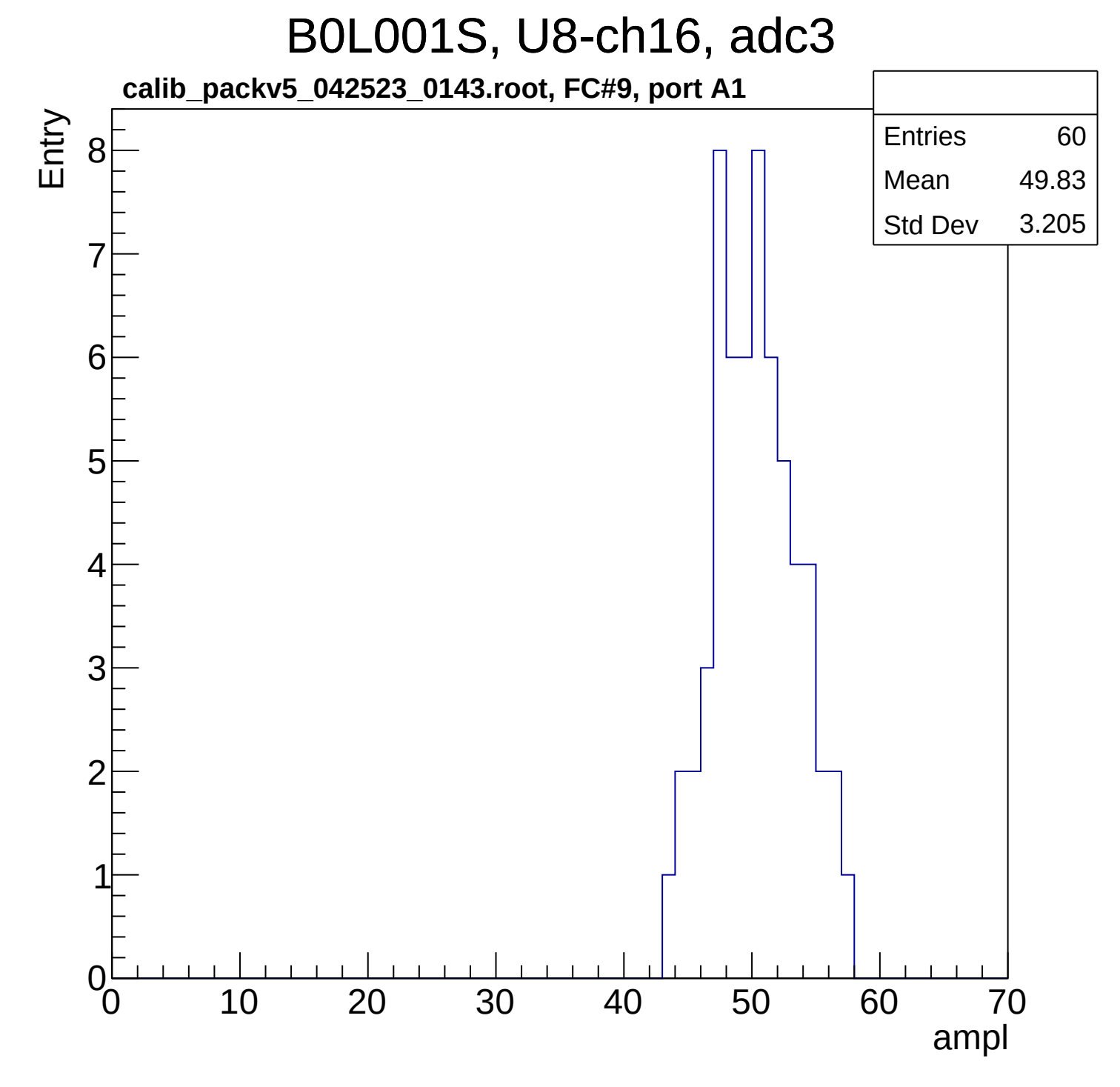
B0L001S, U8-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.83
Std Dev 3.205
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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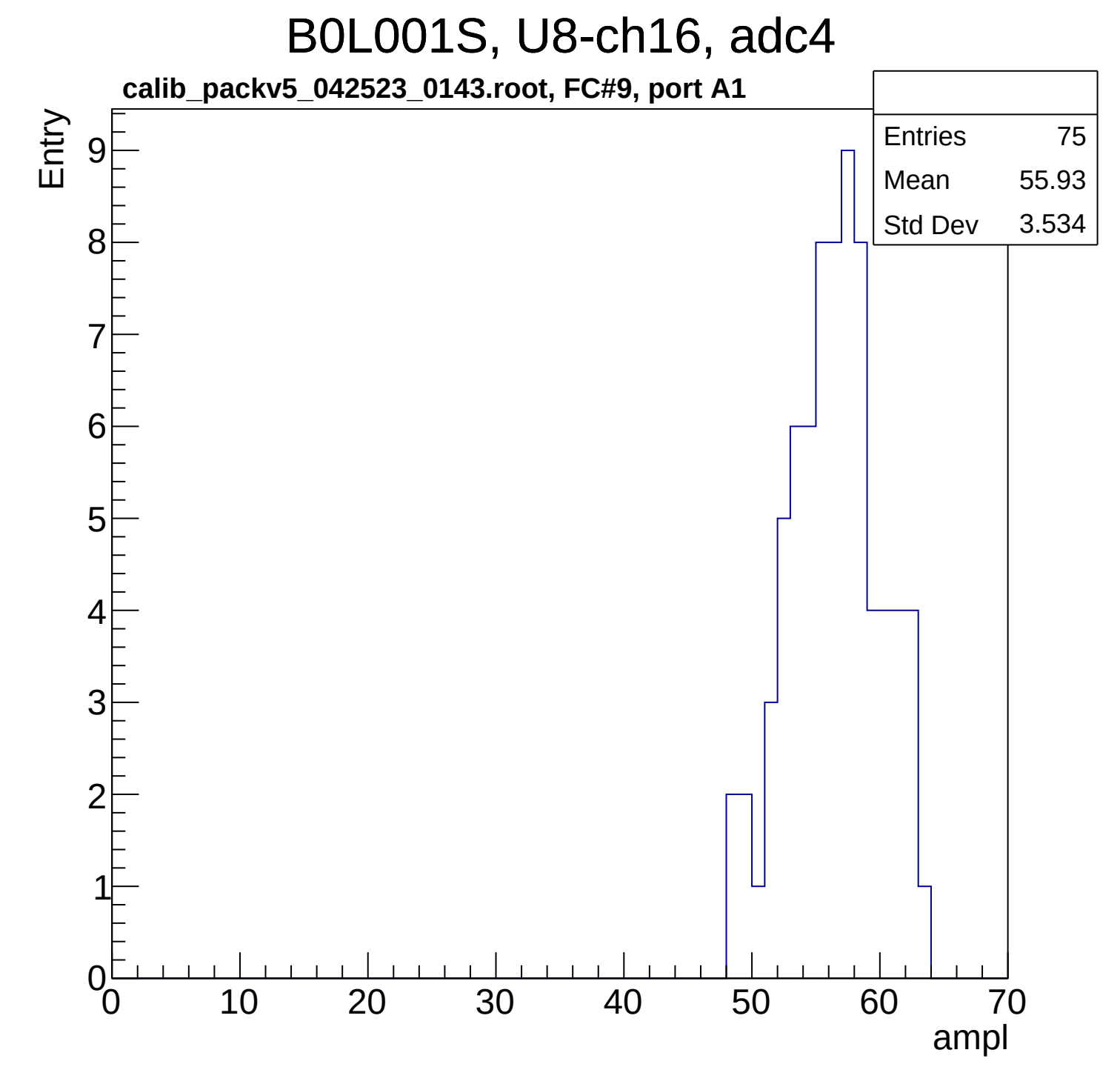
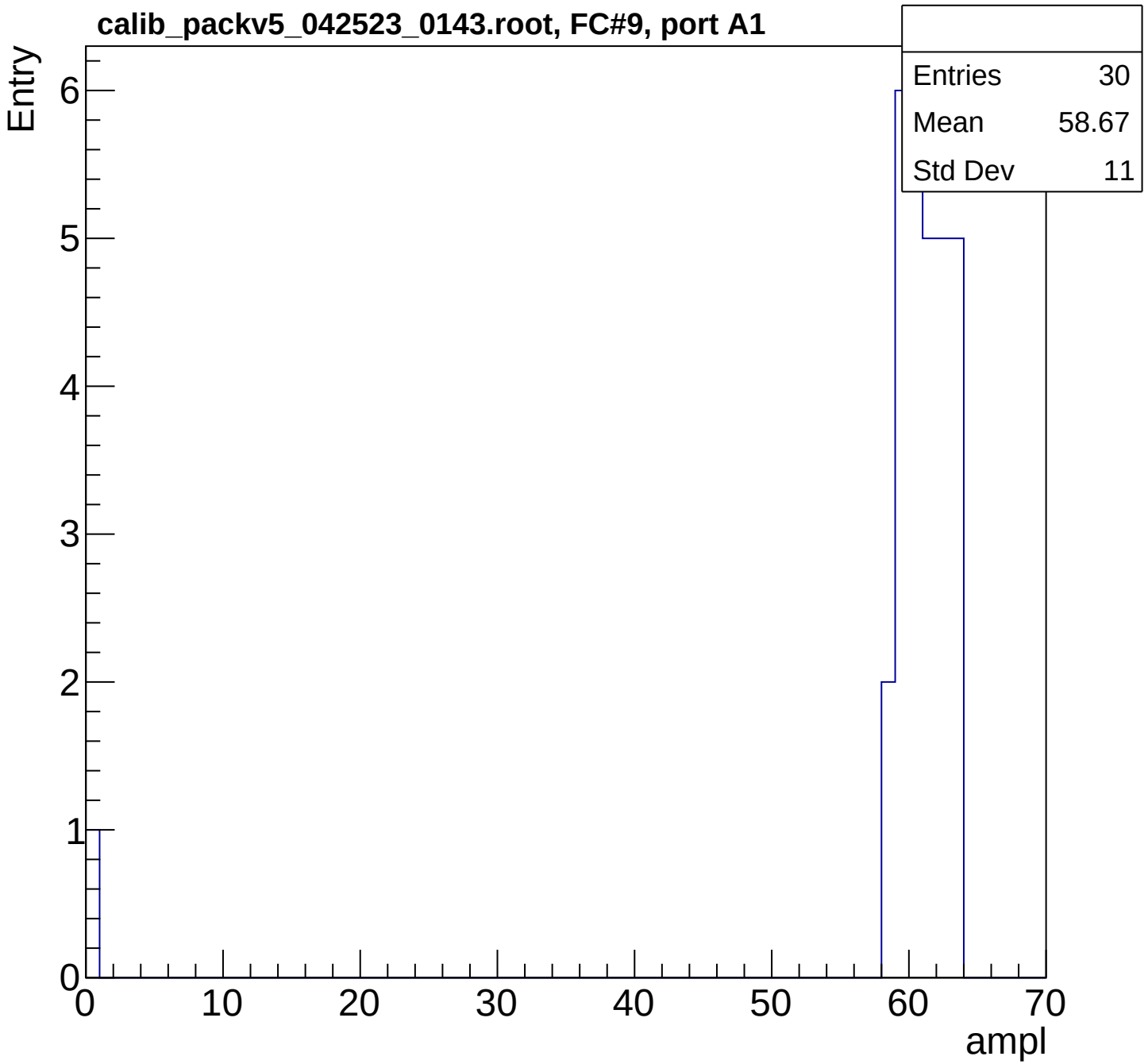
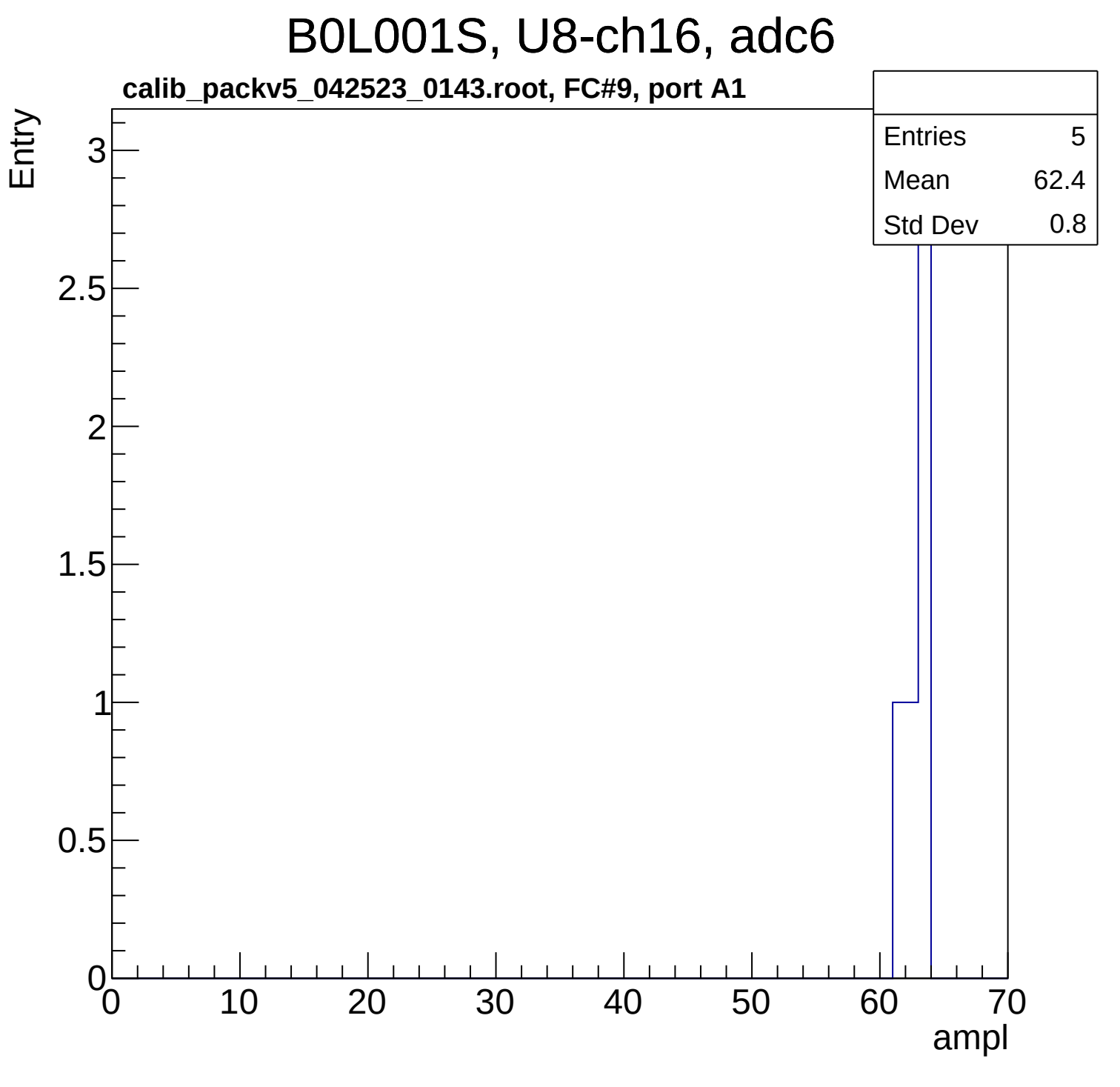
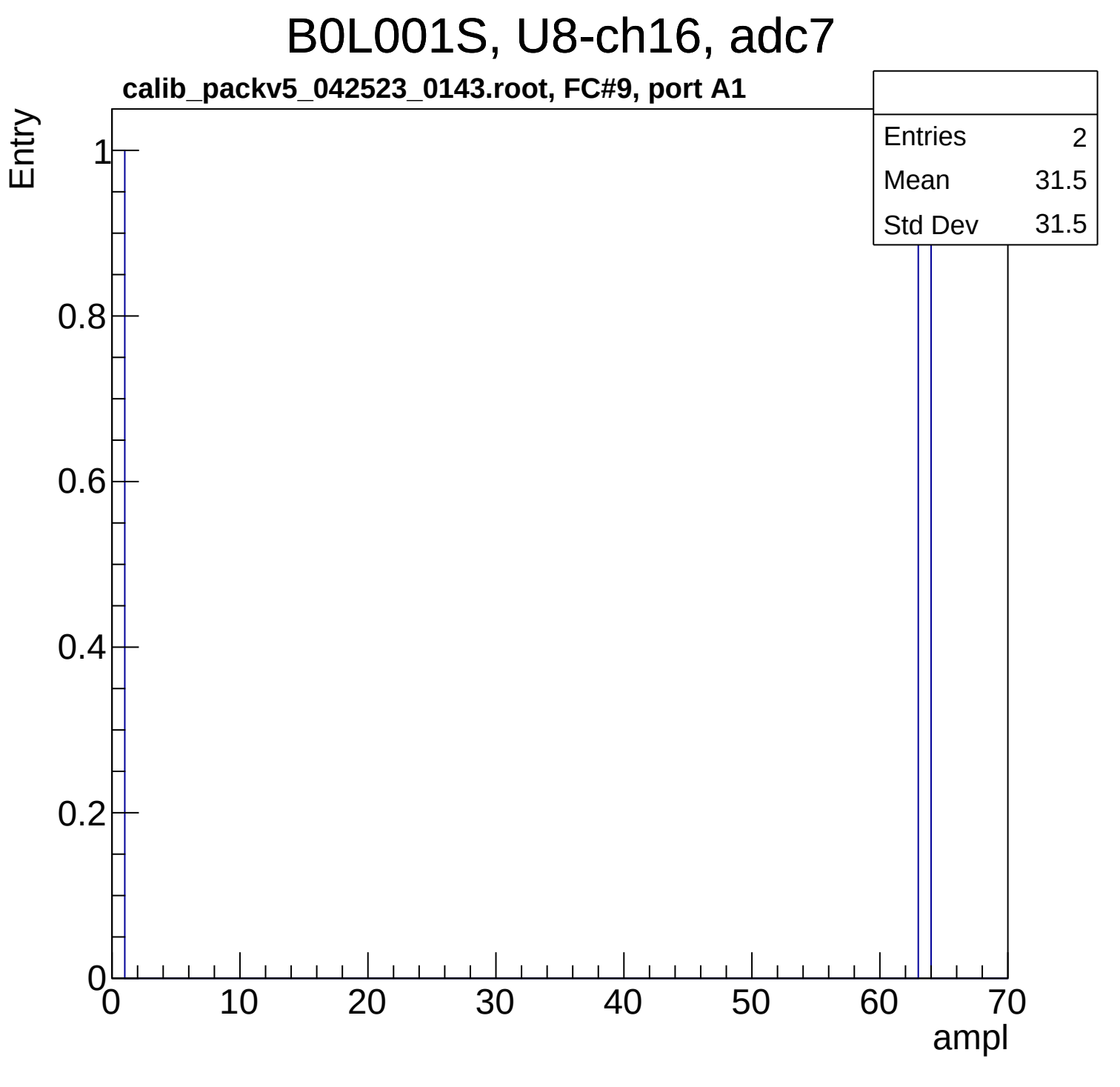
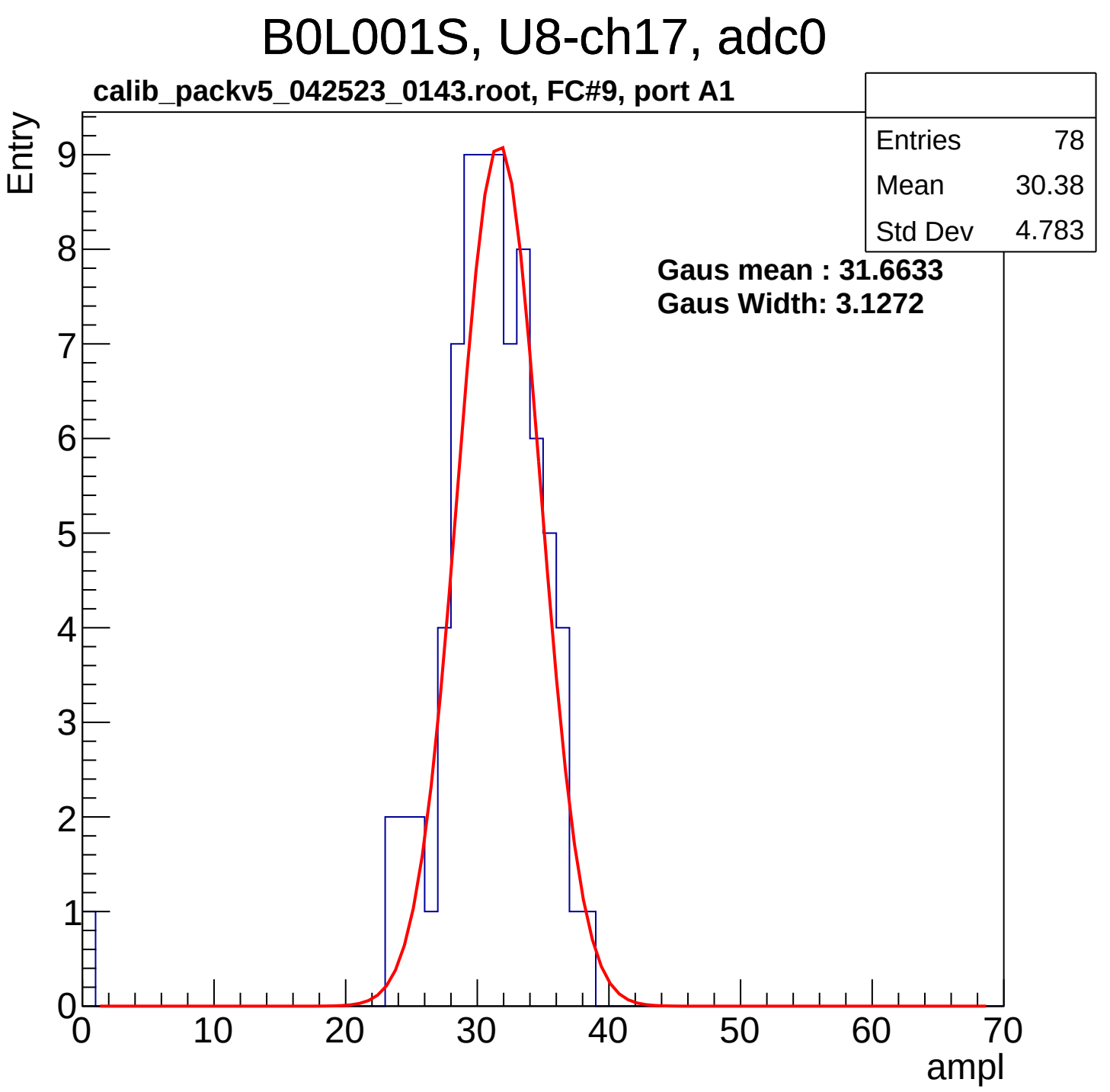
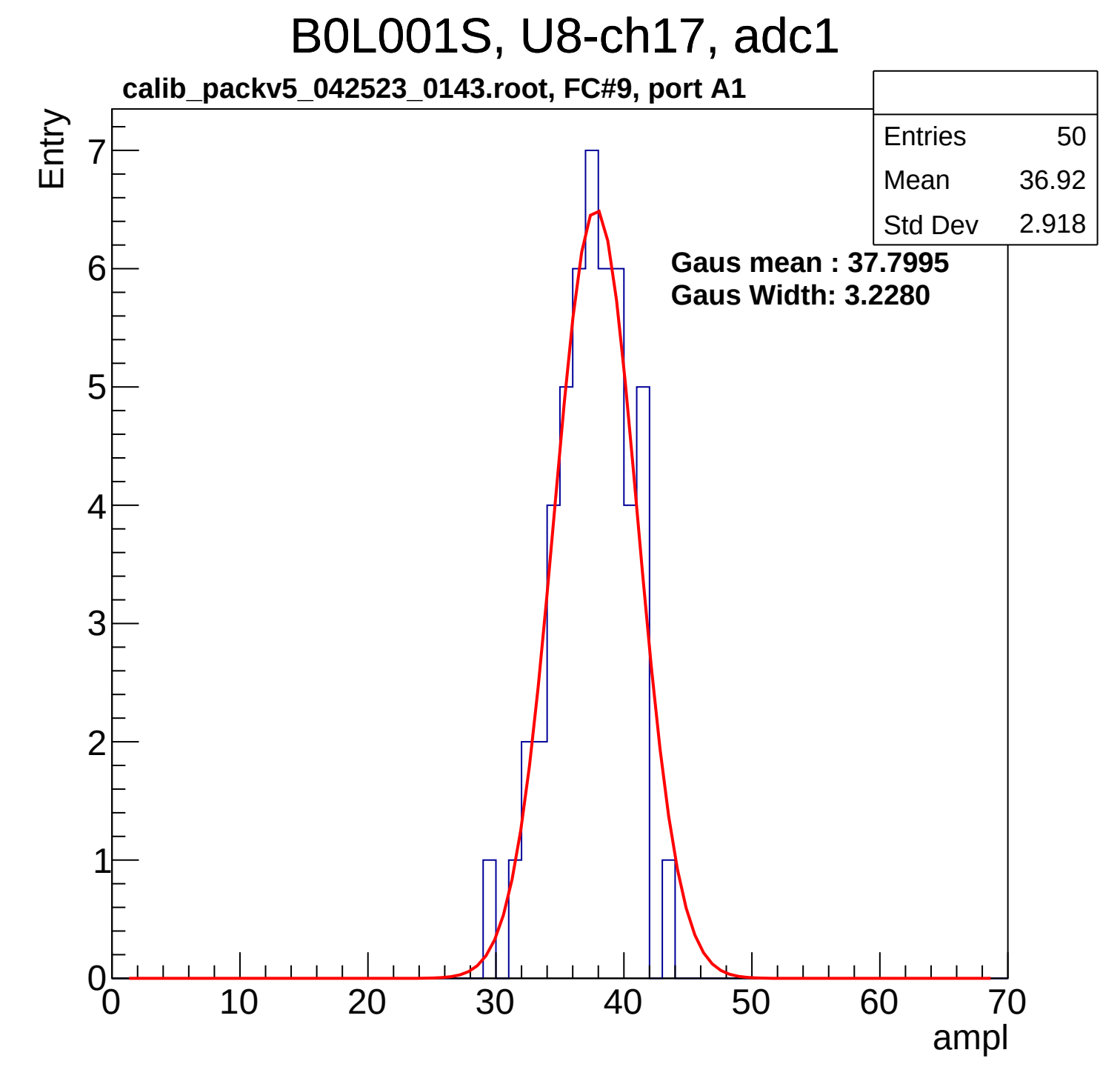

B0L001S, U8-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 55.93
Std Dev 3.534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

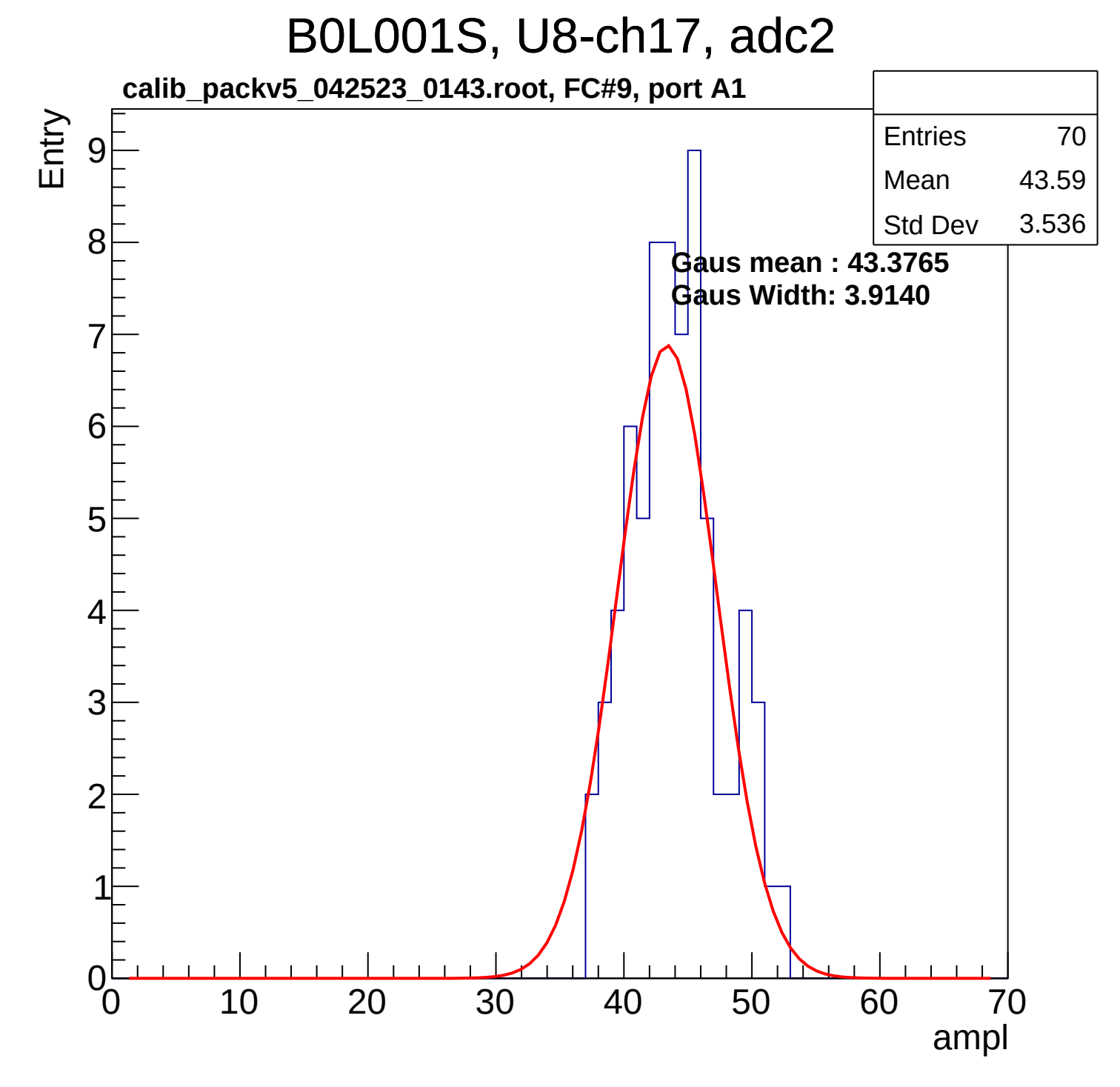

B0L001S, U8-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.67
Std Dev 11
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.38
Std Dev 4.783
Gaus mean : 31.6633
Gaus Width: 3.1272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 36.92
Std Dev 2.918
Gaus mean : 37.7995
Gaus Width: 3.2280
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.59
Std Dev 3.536
Gaus mean : 43.3765
Gaus Width: 3.9140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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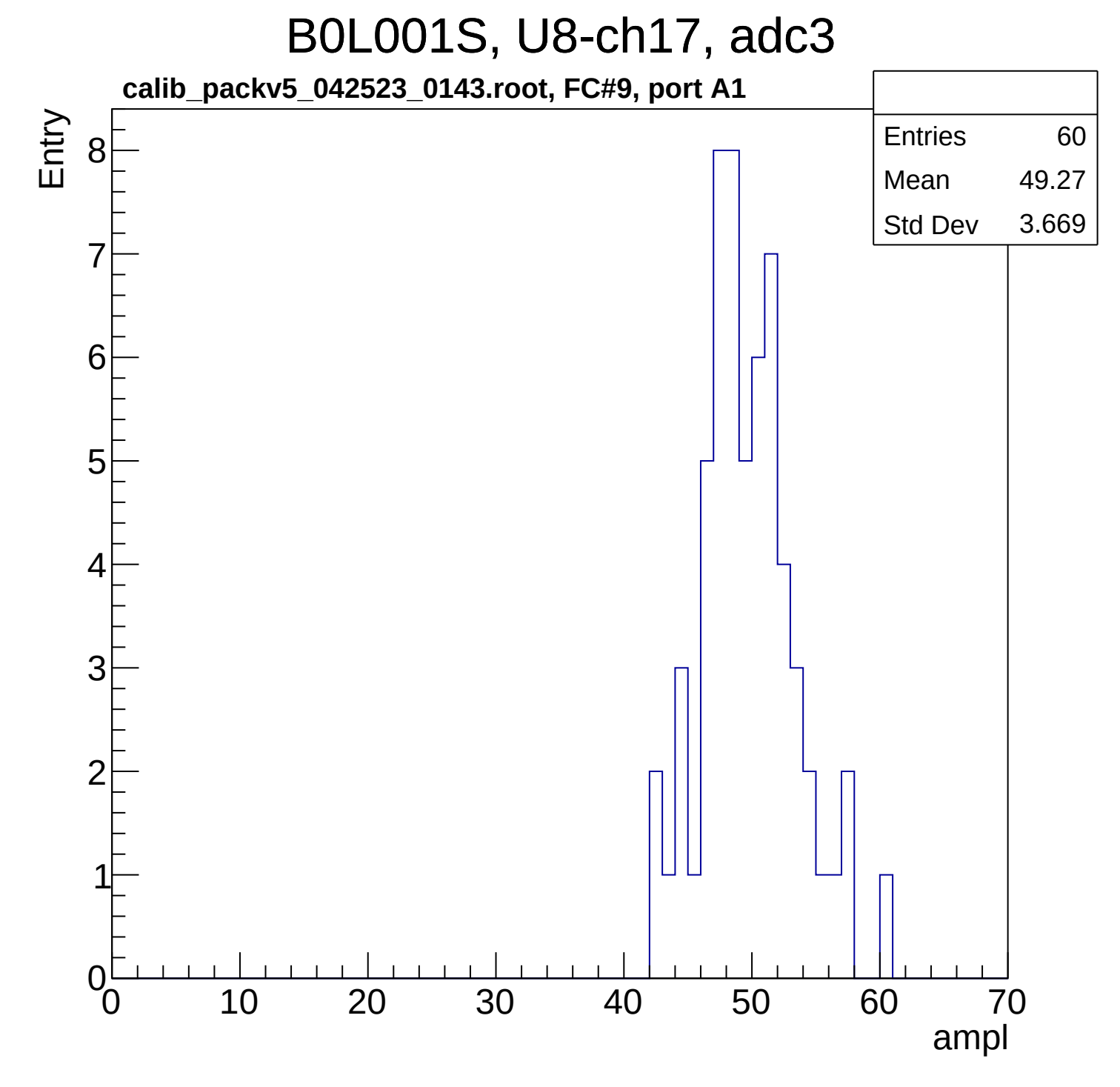
B0L001S, U8-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.27
Std Dev 3.669
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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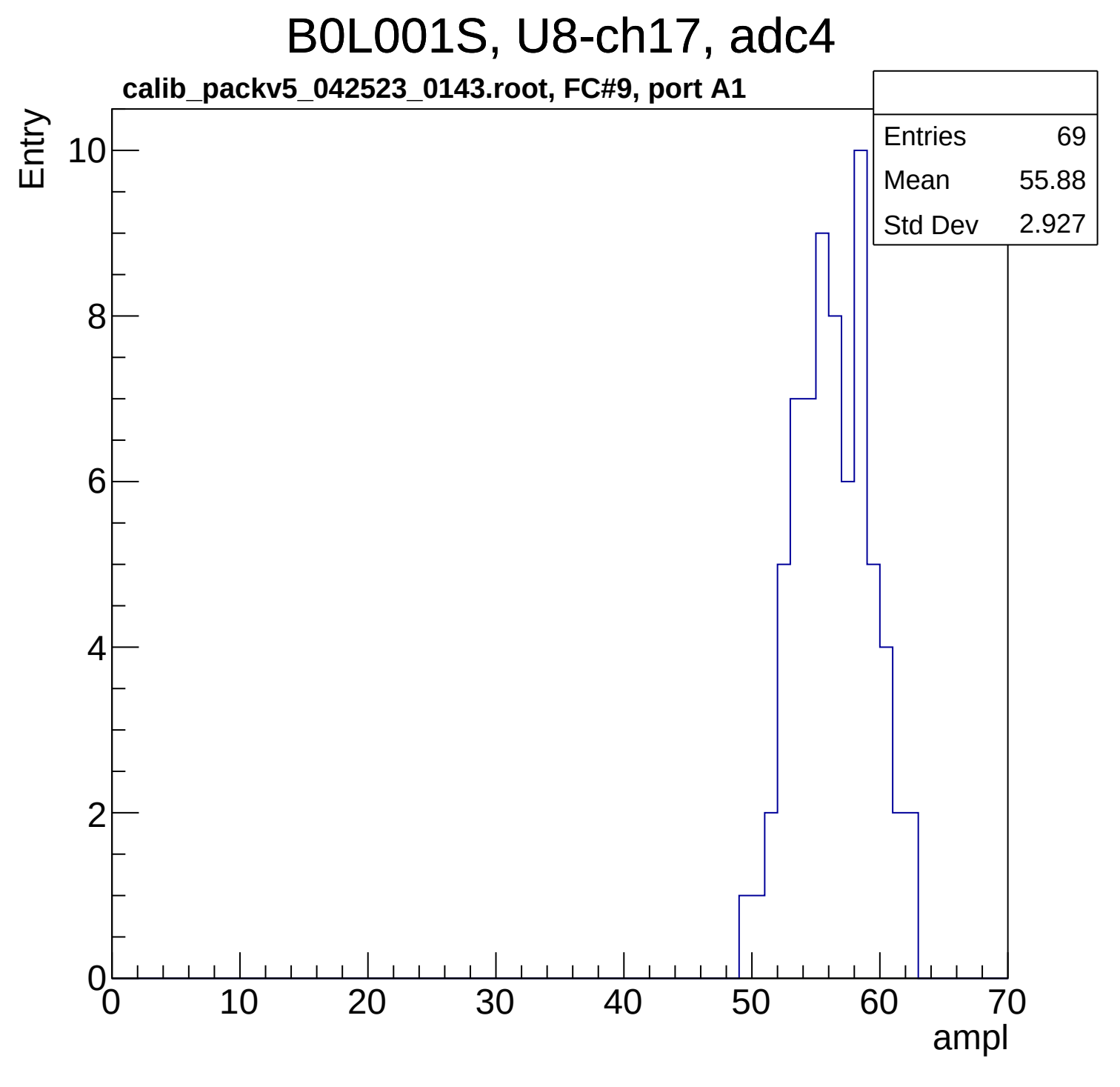
B0L001S, U8-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 55.88
Std Dev 2.927
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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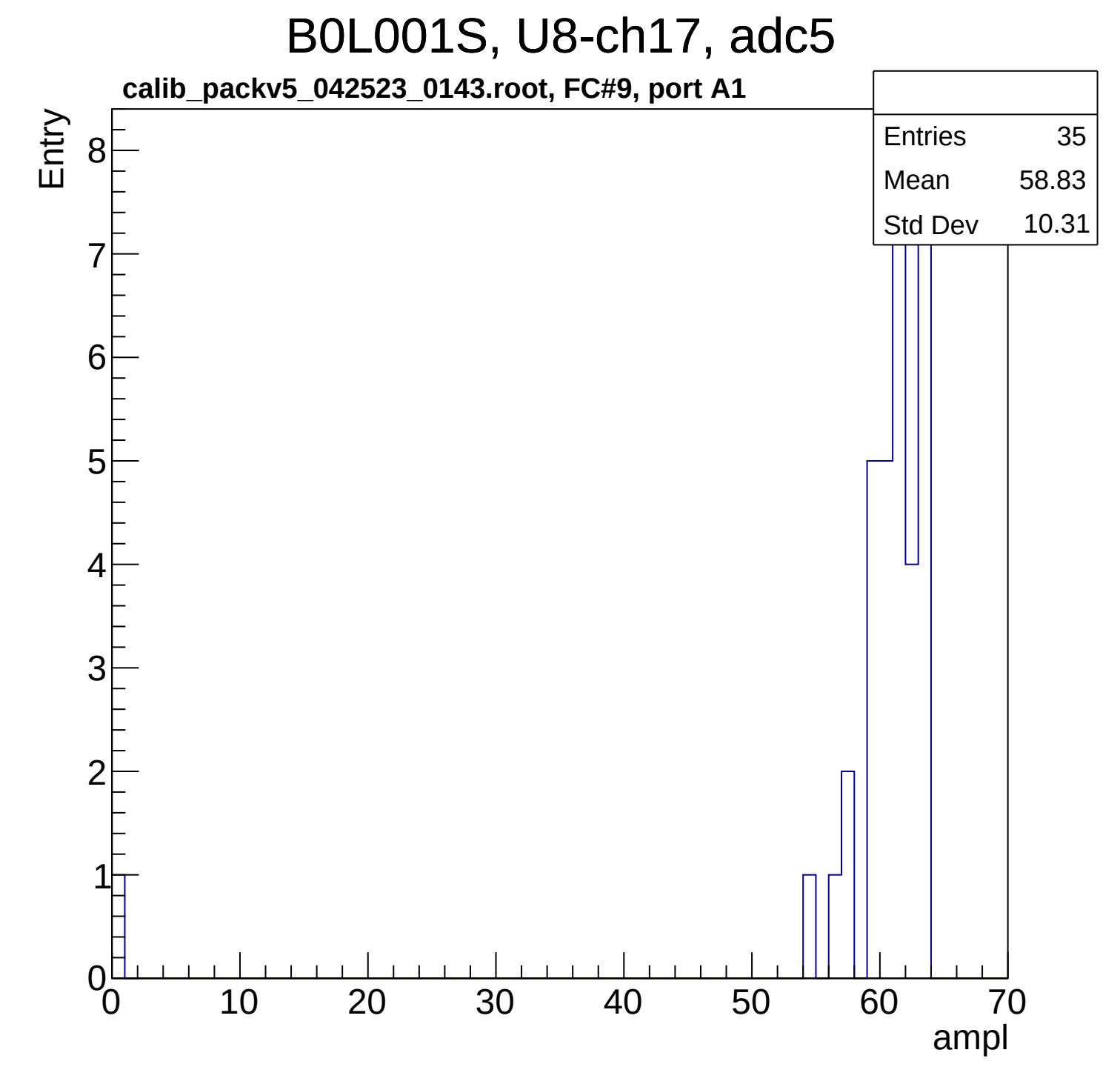
B0L001S, U8-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.83
Std Dev 10.31
Entry
ampl
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4
5
6
7
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0
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40
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60
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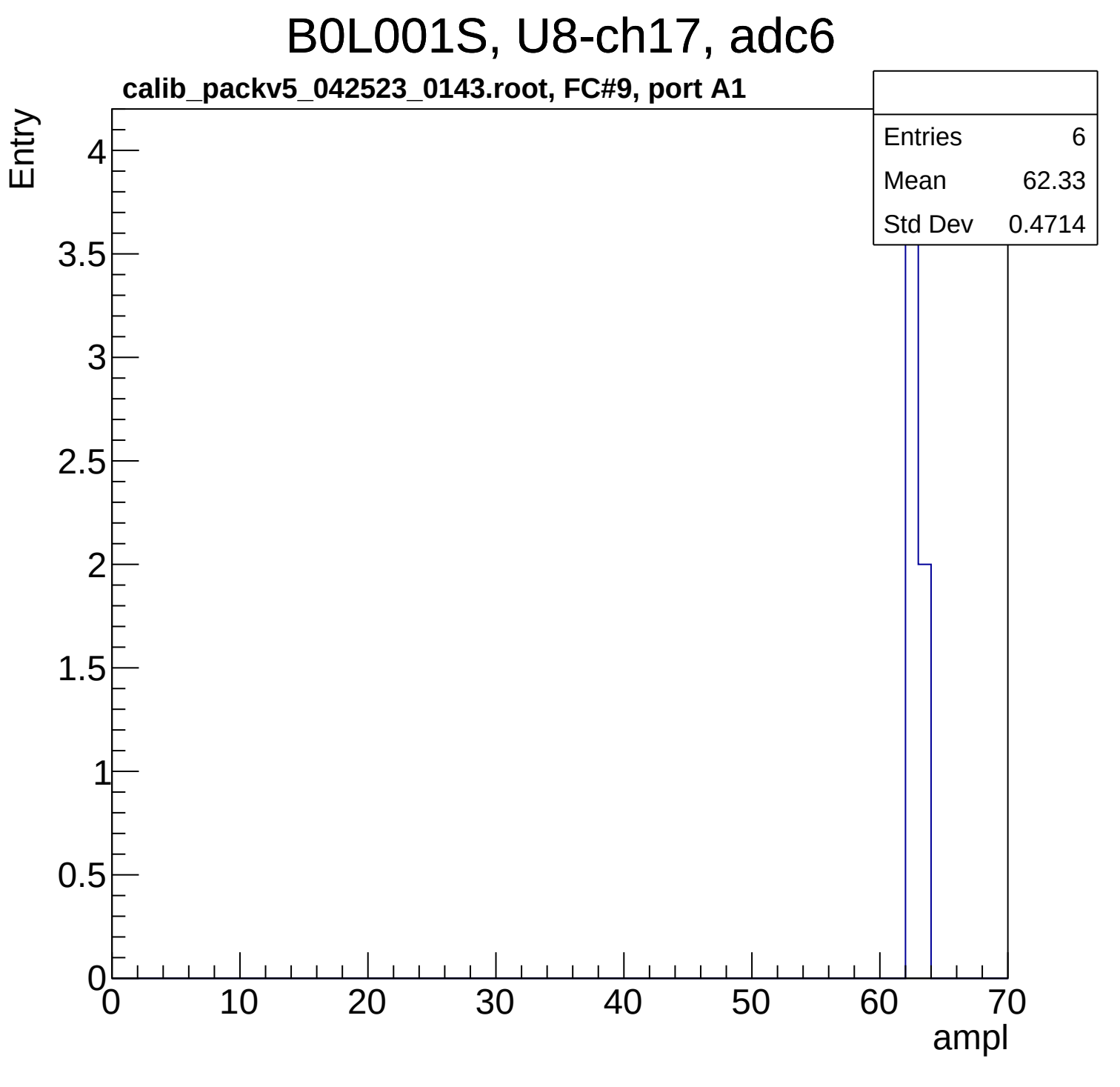
B0L001S, U8-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 0.4714
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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60
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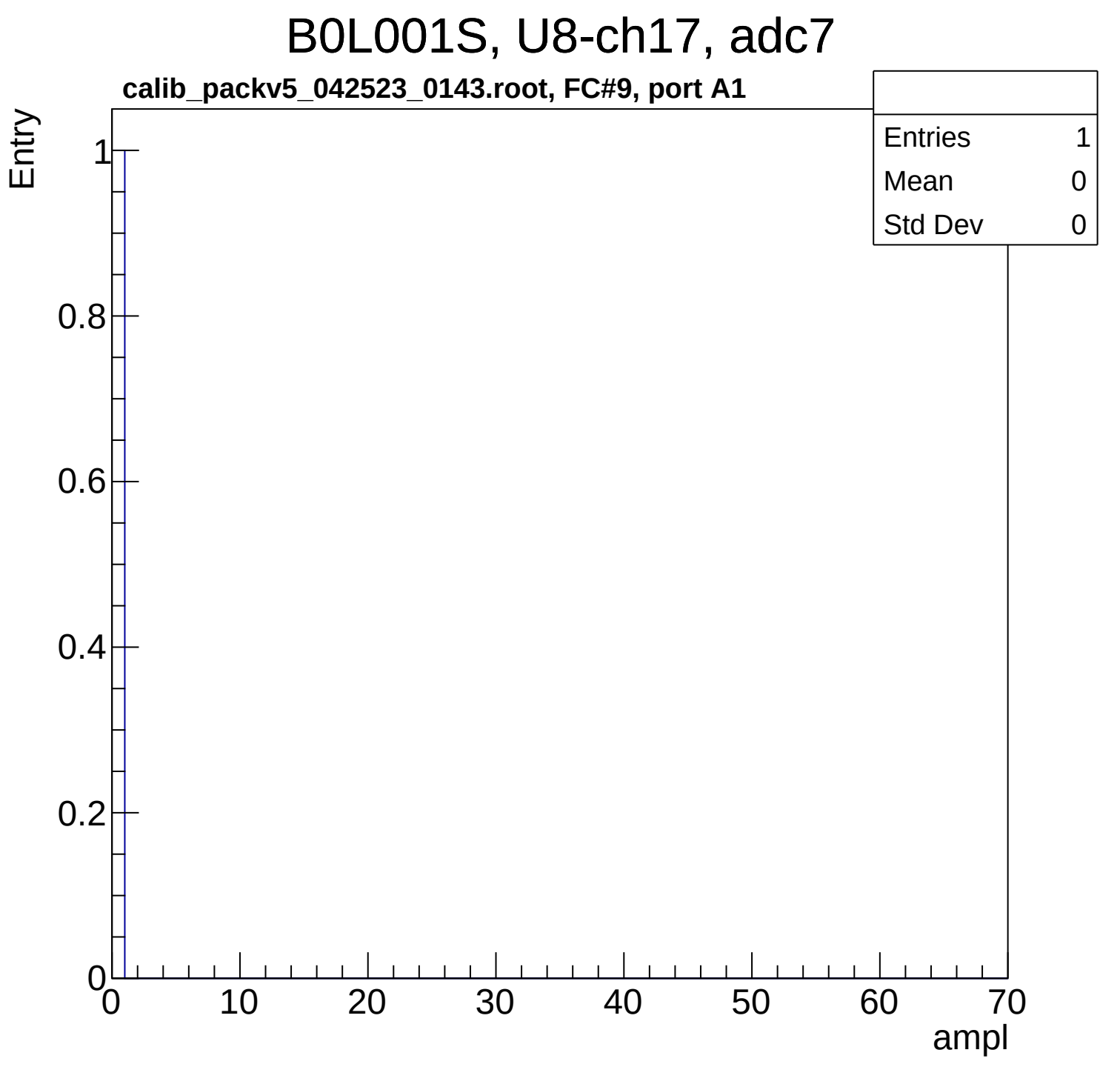
B0L001S, U8-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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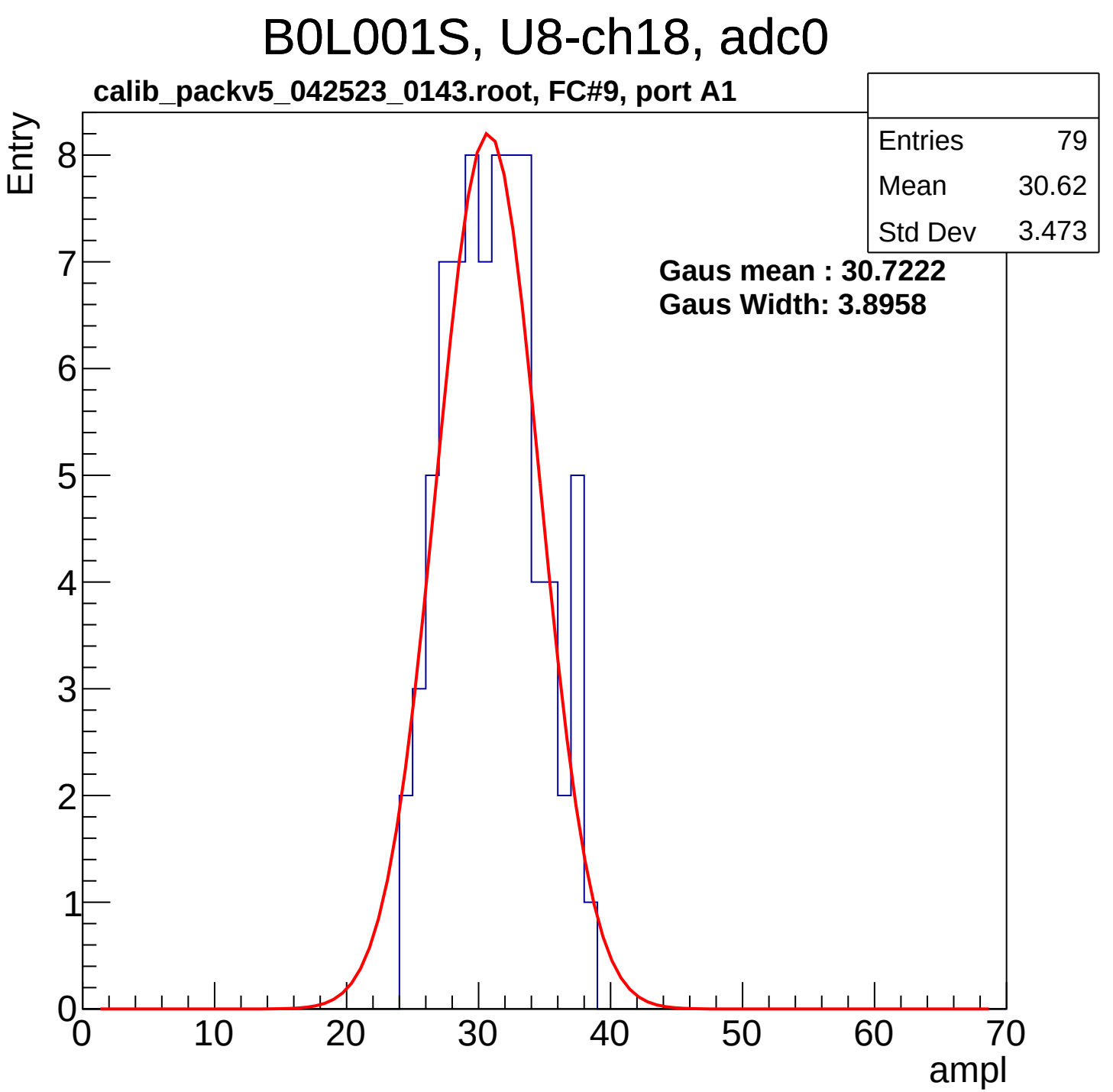
30
40
50
60
70
ampl


B0L001S, U8-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.62
Std Dev 3.473
Gaus mean : 30.7222
Gaus Width: 3.8958
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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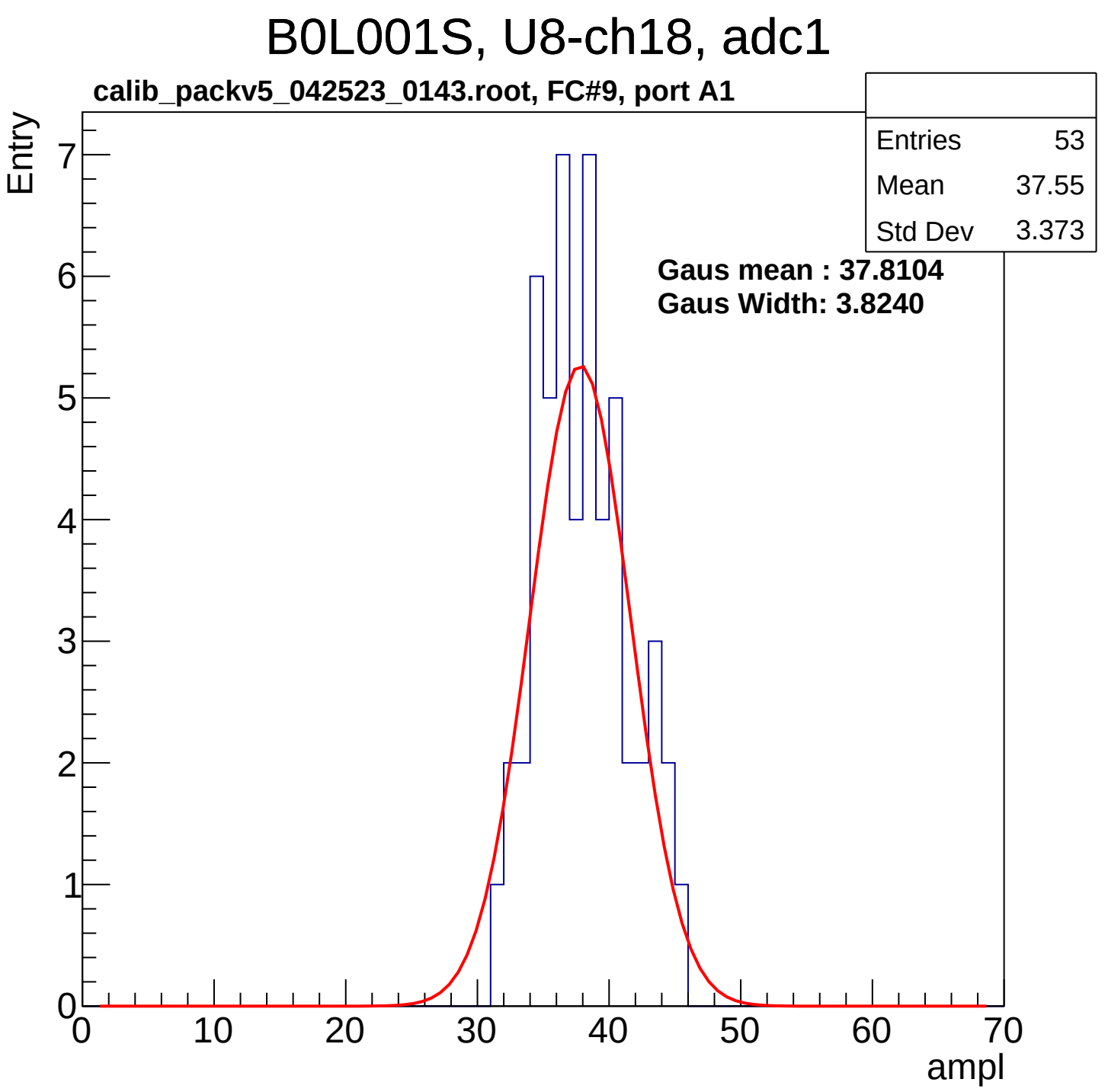
B0L001S, U8-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 37.55
Std Dev 3.373
Gaus mean : 37.8104
Gaus Width: 3.8240
Entry
ampl
0
1
2
3
4
5
6
7
0
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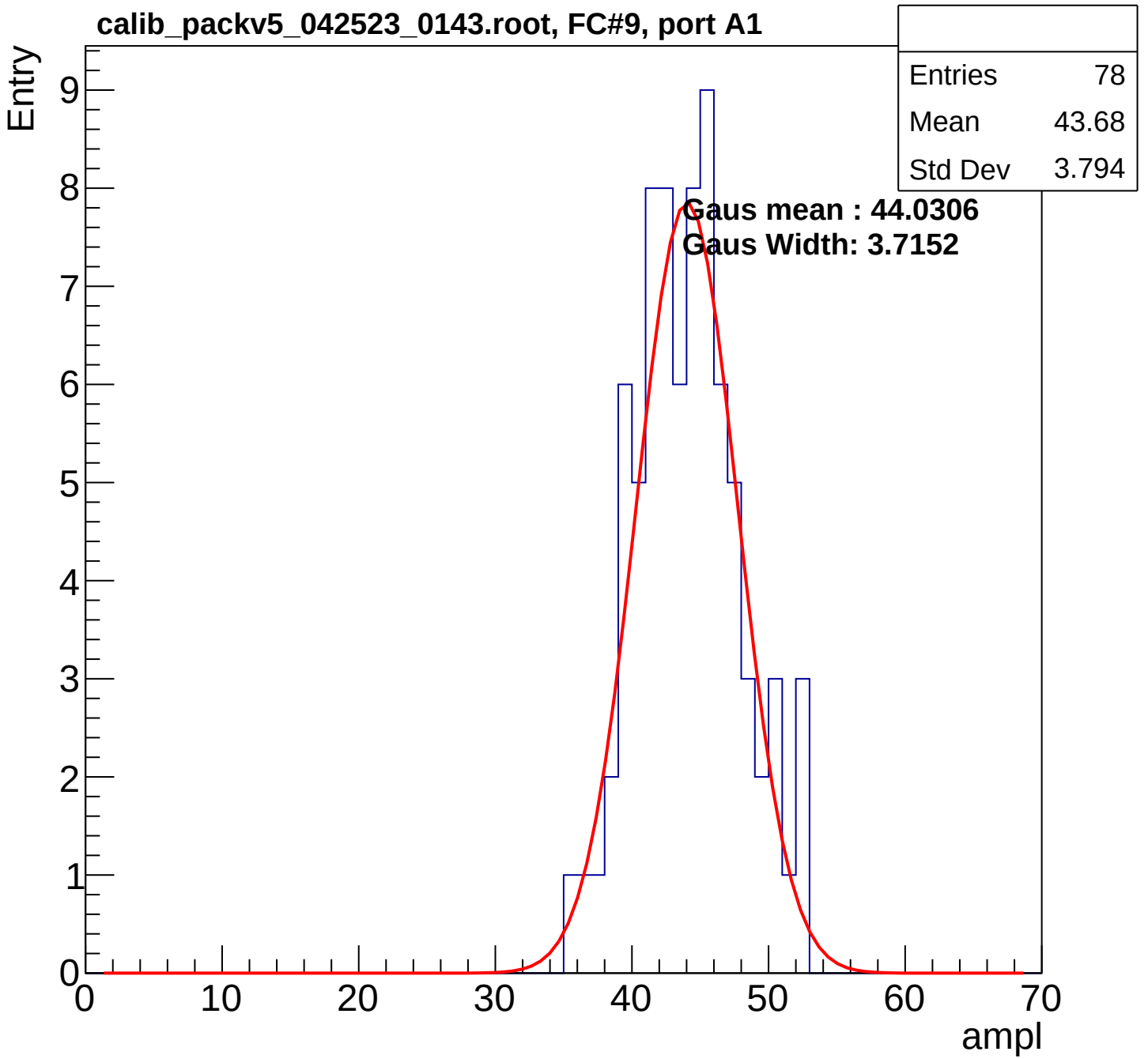
20
30
40
50
60
70


B0L001S, U8-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.68
Std Dev 3.794
Gaus mean : 44.0306
Gaus Width: 3.7152
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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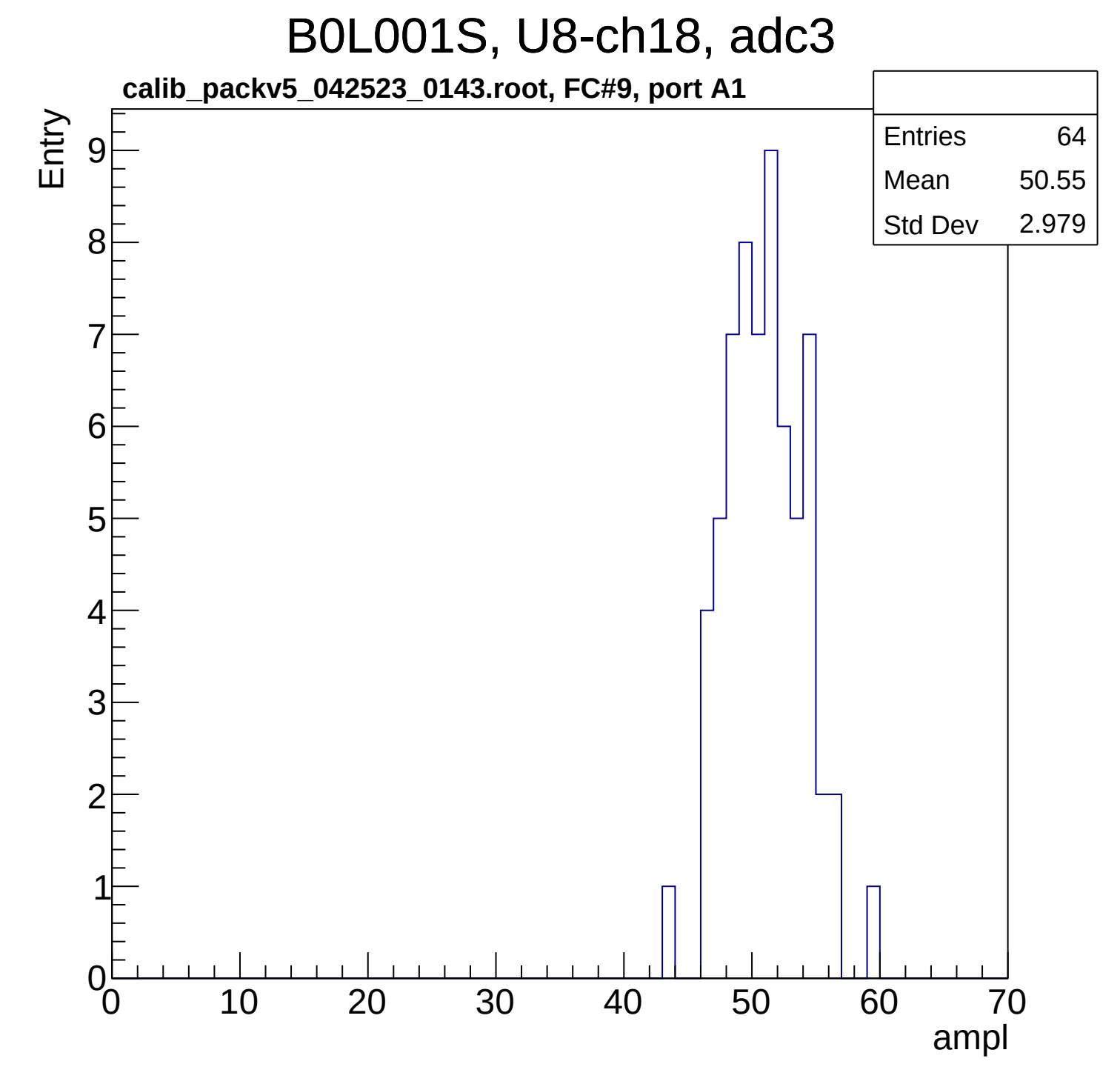
B0L001S, U8-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.55
Std Dev 2.979
Entry
ampl
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3
4
5
6
7
8
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0
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60
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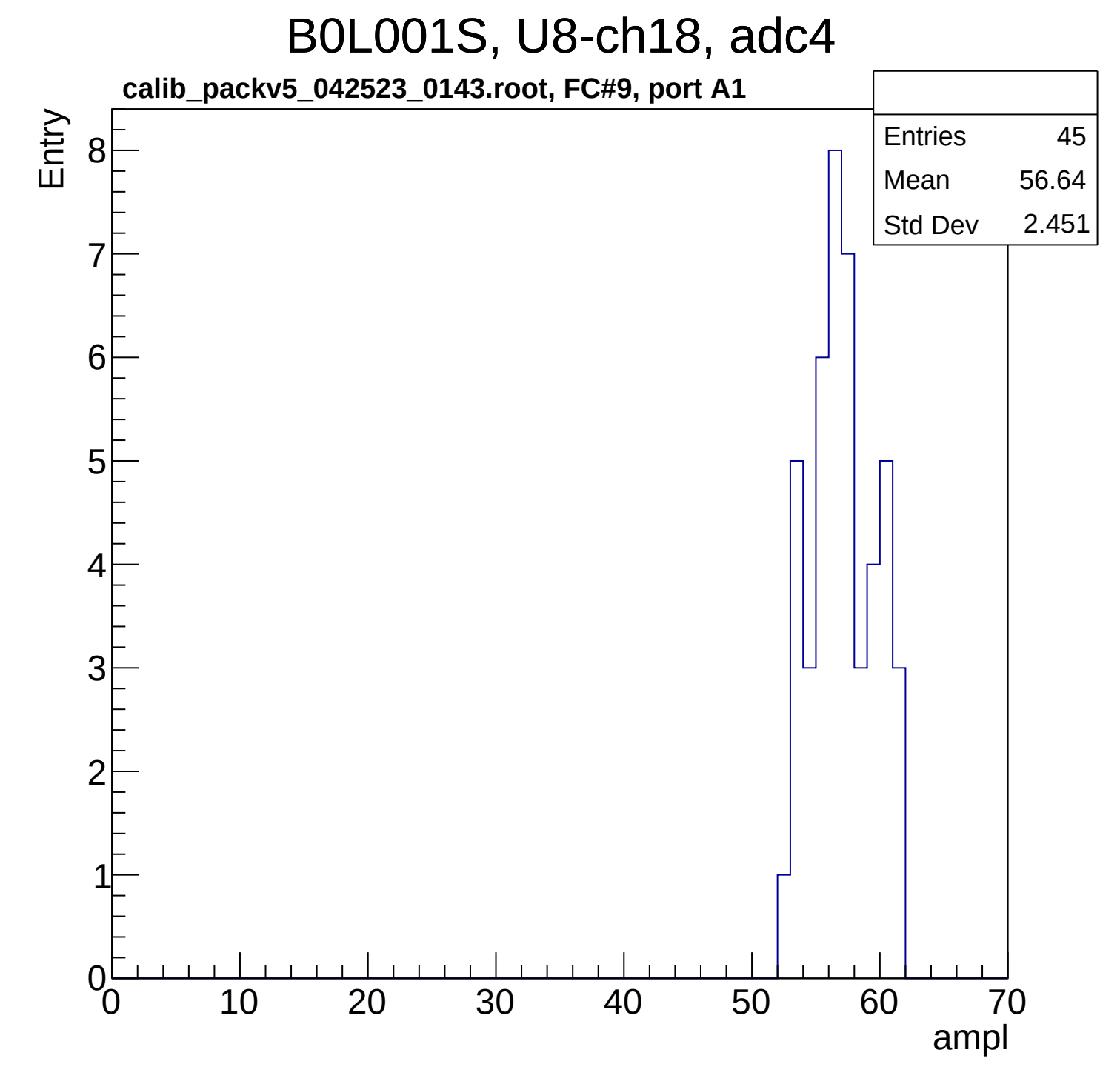
B0L001S, U8-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.64
Std Dev 2.451
Entry
ampl
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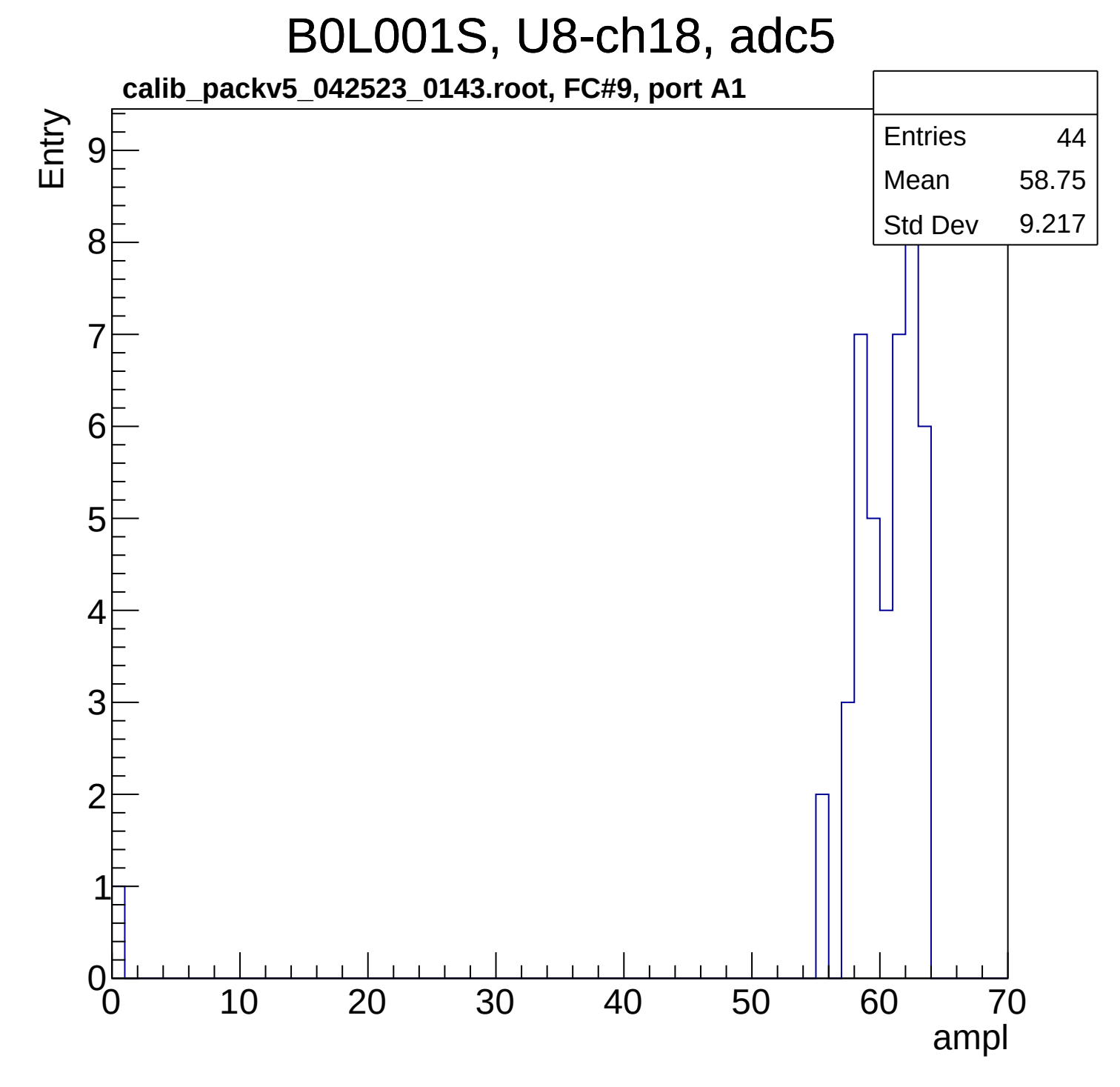
B0L001S, U8-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.75
Std Dev 9.217
Entry
ampl
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2
3
4
5
6
7
8
9
0
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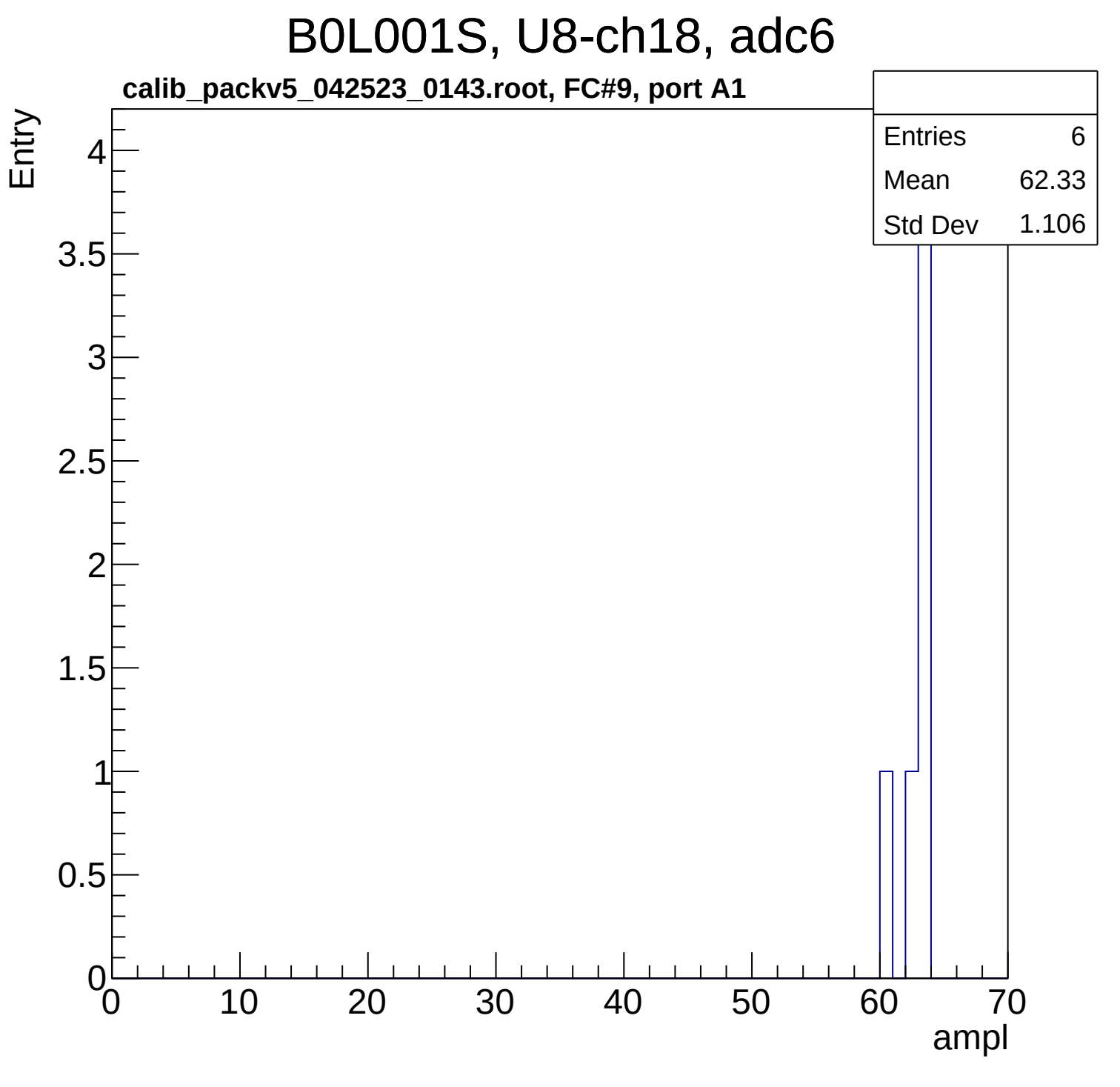
B0L001S, U8-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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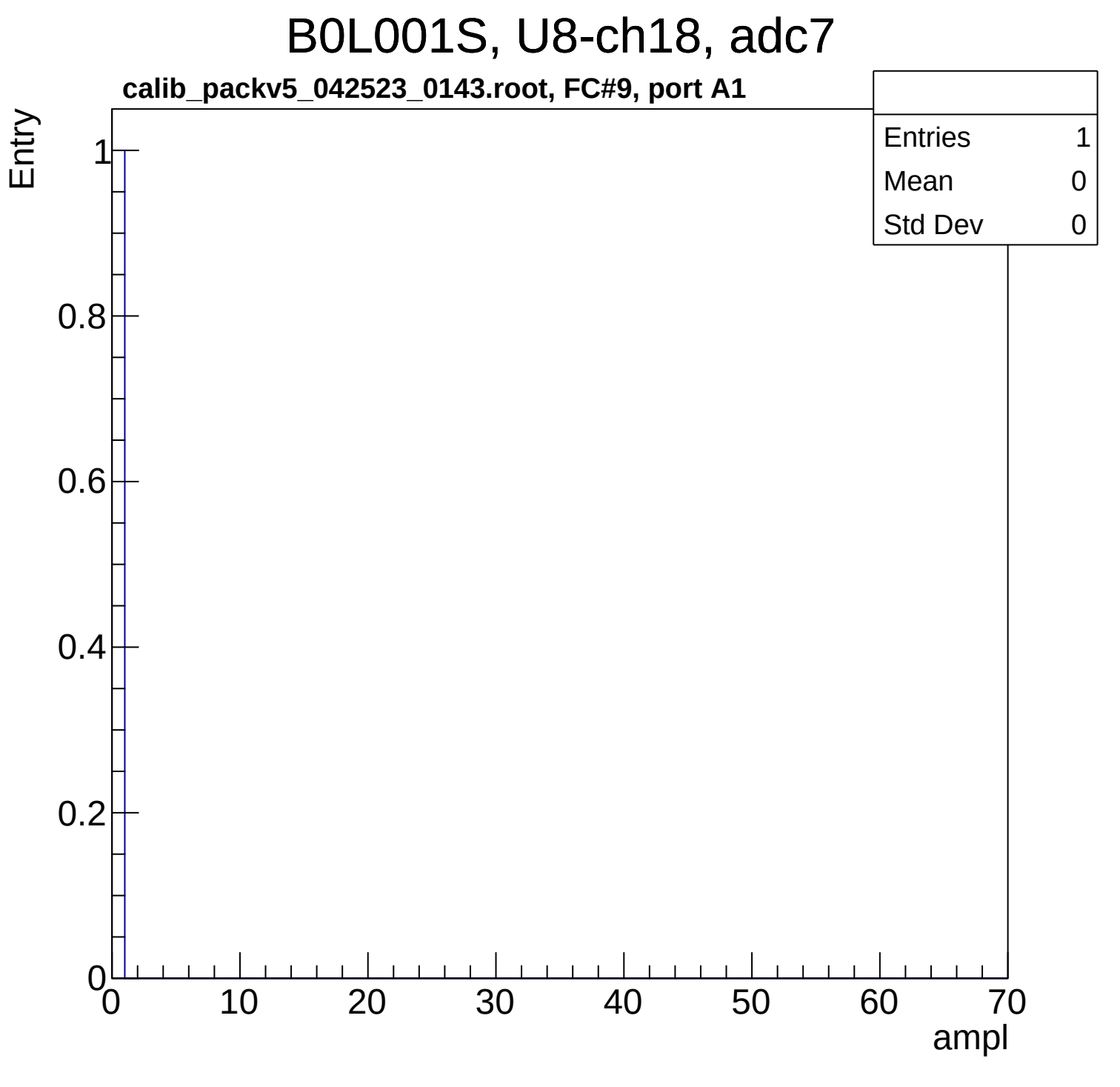
B0L001S, U8-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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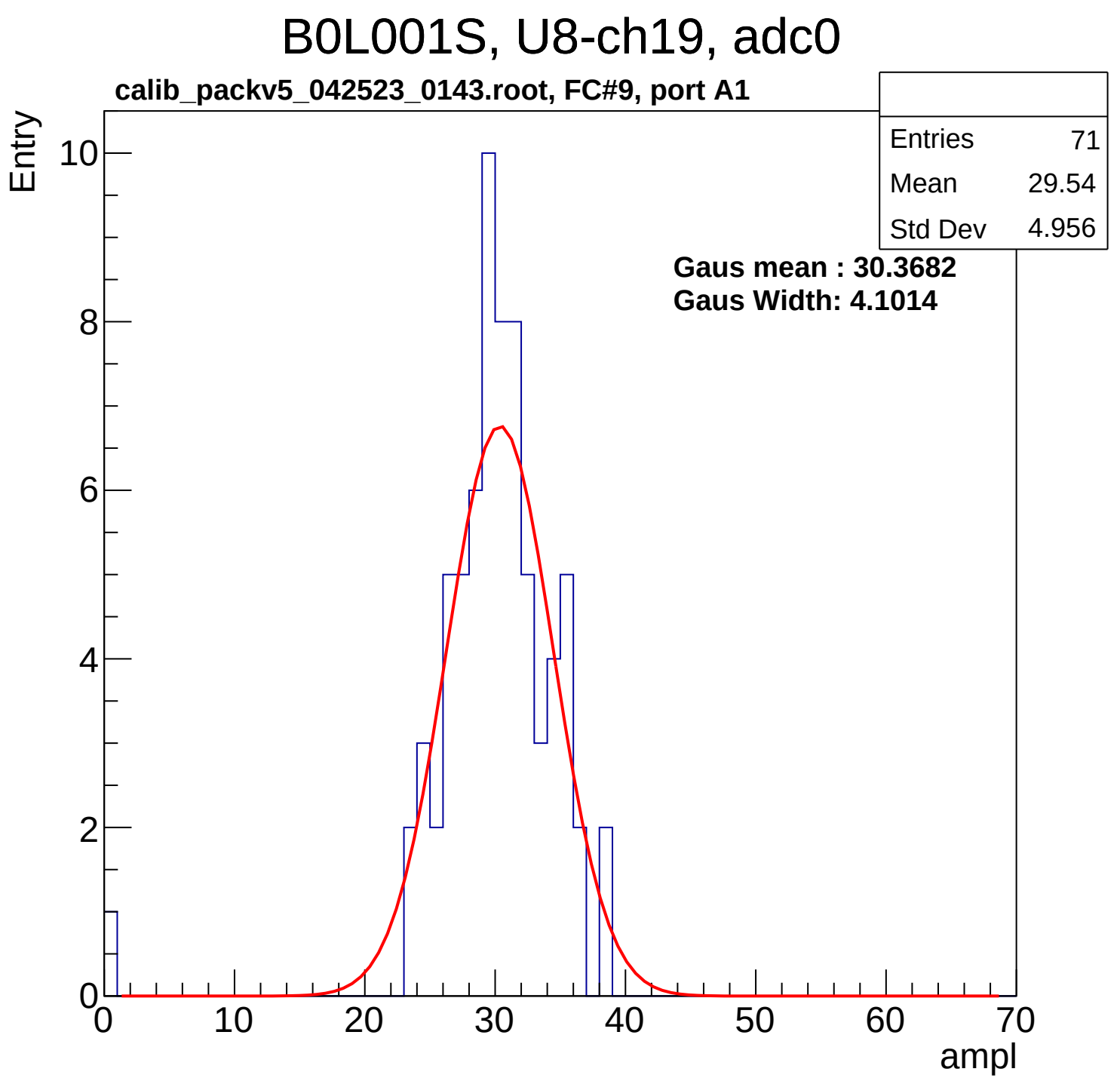
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.54
Std Dev 4.956
Gaus mean : 30.3682
Gaus Width: 4.1014
Entry
ampl
0
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6
8
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0
10
20
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40
50
60
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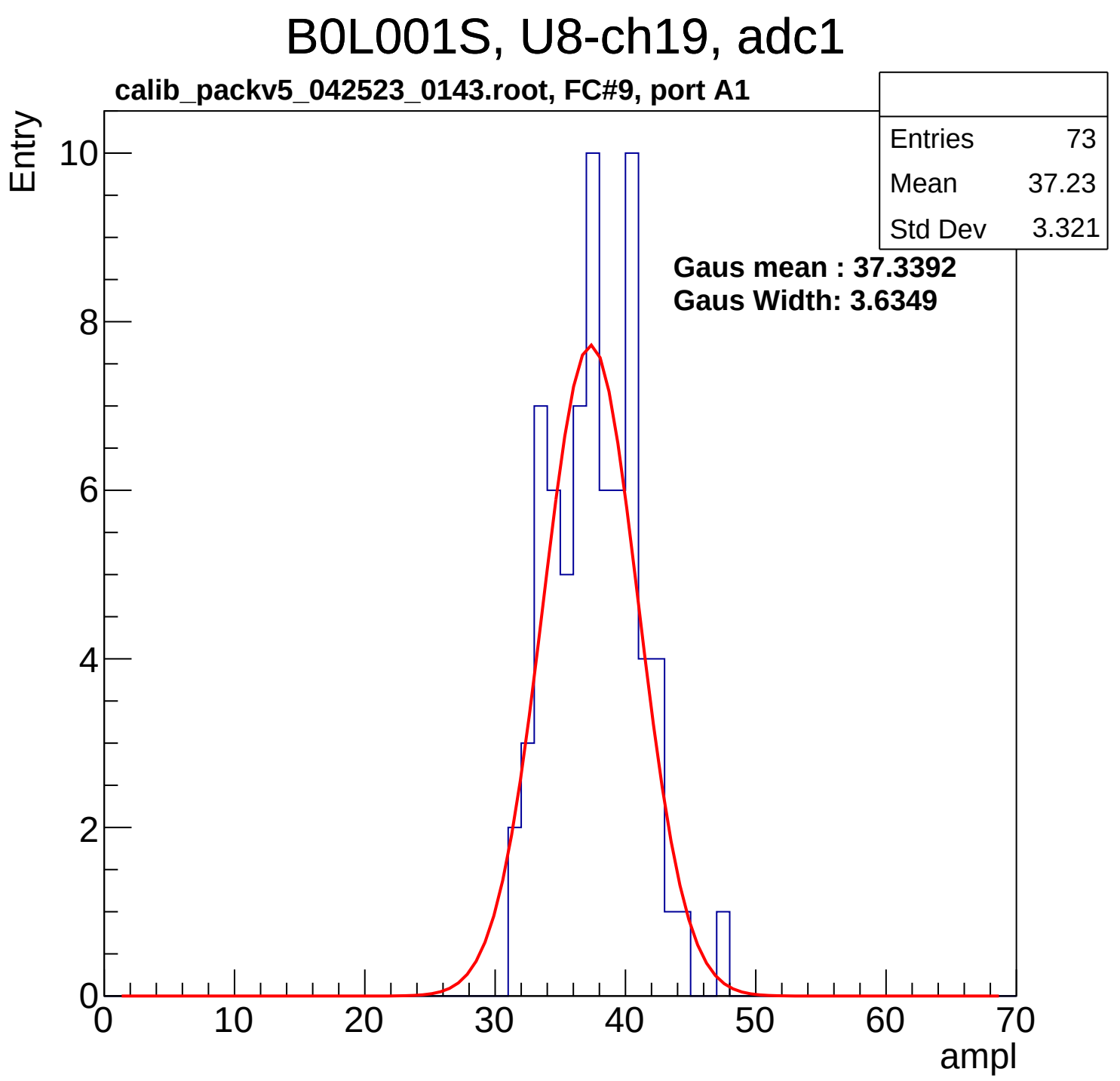
B0L001S, U8-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.23
Std Dev 3.321
Gaus mean : 37.3392
Gaus Width: 3.6349
Entry
ampl
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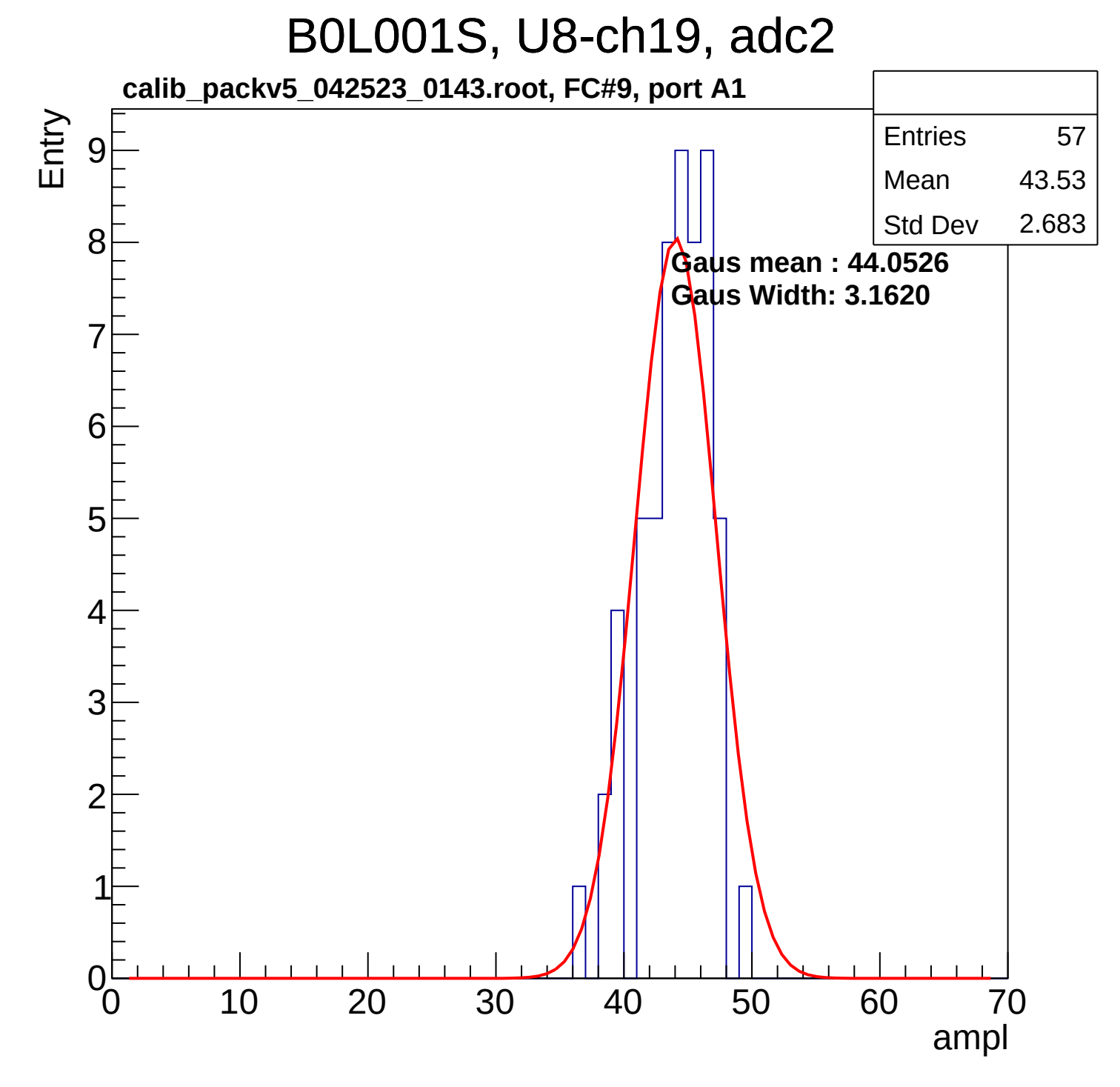
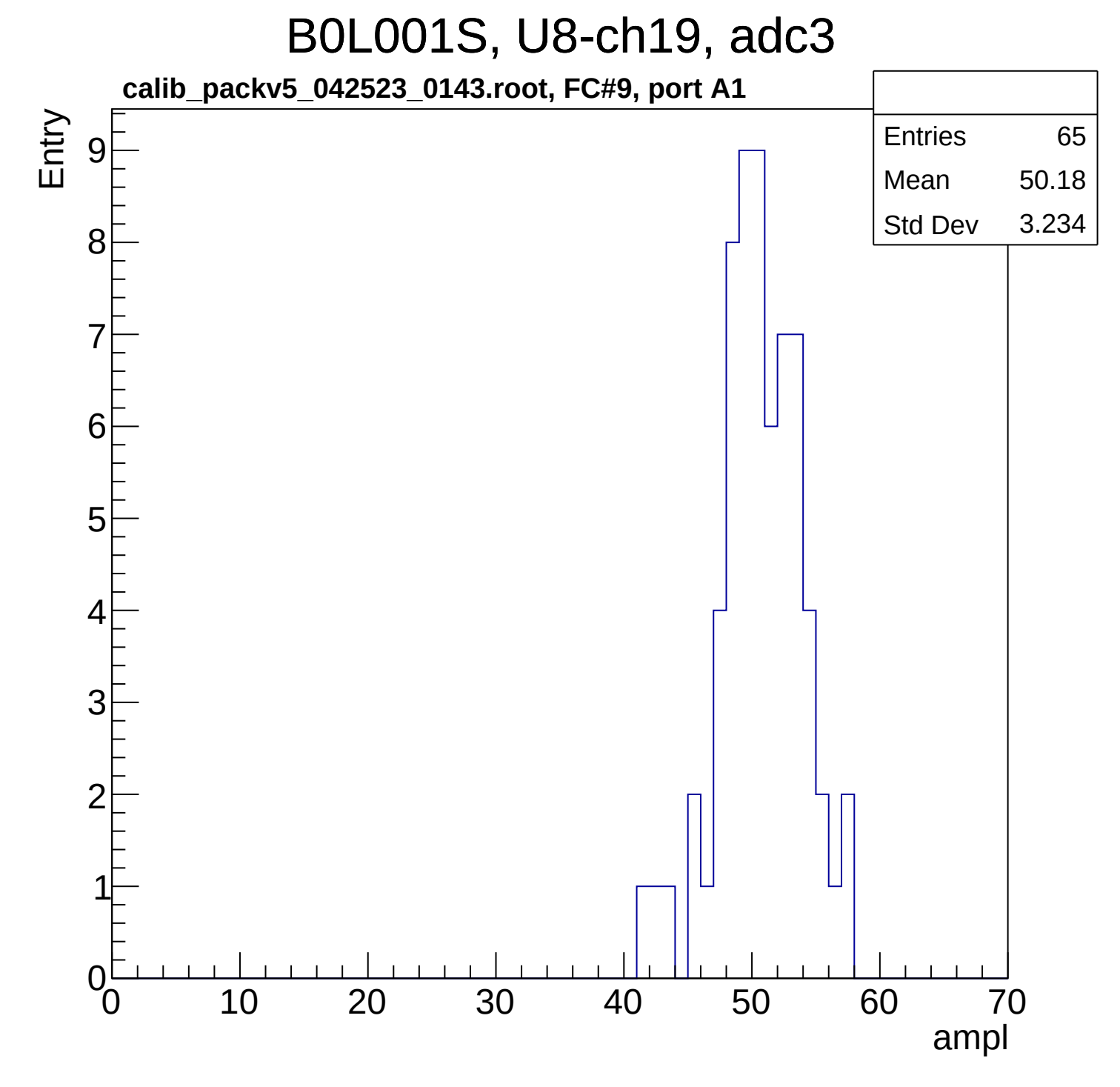
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 43.53
Std Dev 2.683
Gaus mean : 44.0526
Gaus Width: 3.1620
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.18
Std Dev 3.234
Entry
ampl
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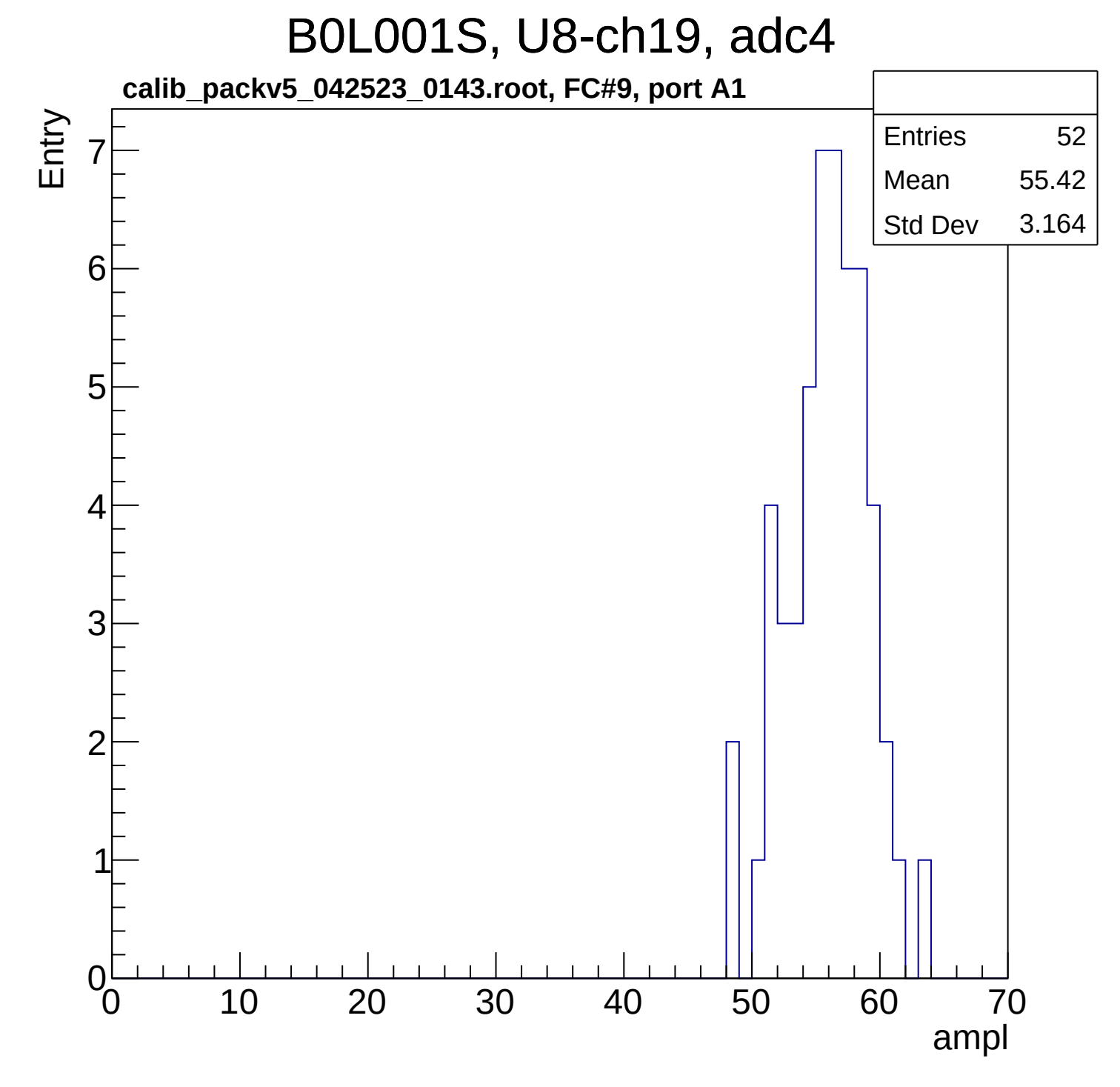
B0L001S, U8-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.42
Std Dev 3.164
Entry
ampl
0
1
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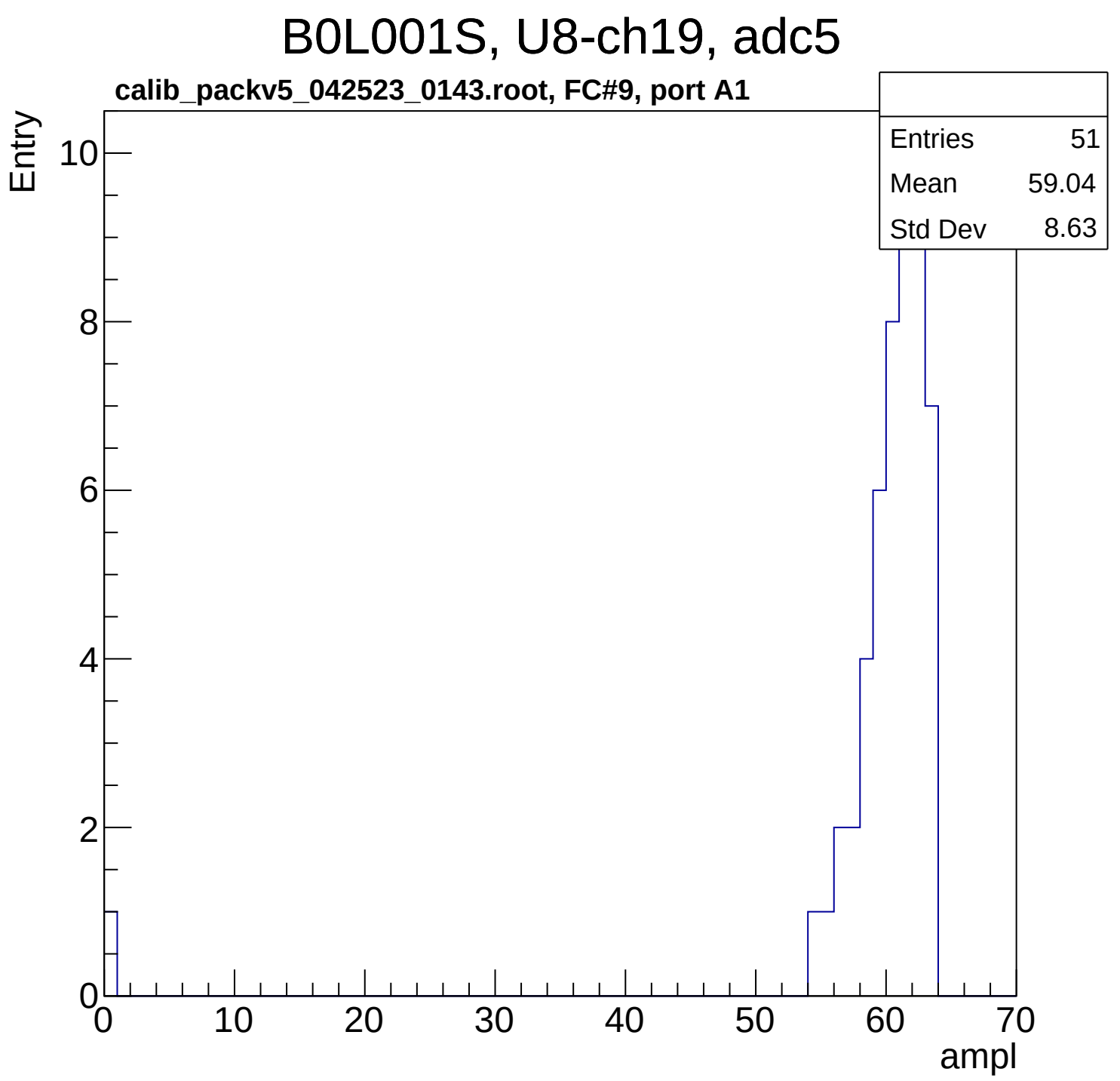
B0L001S, U8-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 59.04
Std Dev 8.63
Entry
0
2
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6
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10
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10
20
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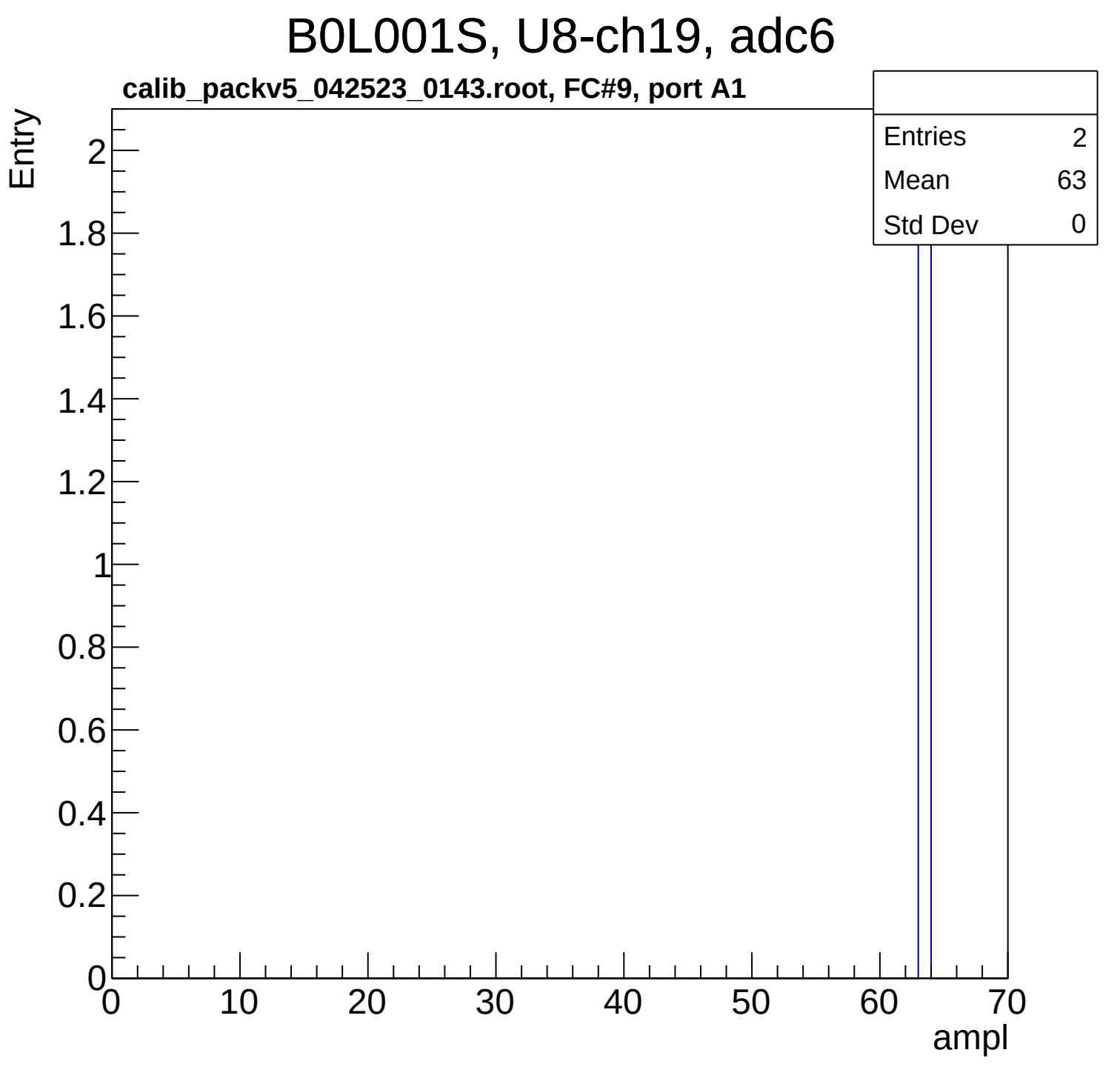
50
60
70
ampl


B0L001S, U8-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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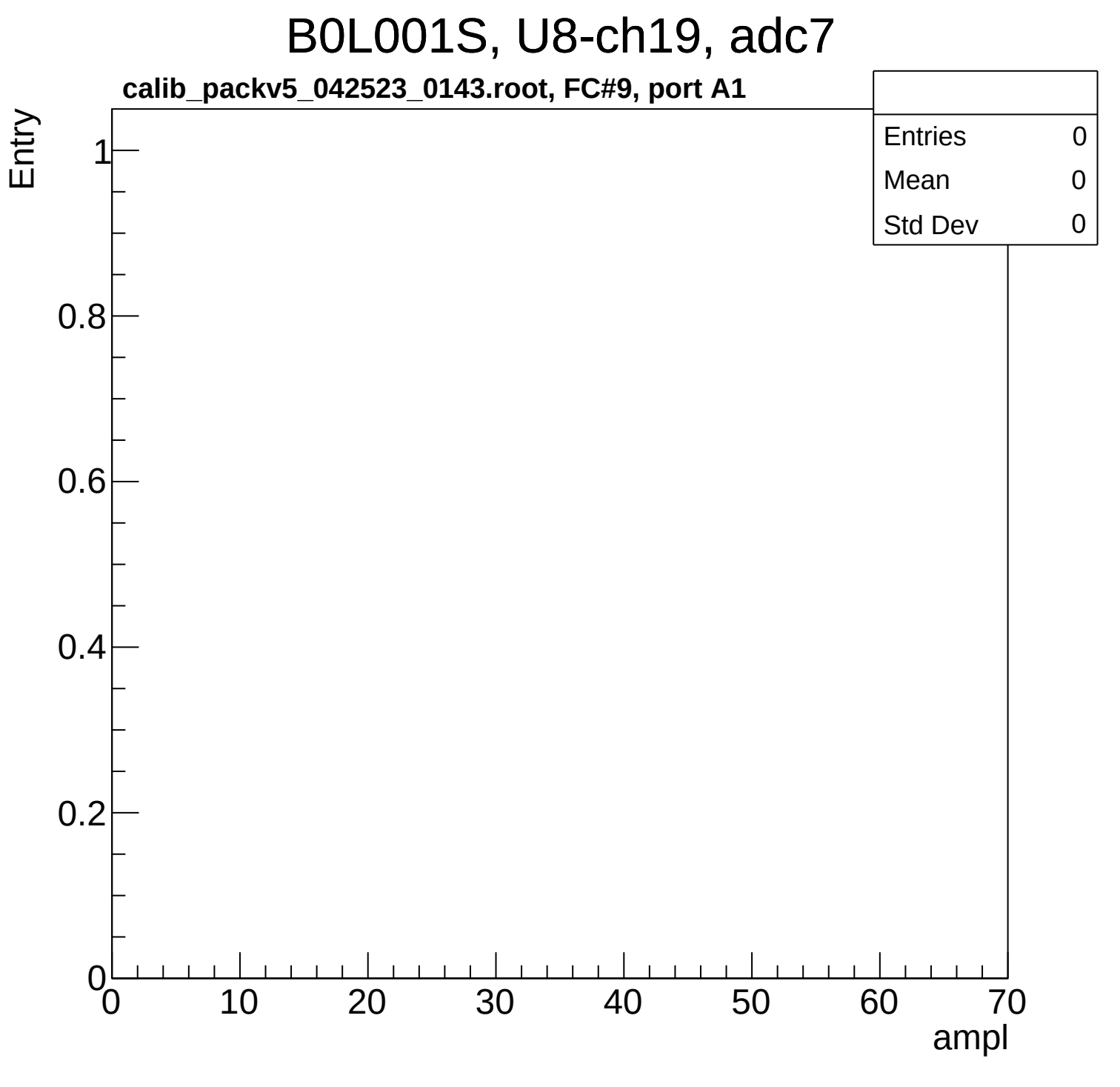
B0L001S, U8-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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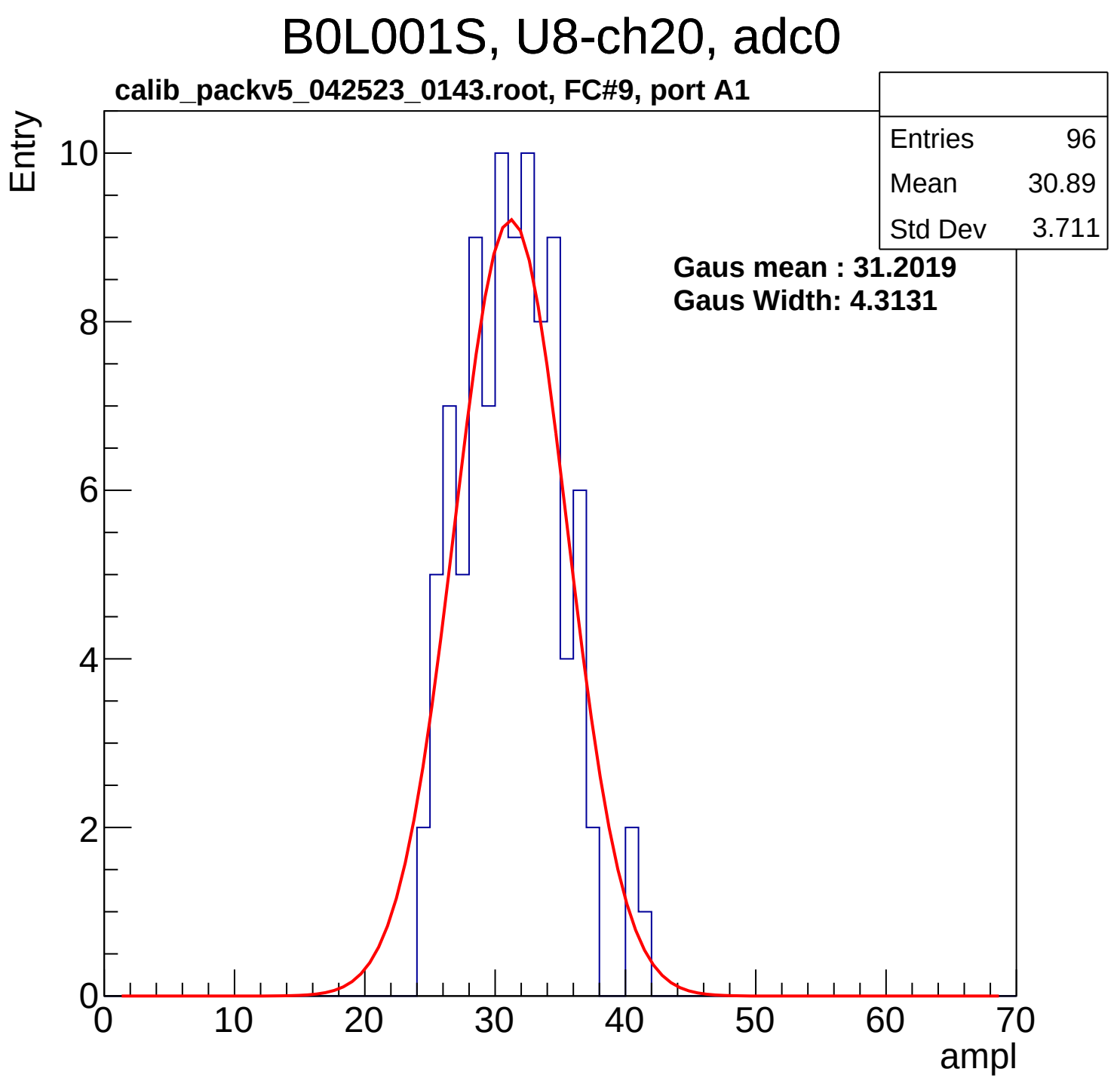
50
60
70
ampl


B0L001S, U8-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 96
Mean 30.89
Std Dev 3.711
Gaus mean : 31.2019
Gaus Width: 4.3131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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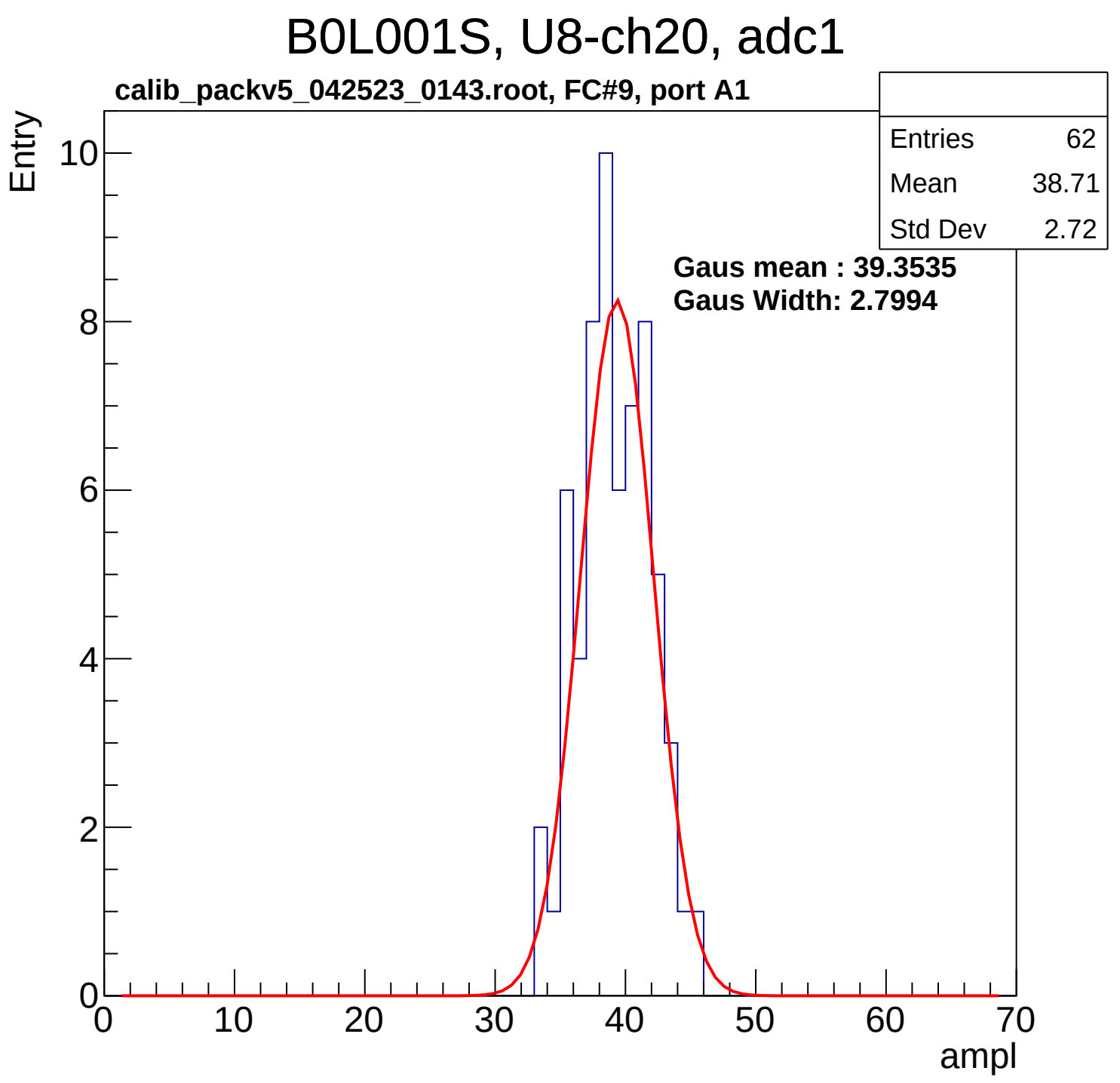
B0L001S, U8-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.71
Std Dev 2.72
Gaus mean : 39.3535
Gaus Width: 2.7994
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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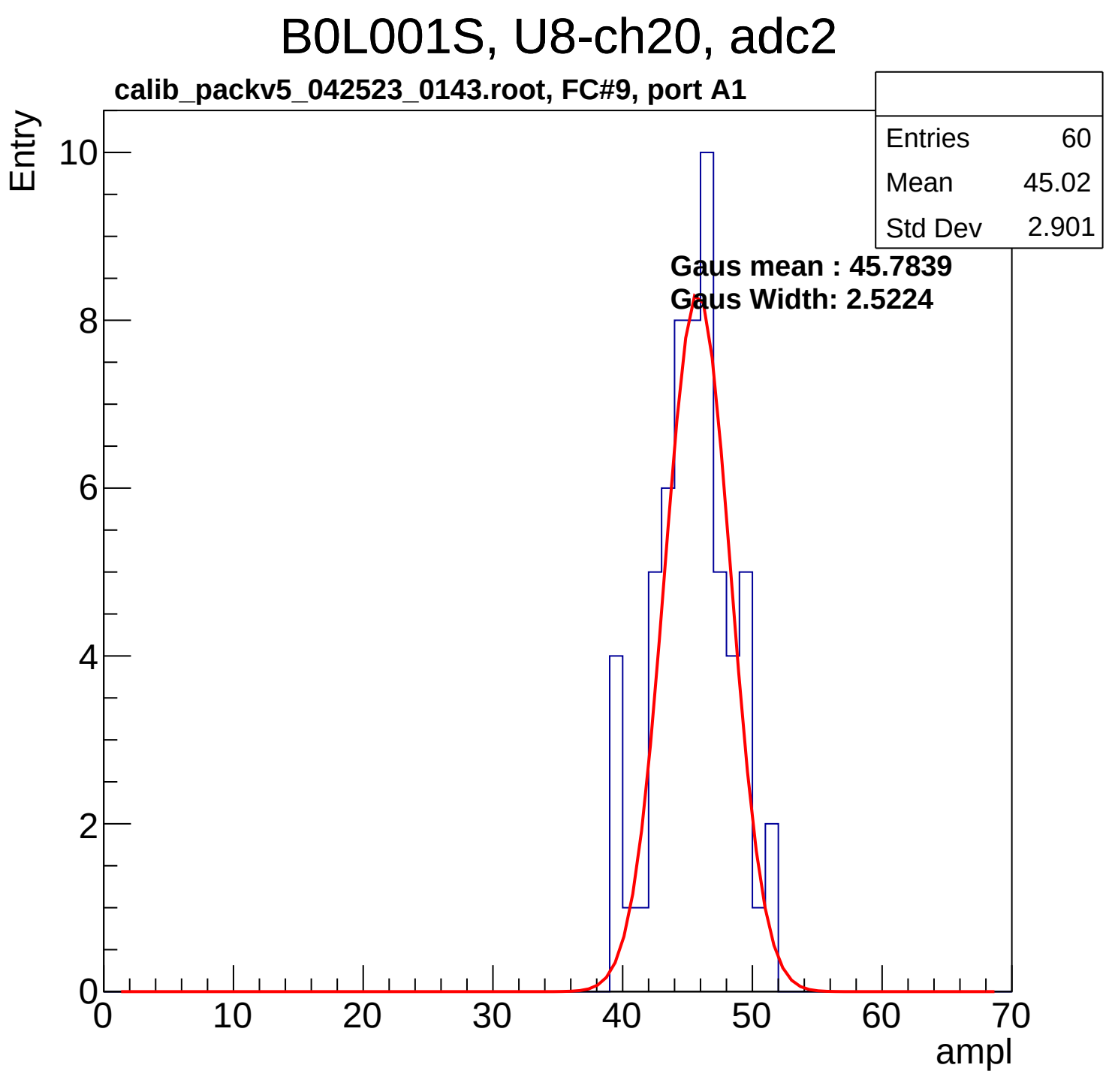
B0L001S, U8-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.02
Std Dev 2.901
Gaus mean : 45.7839
Gaus Width: 2.5224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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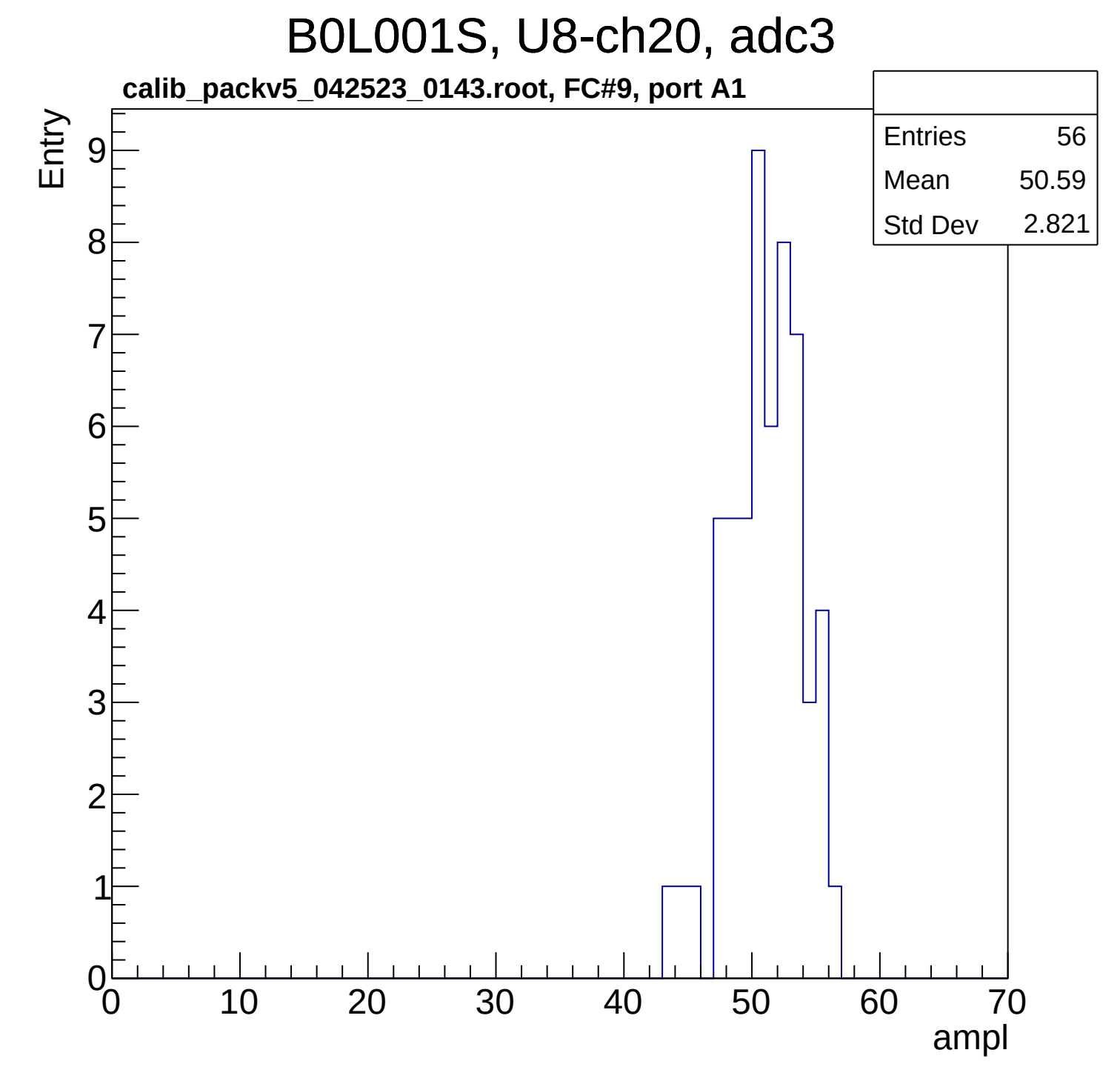
B0L001S, U8-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.59
Std Dev 2.821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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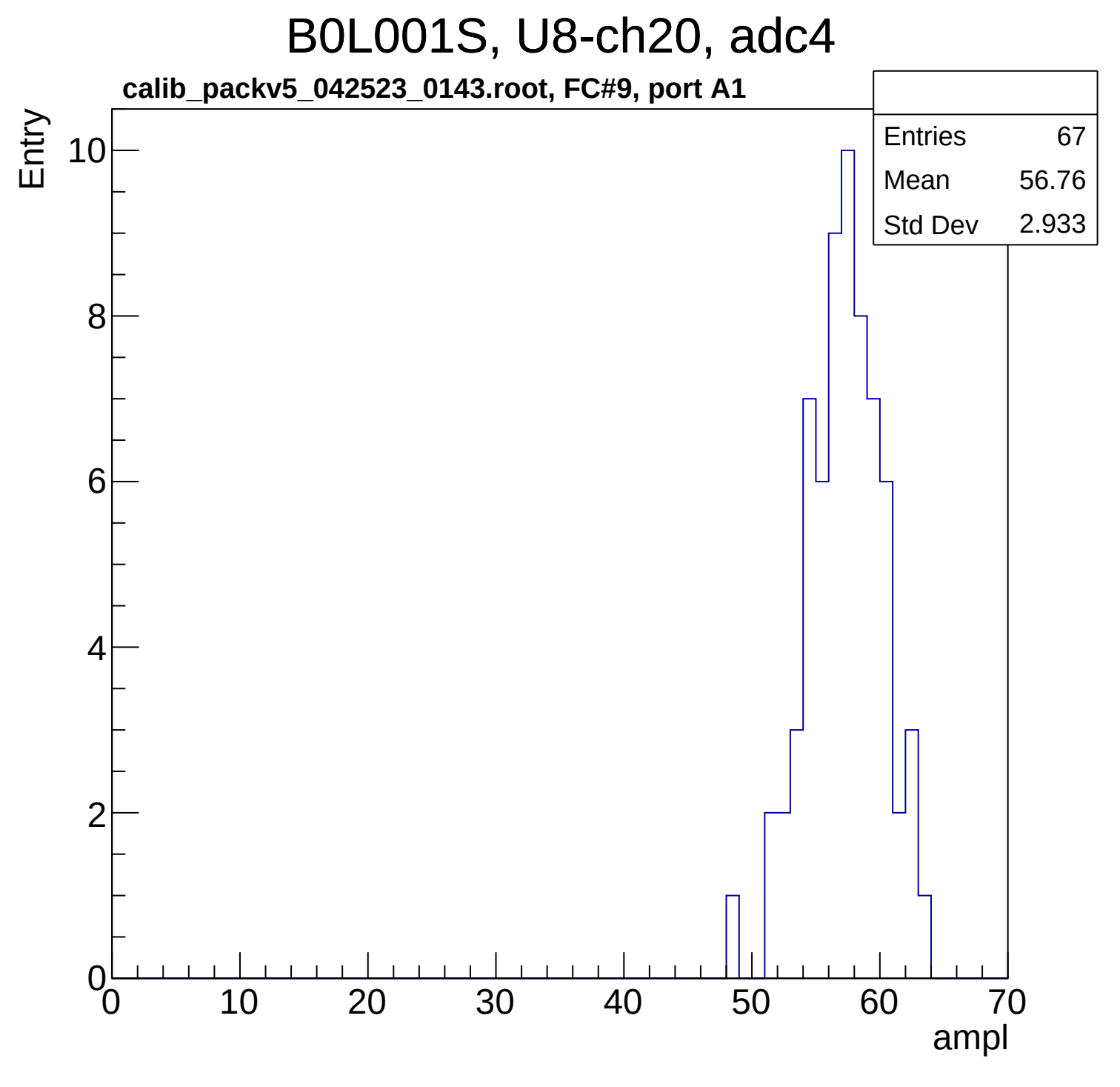
B0L001S, U8-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.76
Std Dev 2.933
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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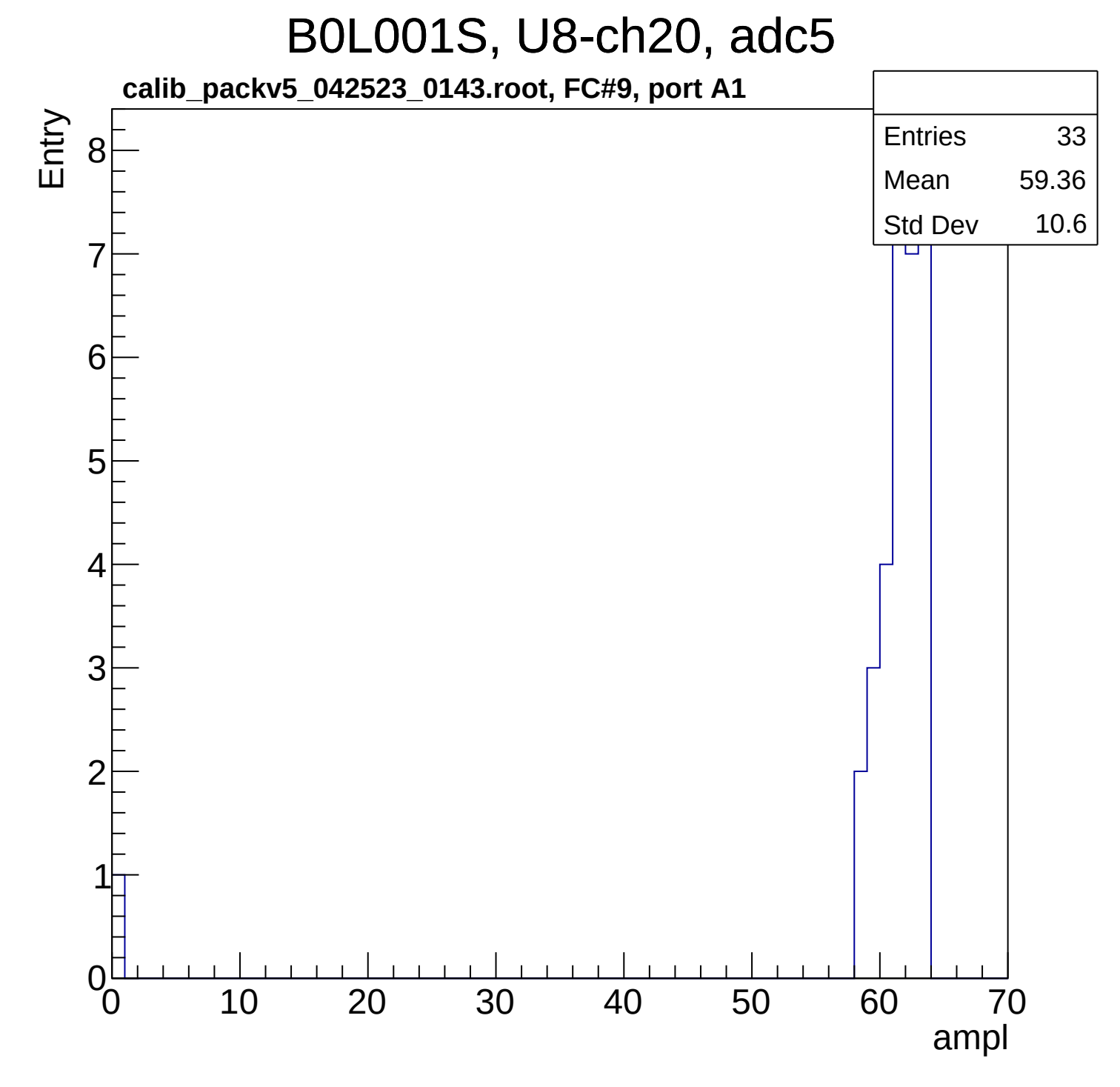
B0L001S, U8-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 59.36
Std Dev 10.6
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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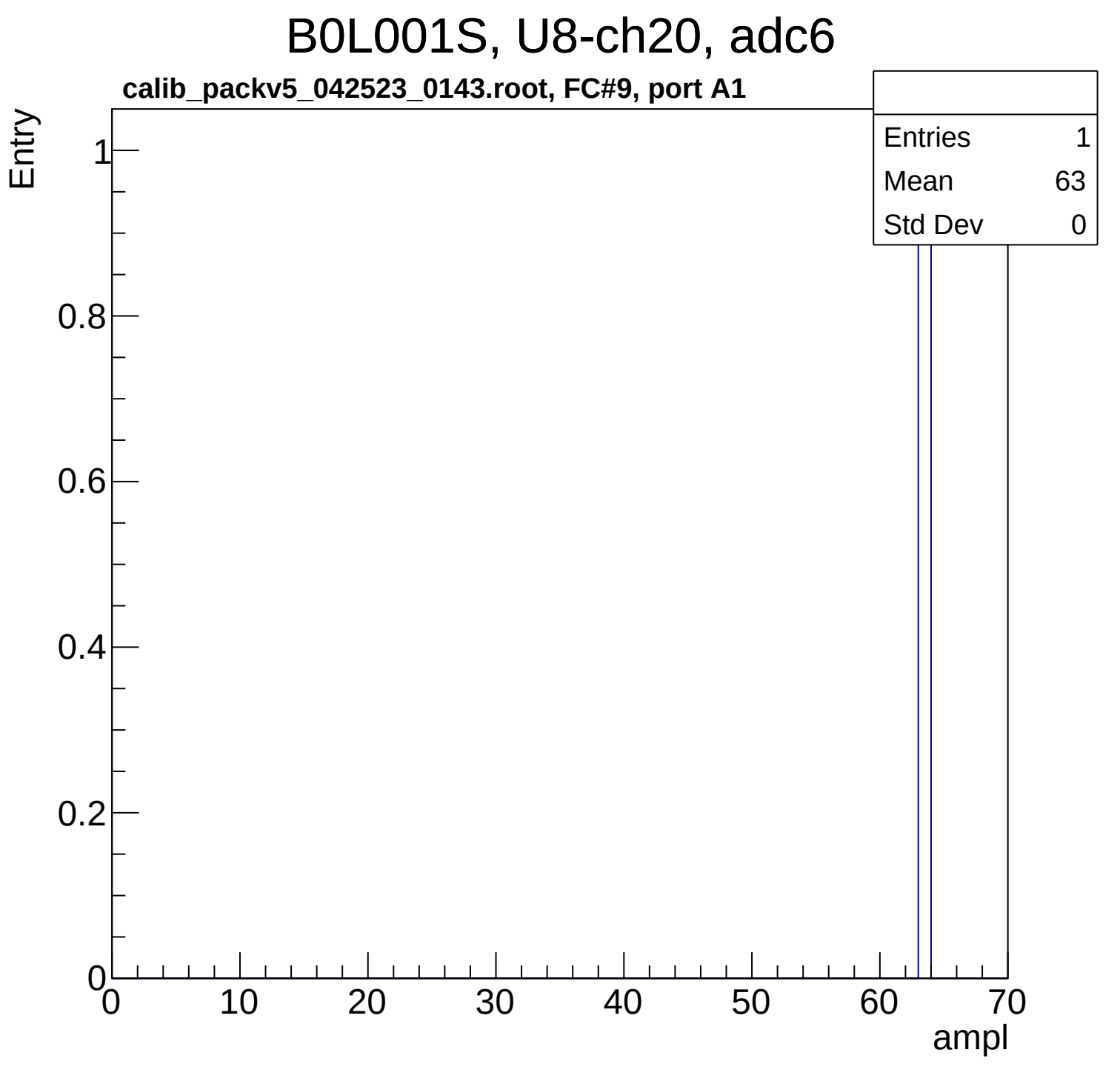
B0L001S, U8-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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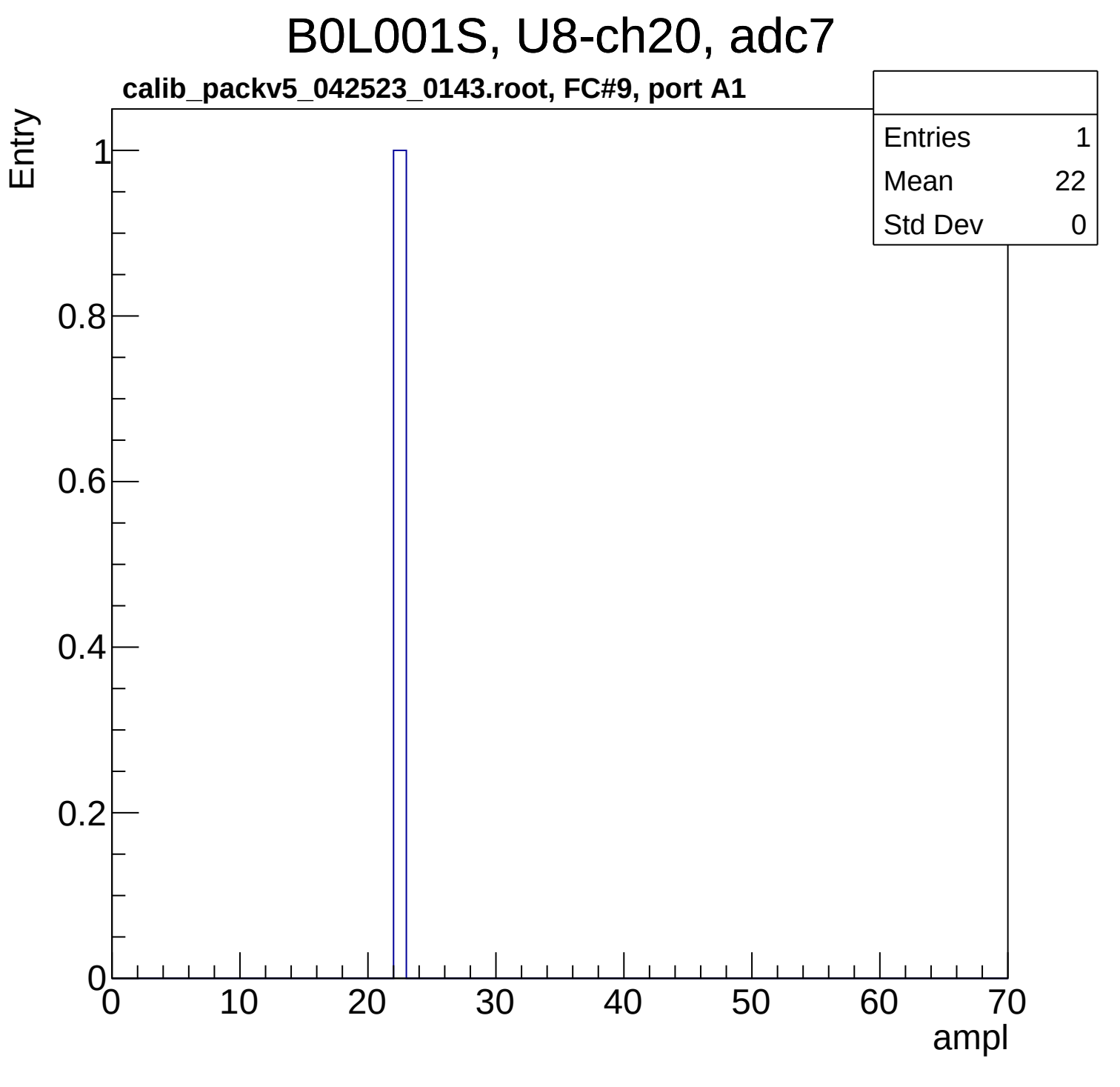
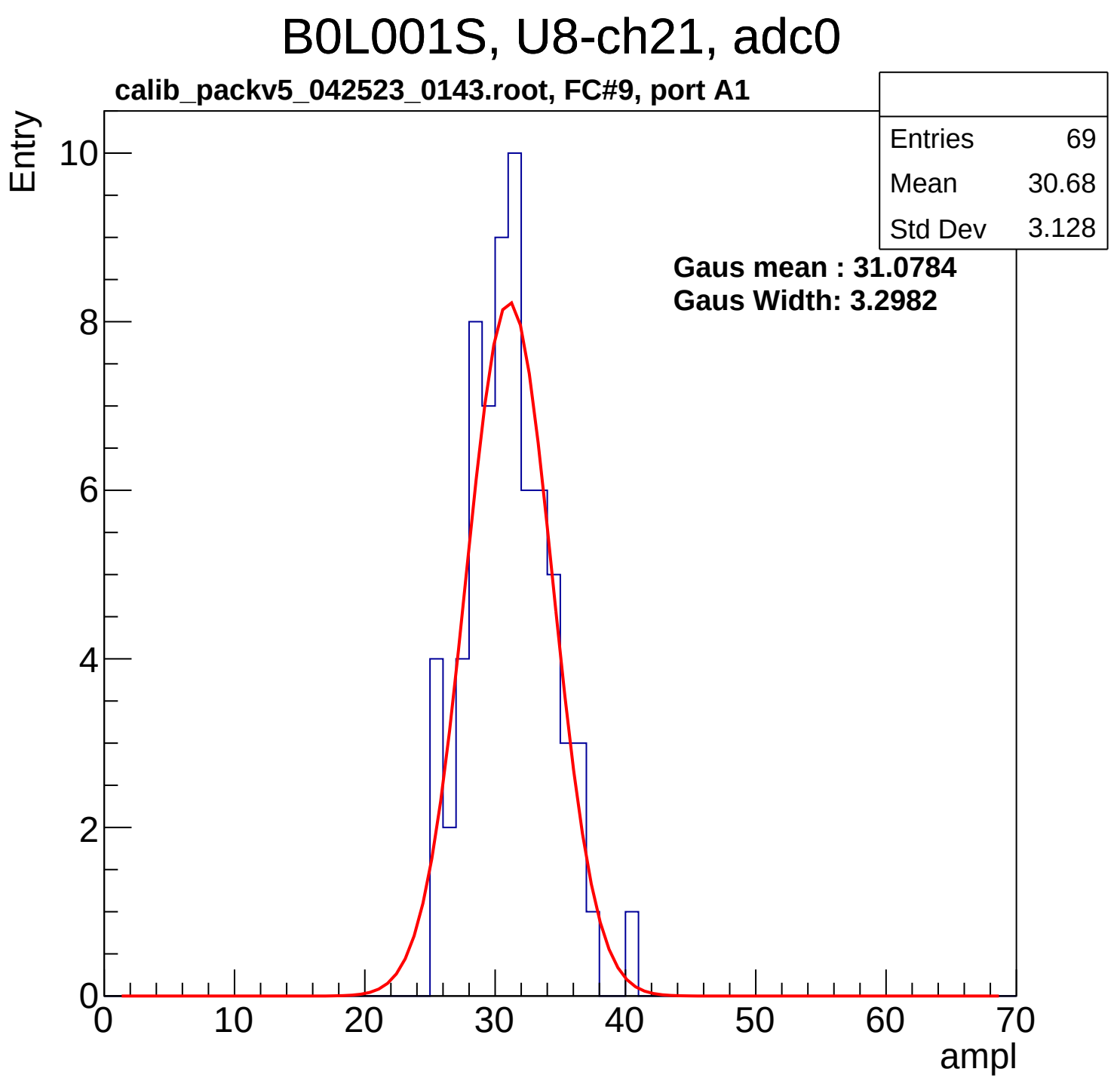
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.68
Std Dev 3.128
Gaus mean : 31.0784
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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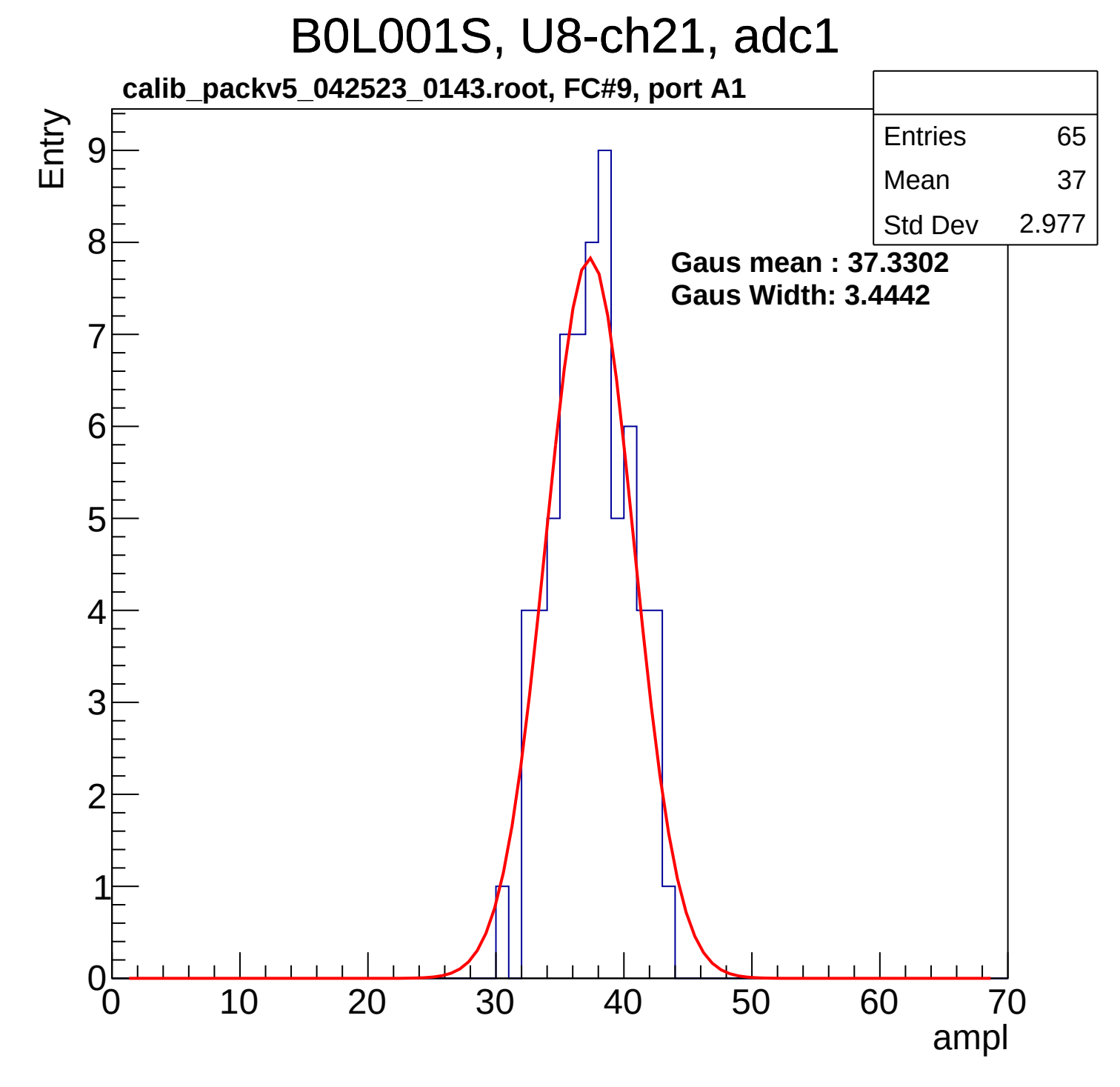
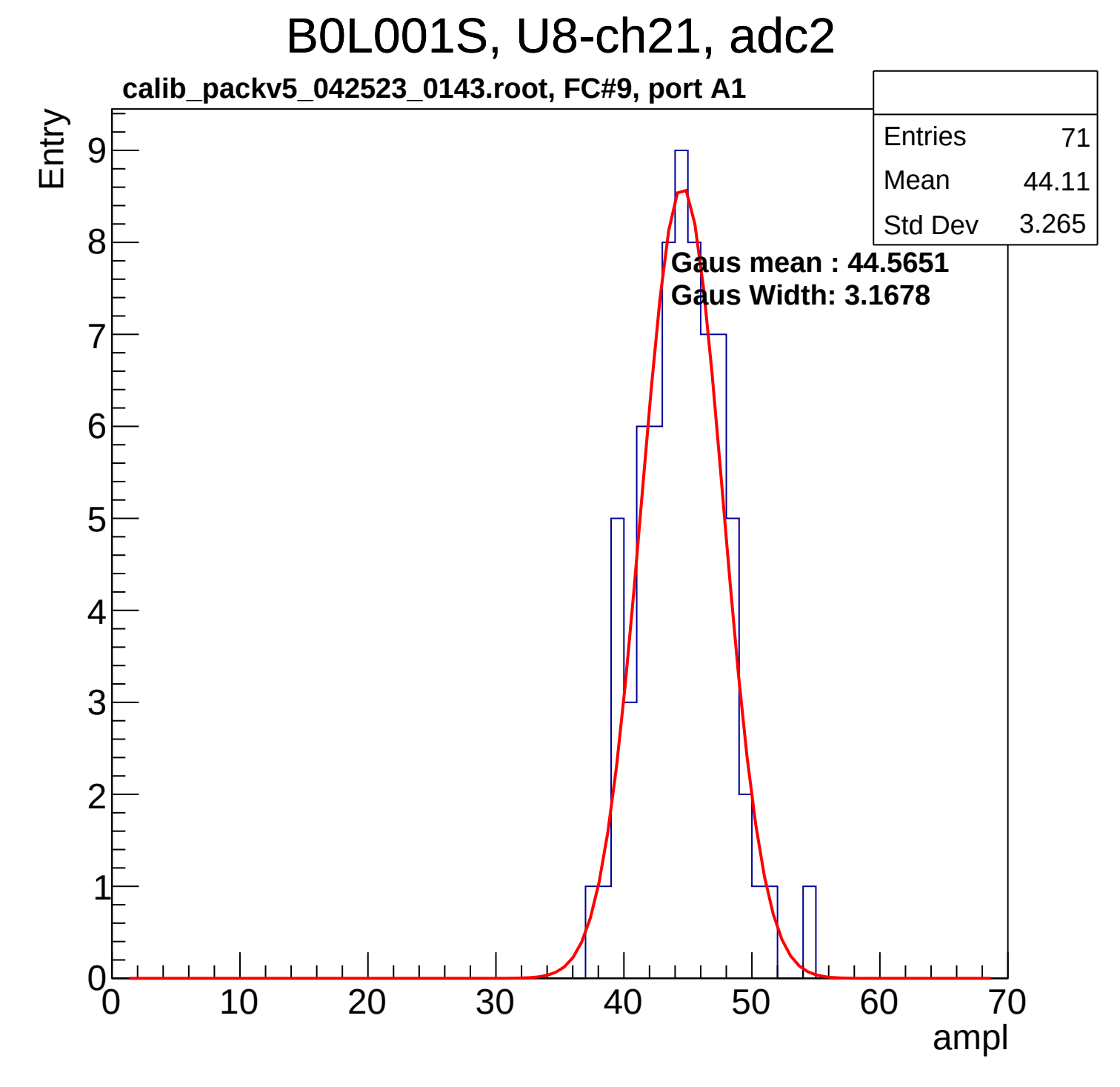

B0L001S, U8-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37
Std Dev 2.977
Gaus mean : 37.3302
Gaus Width: 3.4442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.11
Std Dev 3.265
Gaus mean : 44.5651
Gaus Width: 3.1678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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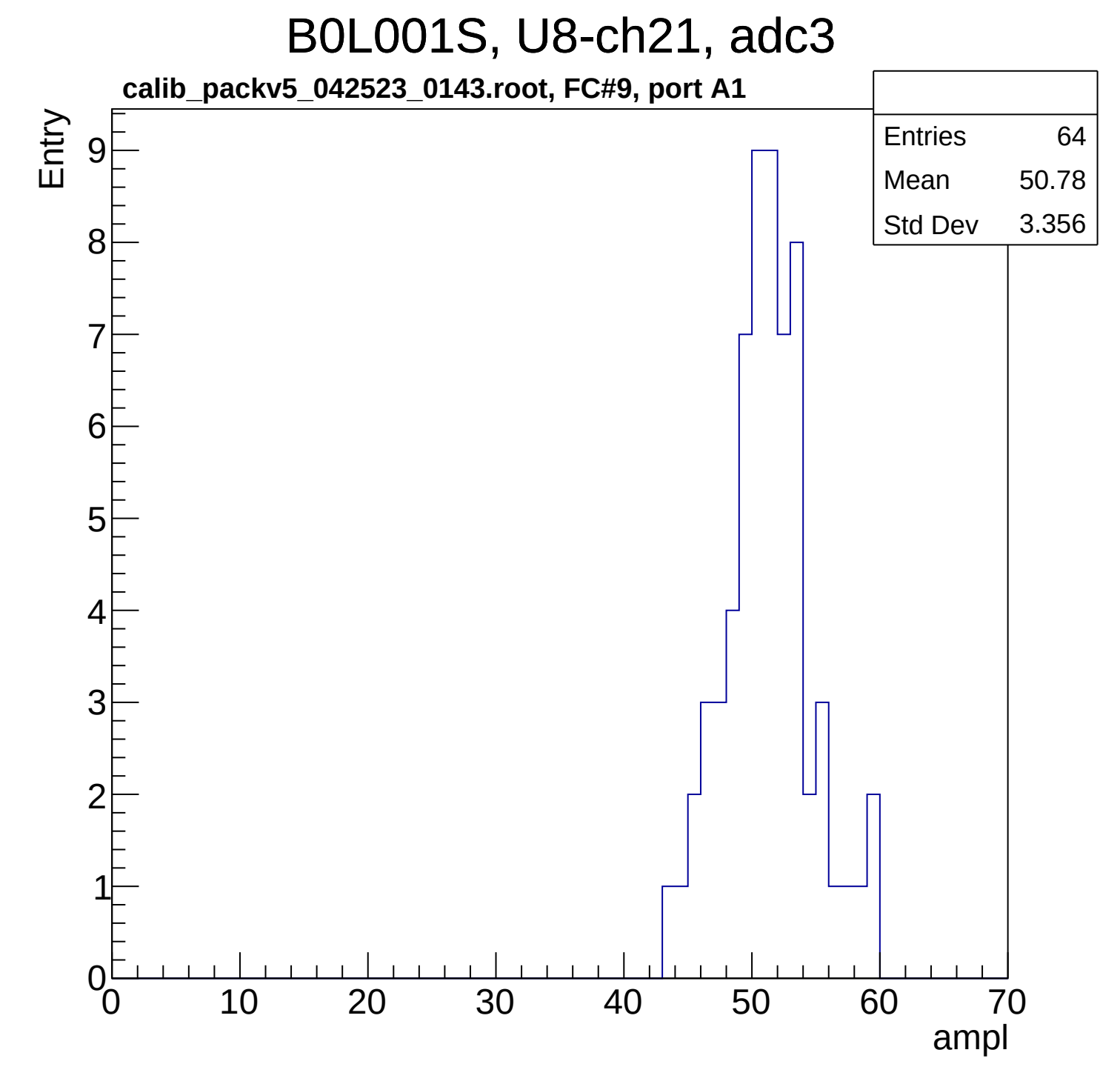
B0L001S, U8-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.78
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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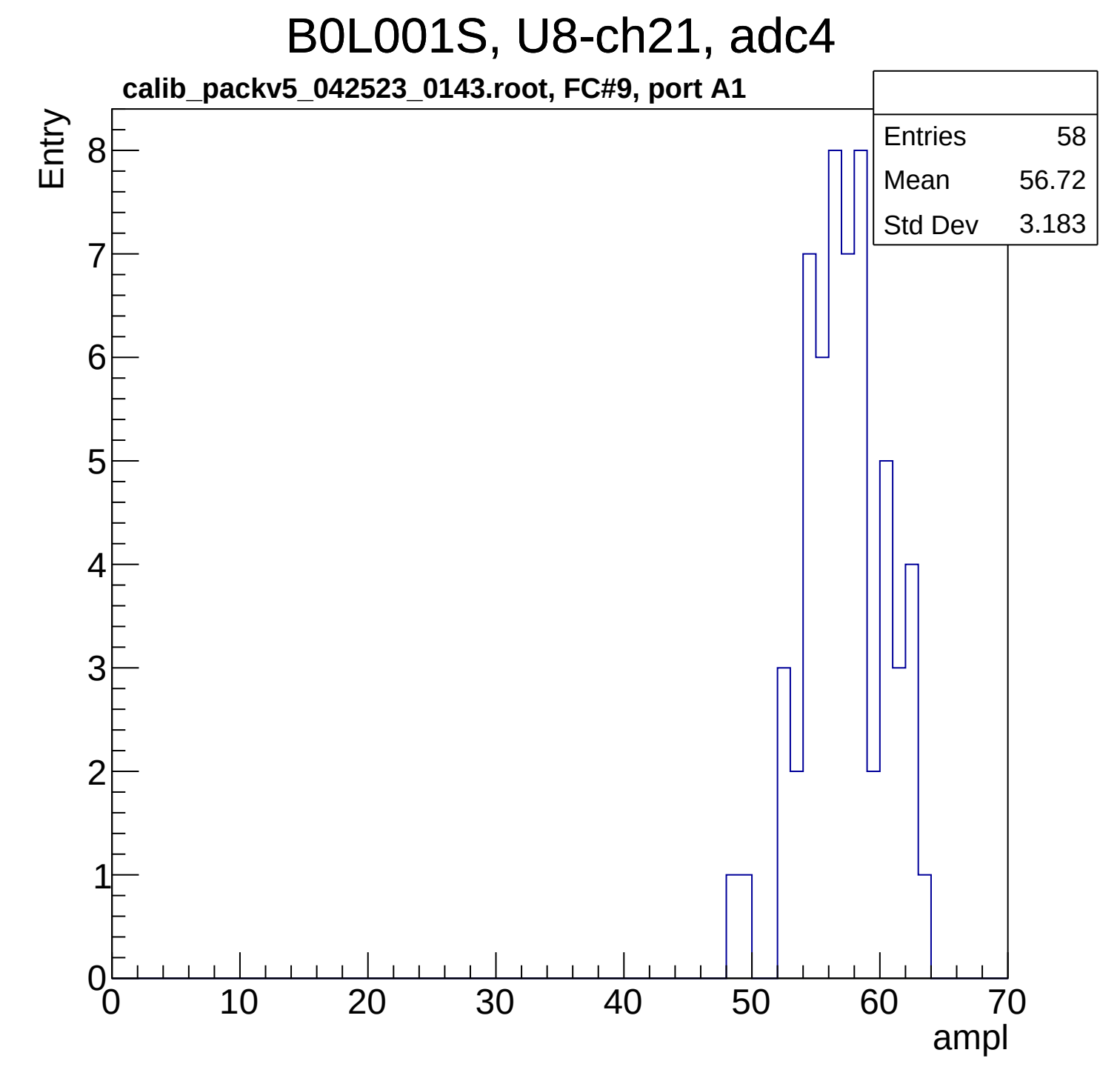
B0L001S, U8-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.72
Std Dev 3.183
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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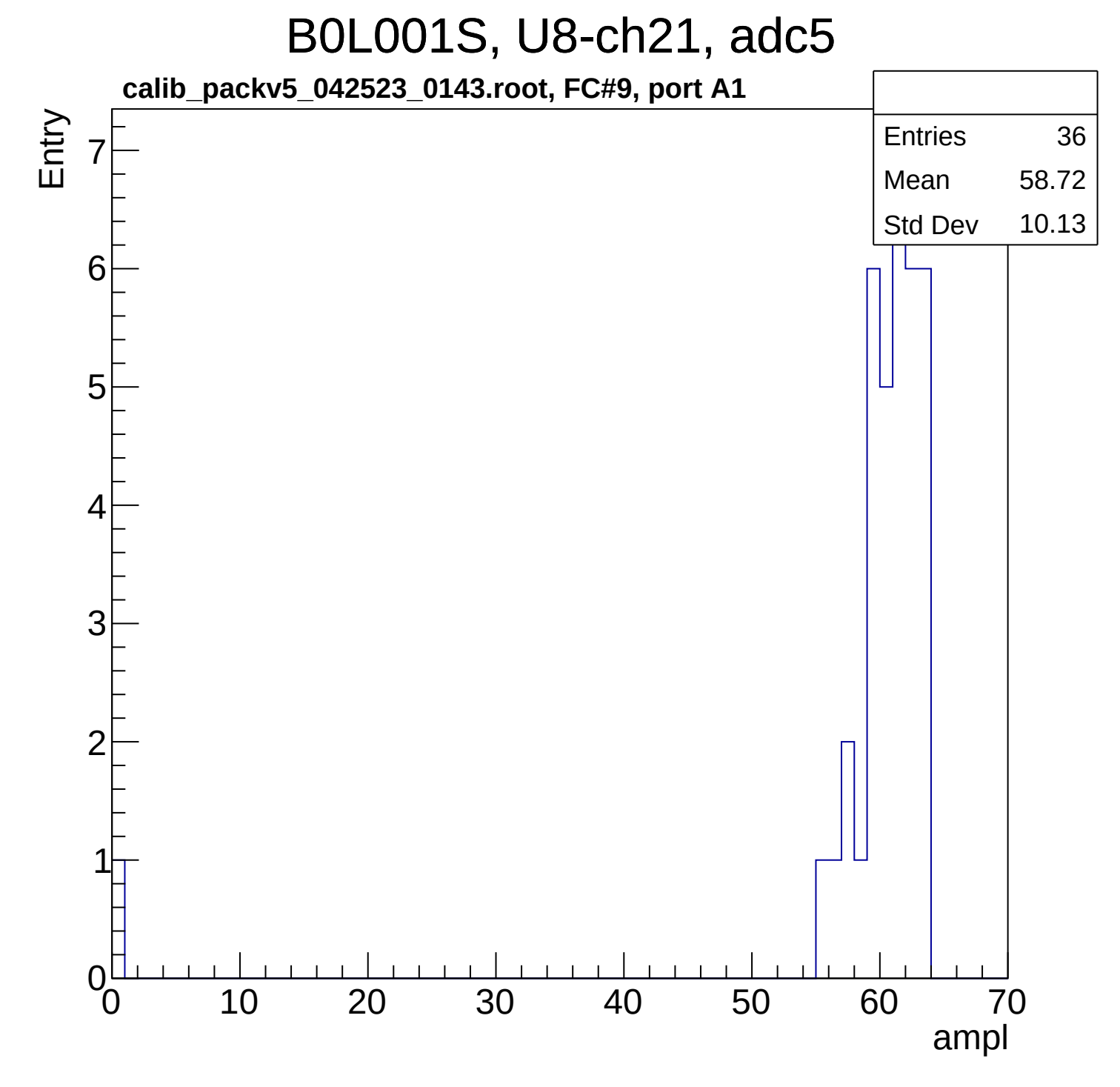
60
70


B0L001S, U8-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.72
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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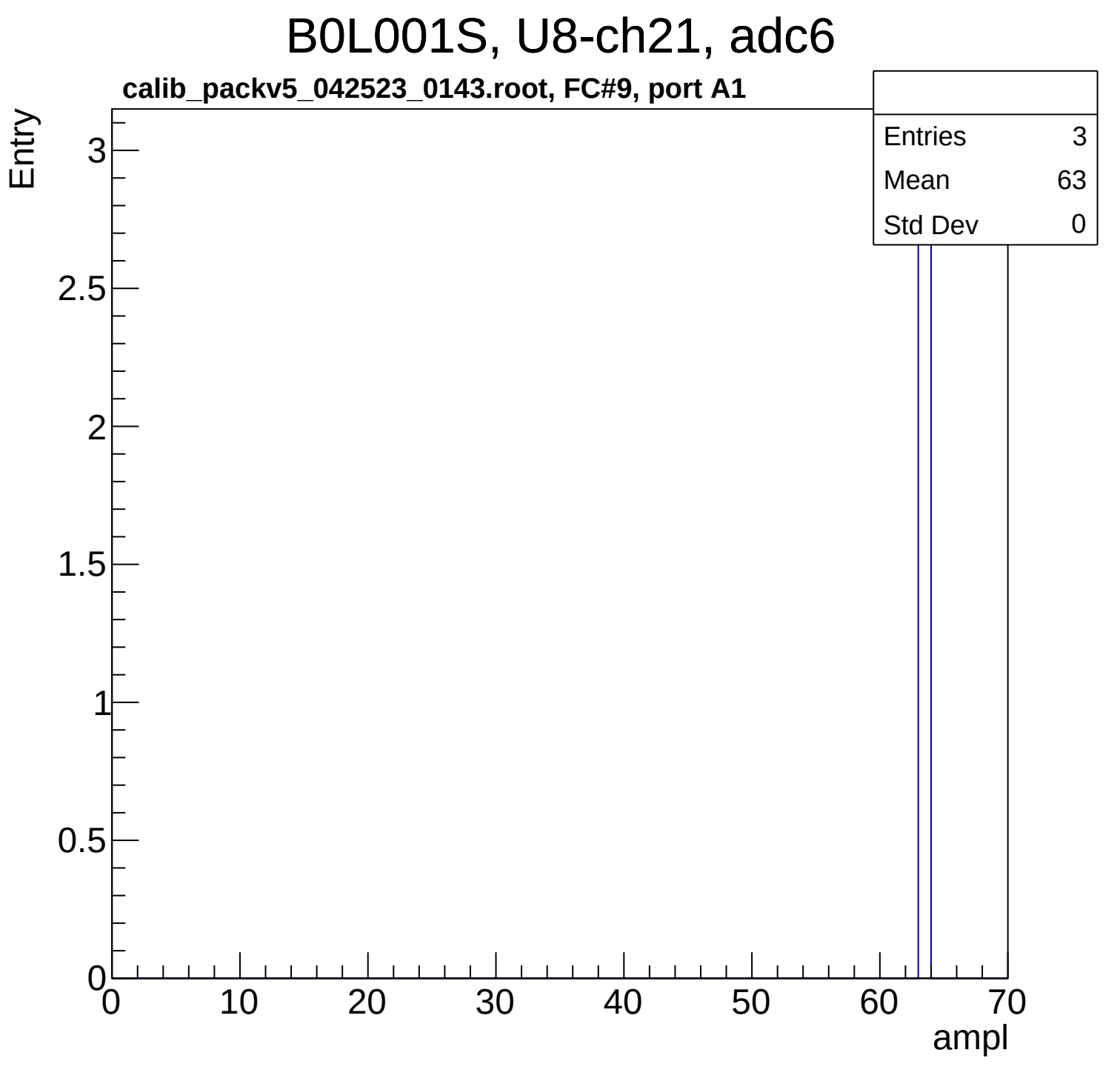
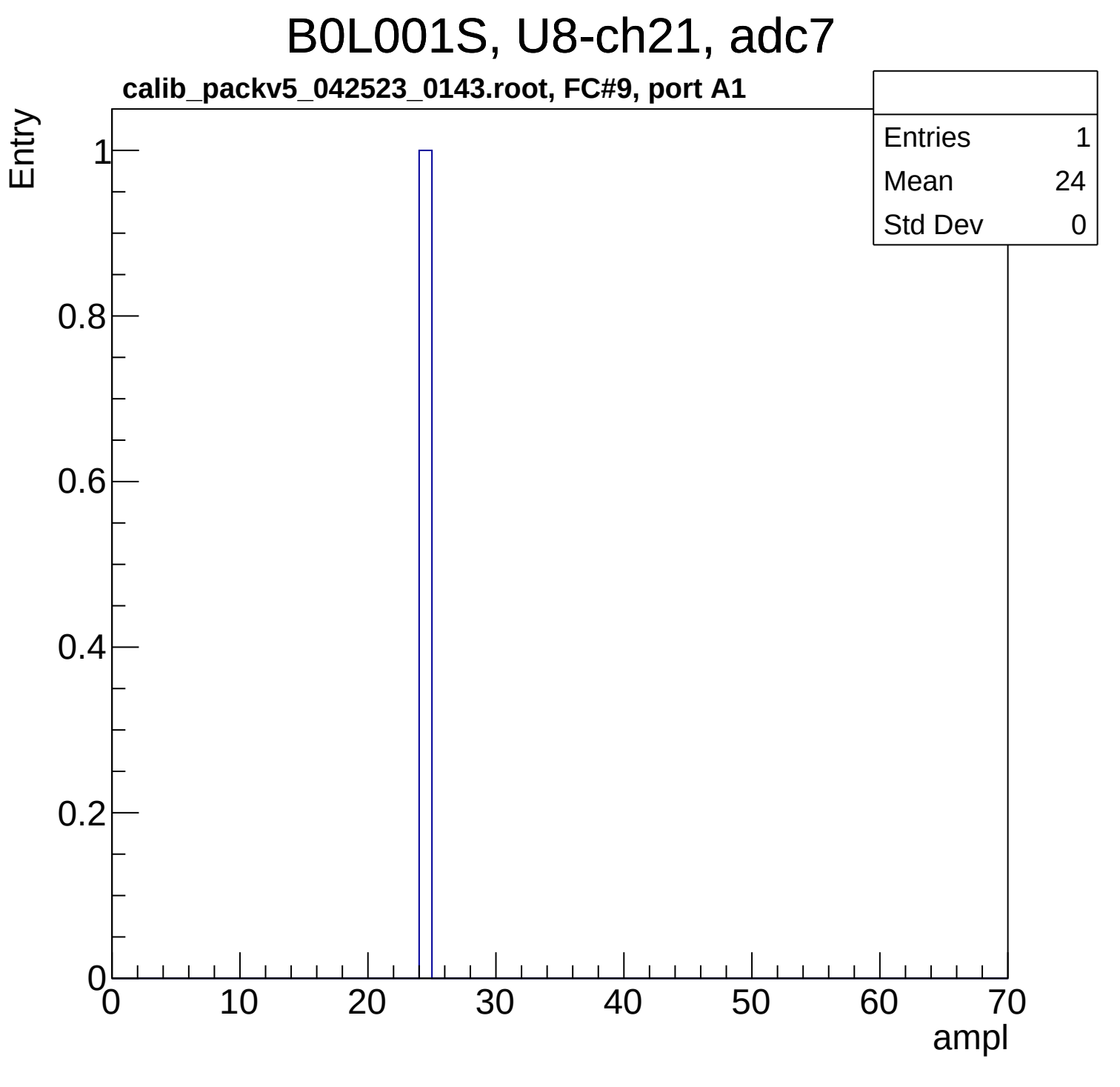
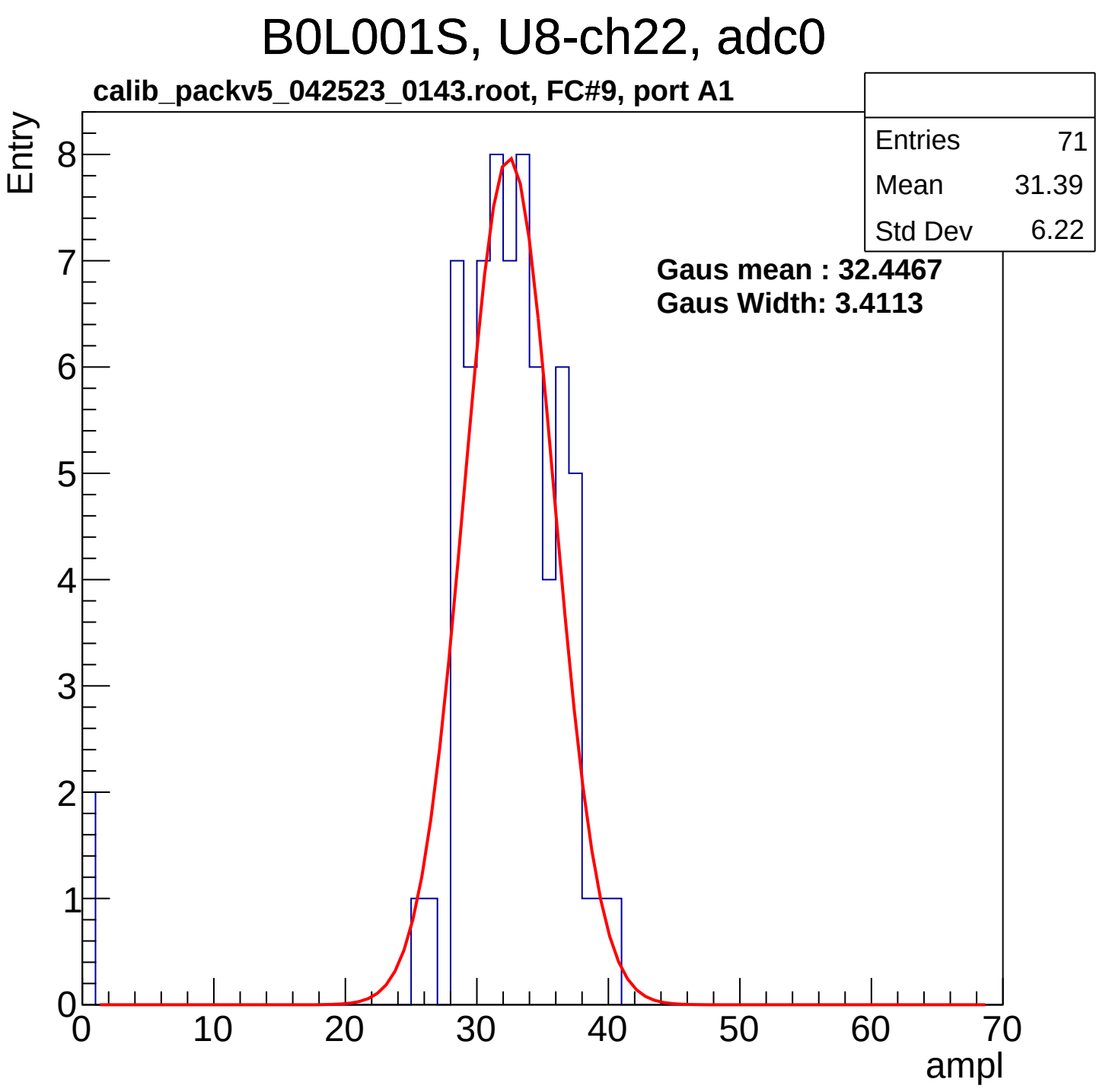

B0L001S, U8-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.39
Std Dev 6.22
Gaus mean : 32.4467
Gaus Width: 3.4113
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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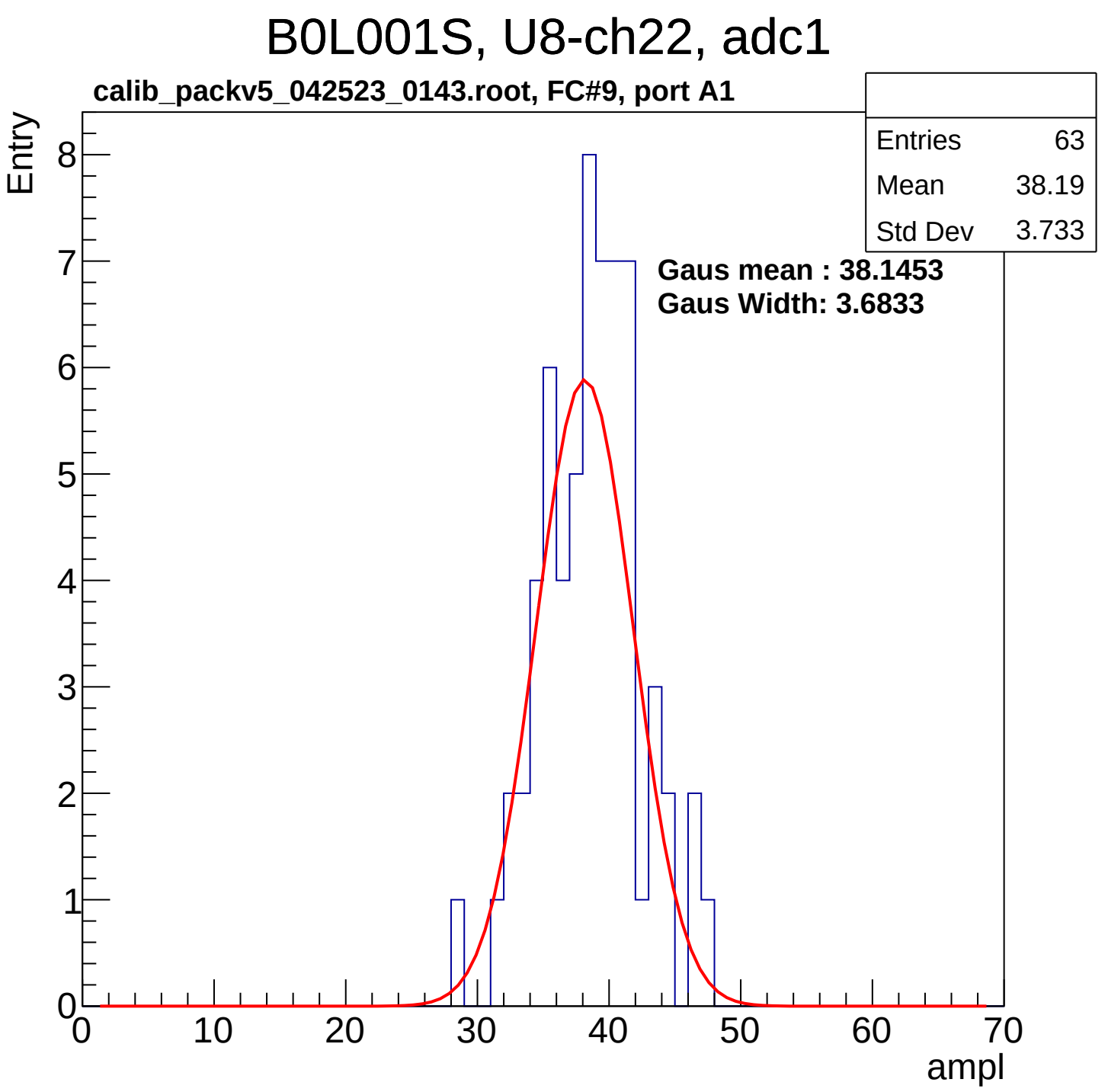
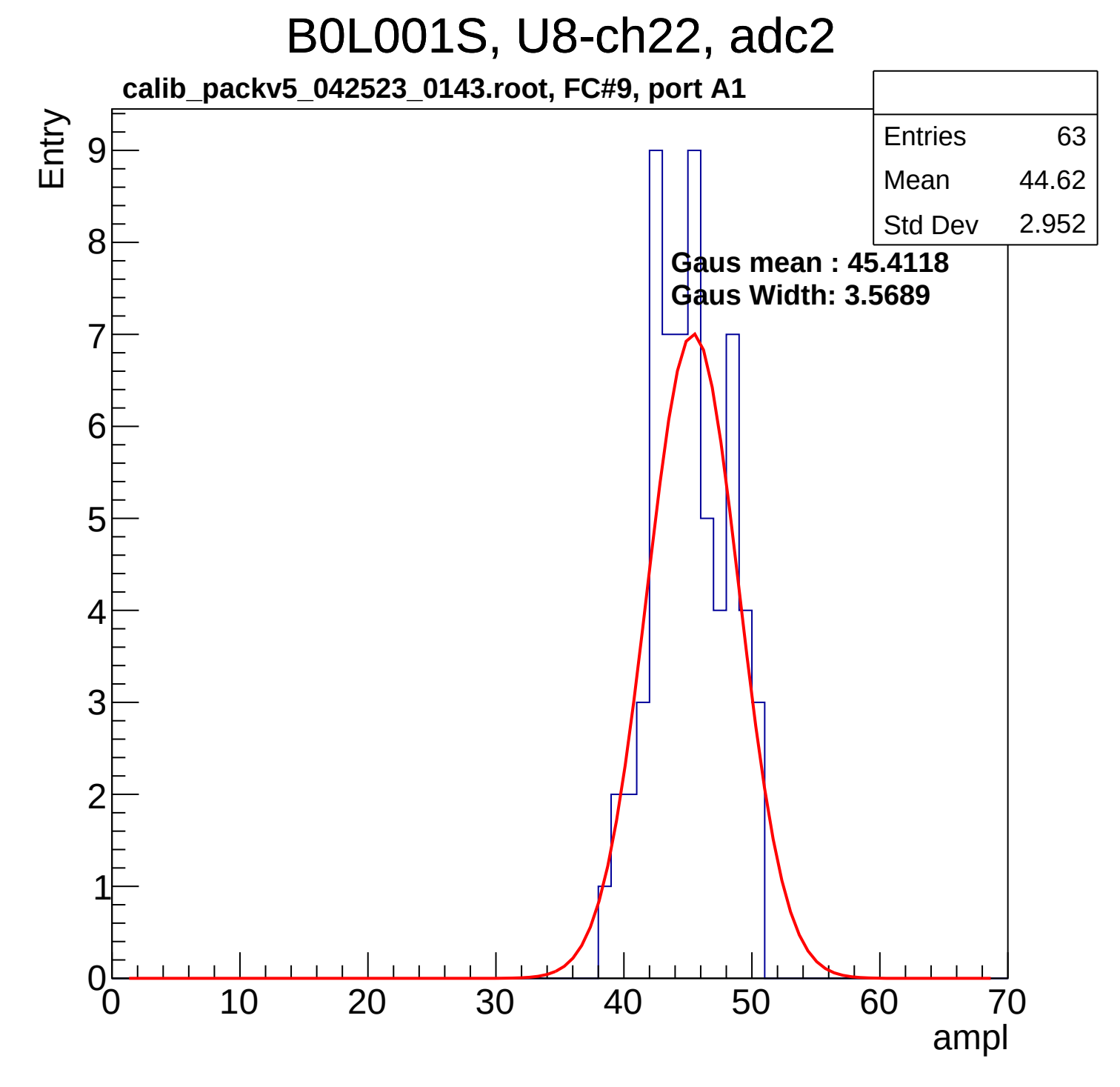
60
70


B0L001S, U8-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.19
Std Dev 3.733
Gaus mean : 38.1453
Gaus Width: 3.6833
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.62
Std Dev 2.952
Gaus mean : 45.4118
Gaus Width: 3.5689
Entry
ampl
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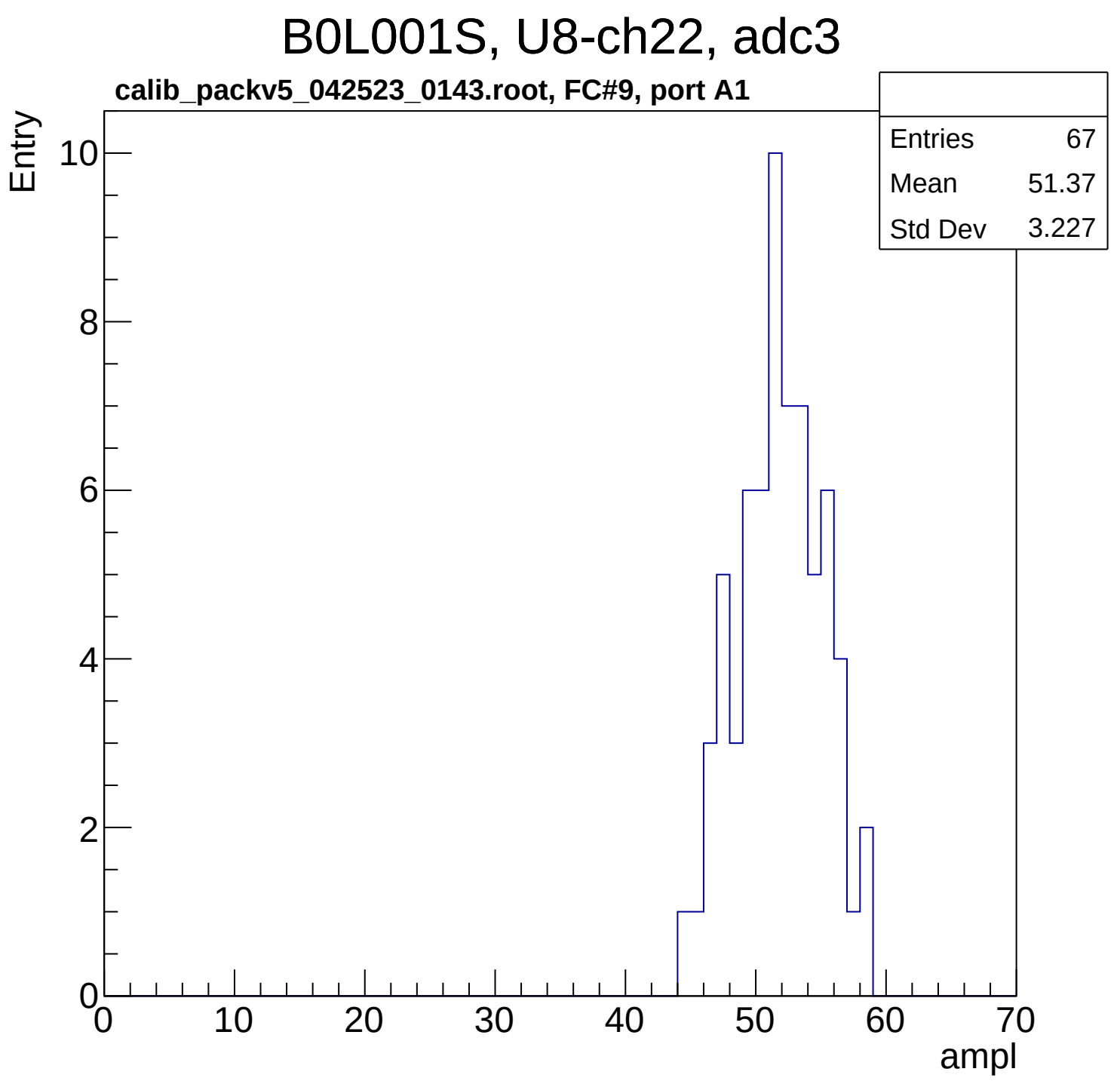
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.37
Std Dev 3.227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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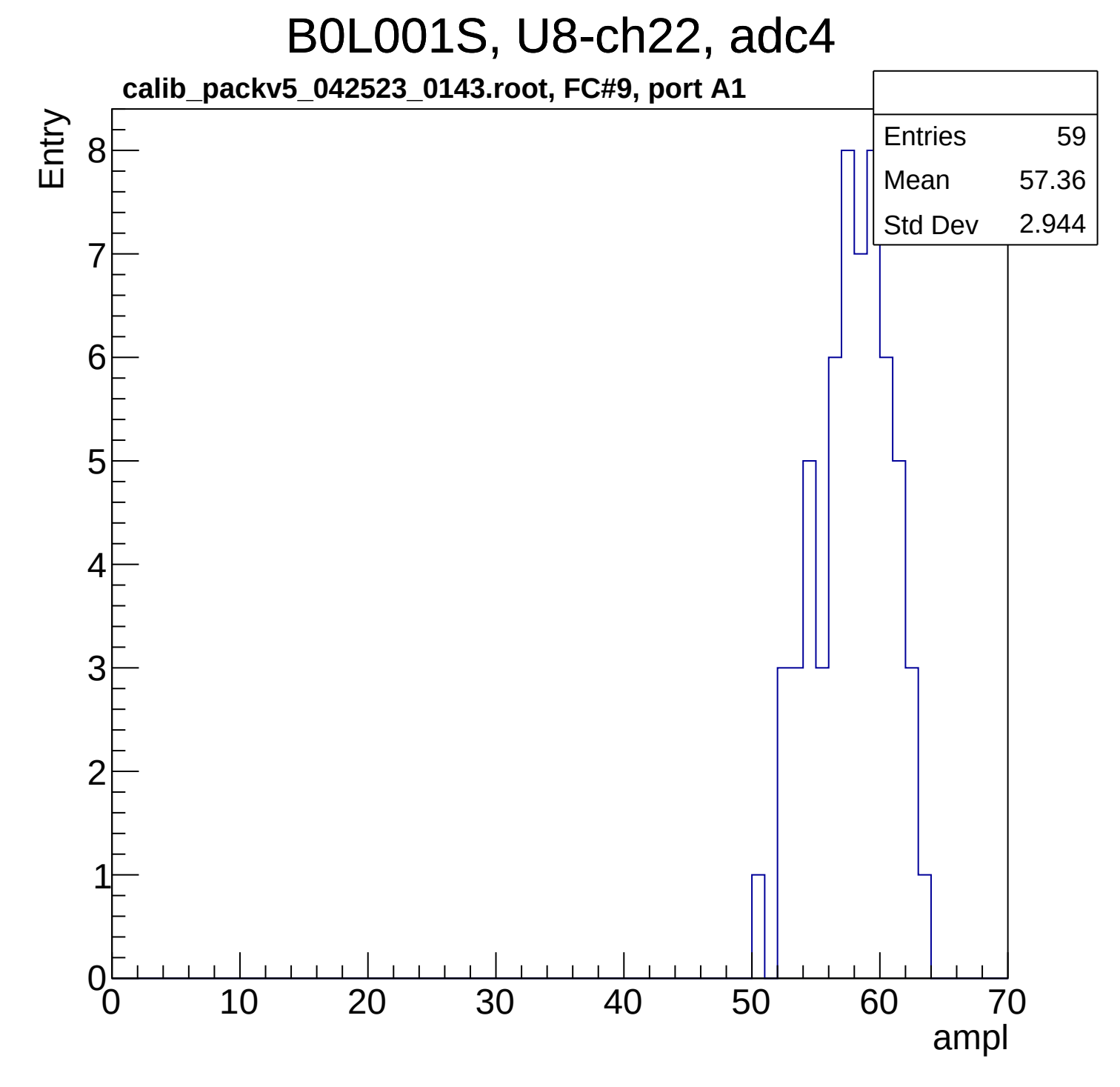
B0L001S, U8-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.36
Std Dev 2.944
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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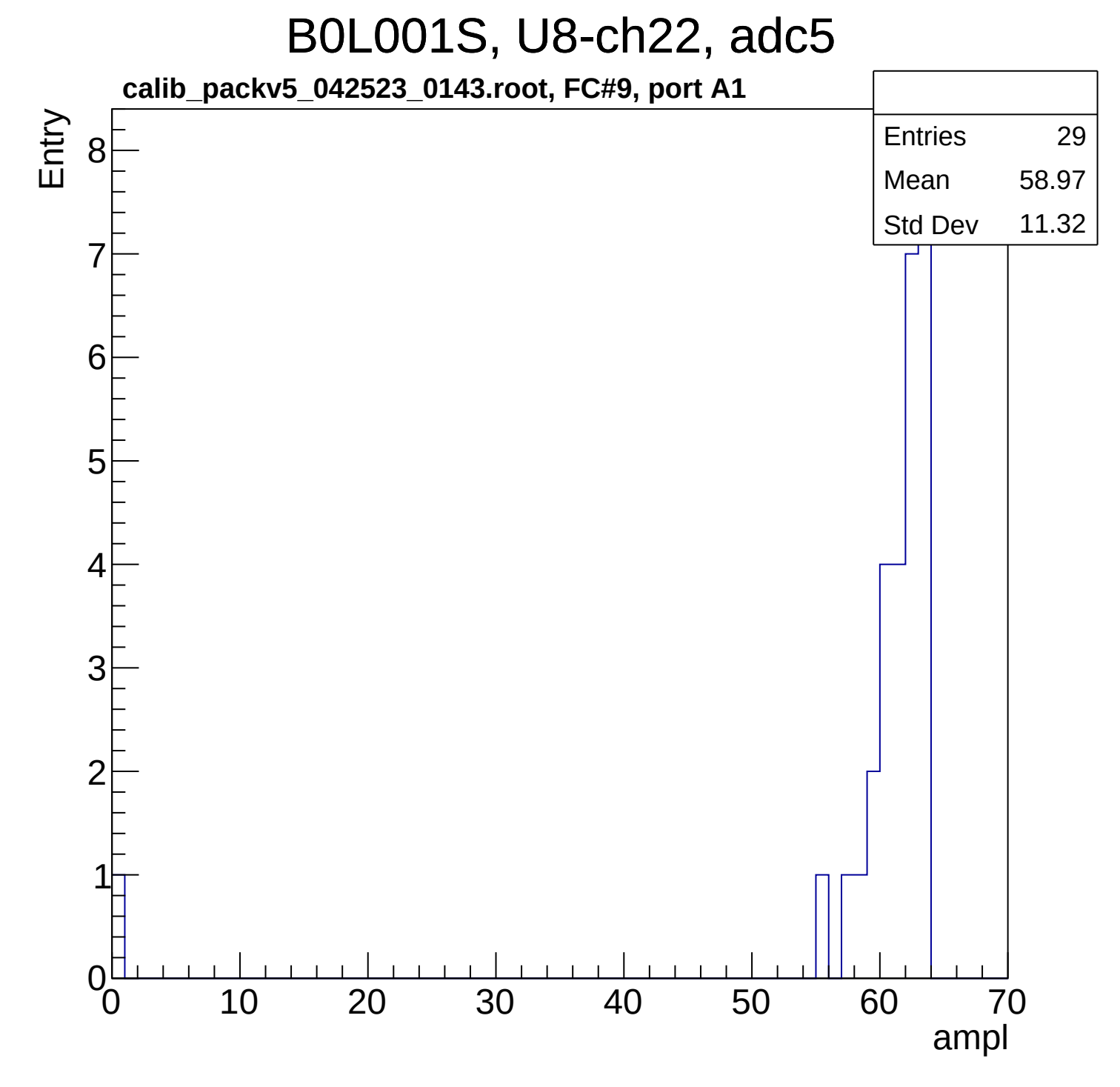
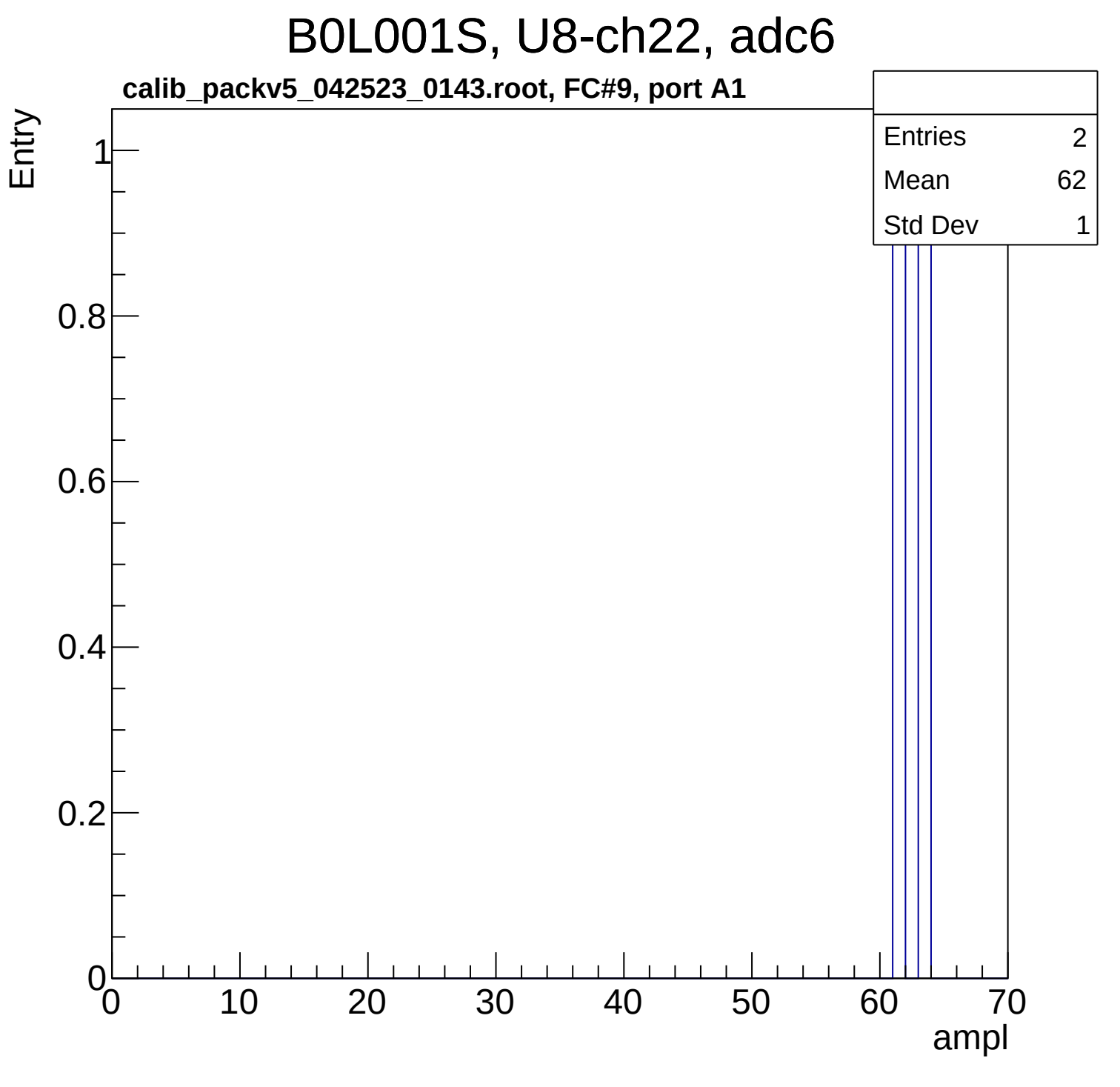
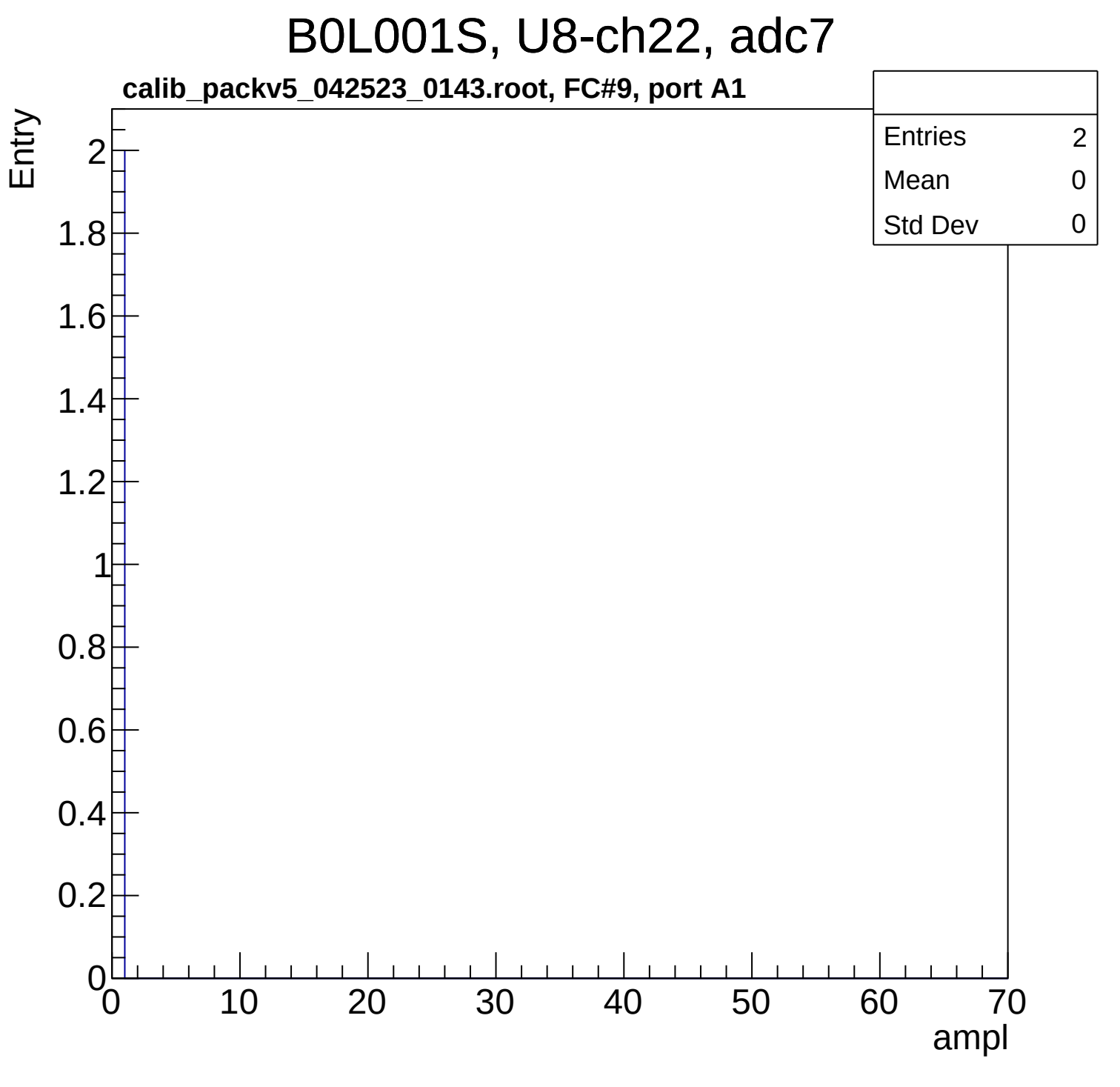

B0L001S, U8-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.97
Std Dev 11.32
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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0
10
20
30
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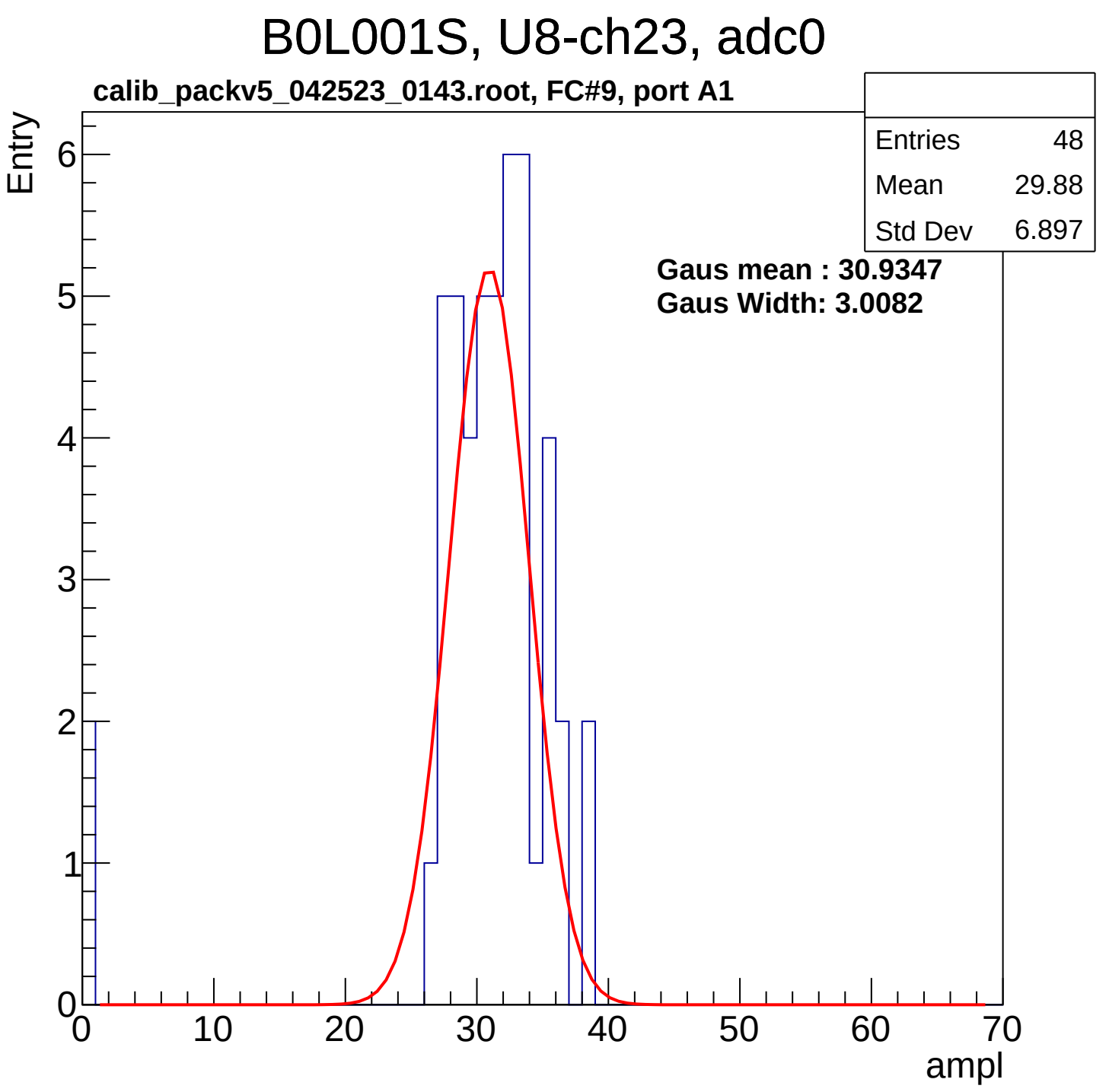
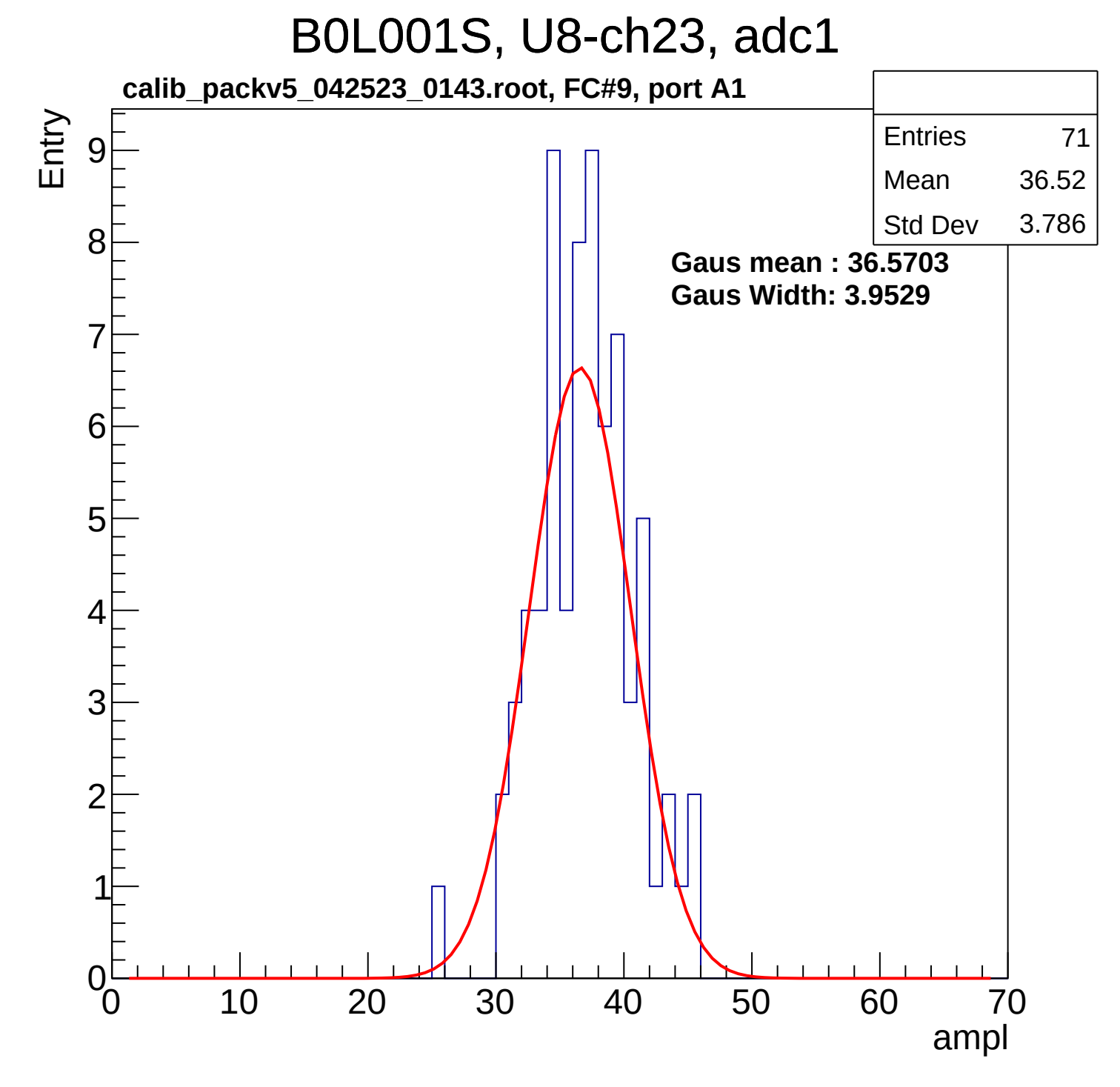
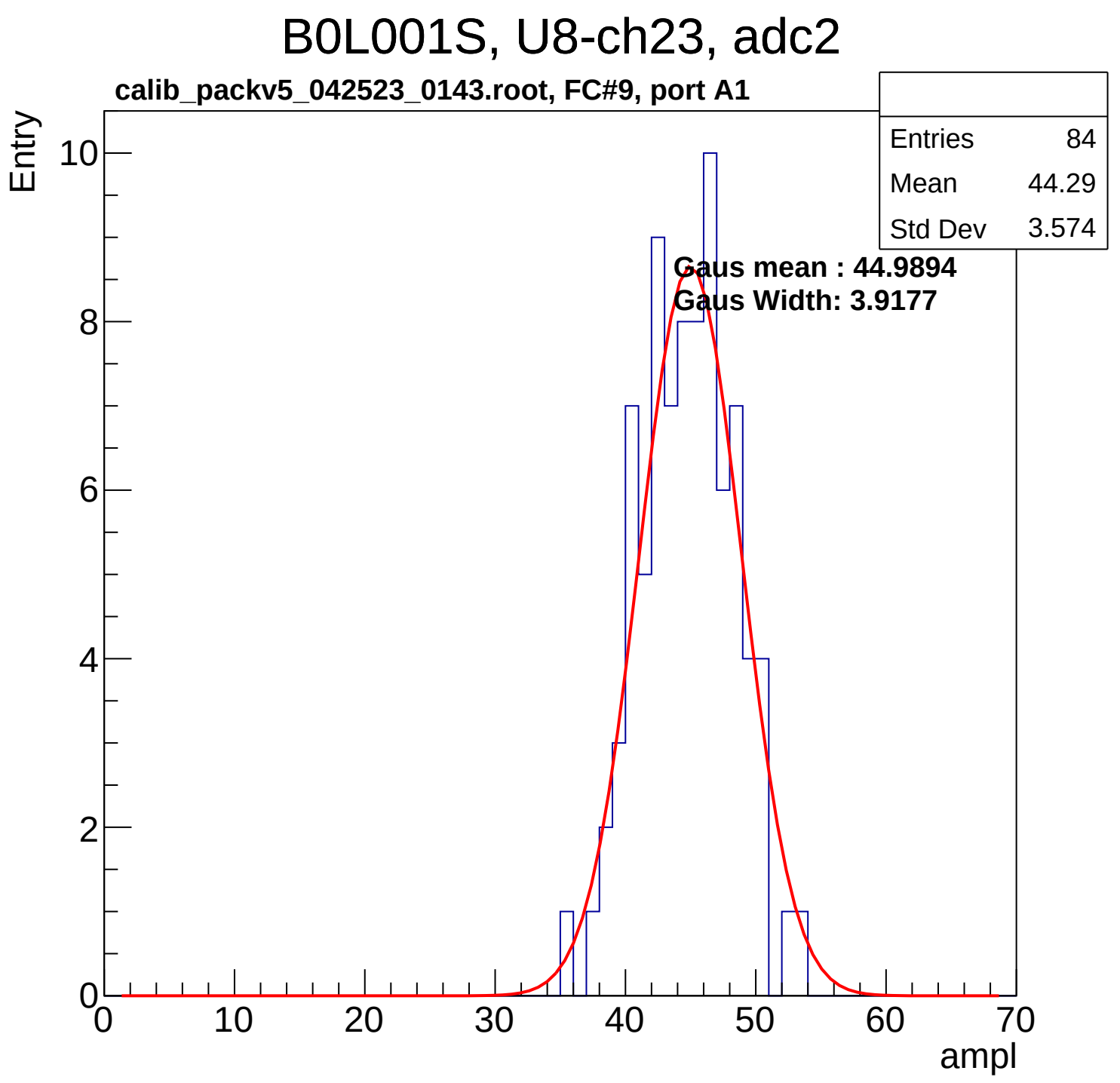
50
60
70
ampl


B0L001S, U8-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 29.88
Std Dev 6.897
Gaus mean : 30.9347
Gaus Width: 3.0082
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U8-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.52
Std Dev 3.786
Gaus mean : 36.5703
Gaus Width: 3.9529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 44.29
Std Dev 3.574
Gaus mean : 44.9894
Gaus Width: 3.9177
Entry
ampl
0
2
4
6
8
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0
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40
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60
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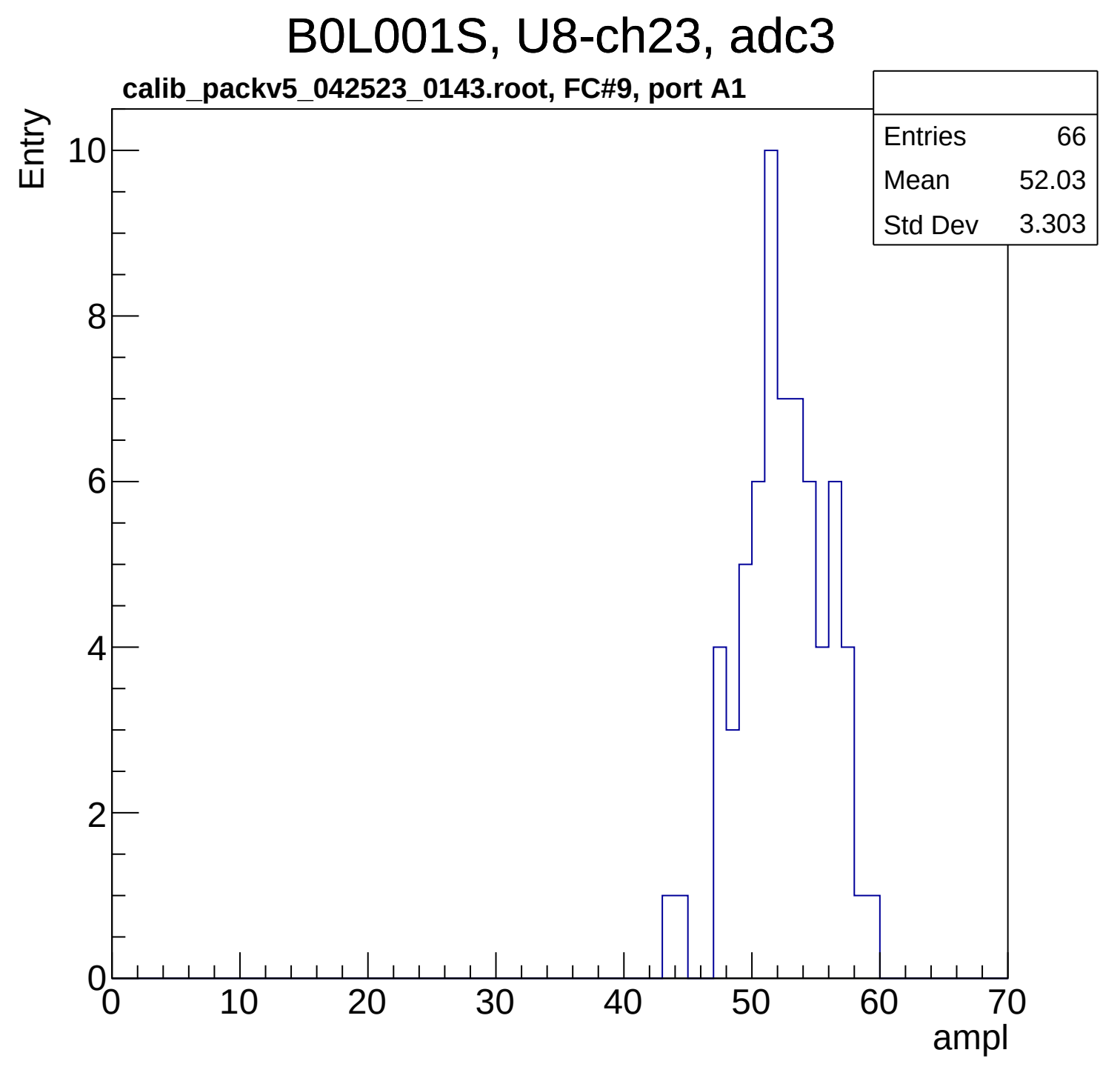
B0L001S, U8-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.03
Std Dev 3.303
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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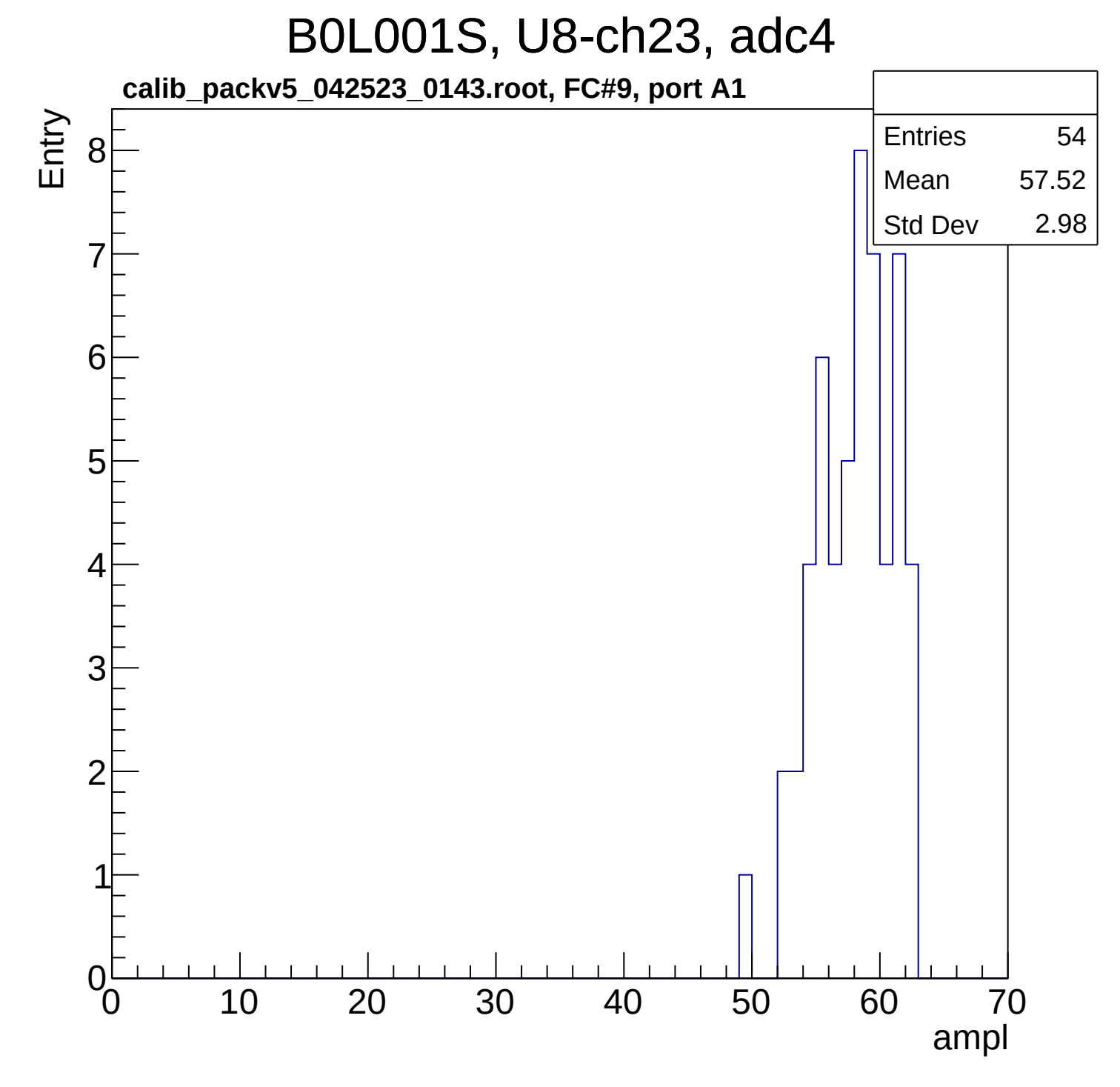
B0L001S, U8-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.52
Std Dev 2.98
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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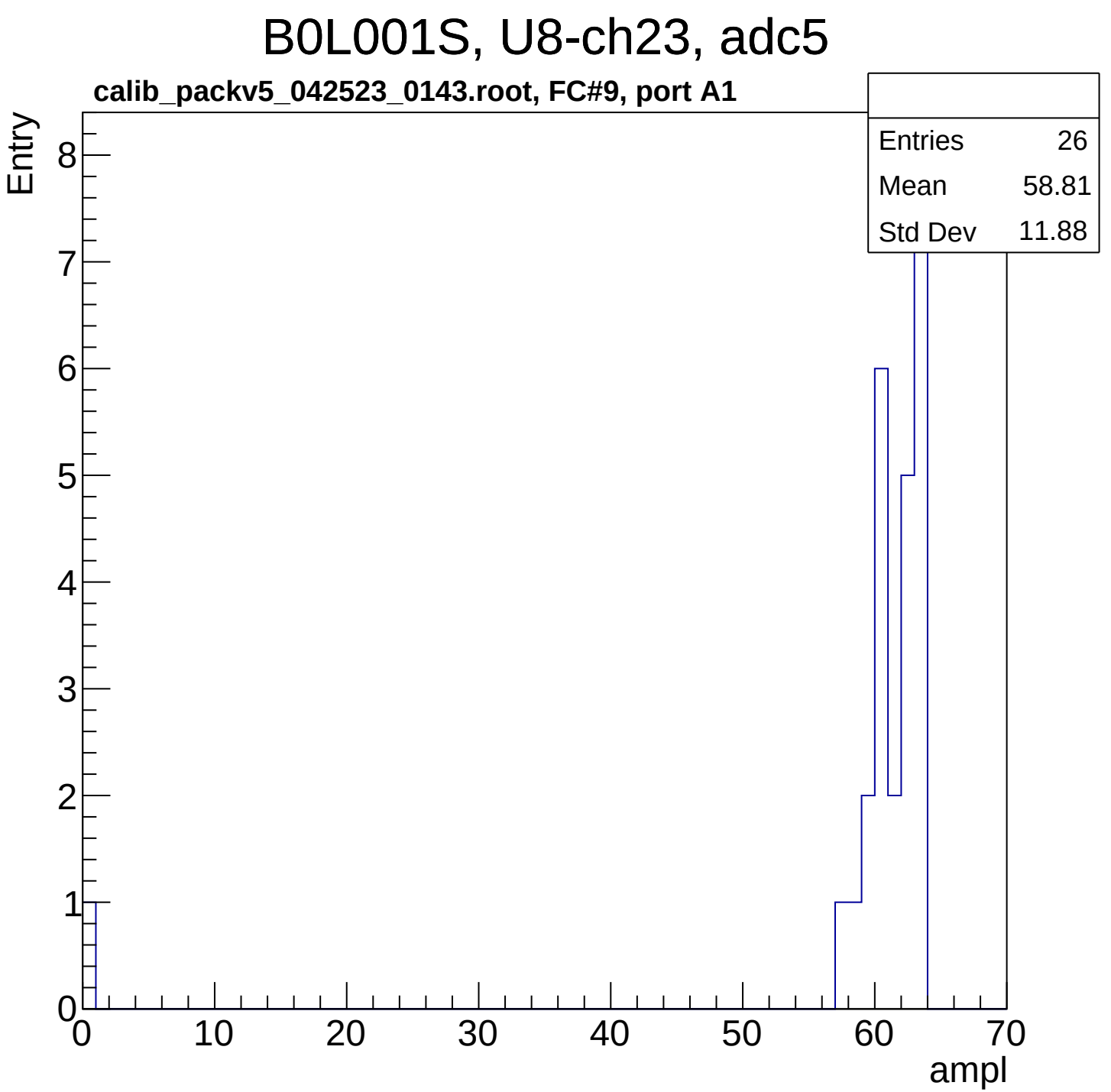
B0L001S, U8-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 58.81
Std Dev 11.88
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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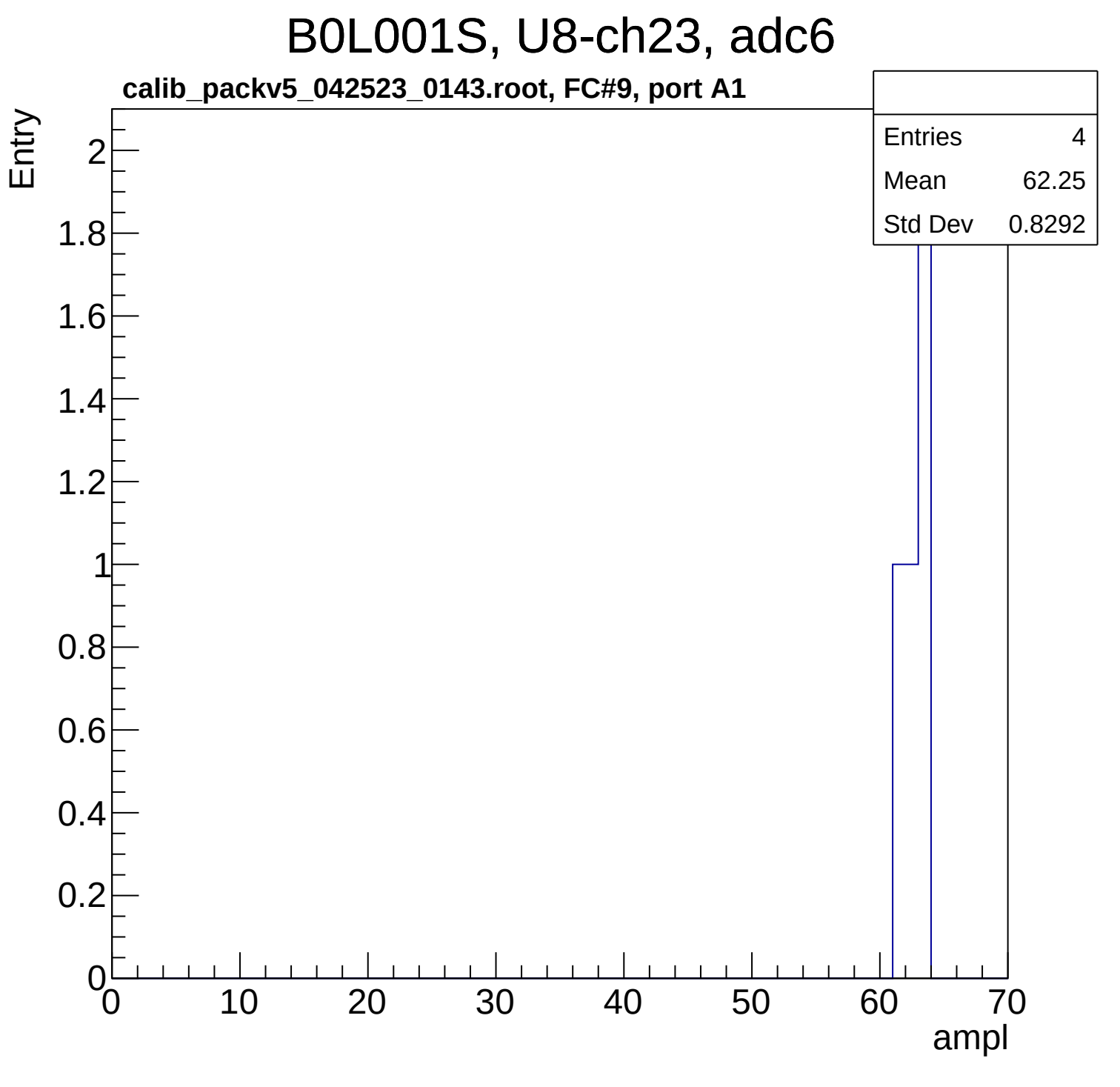
B0L001S, U8-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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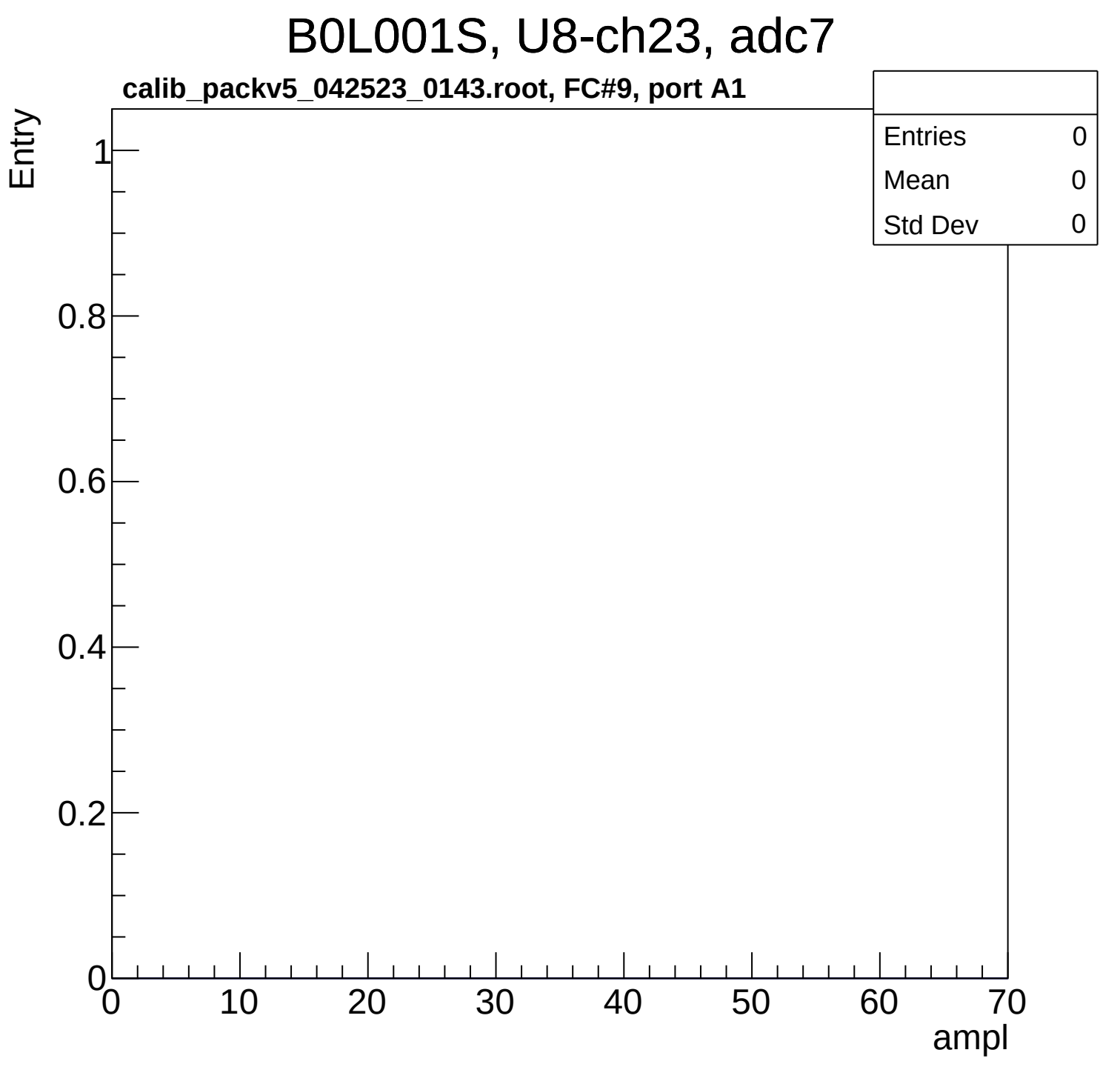
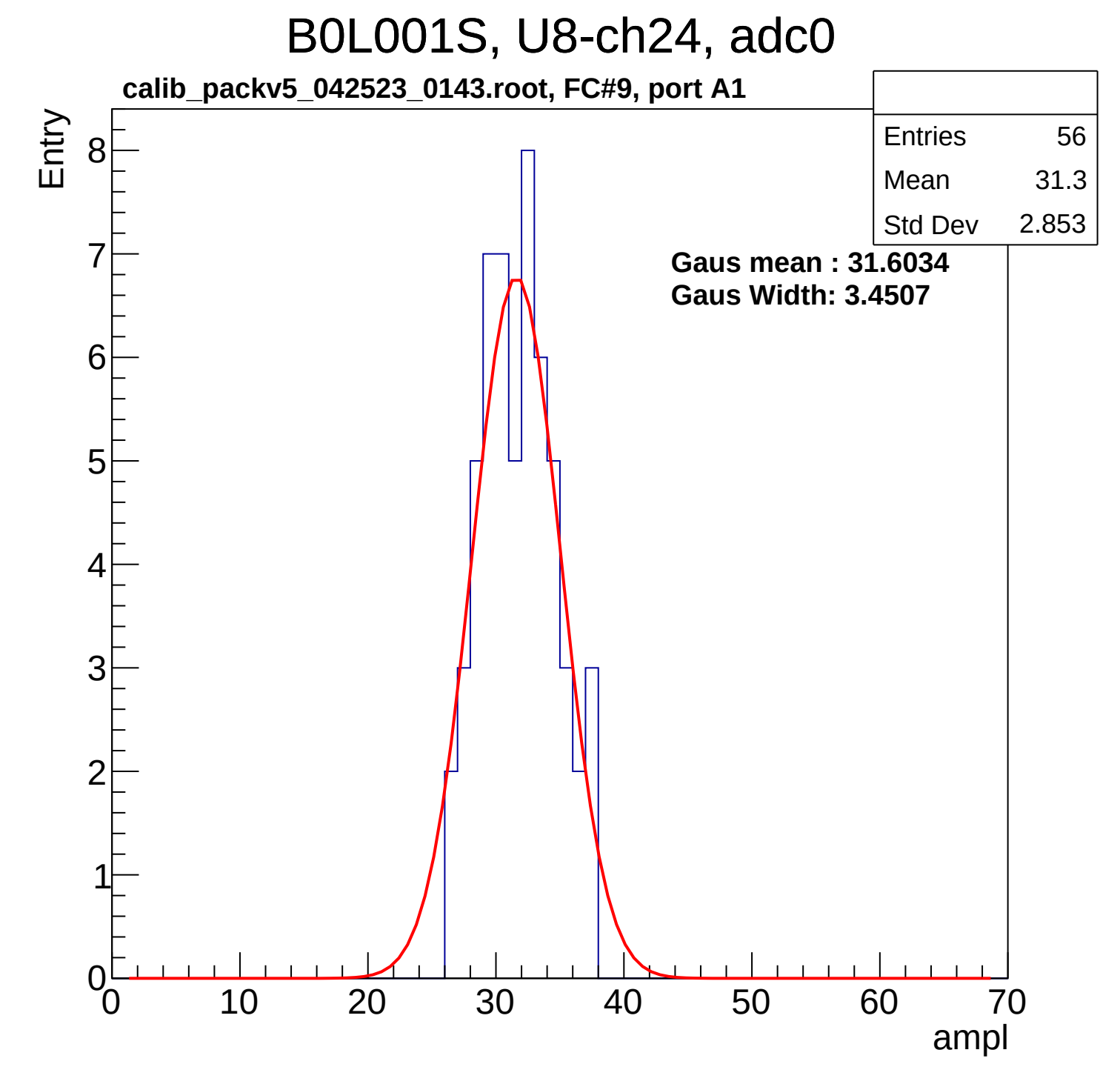

B0L001S, U8-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 31.3
Std Dev 2.853
Gaus mean : 31.6034
Gaus Width: 3.4507
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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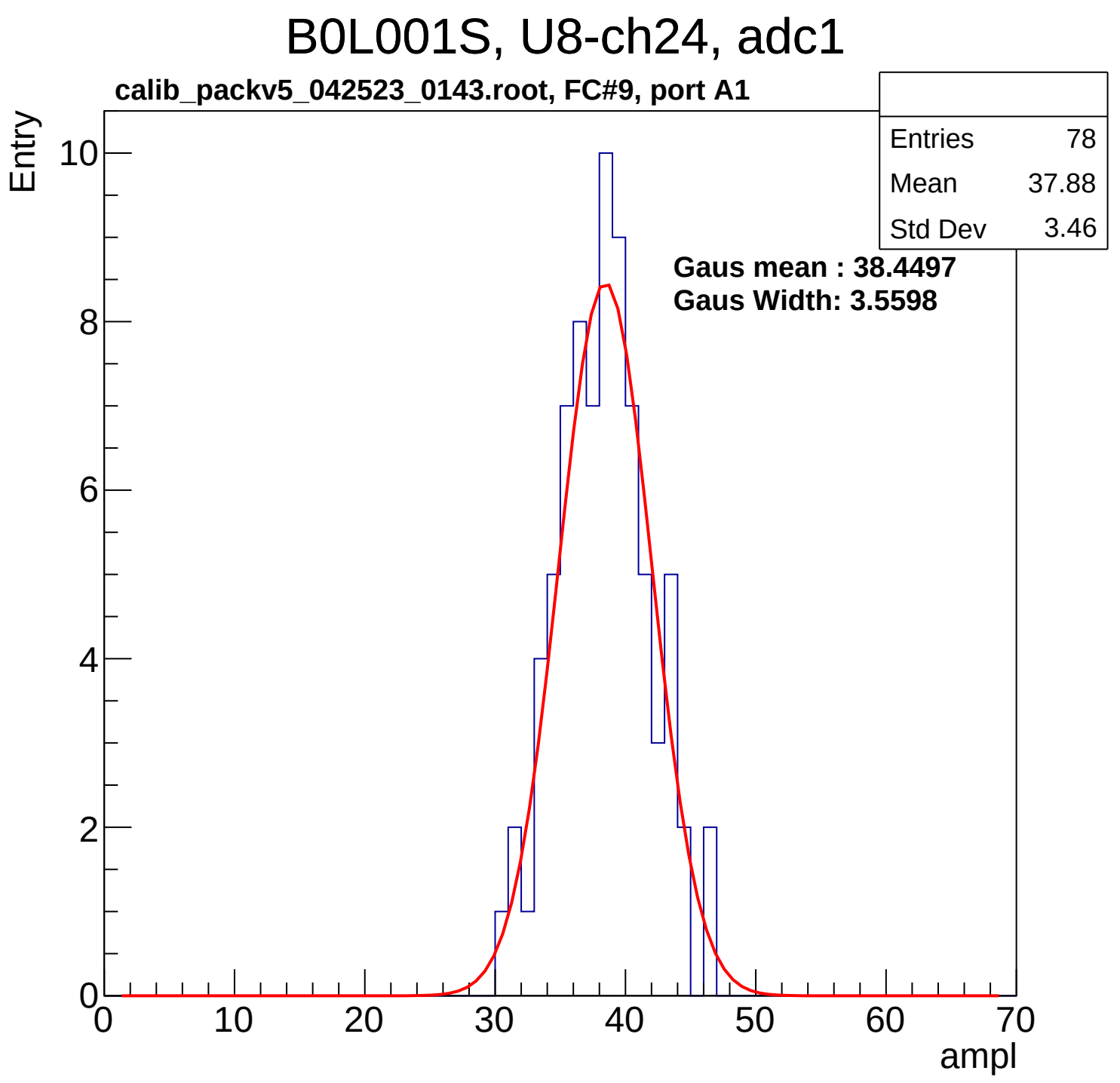
B0L001S, U8-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.88
Std Dev 3.46
Gaus mean : 38.4497
Gaus Width: 3.5598
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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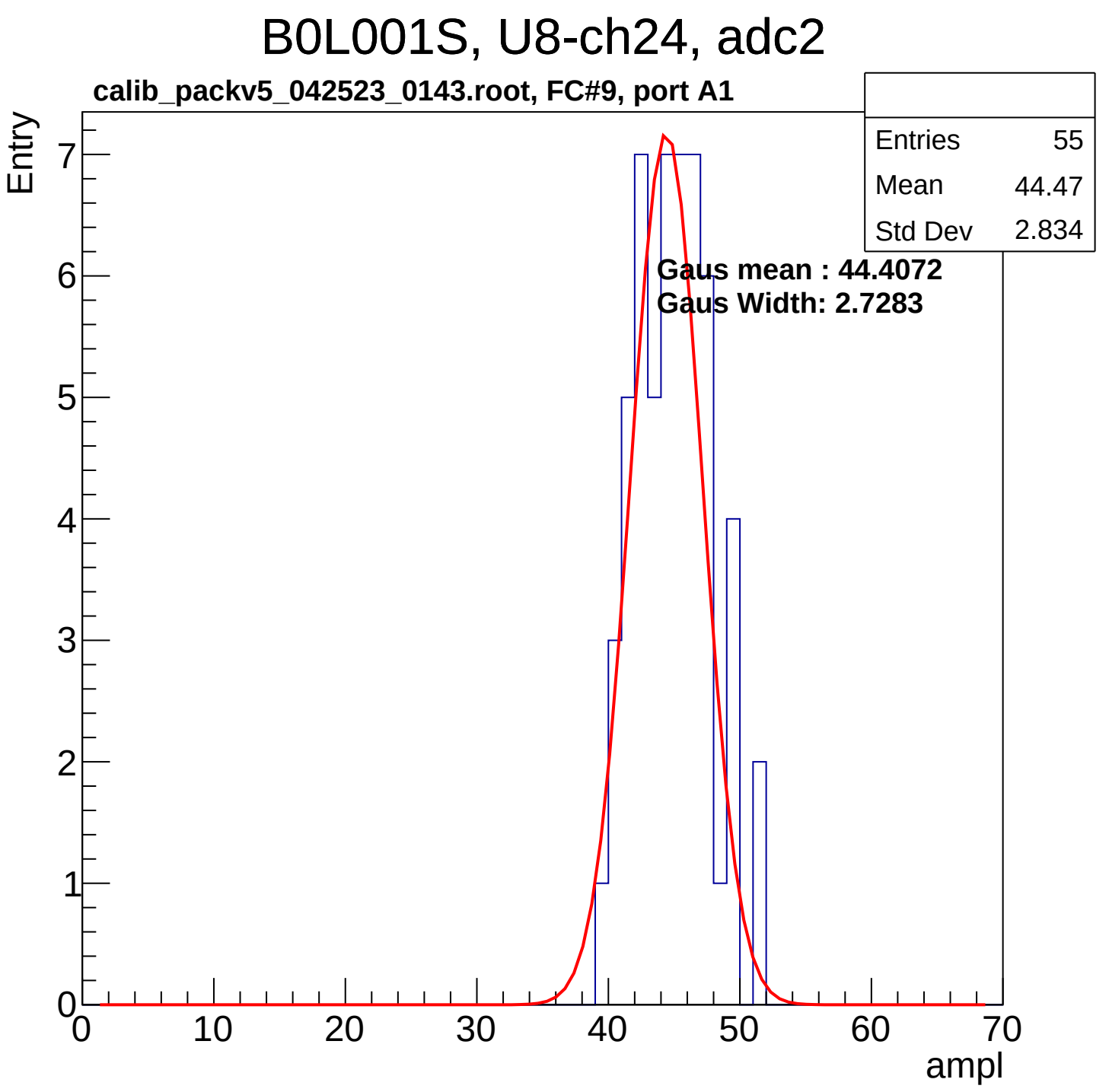
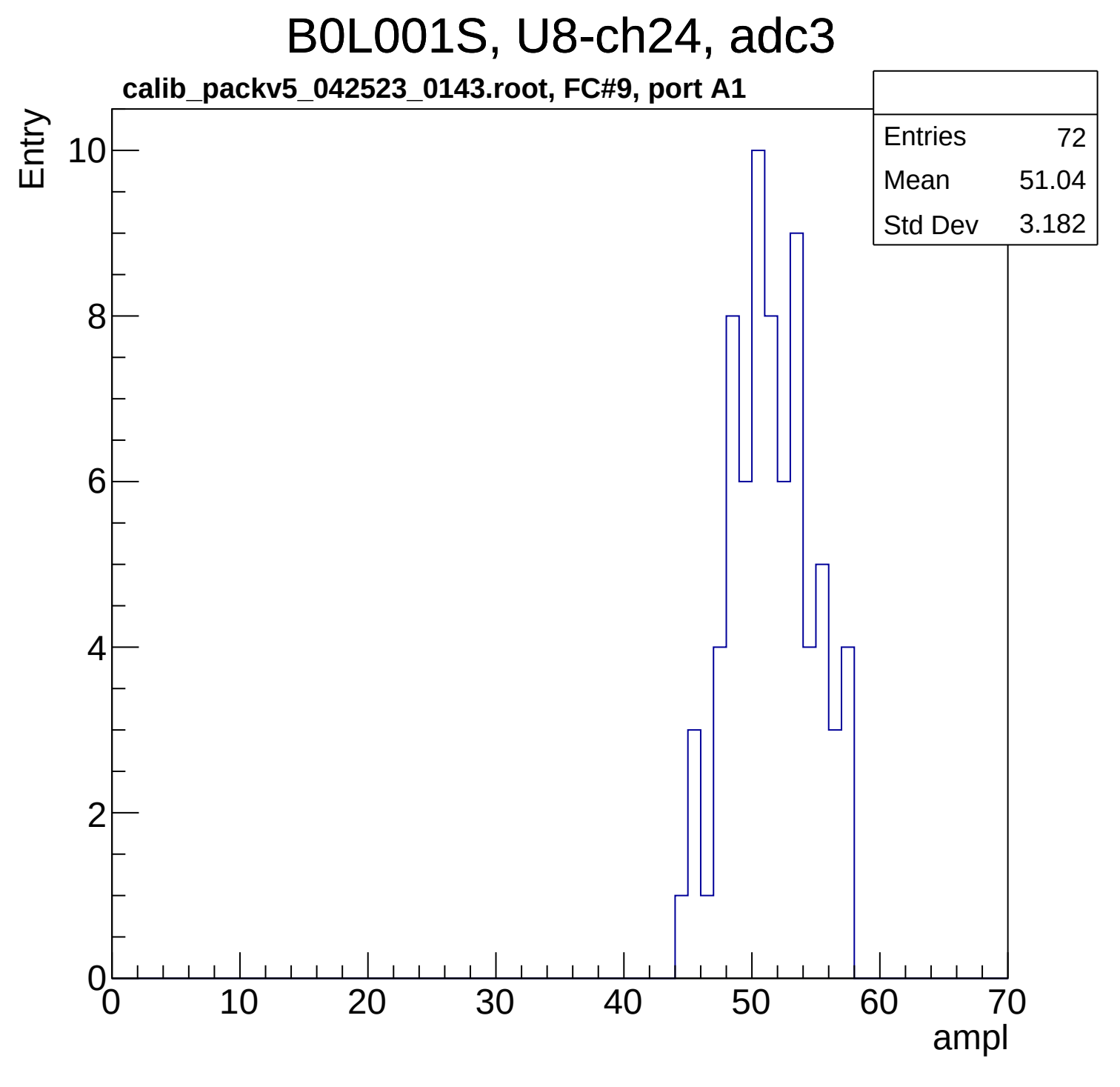
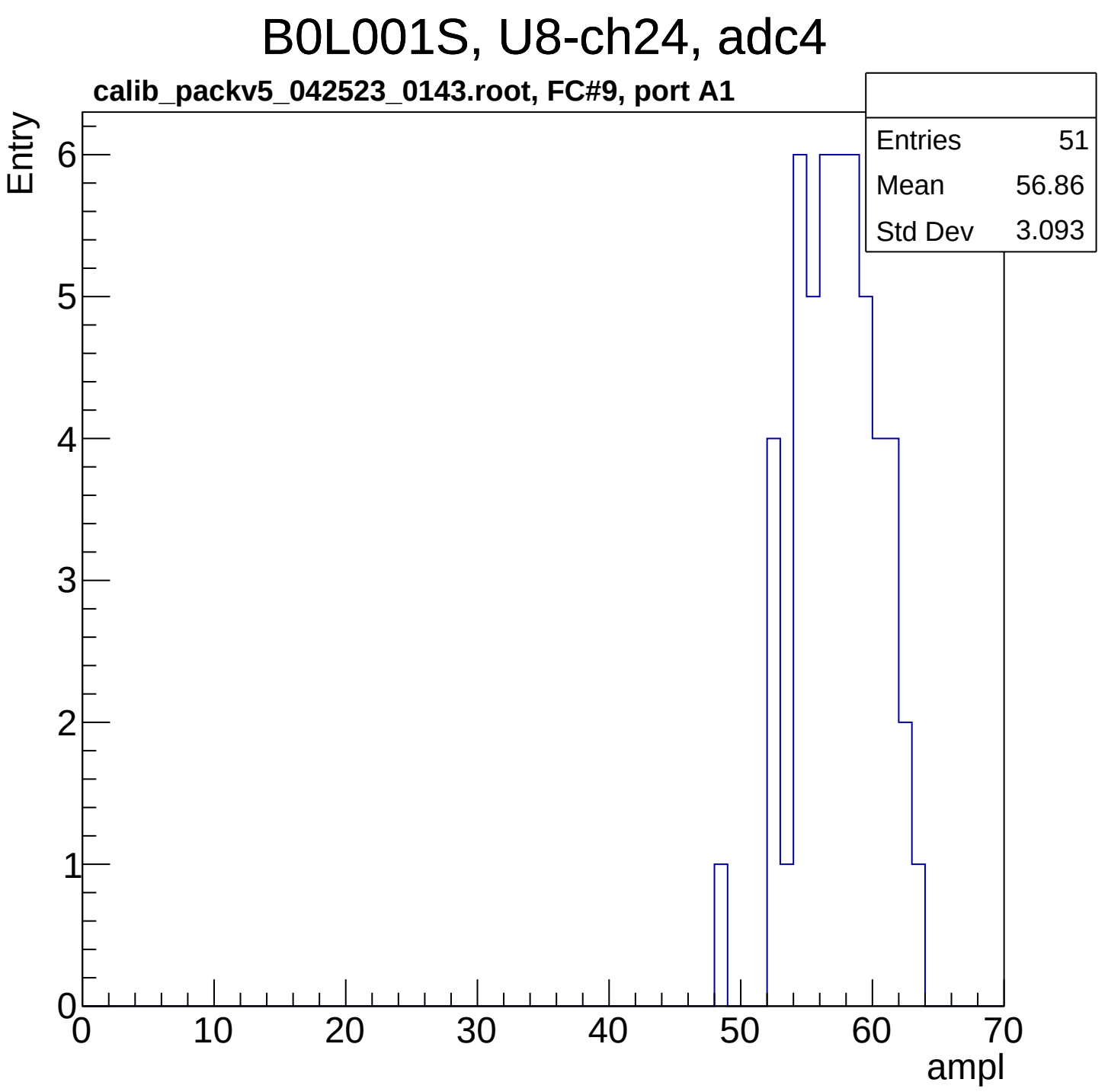
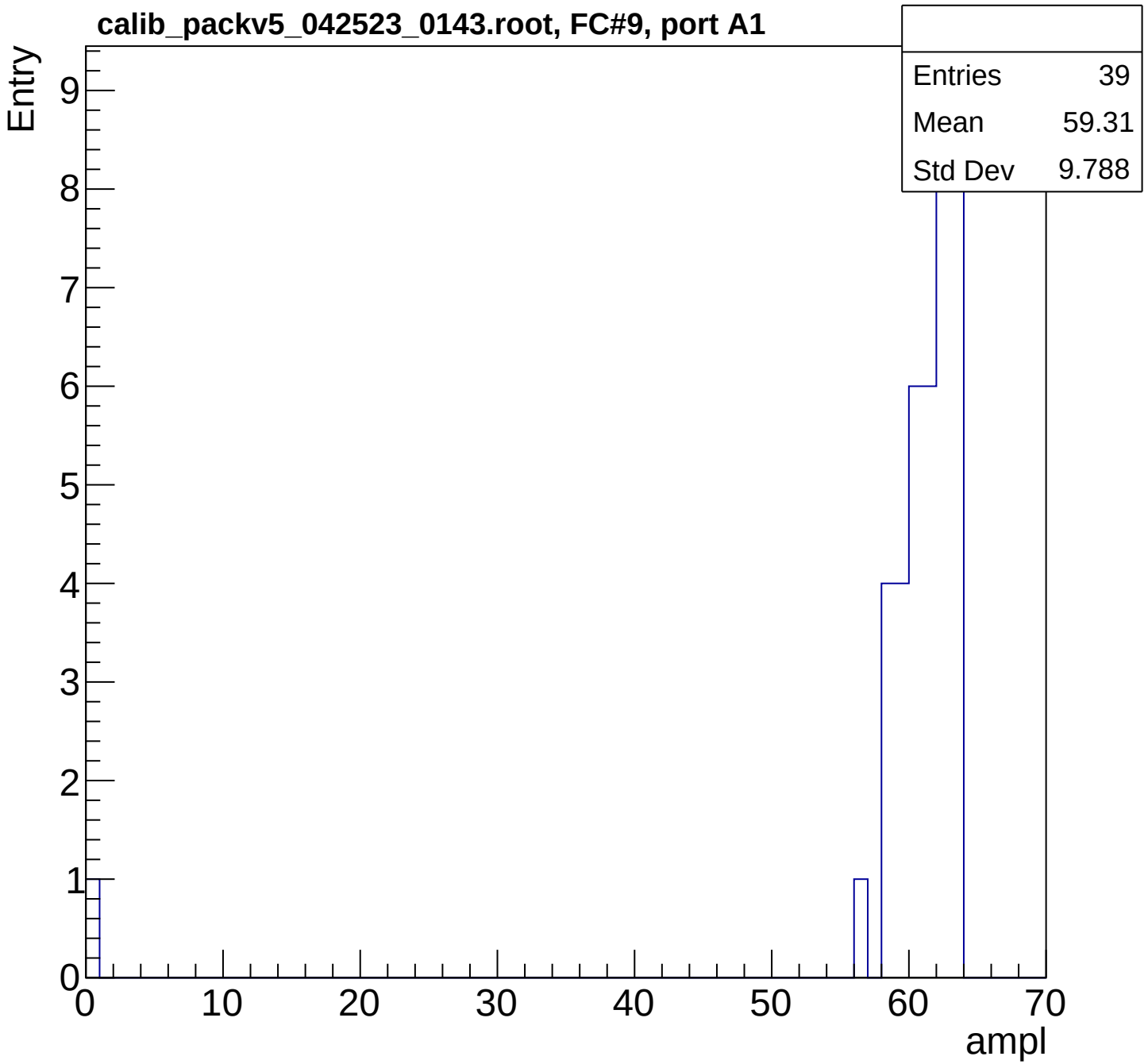
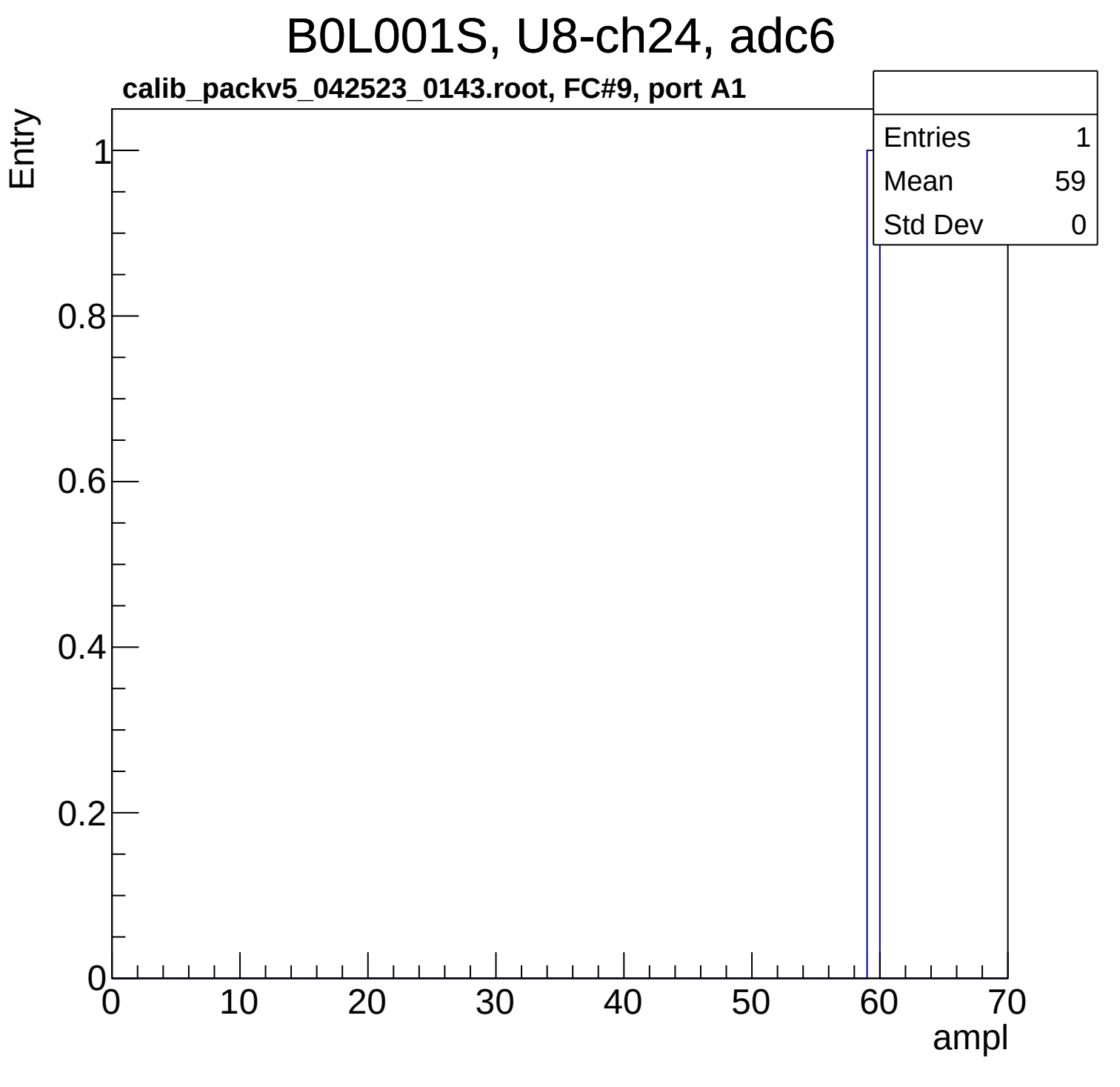
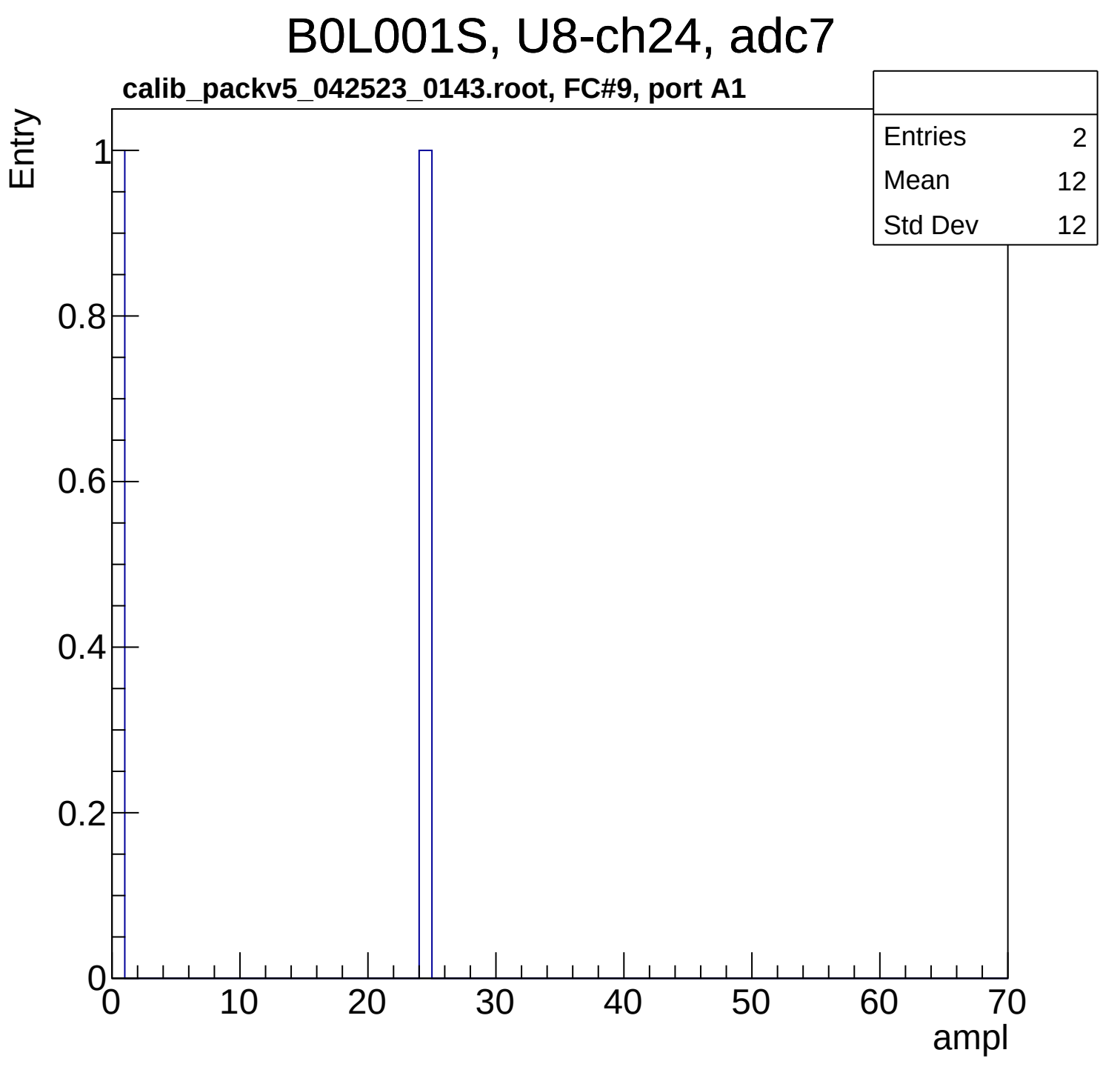

B0L001S, U8-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.47
Std Dev 2.834
Gaus mean : 44.4072
Gaus Width: 2.7283
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

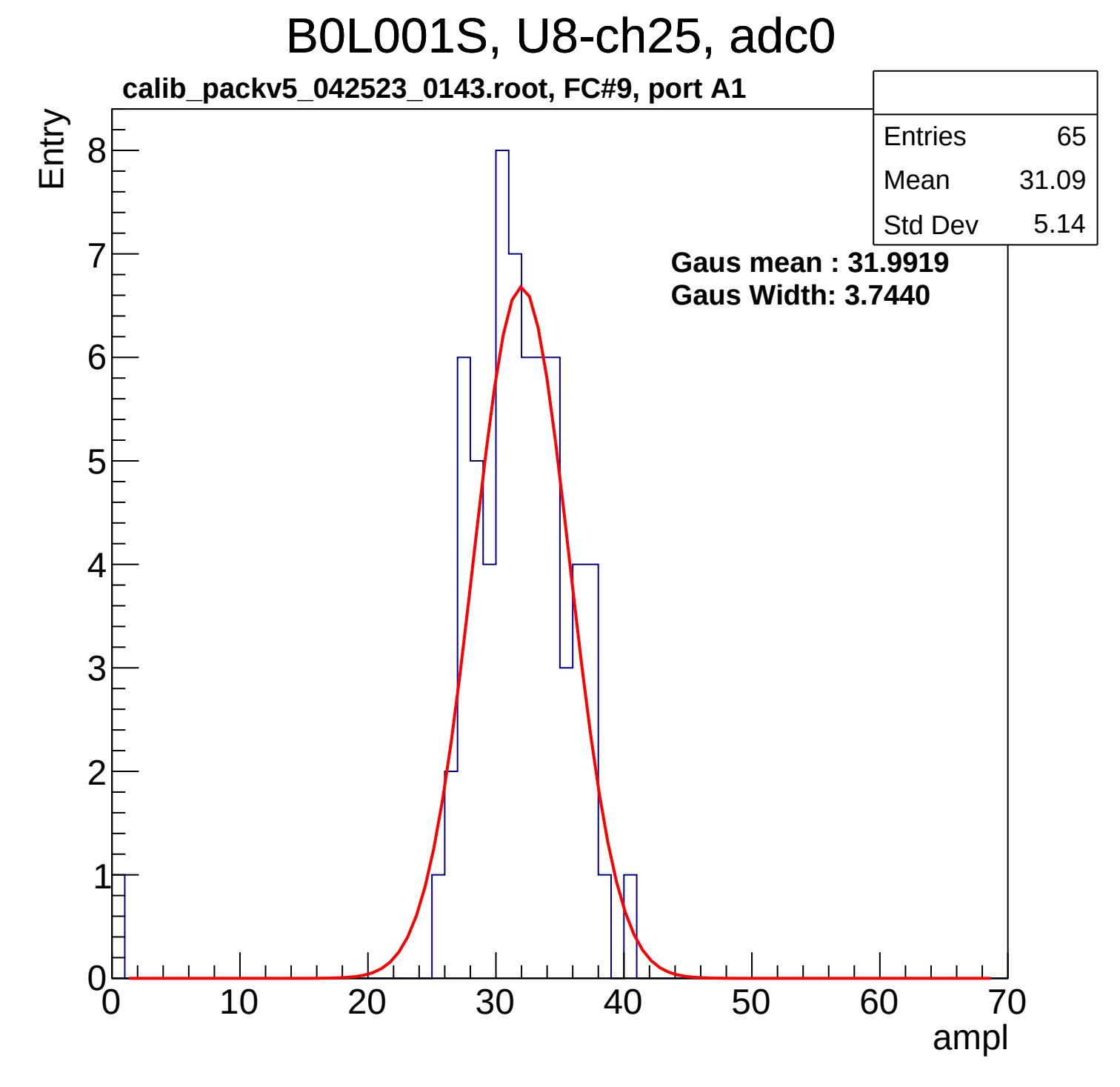

B0L001S, U8-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.04
Std Dev 3.182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

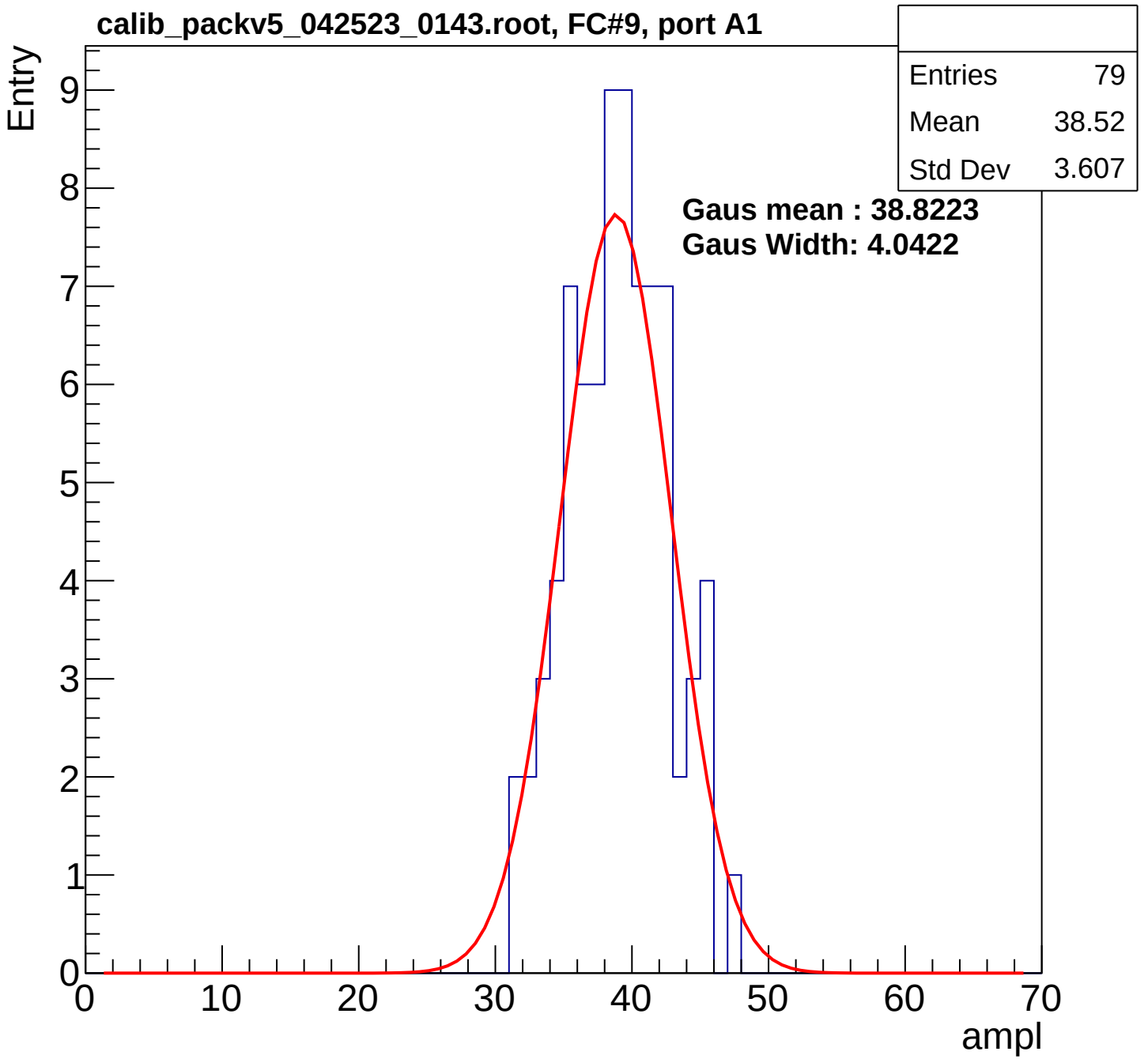

B0L001S, U8-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.86
Std Dev 3.093
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

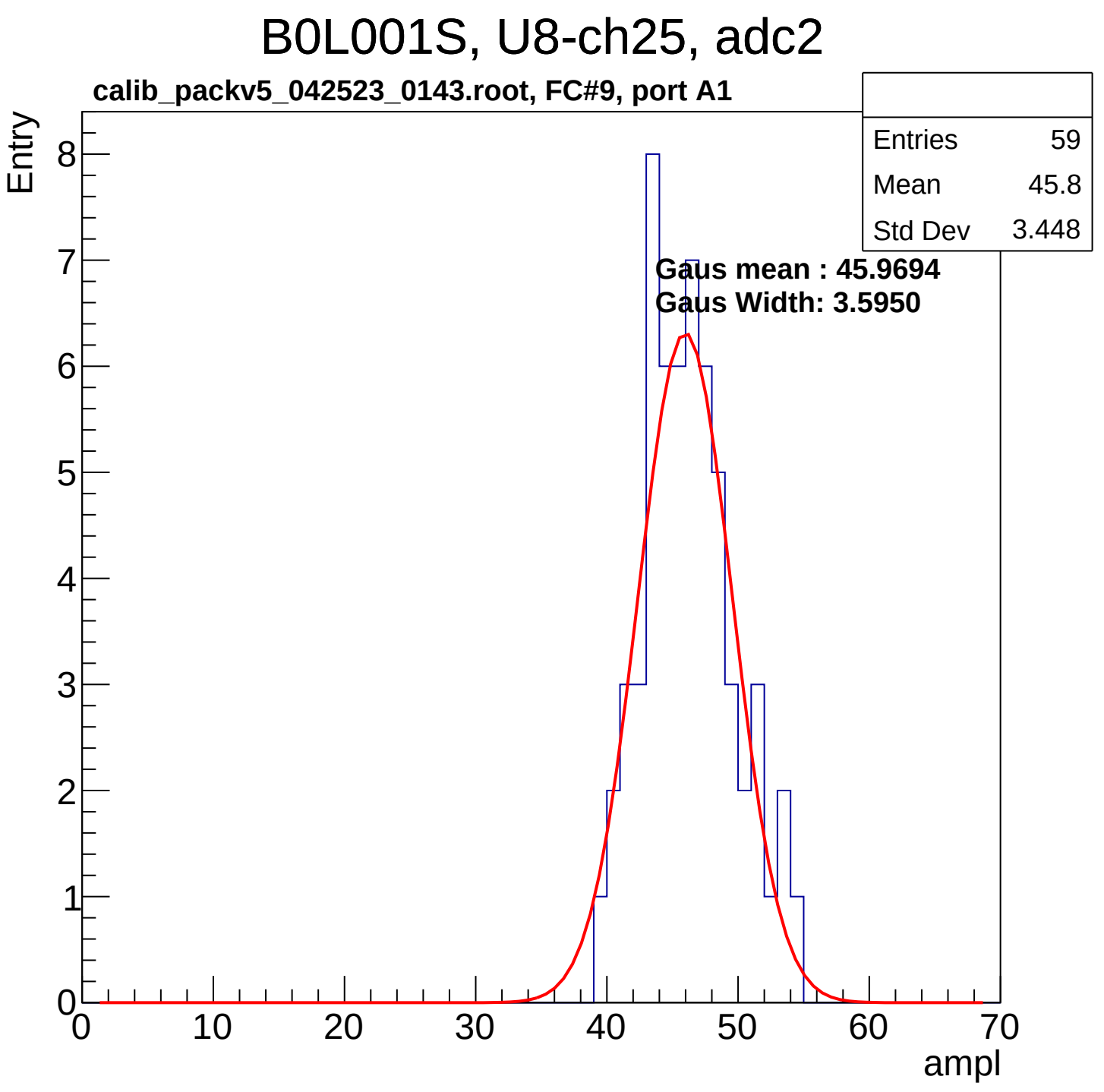

B0L001S, U8-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.31
Std Dev 9.788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

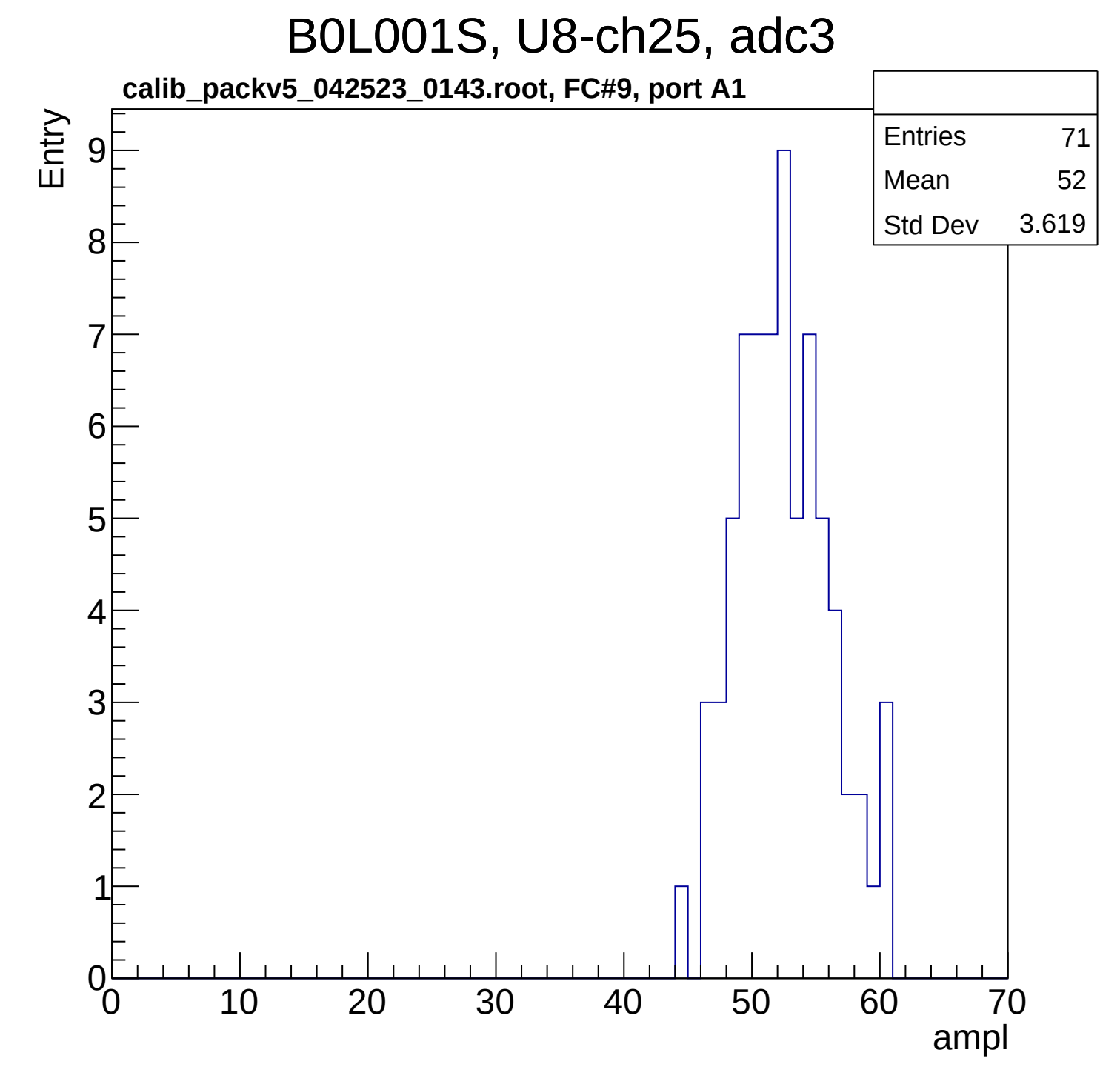

B0L001S, U8-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

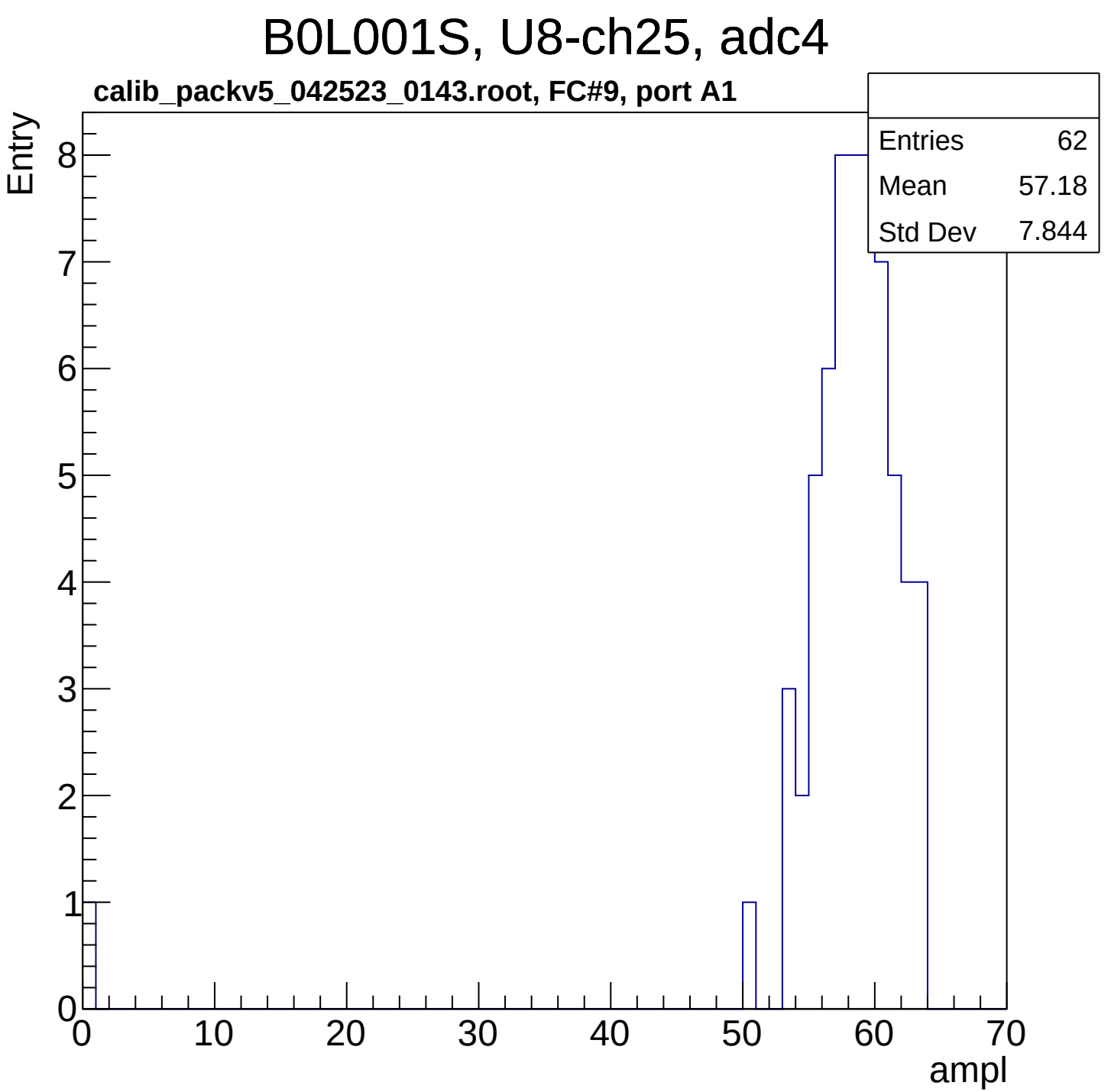

B0L001S, U8-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 31.09
Std Dev 5.14
Gaus mean : 31.9919
Gaus Width: 3.7440
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 38.52
Std Dev 3.607
Gaus mean : 38.8223
Gaus Width: 4.0422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.8
Std Dev 3.448
Gaus mean : 45.9694
Gaus Width: 3.5950
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 52
Std Dev 3.619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 57.18
Std Dev 7.844
Entry
ampl
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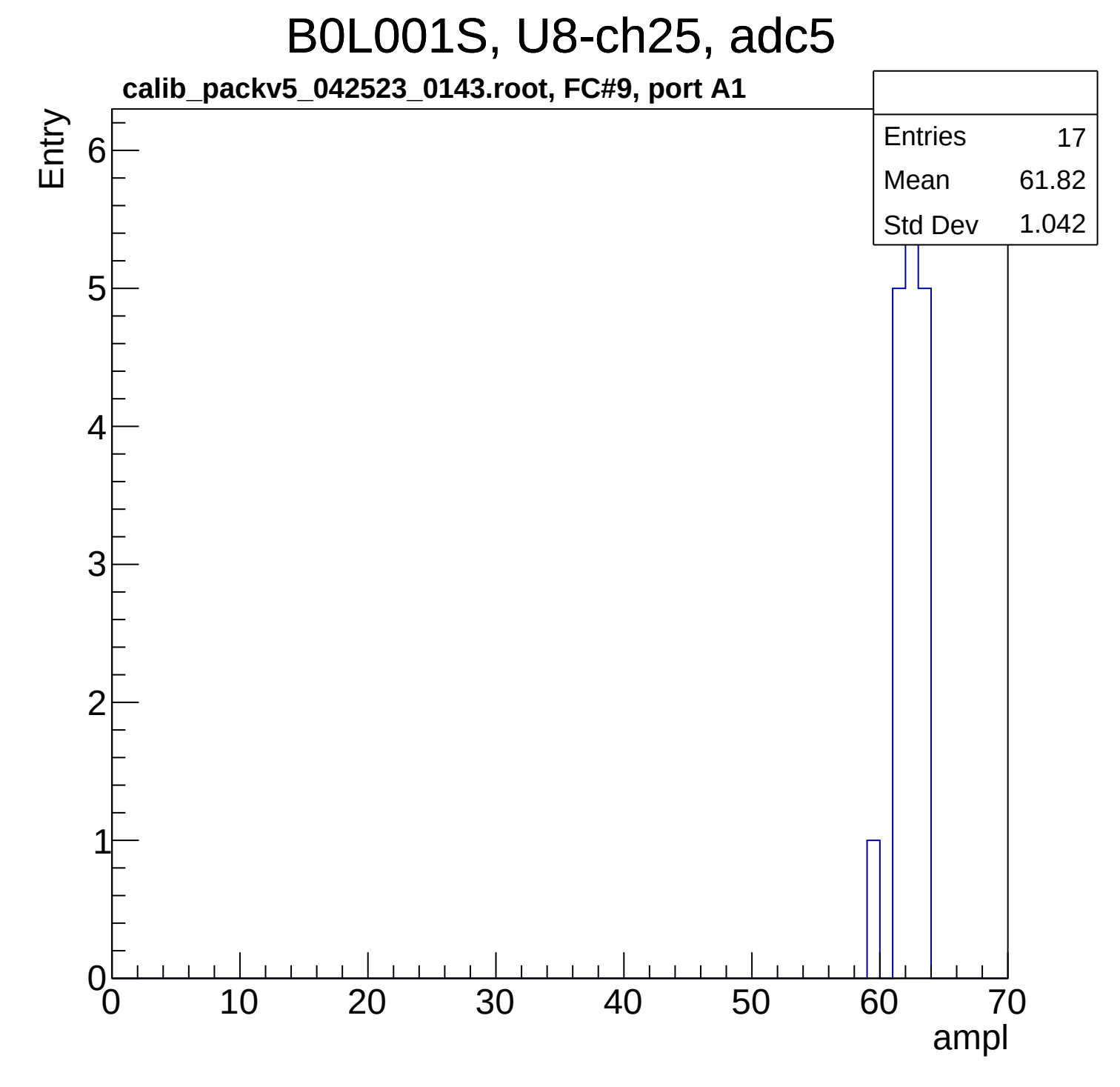
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 61.82
Std Dev 1.042
Entry
0
1
2
3
4
5
6
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10
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30
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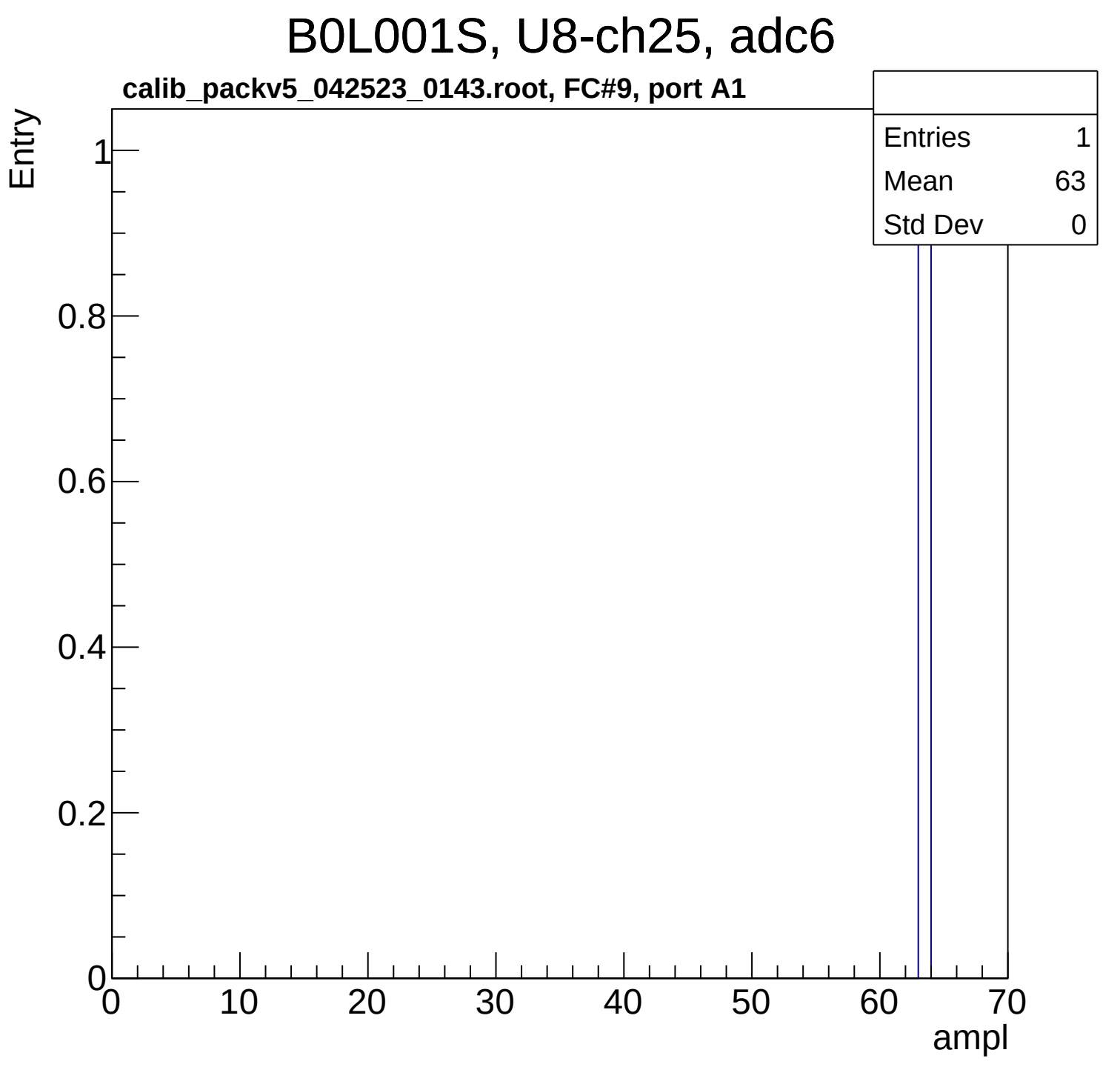
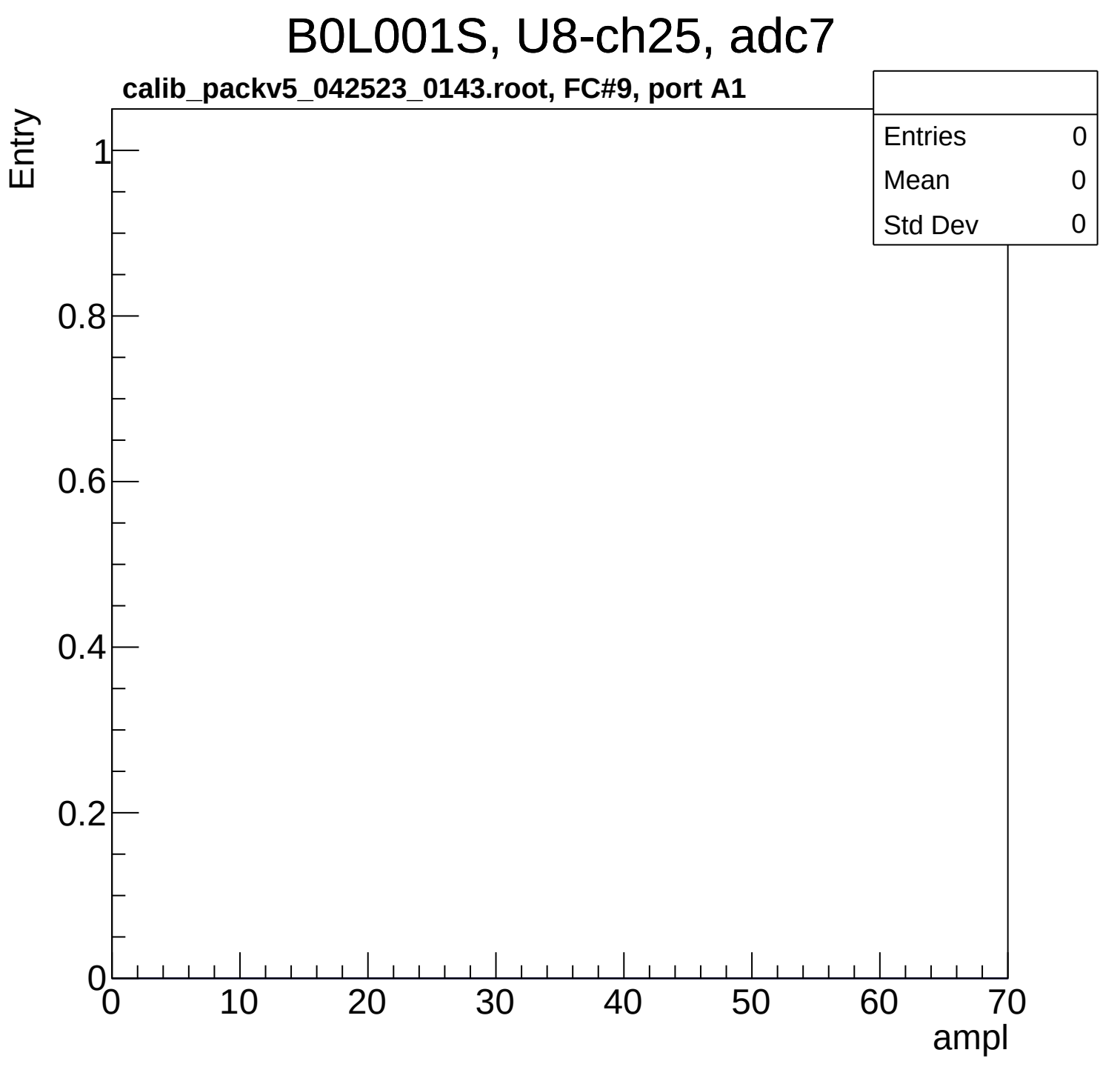
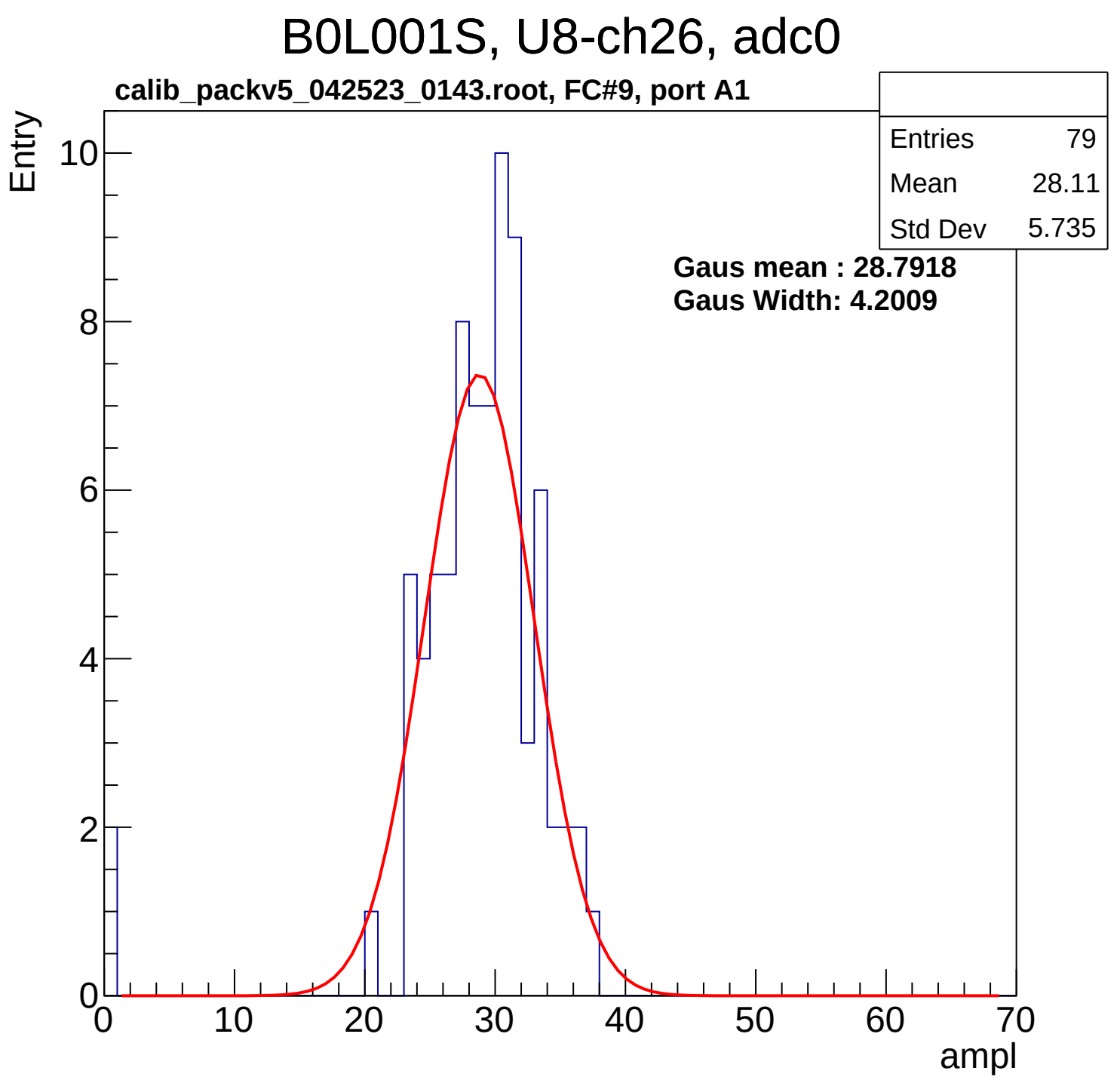
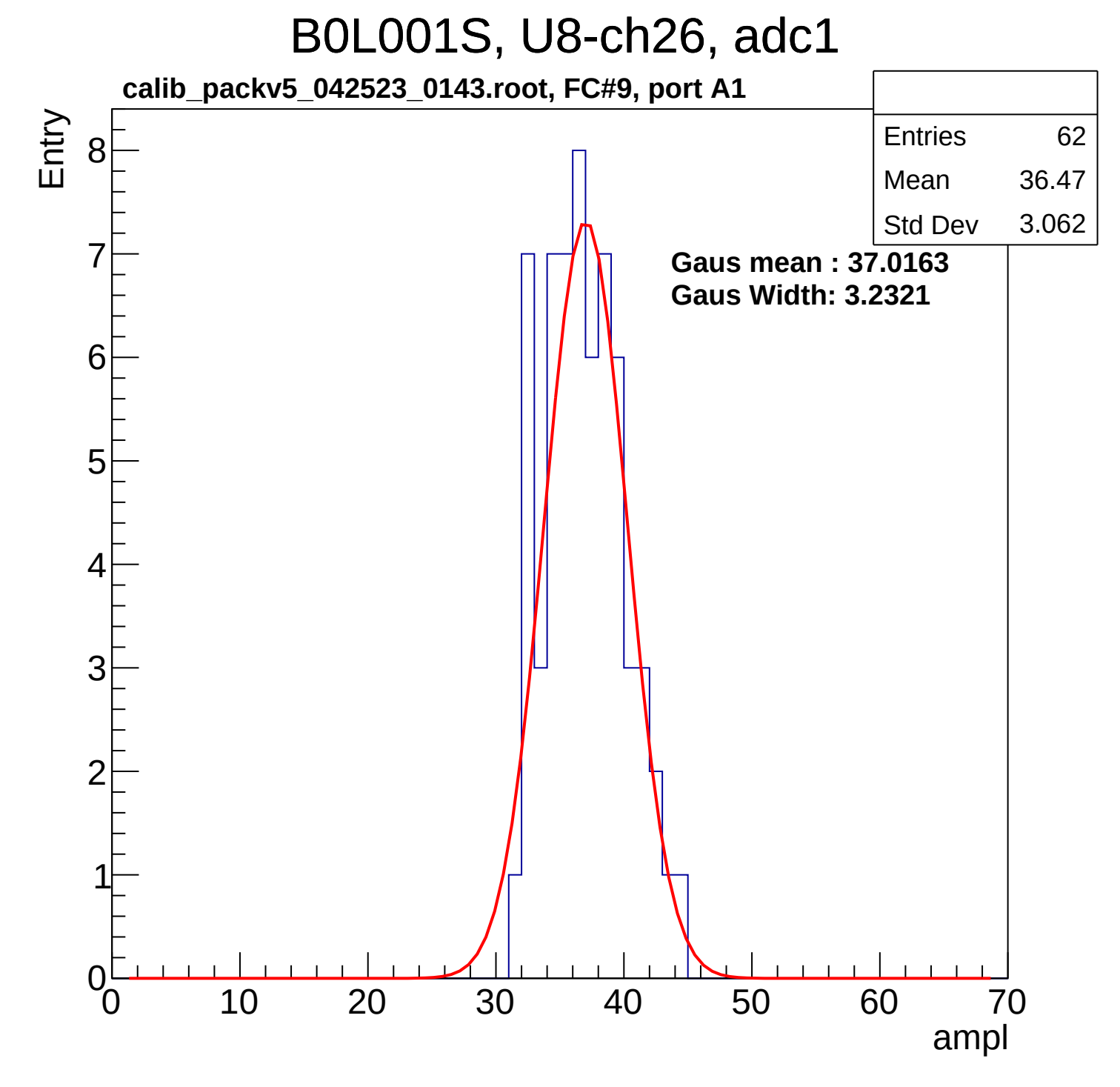
50
60
70
ampl


B0L001S, U8-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 28.11
Std Dev 5.735
Gaus mean : 28.7918
Gaus Width: 4.2009
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.47
Std Dev 3.062
Gaus mean : 37.0163
Gaus Width: 3.2321
Entry
ampl
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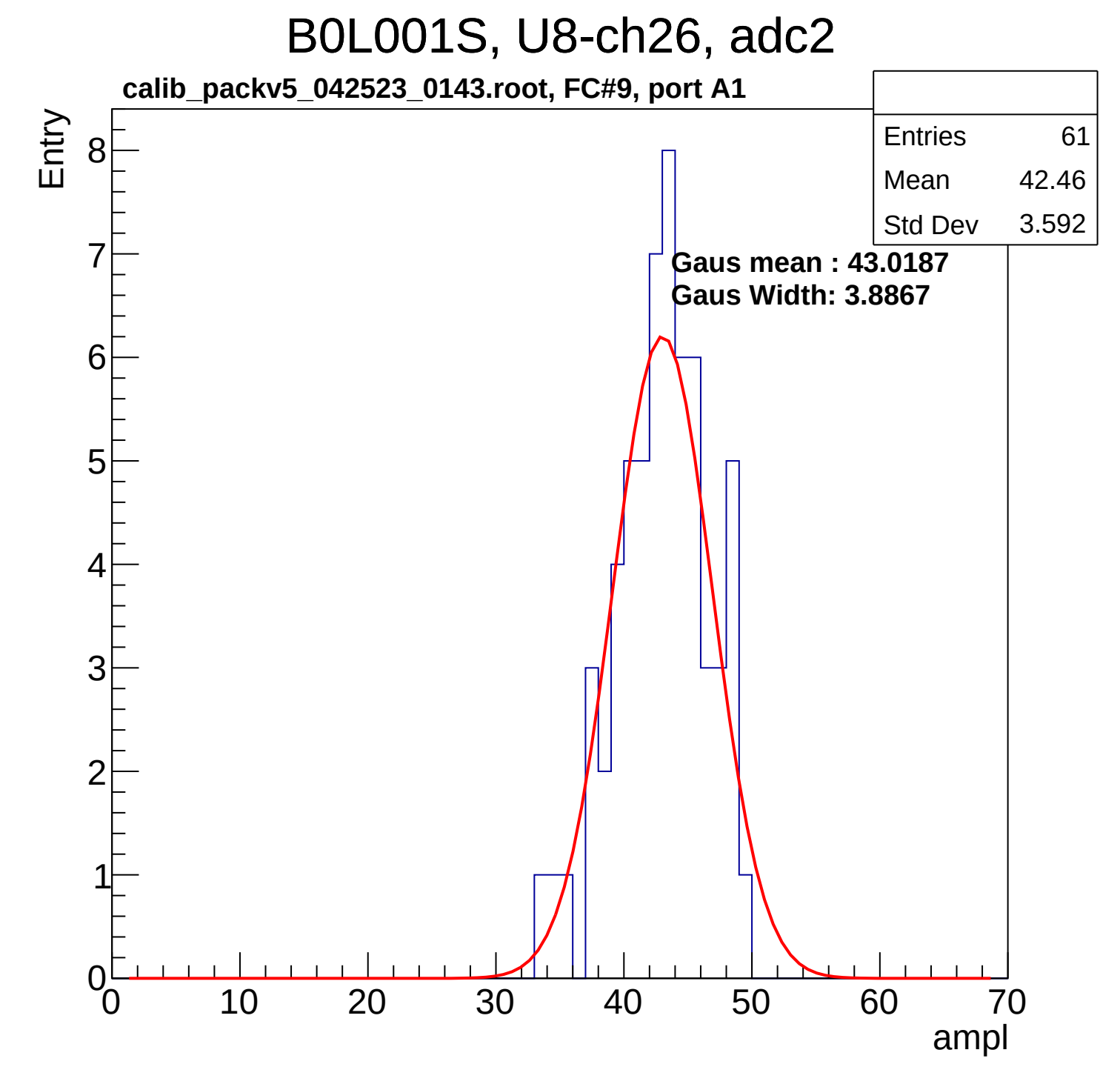
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 42.46
Std Dev 3.592
Gaus mean : 43.0187
Gaus Width: 3.8867
Entry
ampl
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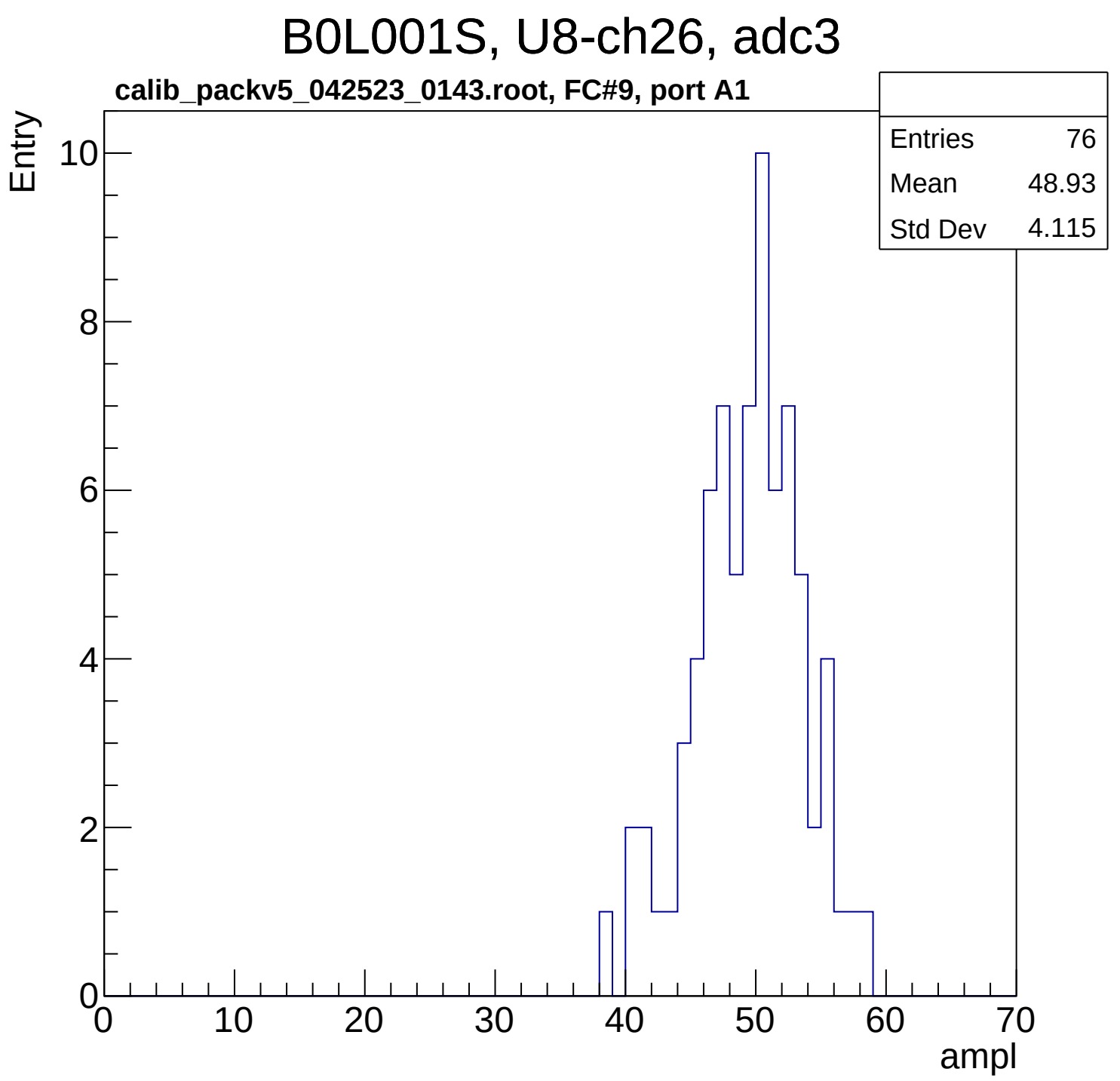
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 48.93
Std Dev 4.115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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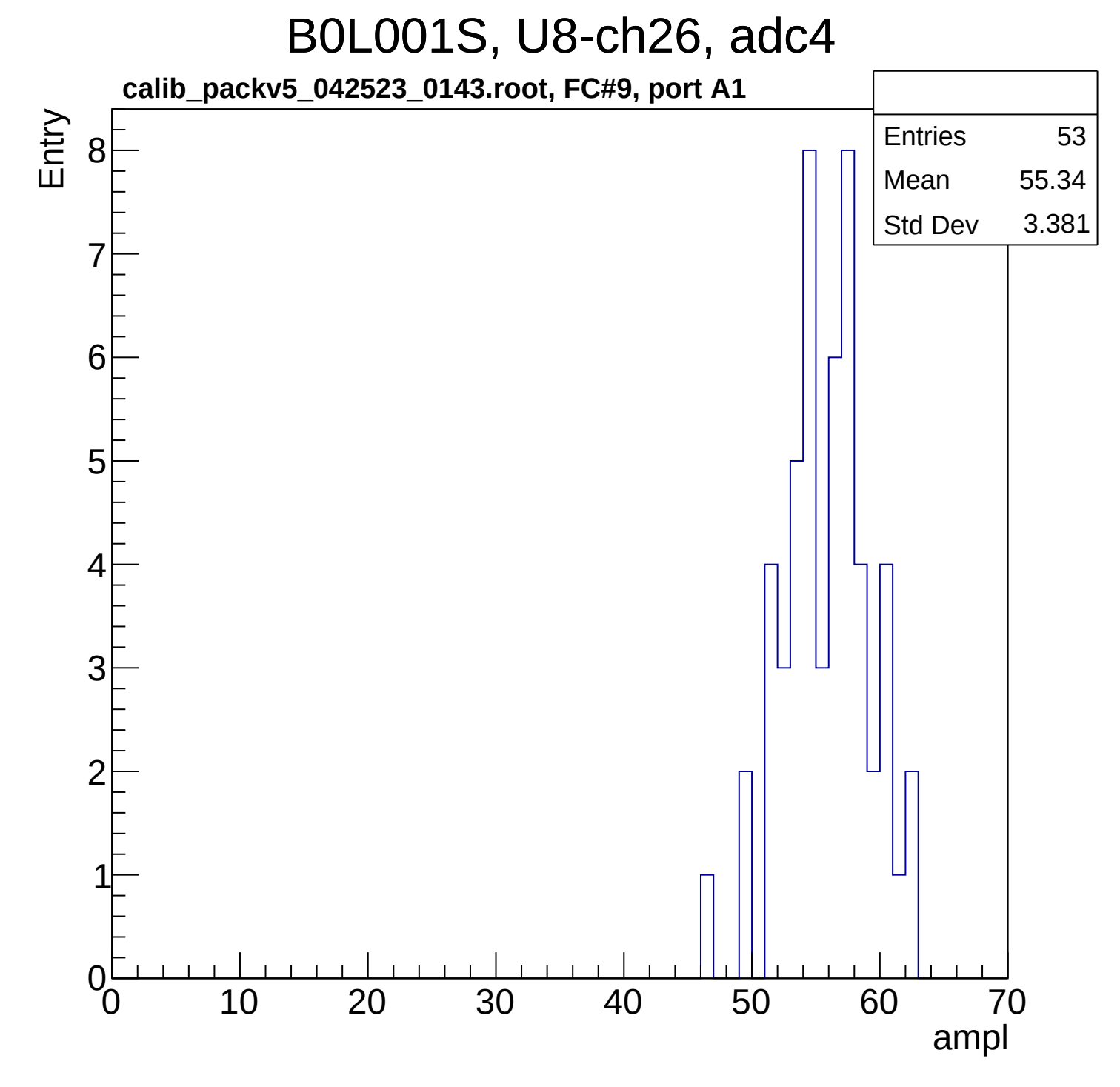
B0L001S, U8-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.34
Std Dev 3.381
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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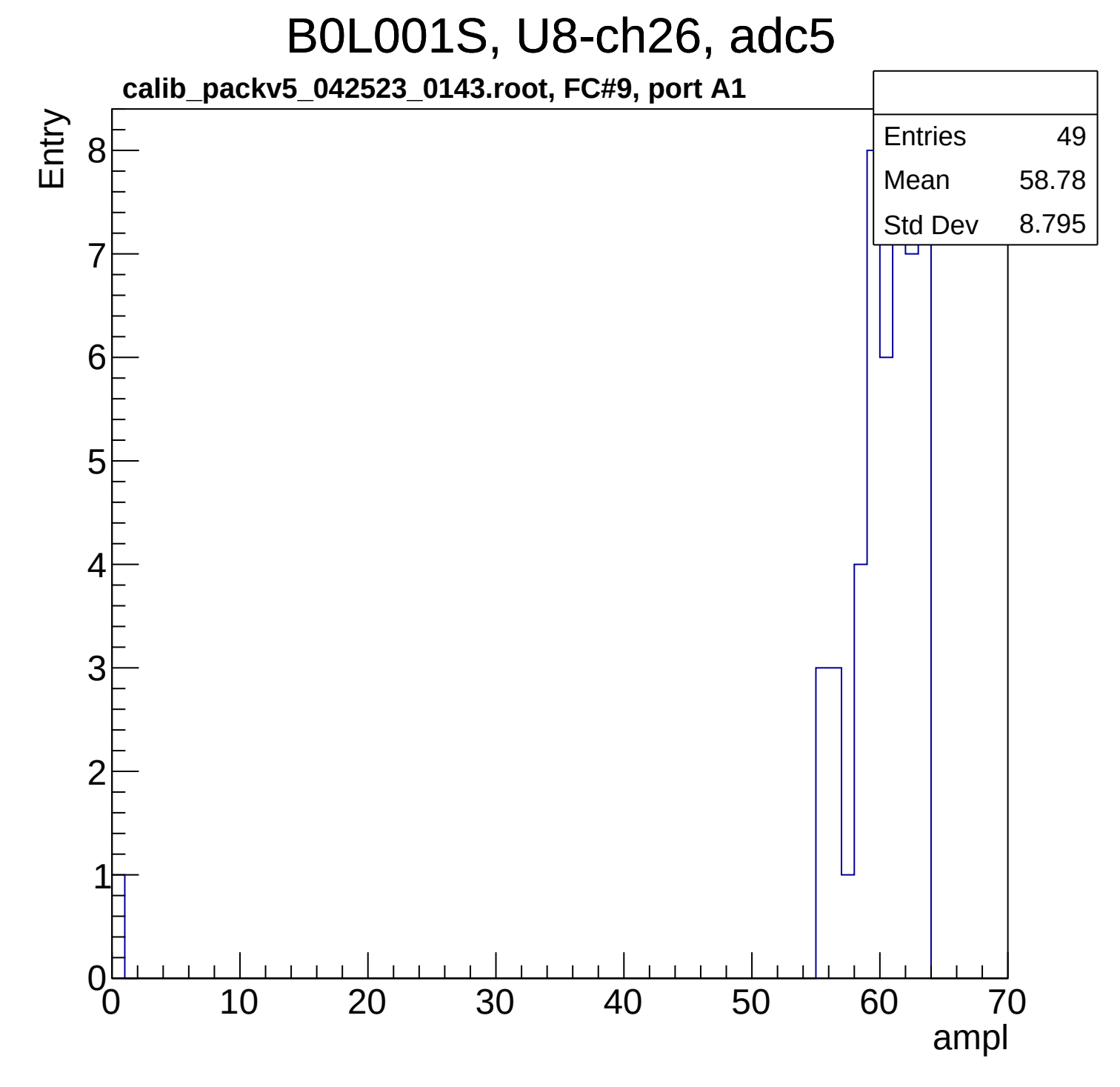
B0L001S, U8-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.78
Std Dev 8.795
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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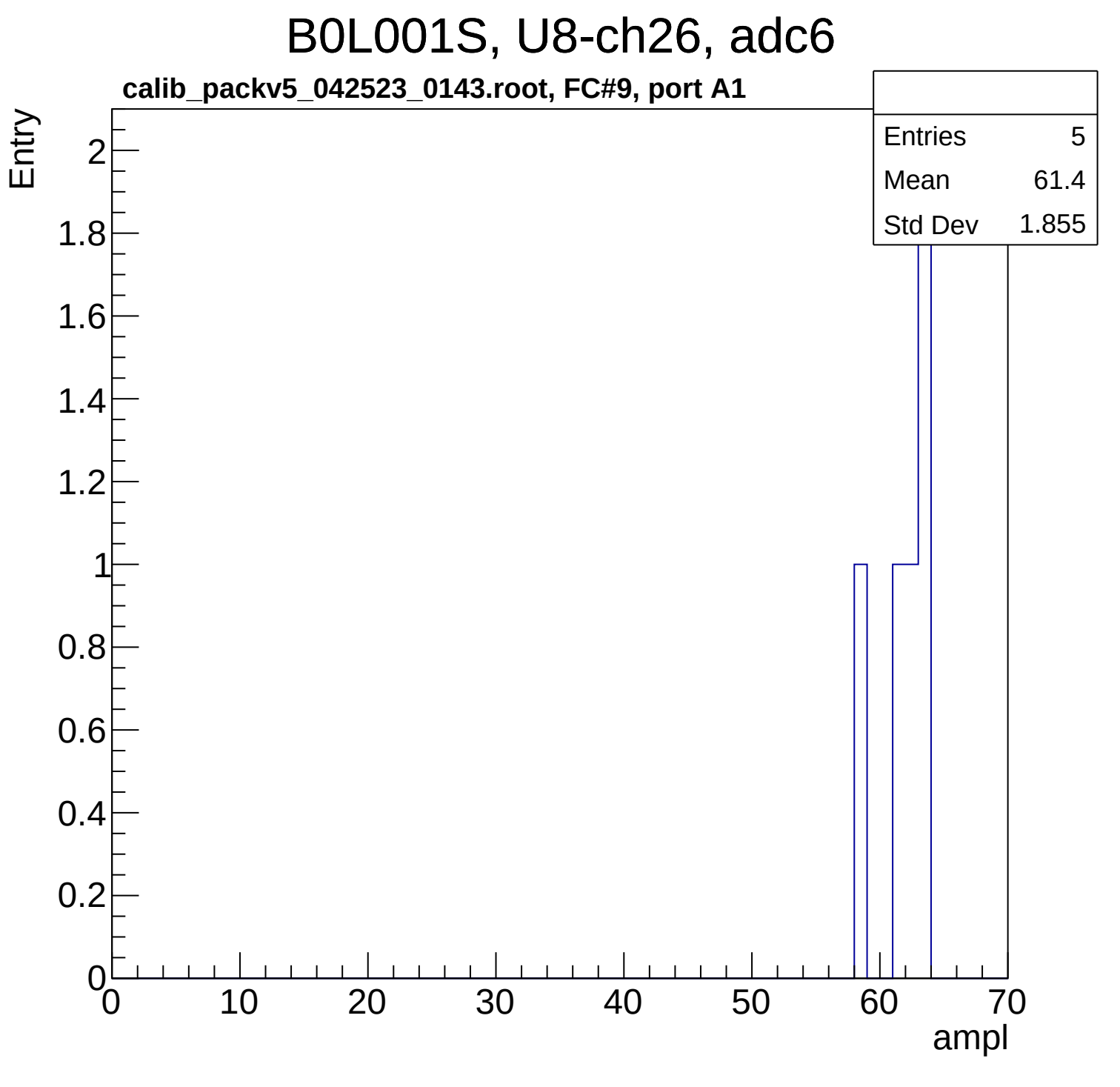
B0L001S, U8-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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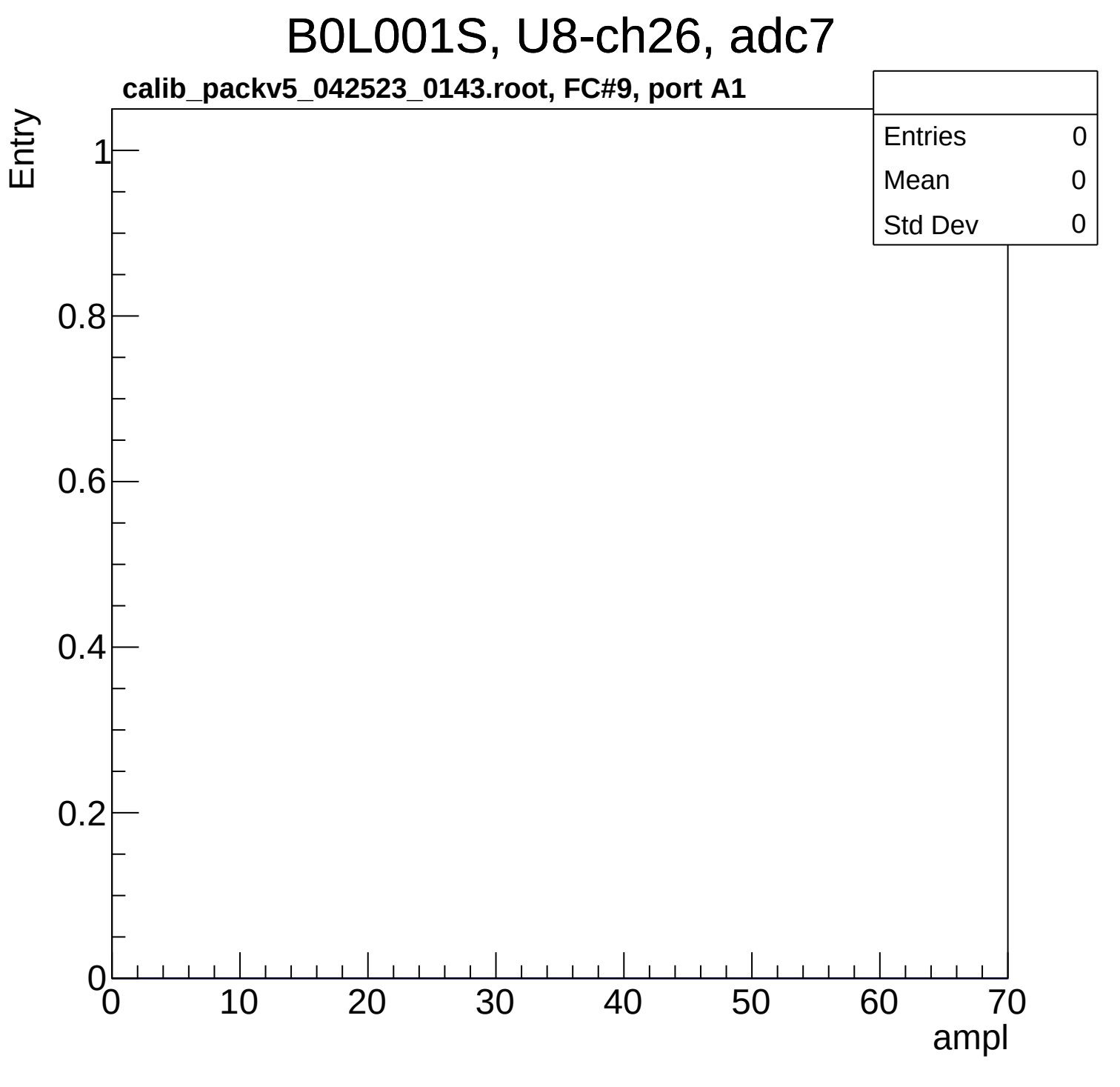
60
70


B0L001S, U8-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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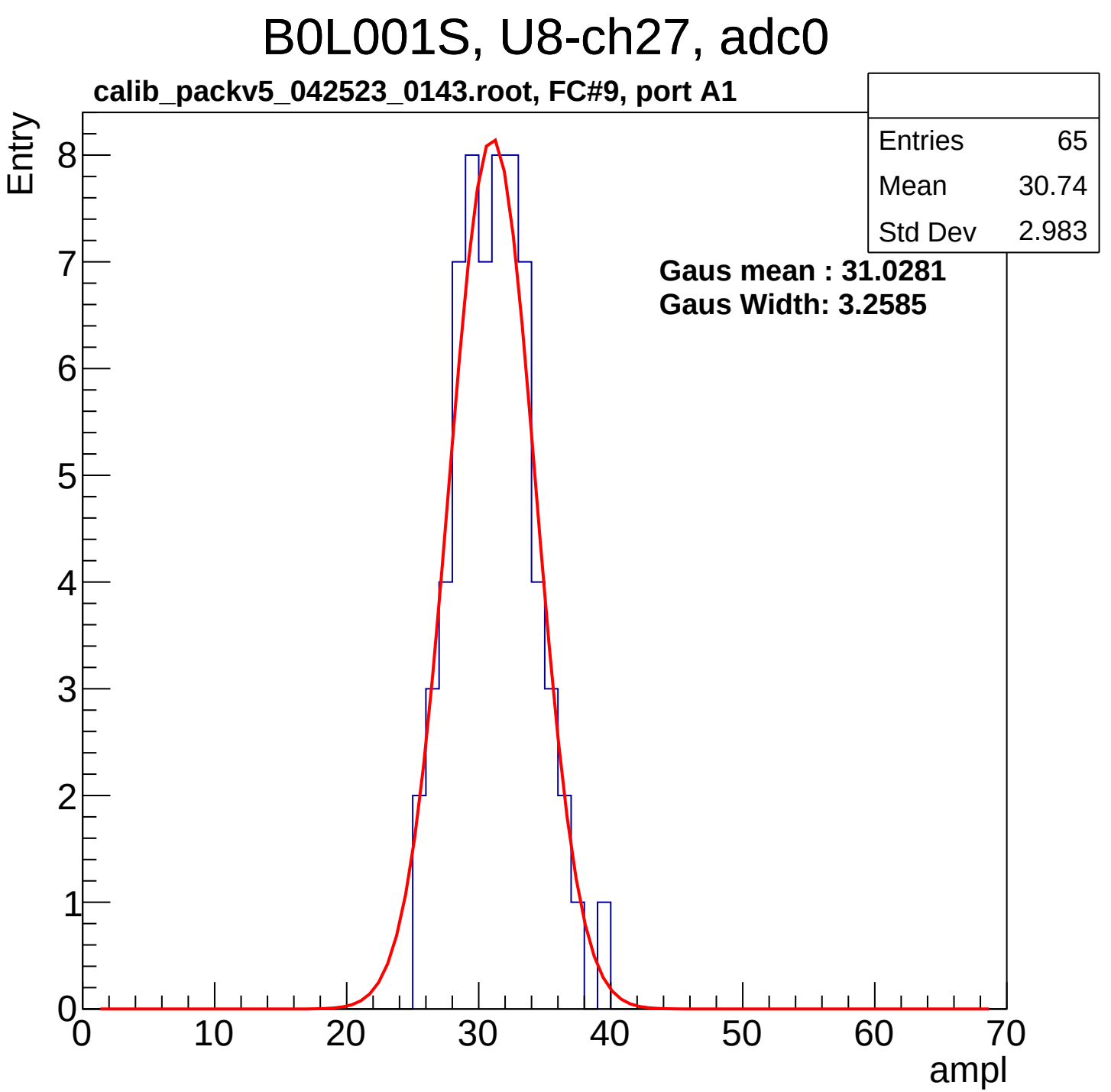
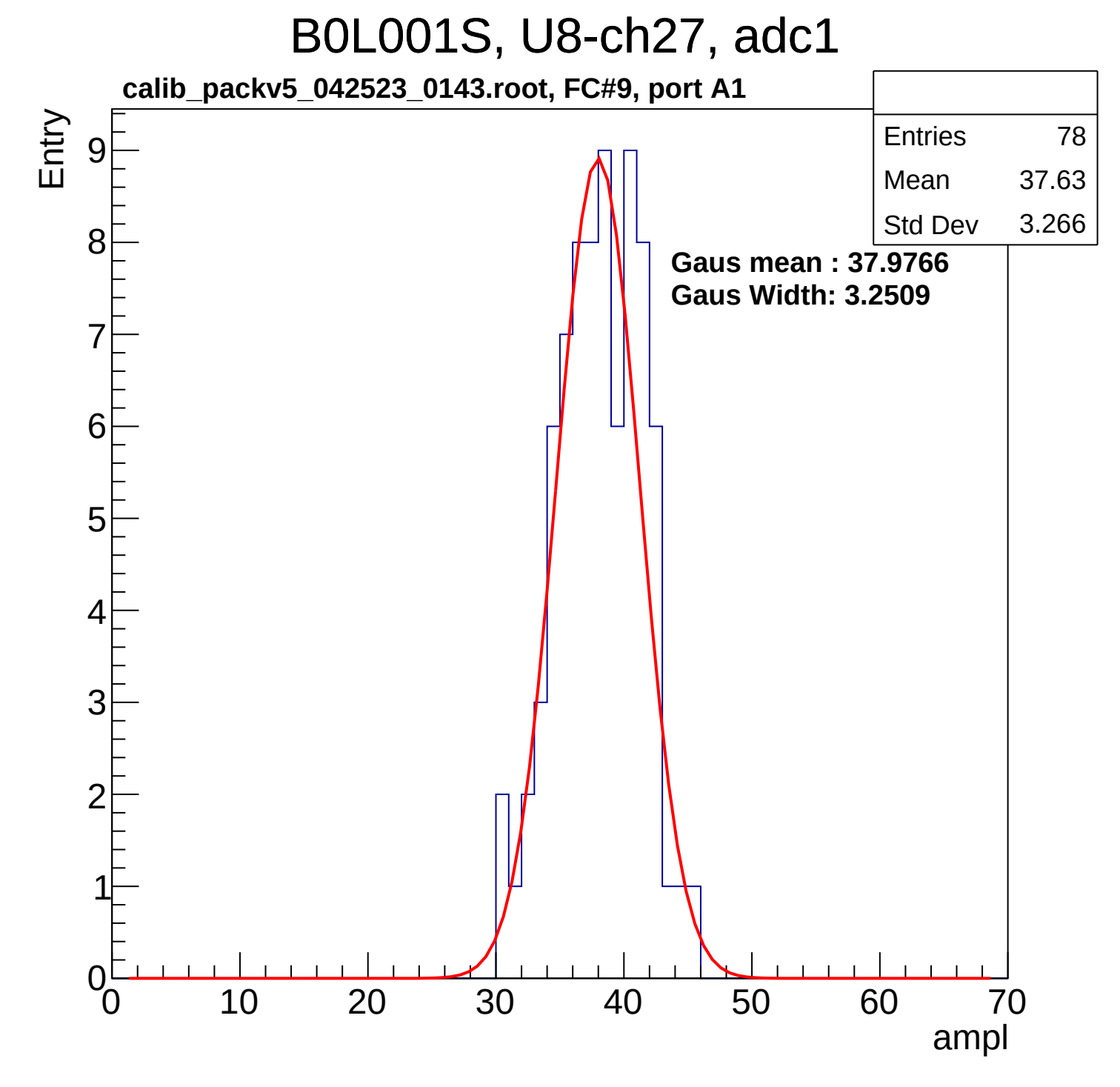
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.74
Std Dev 2.983
Gaus mean : 31.0281
Gaus Width: 3.2585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.63
Std Dev 3.266
Gaus mean : 37.9766
Gaus Width: 3.2509
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
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40
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60
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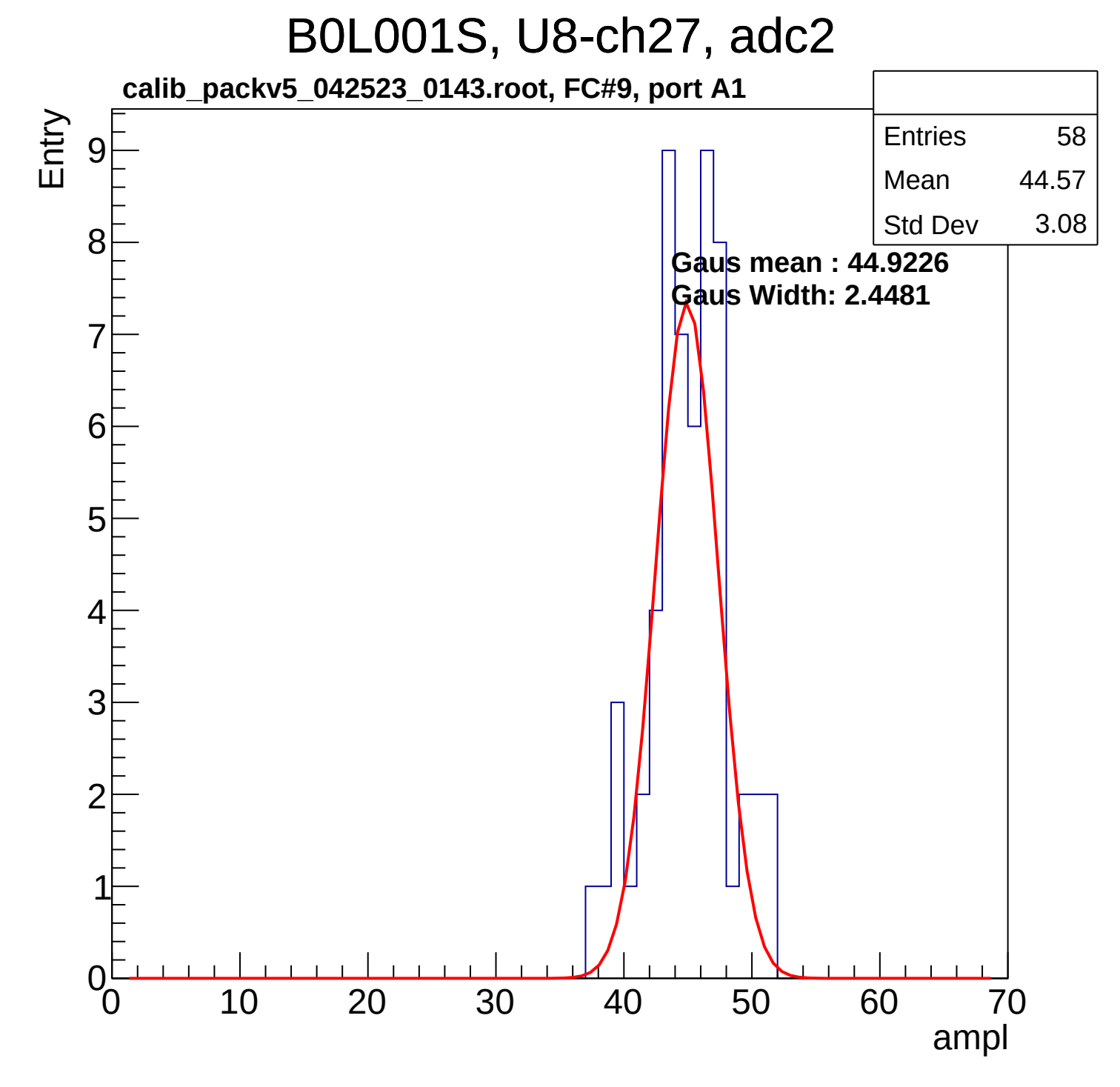
B0L001S, U8-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.57
Std Dev 3.08
Gaus mean : 44.9226
Gaus Width: 2.4481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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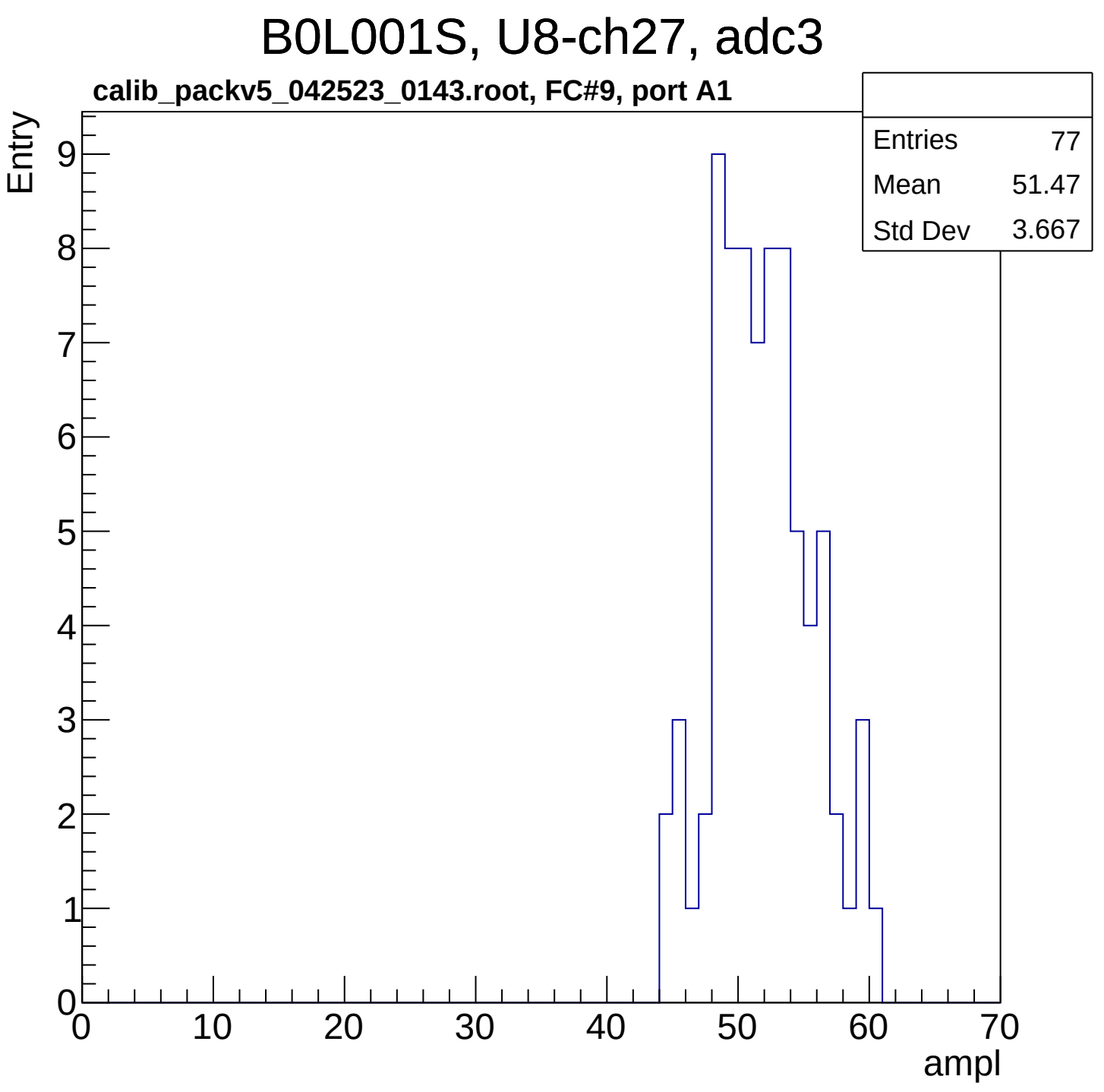
B0L001S, U8-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.47
Std Dev 3.667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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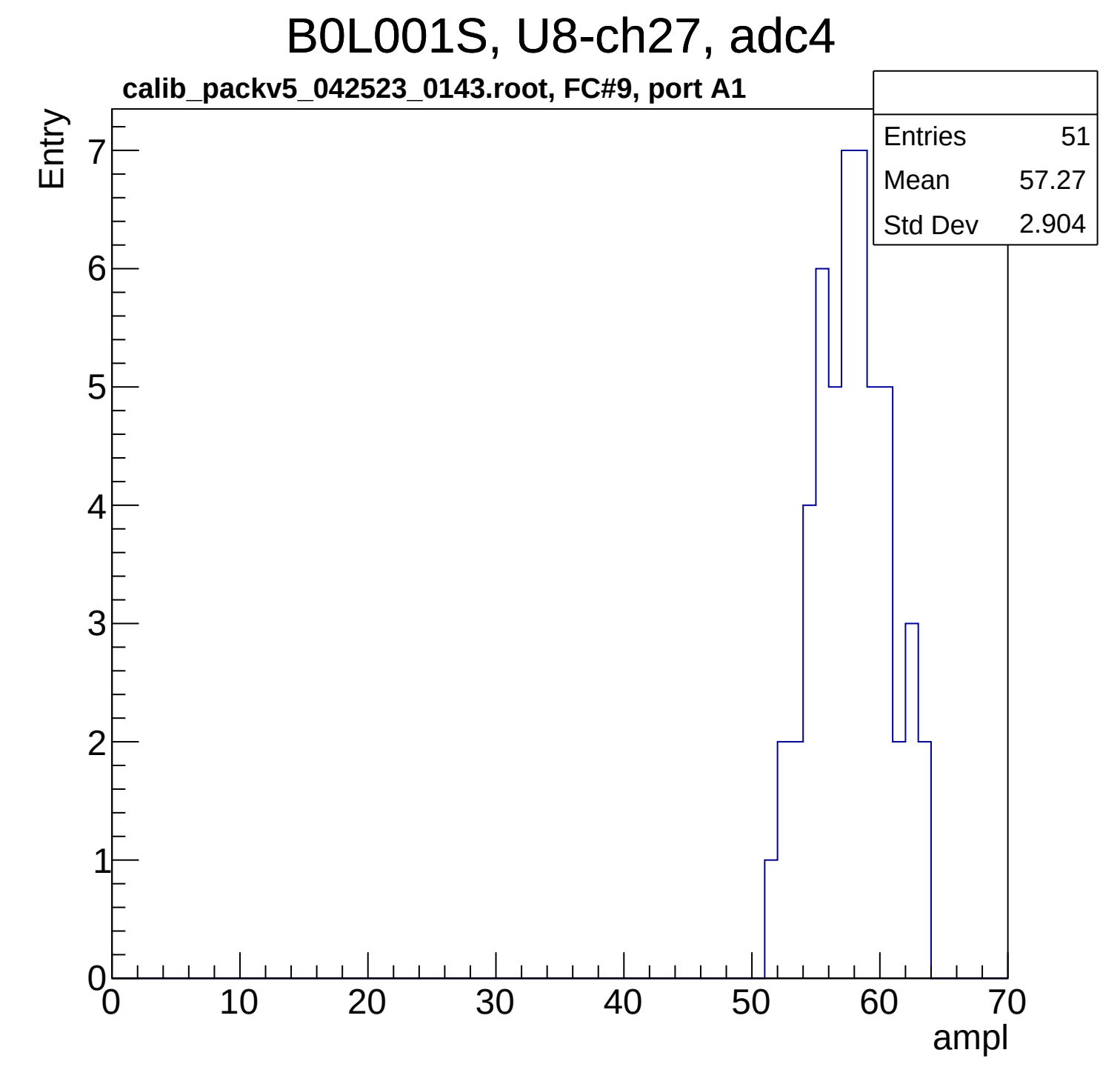
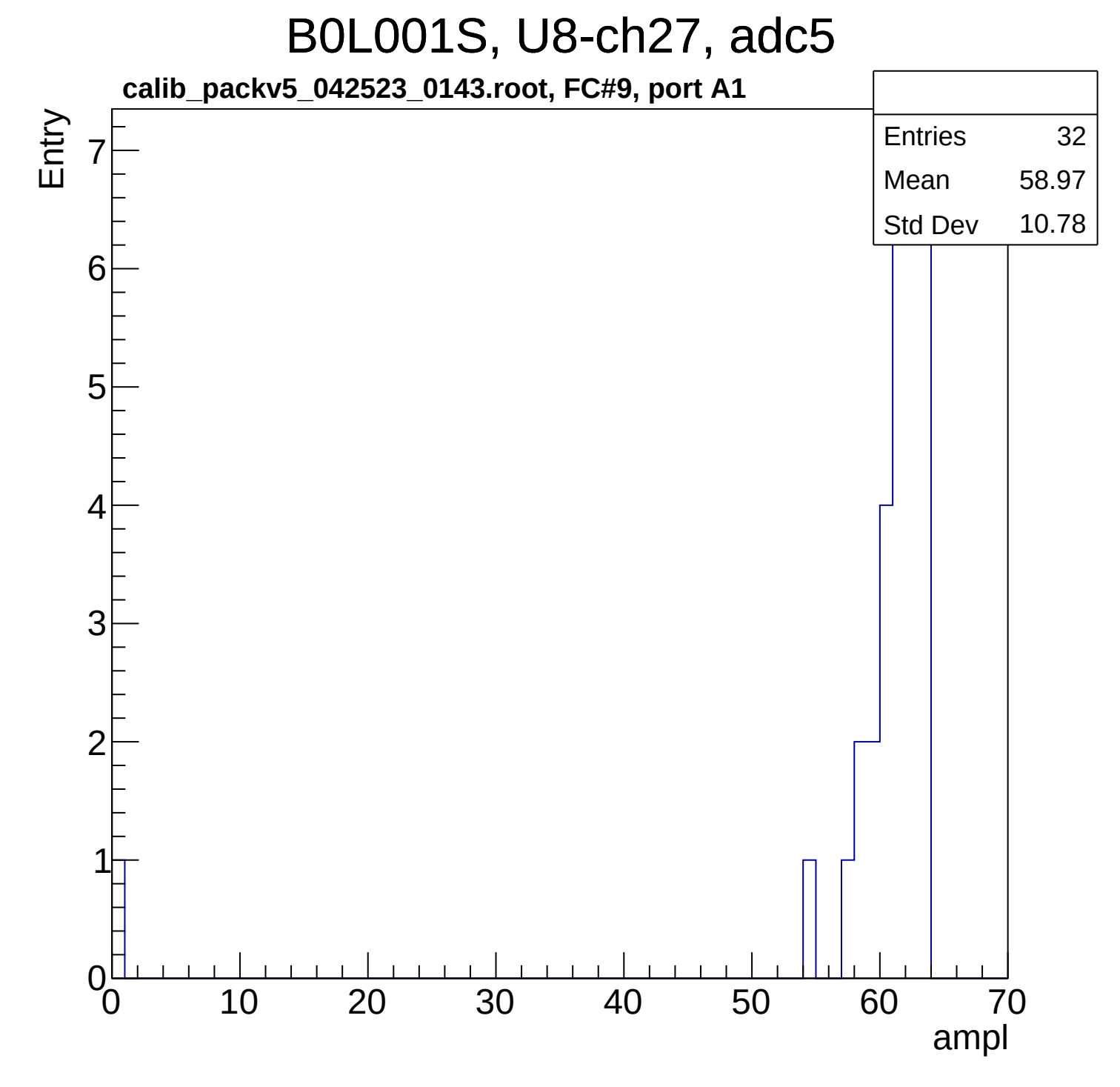
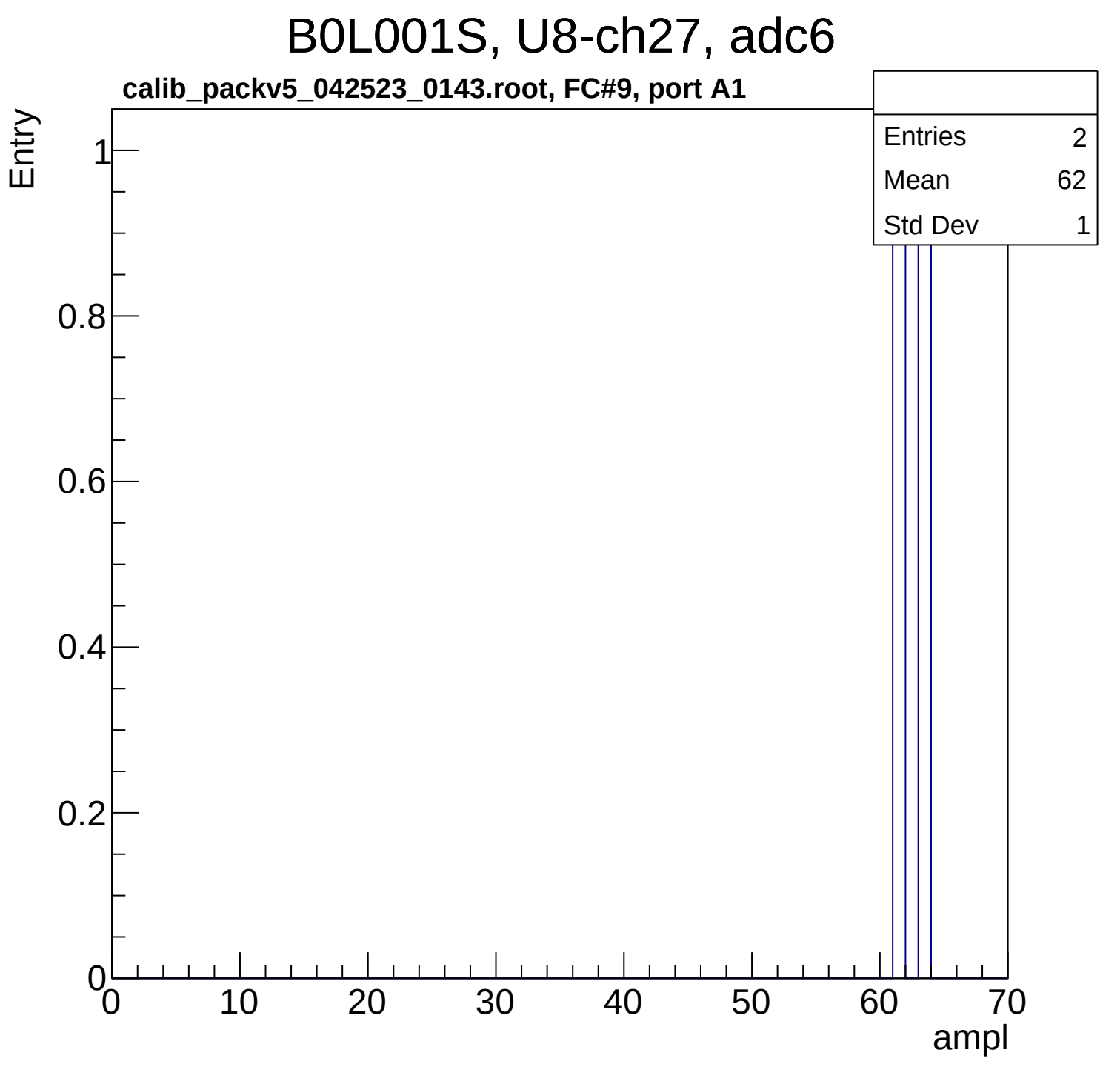
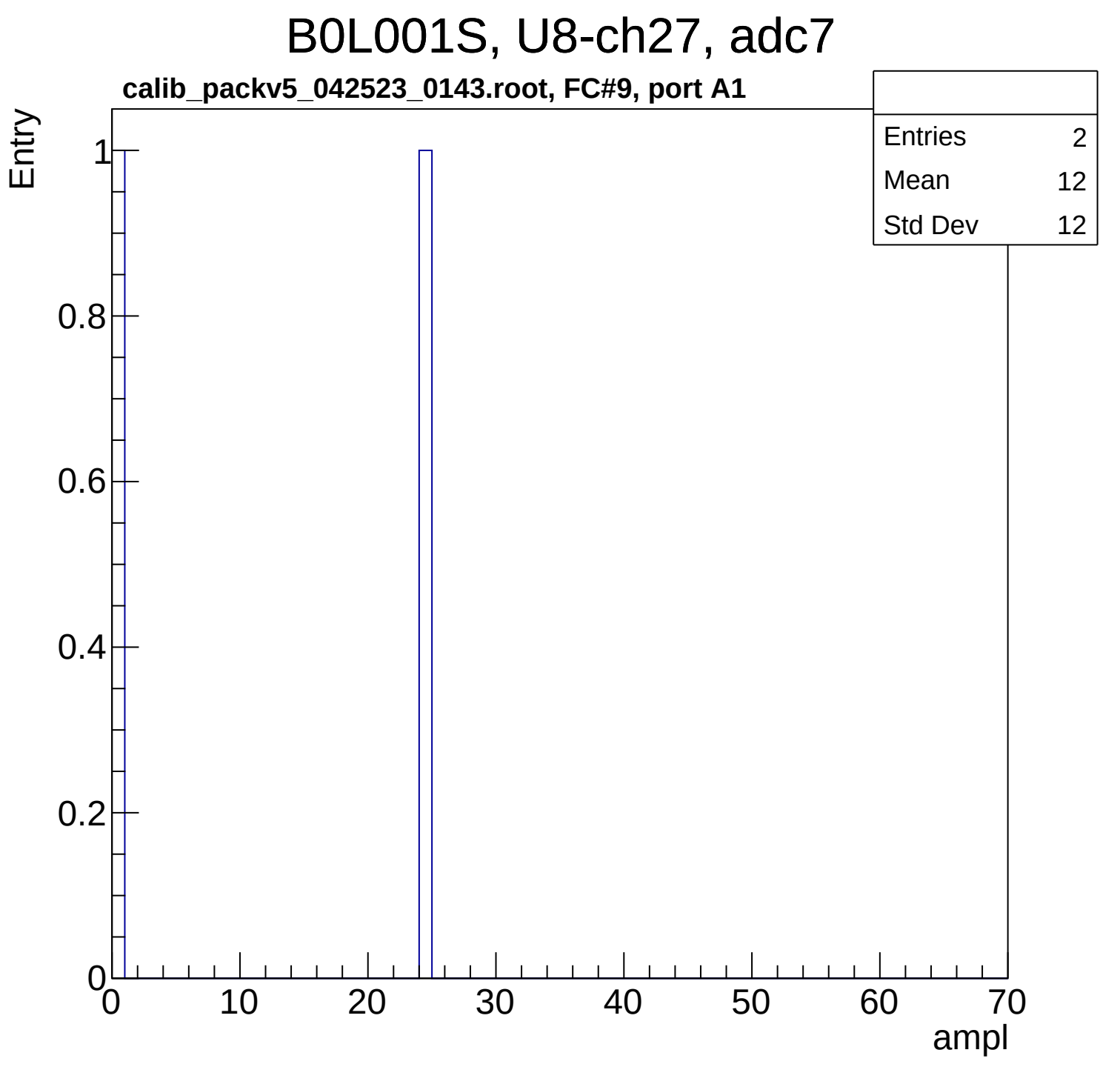
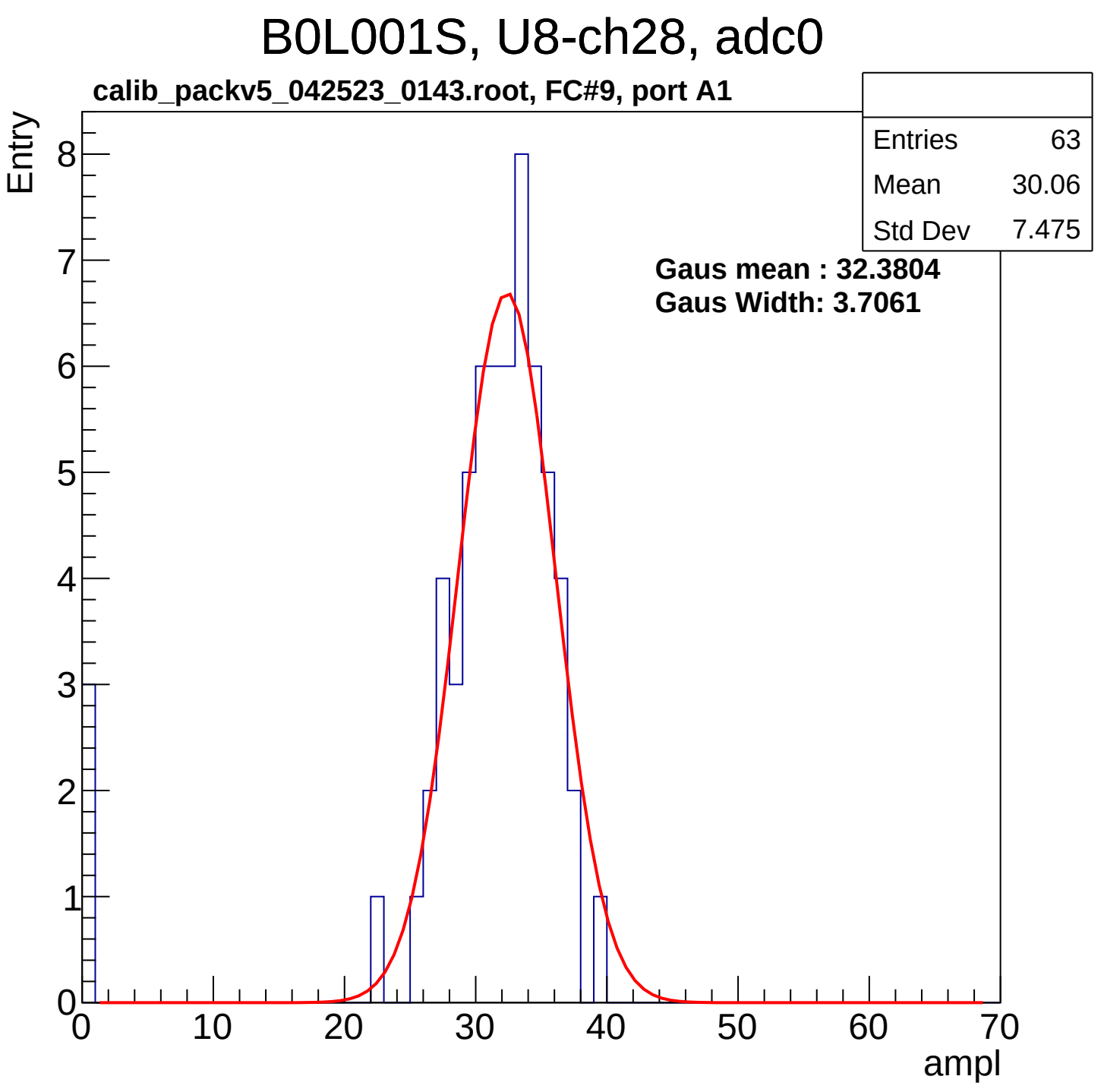
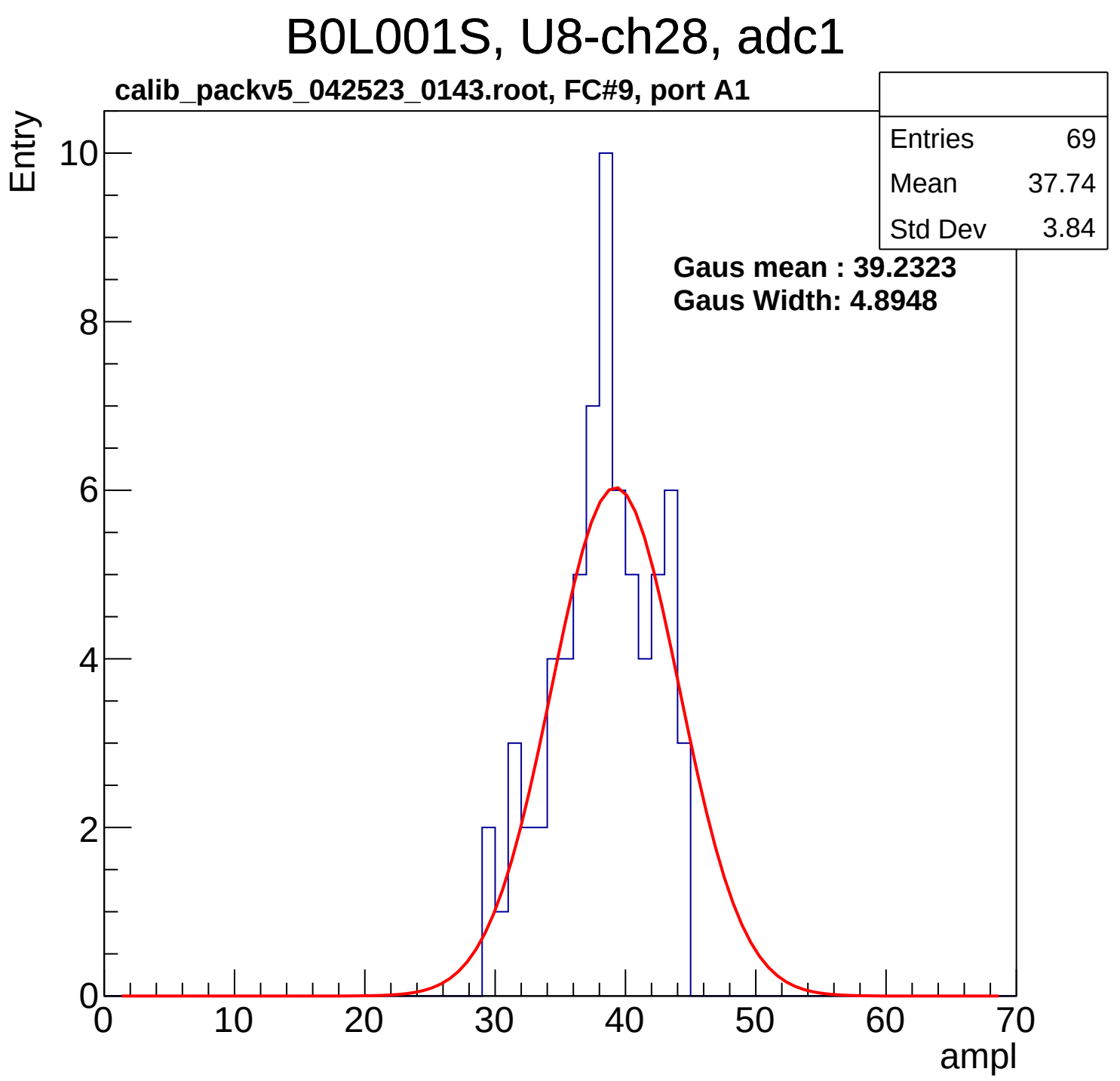

B0L001S, U8-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.27
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

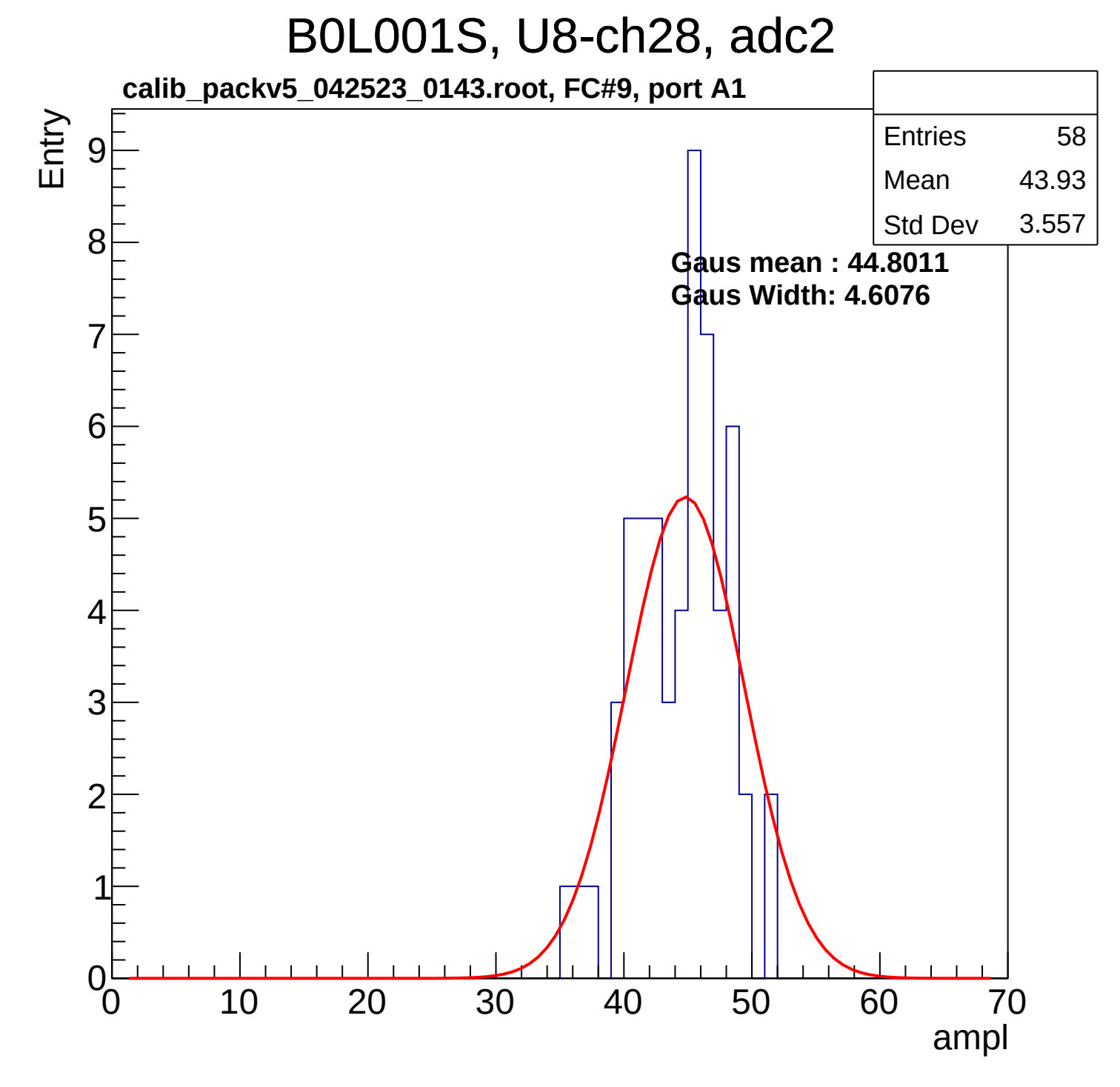

B0L001S, U8-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.97
Std Dev 10.78
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

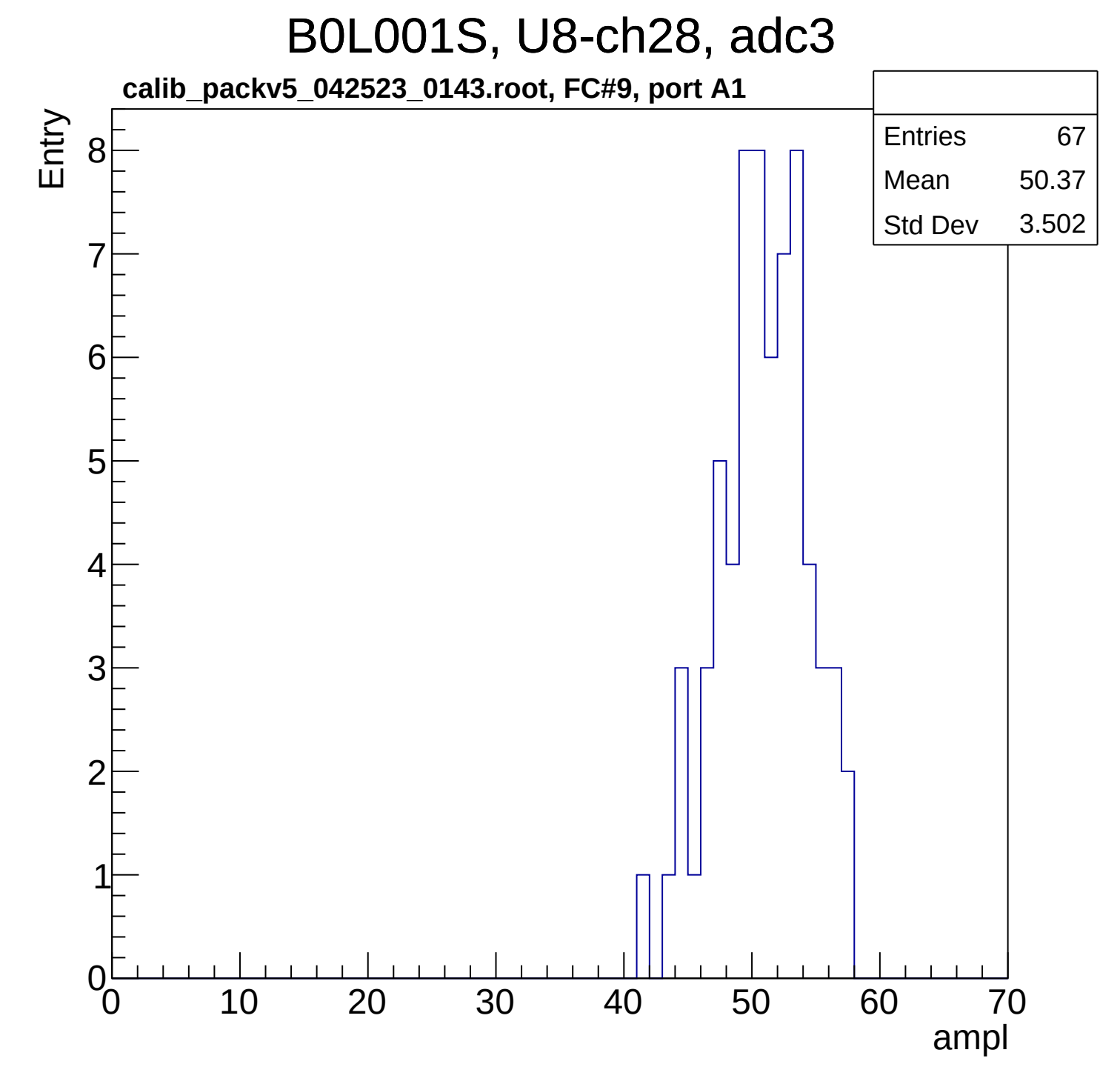

B0L001S, U8-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

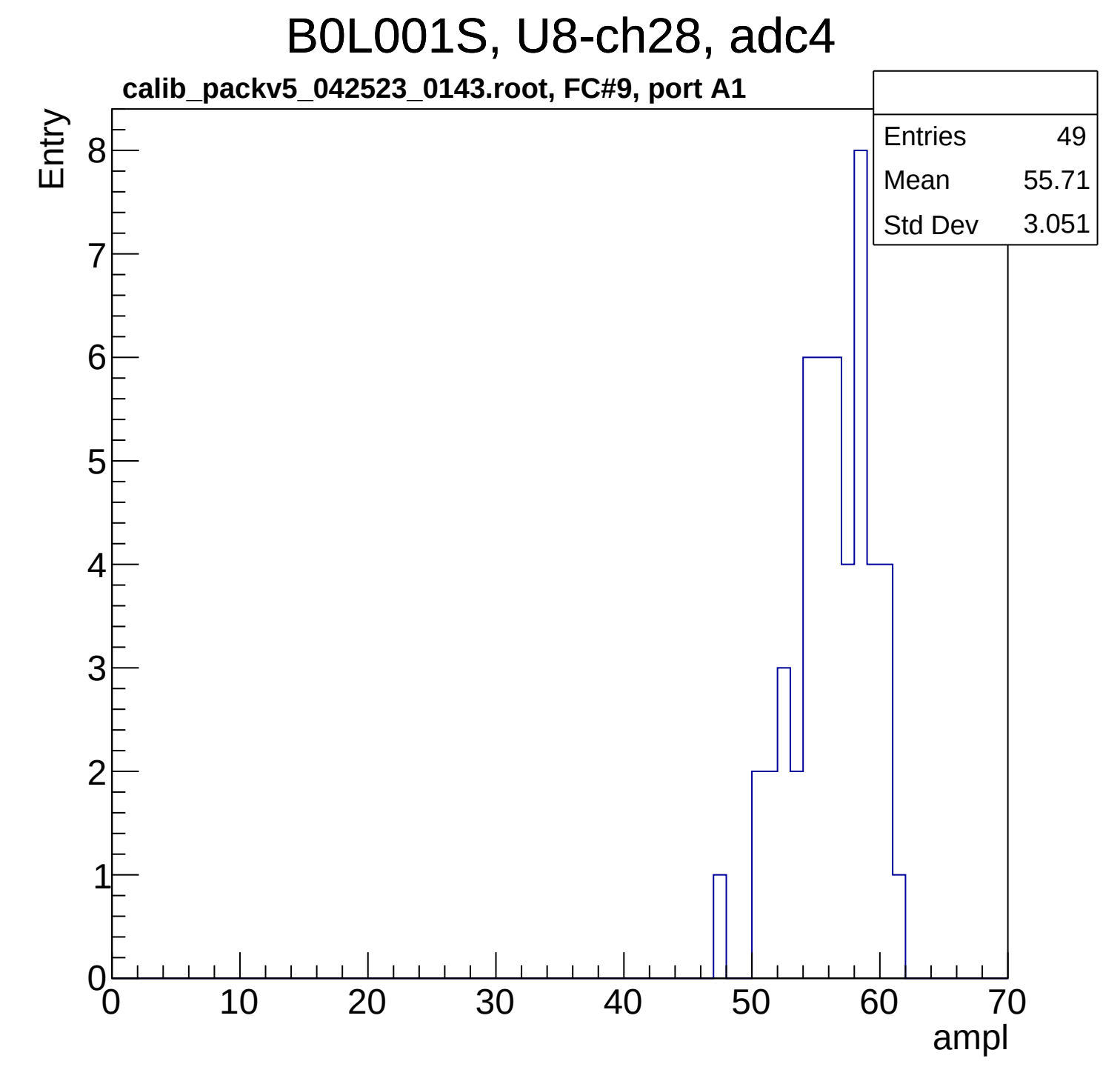

B0L001S, U8-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

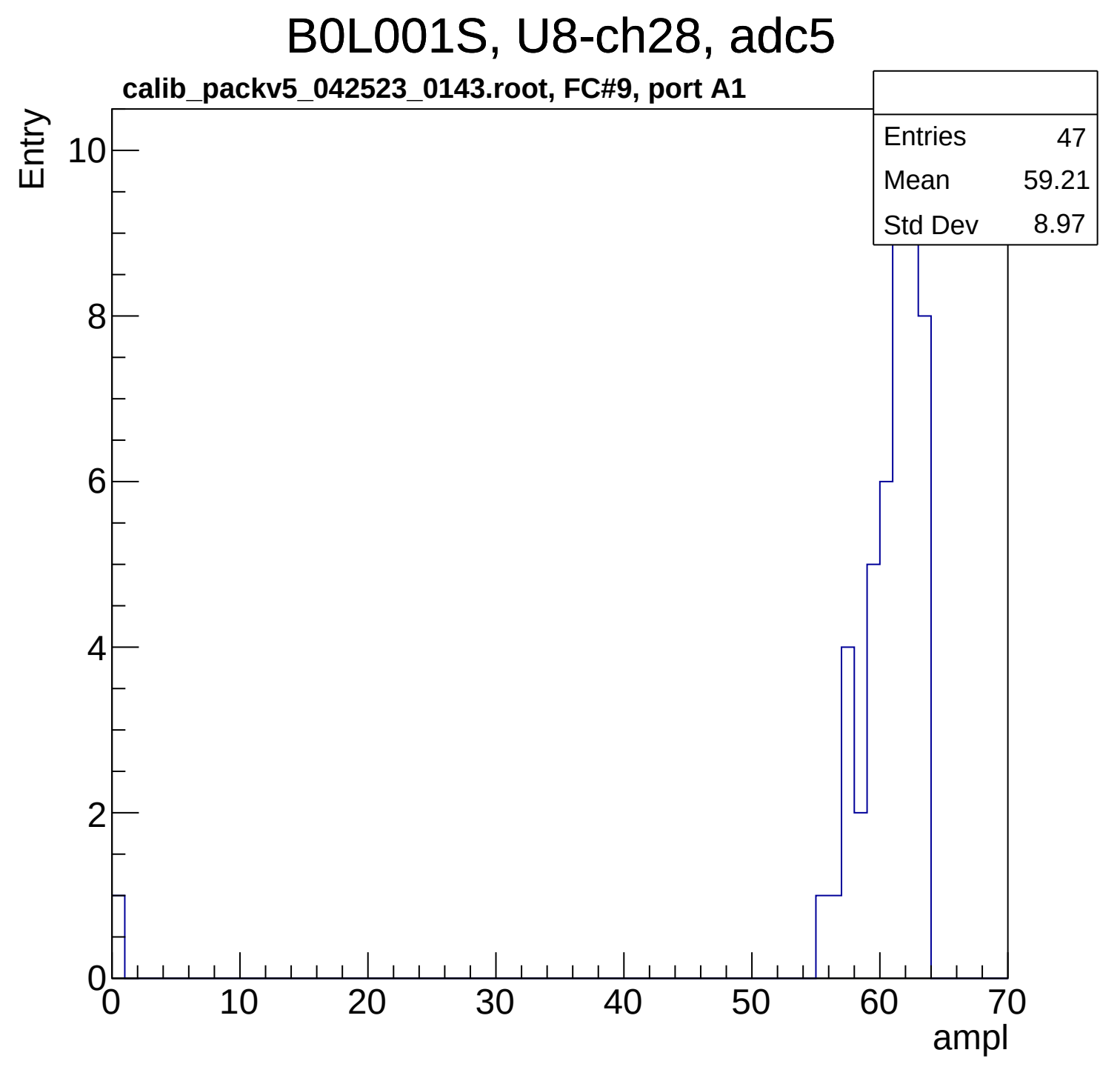

B0L001S, U8-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.06
Std Dev 7.475
Gaus mean : 32.3804
Gaus Width: 3.7061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

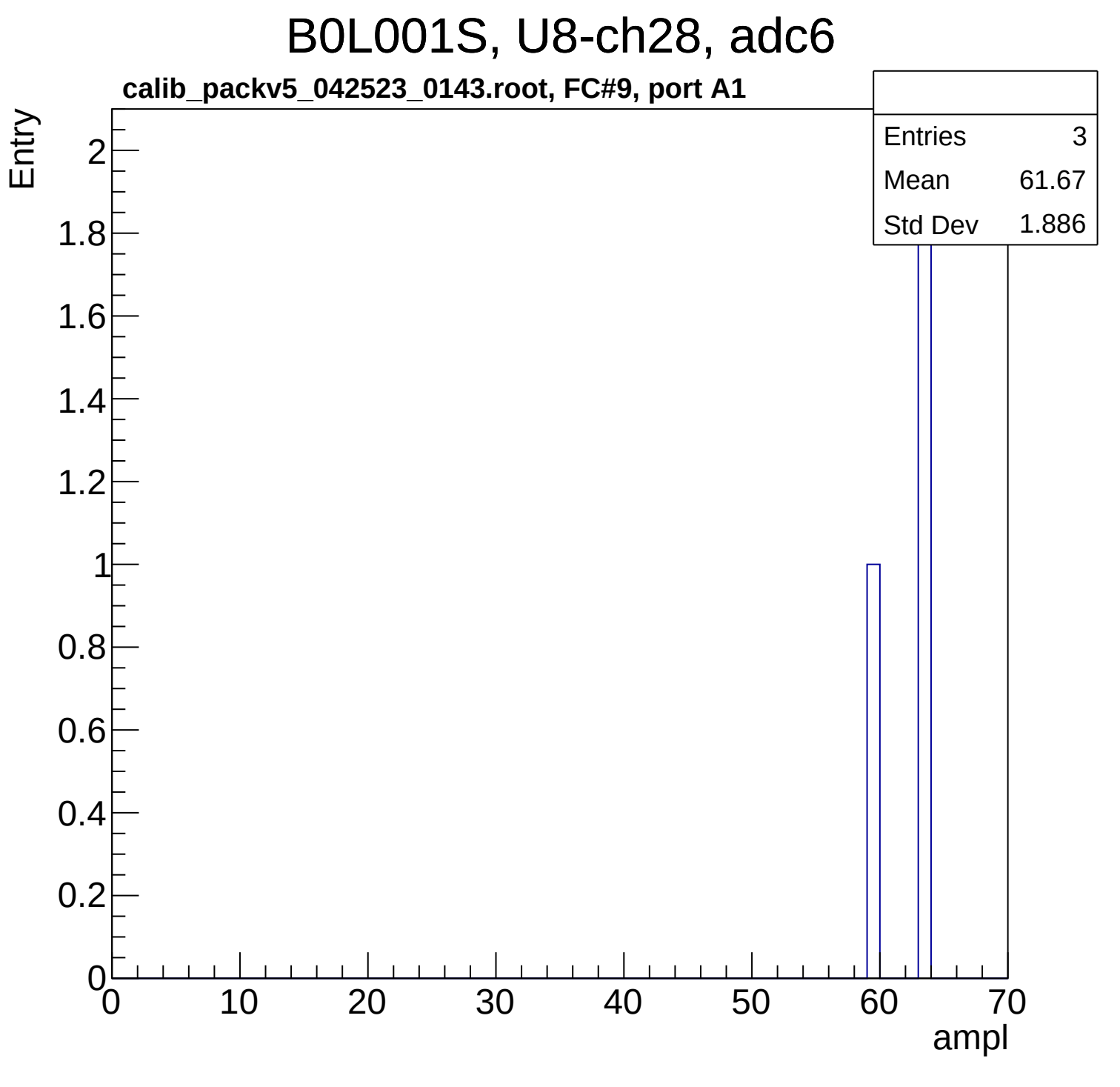

B0L001S, U8-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.74
Std Dev 3.84
Gaus mean : 39.2323
Gaus Width: 4.8948
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

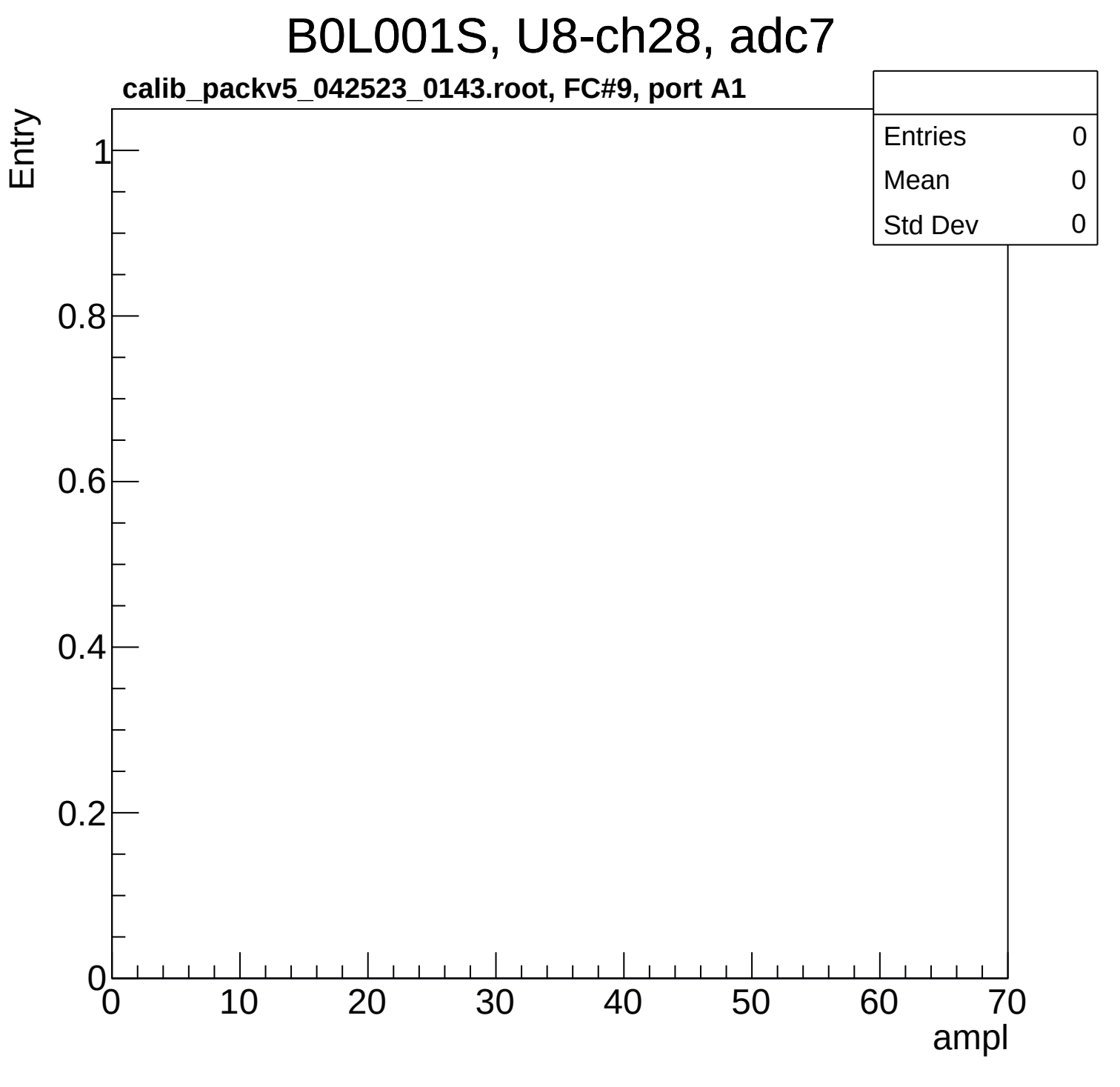

B0L001S, U8-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.93
Std Dev 3.557
Gaus mean : 44.8011
Gaus Width: 4.6076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

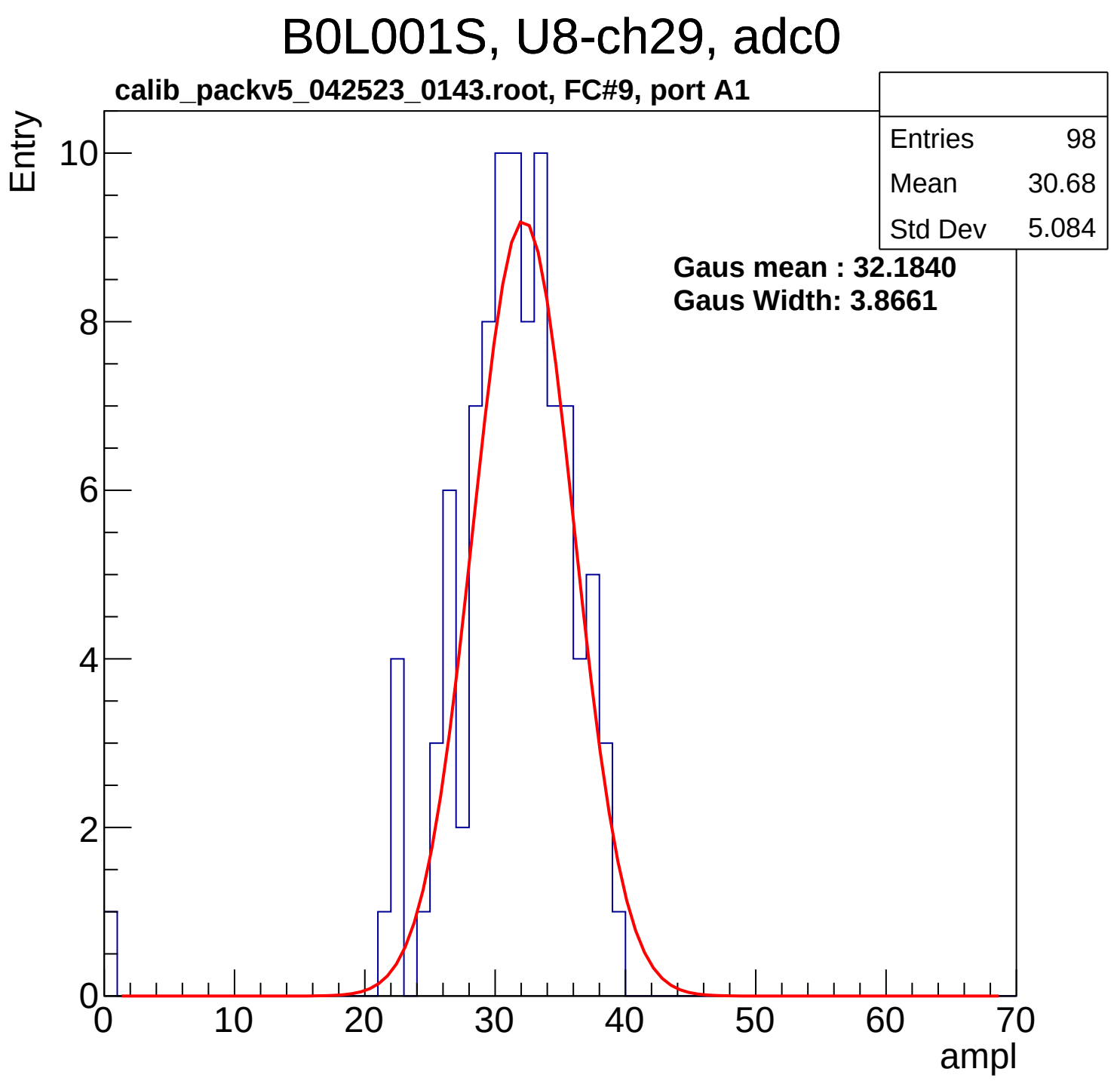

B0L001S, U8-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.37
Std Dev 3.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

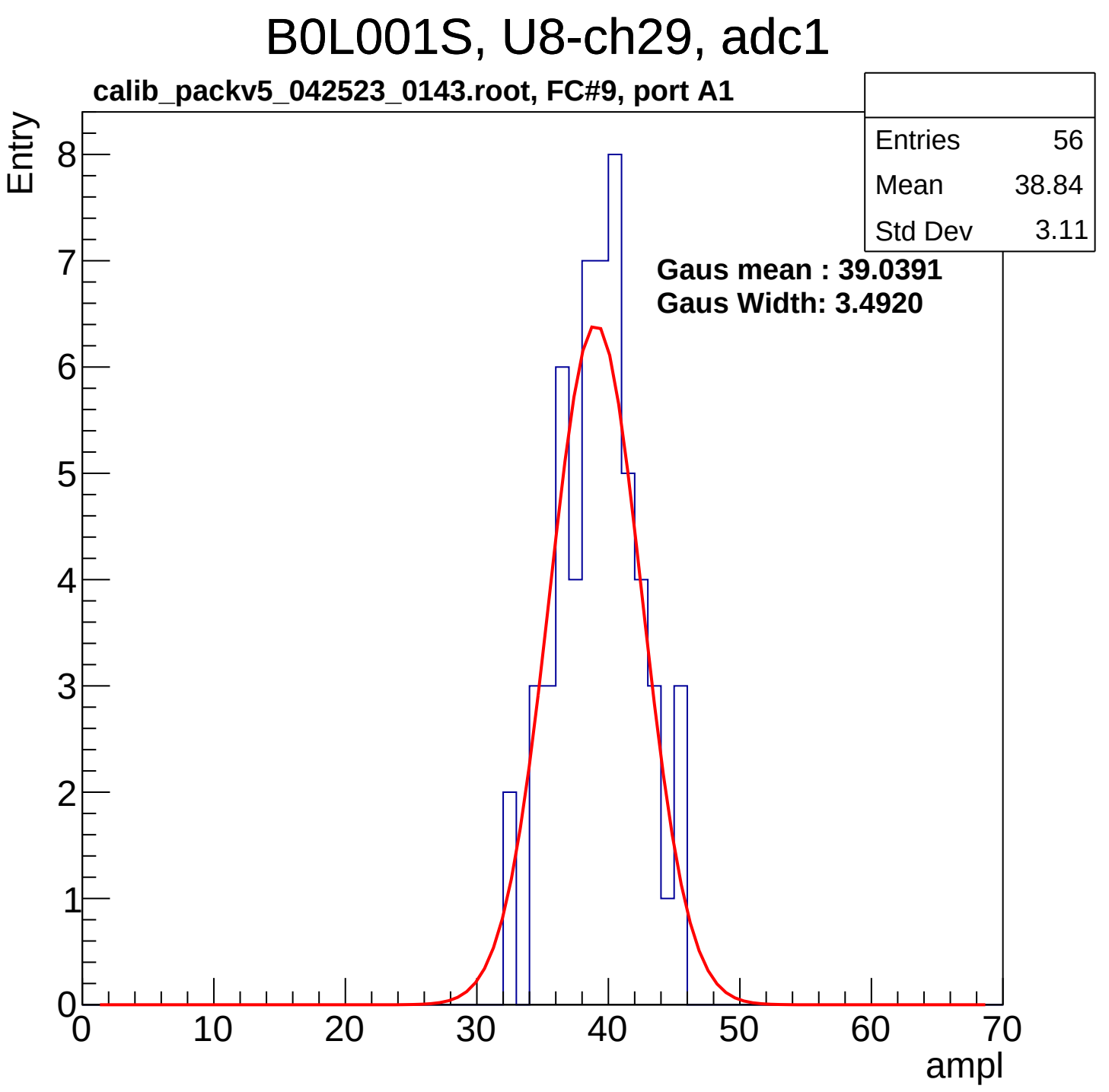

B0L001S, U8-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 55.71
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

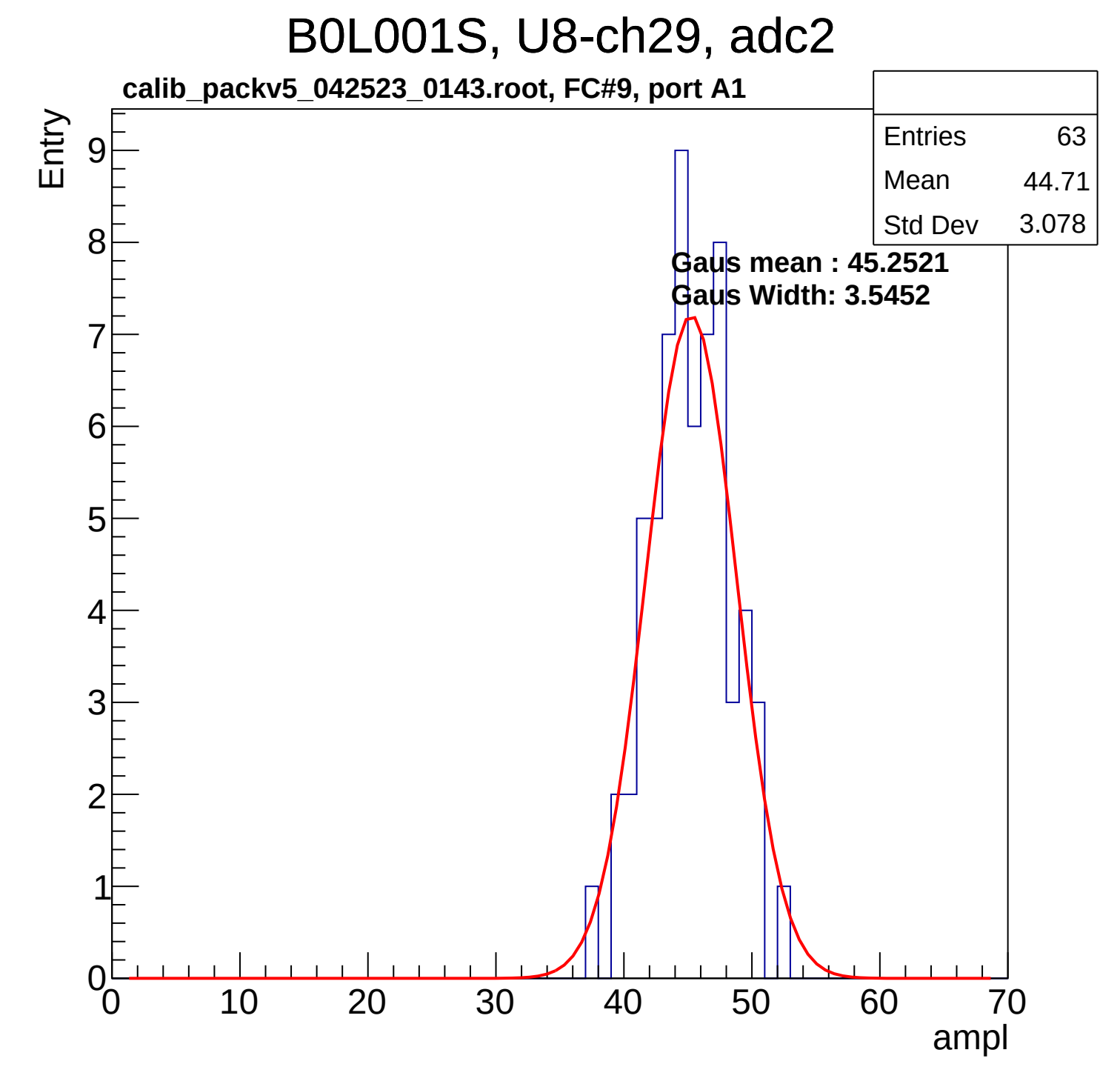

B0L001S, U8-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.21
Std Dev 8.97
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

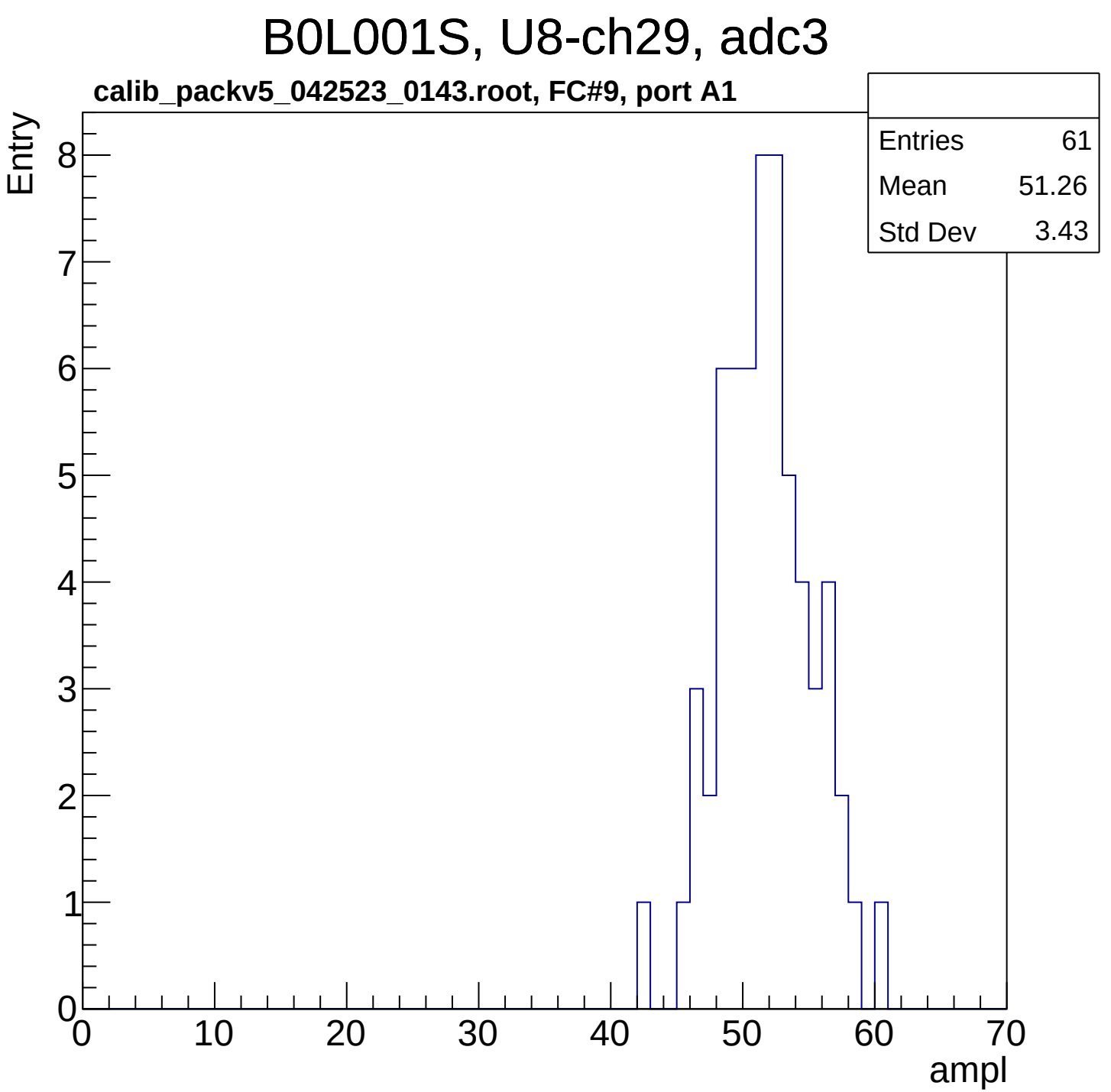

B0L001S, U8-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 98
Mean 30.68
Std Dev 5.084
Gaus mean : 32.1840
Gaus Width: 3.8661
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 38.84
Std Dev 3.11
Gaus mean : 39.0391
Gaus Width: 3.4920
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.71
Std Dev 3.078
Gaus mean : 45.2521
Gaus Width: 3.5452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.26
Std Dev 3.43
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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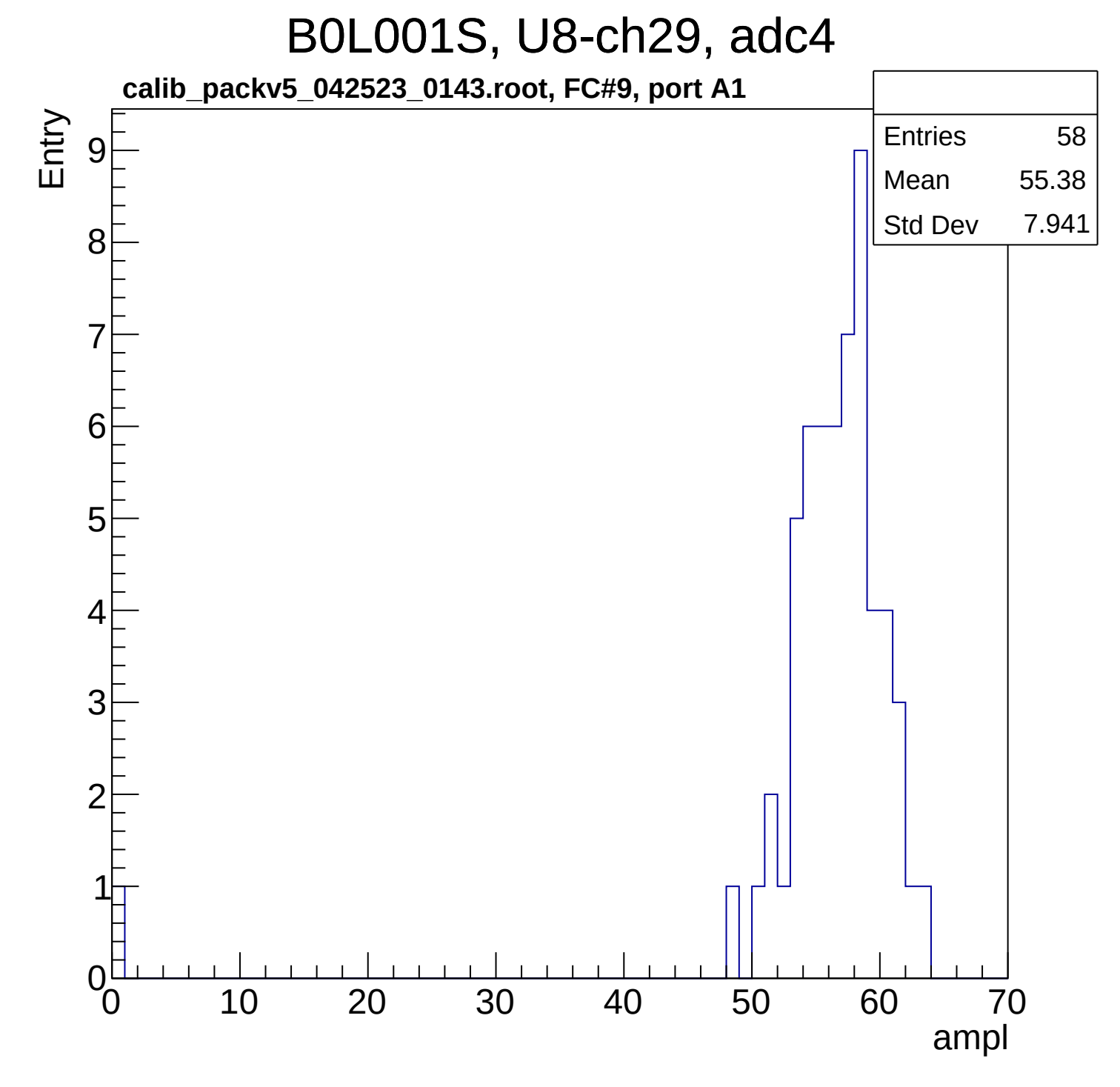
40
50
60
70


B0L001S, U8-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.38
Std Dev 7.941
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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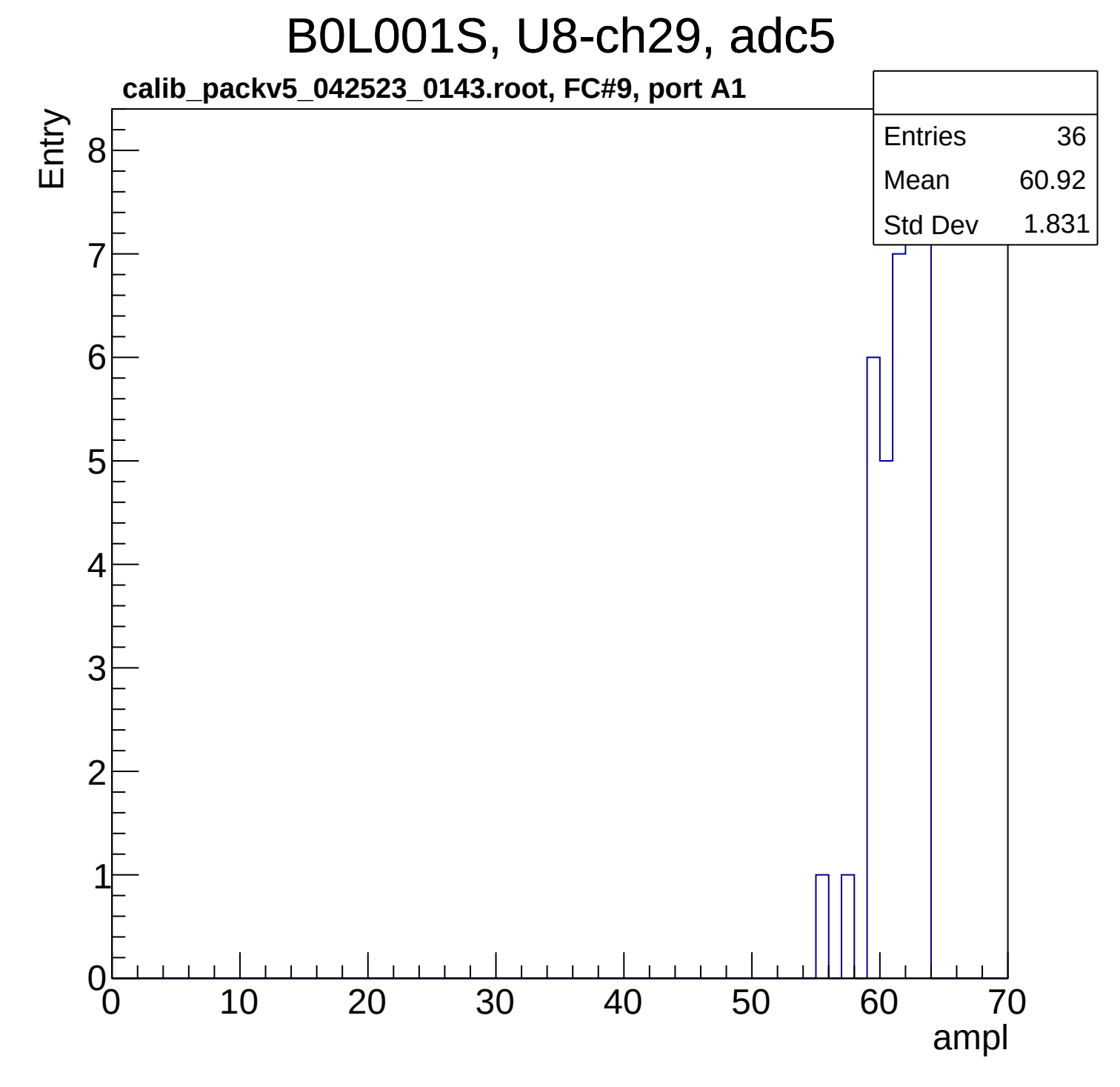
B0L001S, U8-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.92
Std Dev 1.831
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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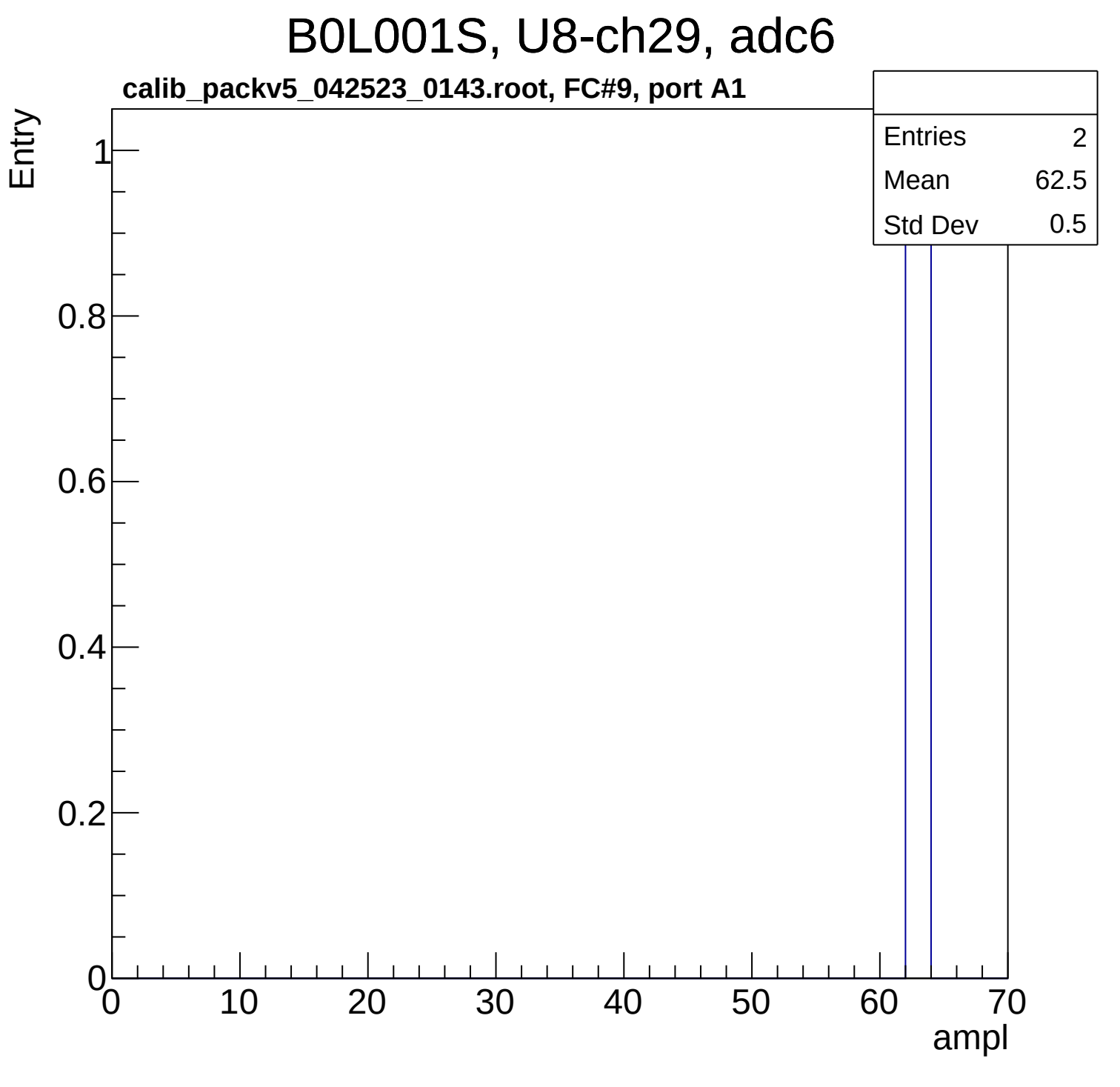
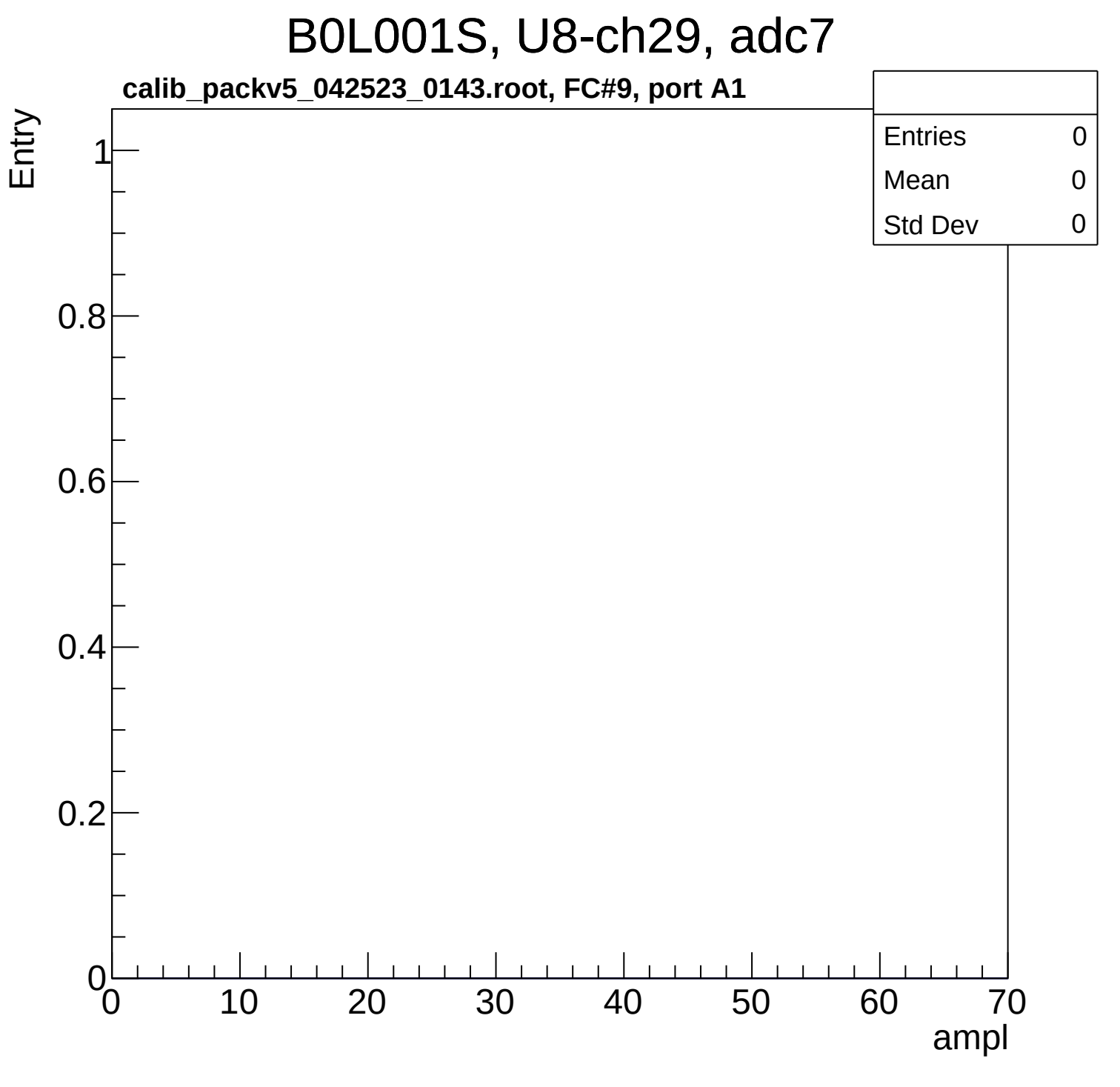

B0L001S, U8-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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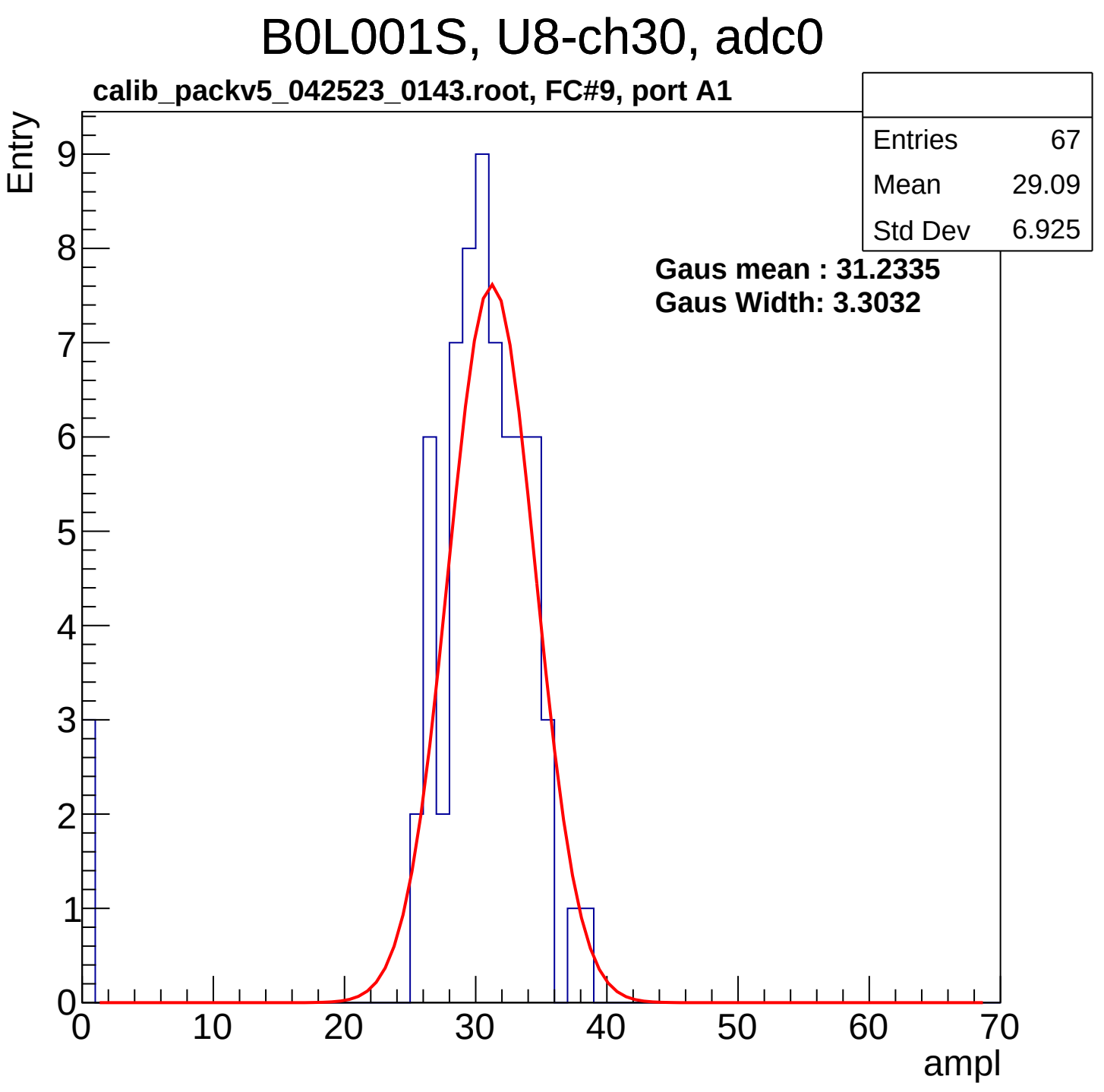
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.09
Std Dev 6.925
Gaus mean : 31.2335
Gaus Width: 3.3032
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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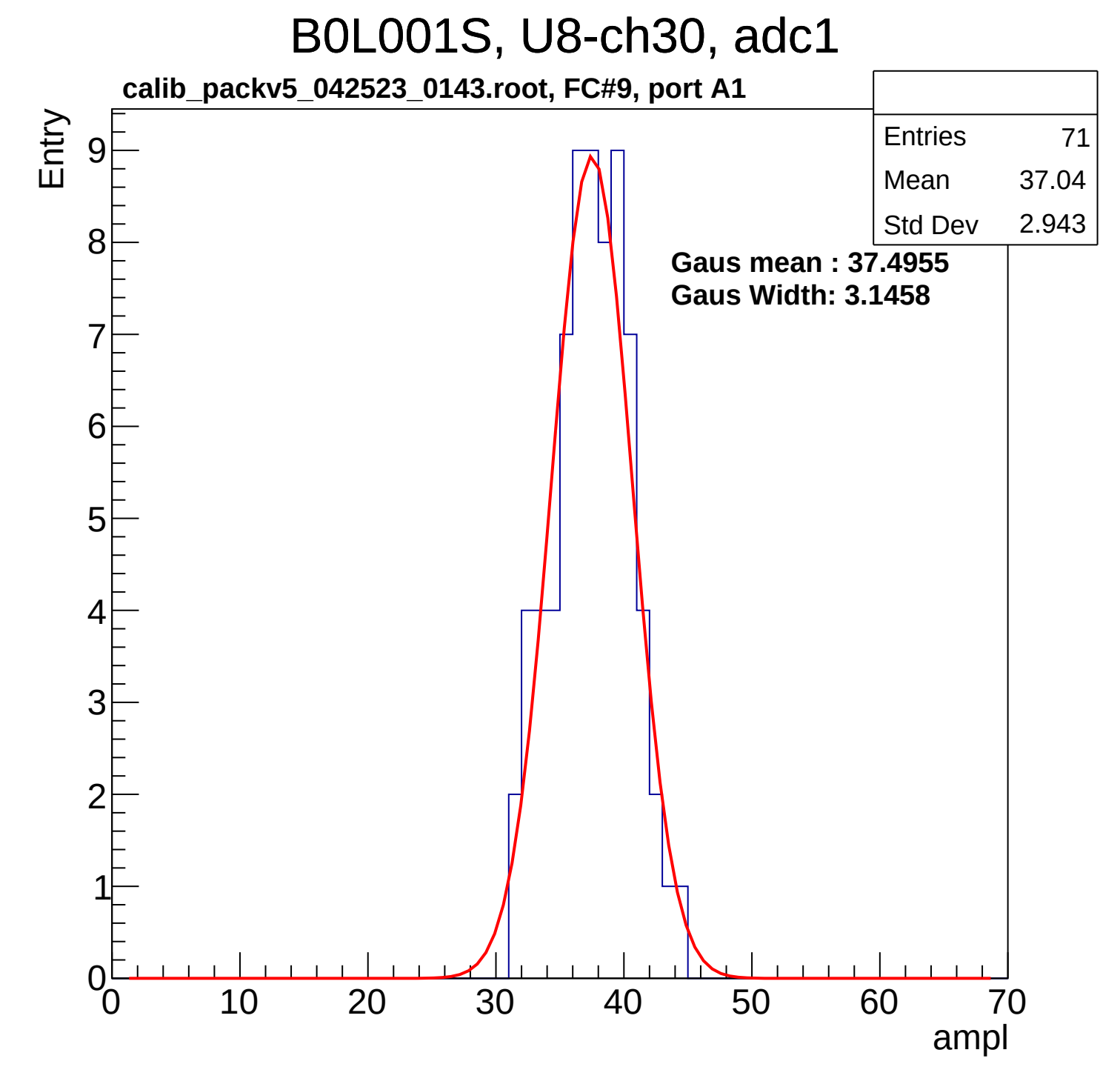
20
30
40
50
60
70


B0L001S, U8-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.04
Std Dev 2.943
Gaus mean : 37.4955
Gaus Width: 3.1458
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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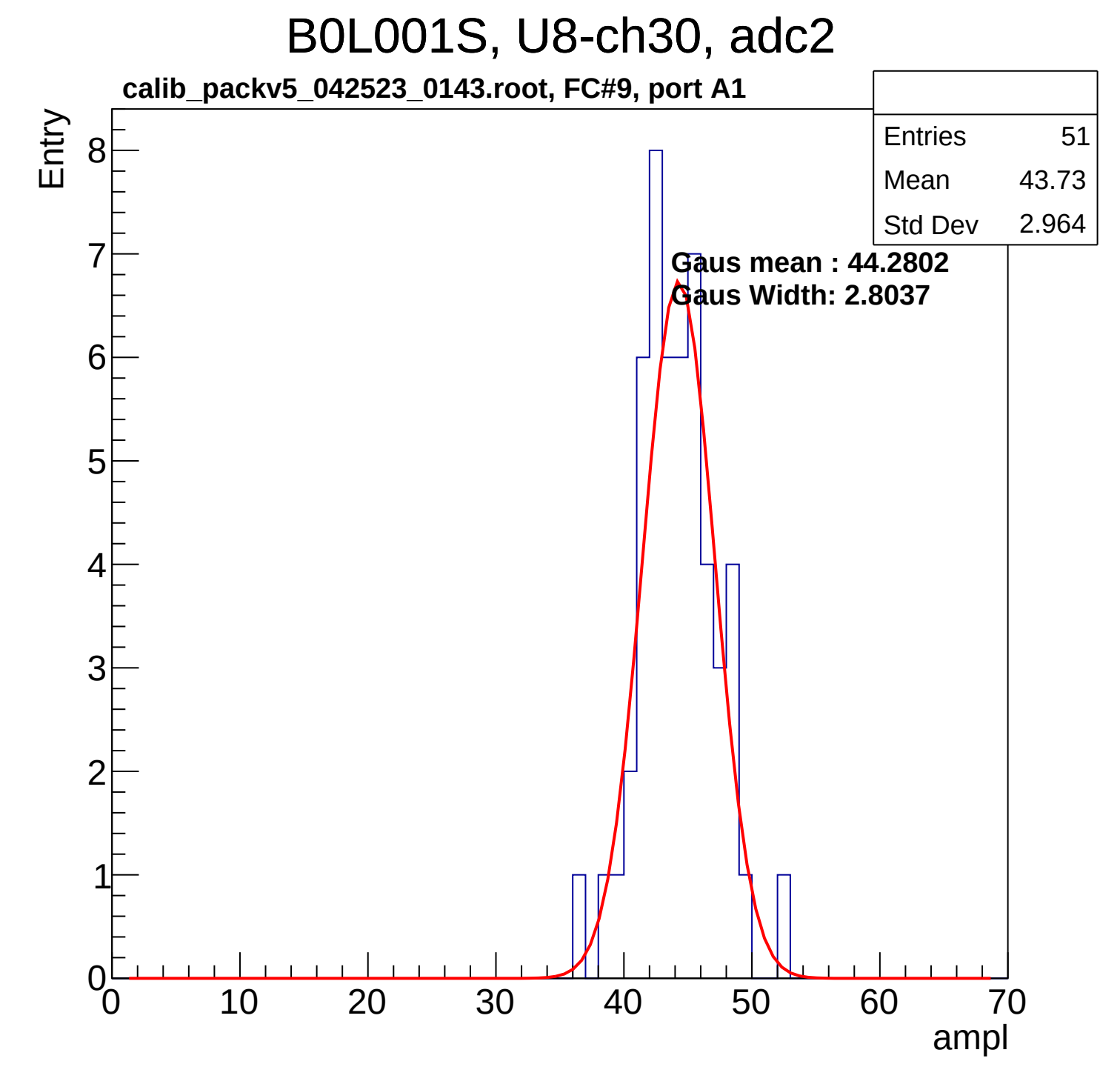
B0L001S, U8-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.73
Std Dev 2.964
Gaus mean : 44.2802
Gaus Width: 2.8037
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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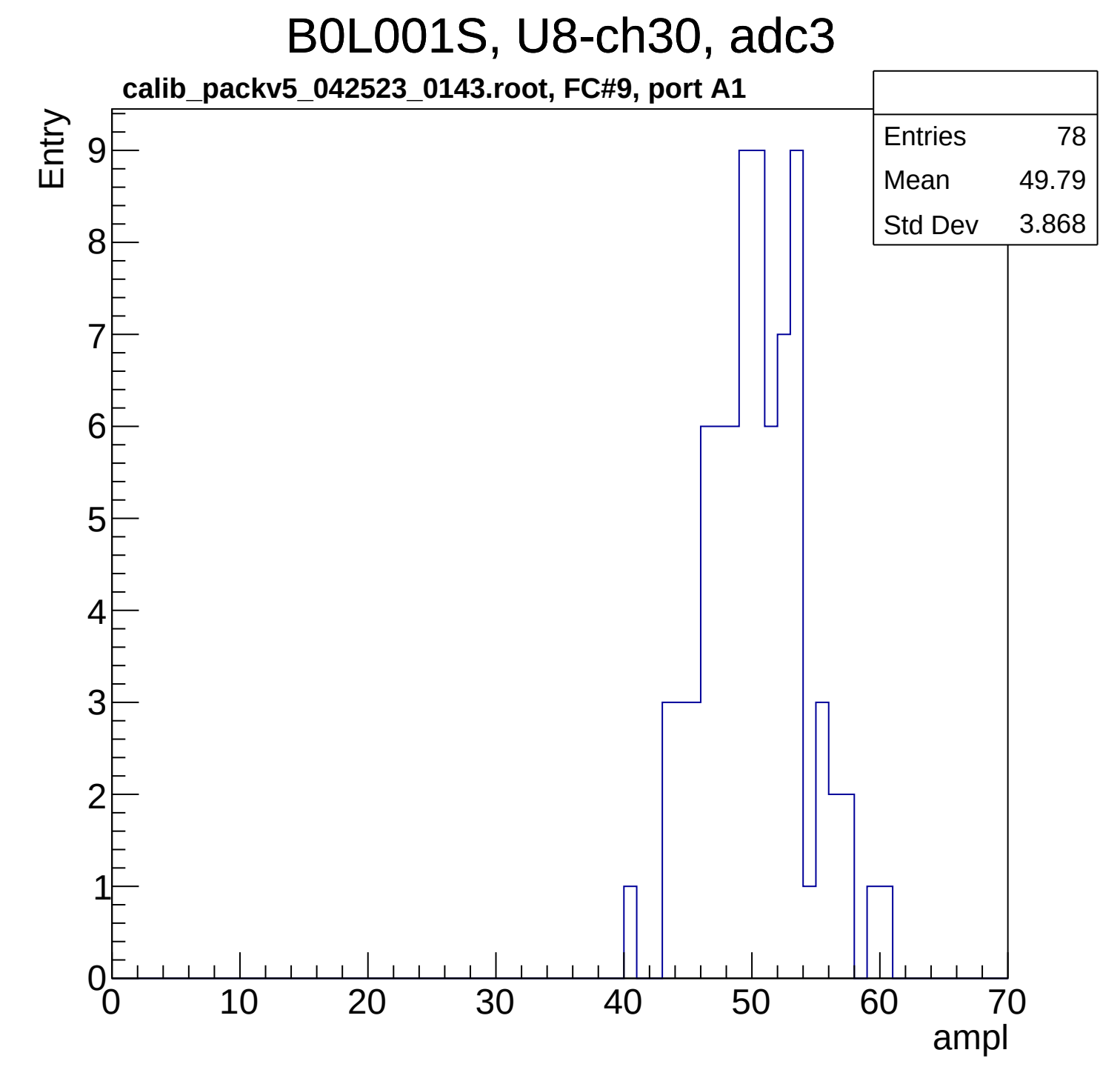
B0L001S, U8-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 49.79
Std Dev 3.868
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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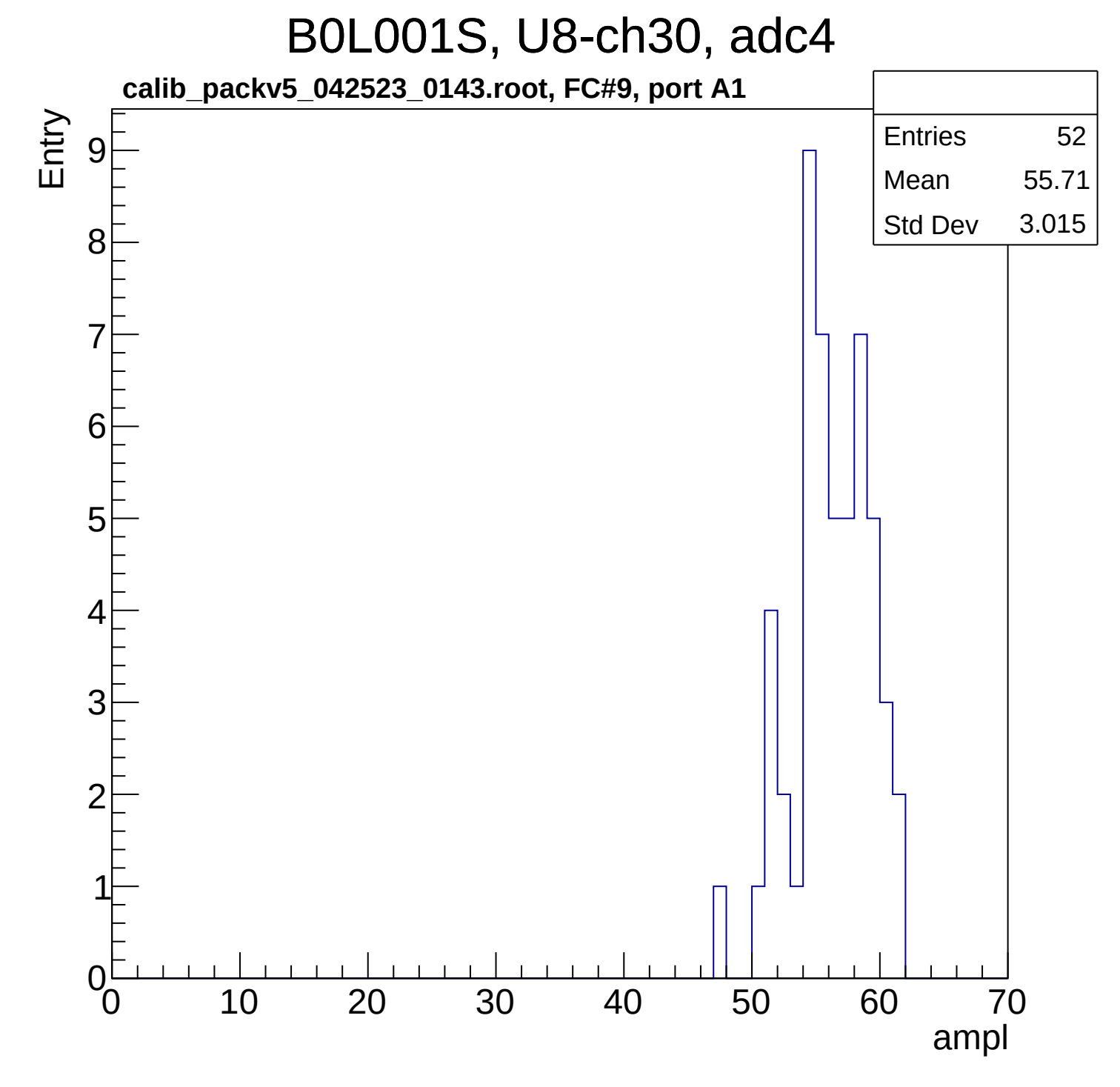
B0L001S, U8-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.71
Std Dev 3.015
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
30
40
50
60
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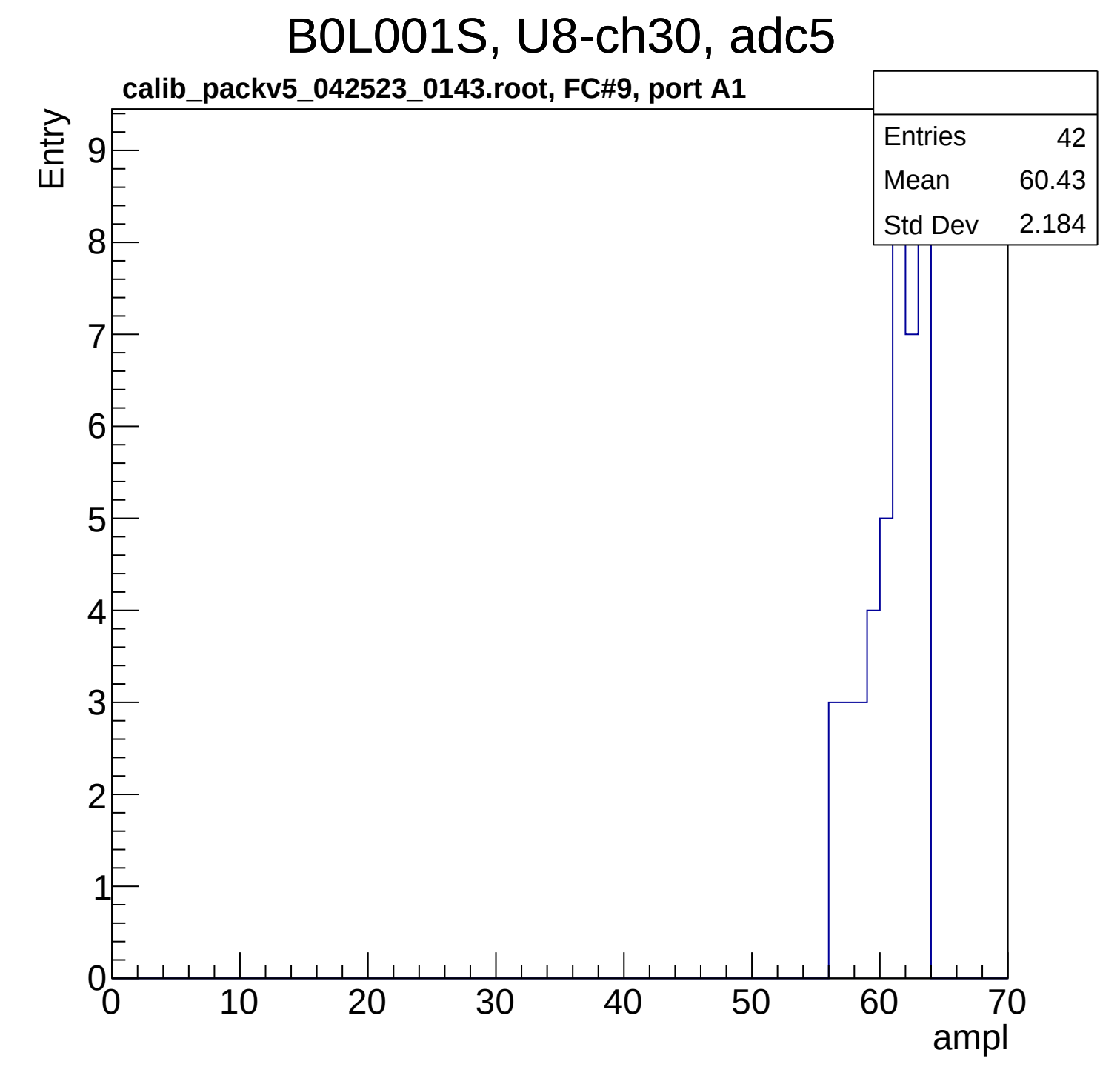
B0L001S, U8-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.43
Std Dev 2.184
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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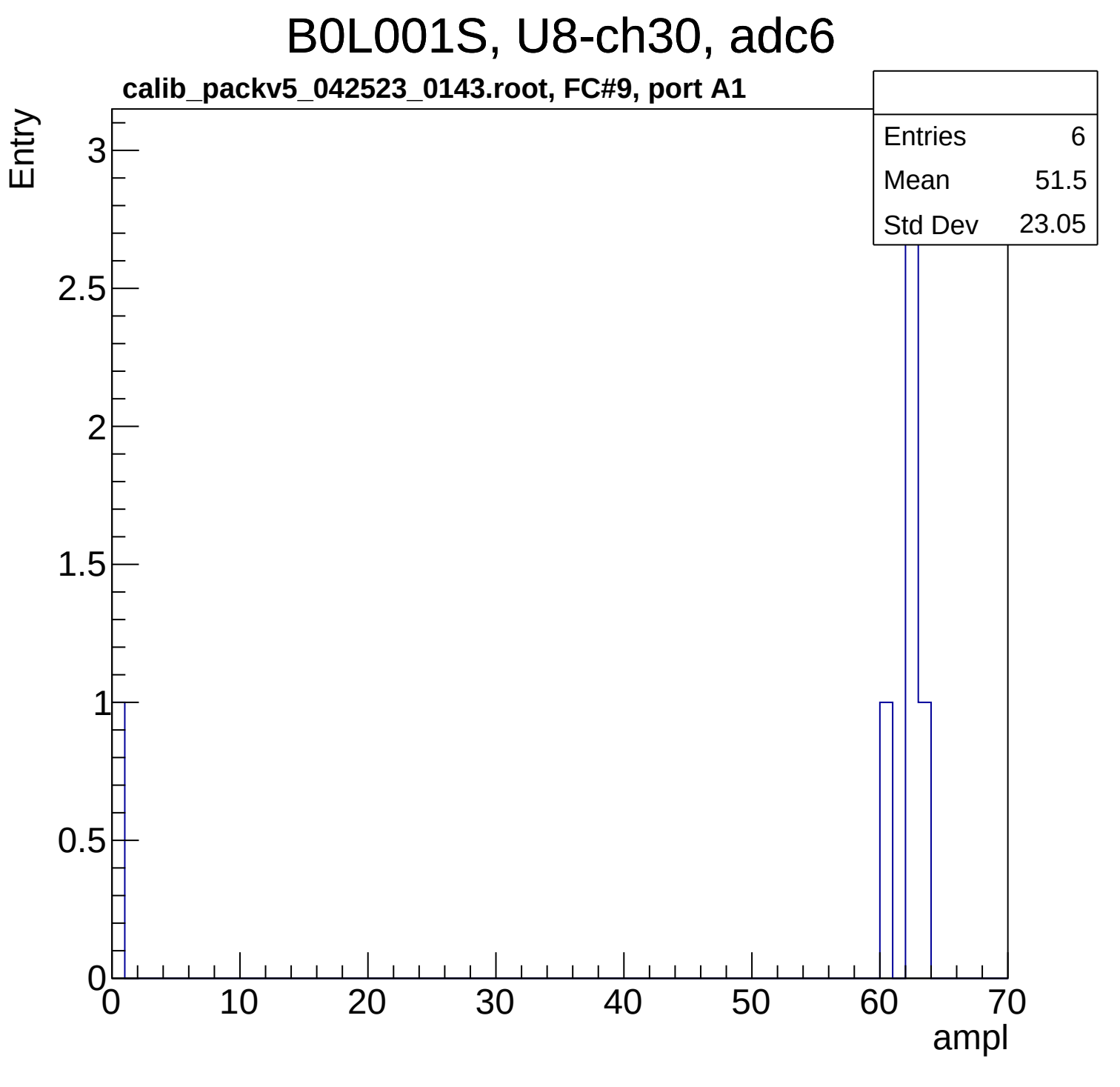
B0L001S, U8-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.5
Std Dev 23.05
Entry
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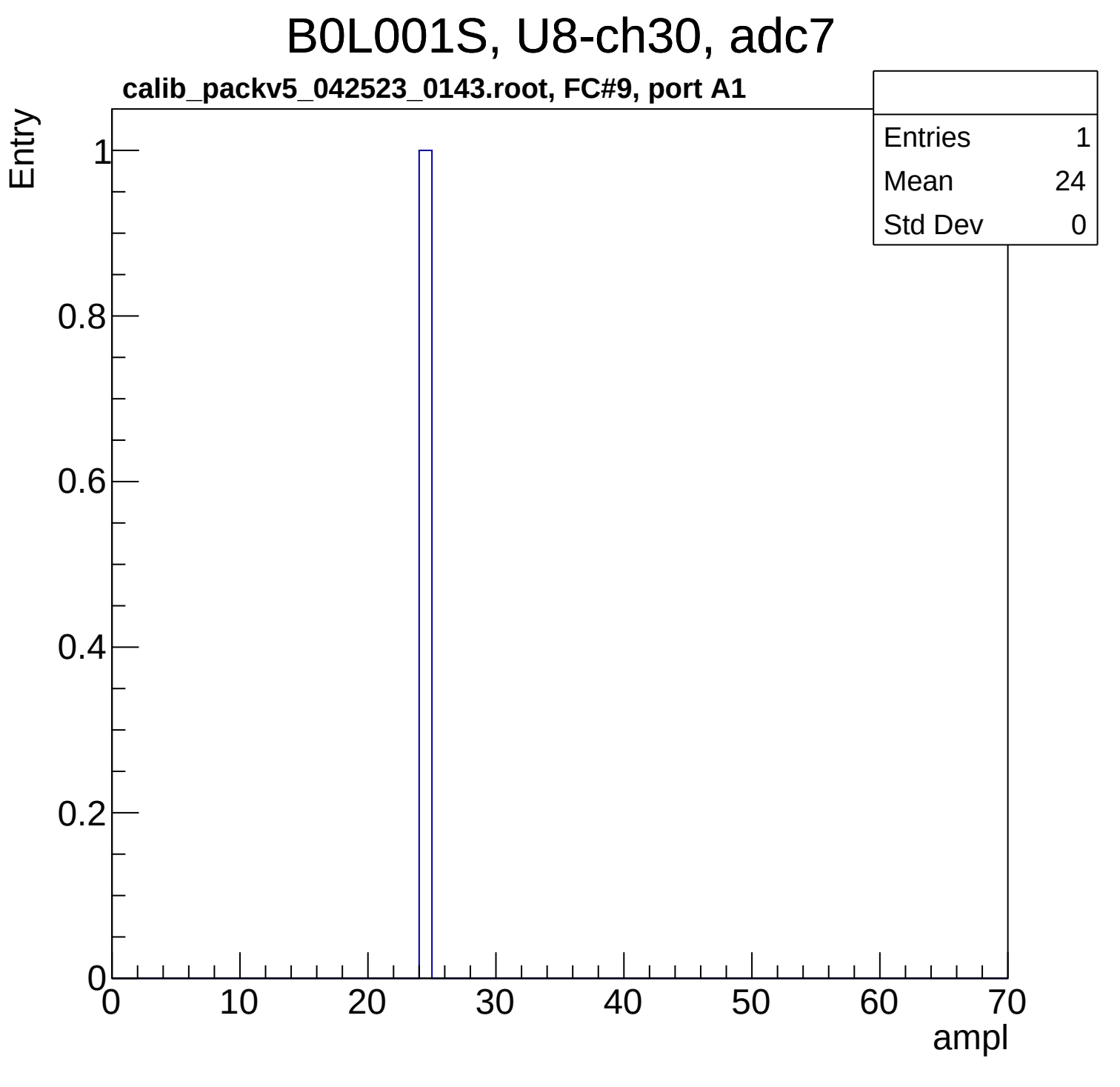
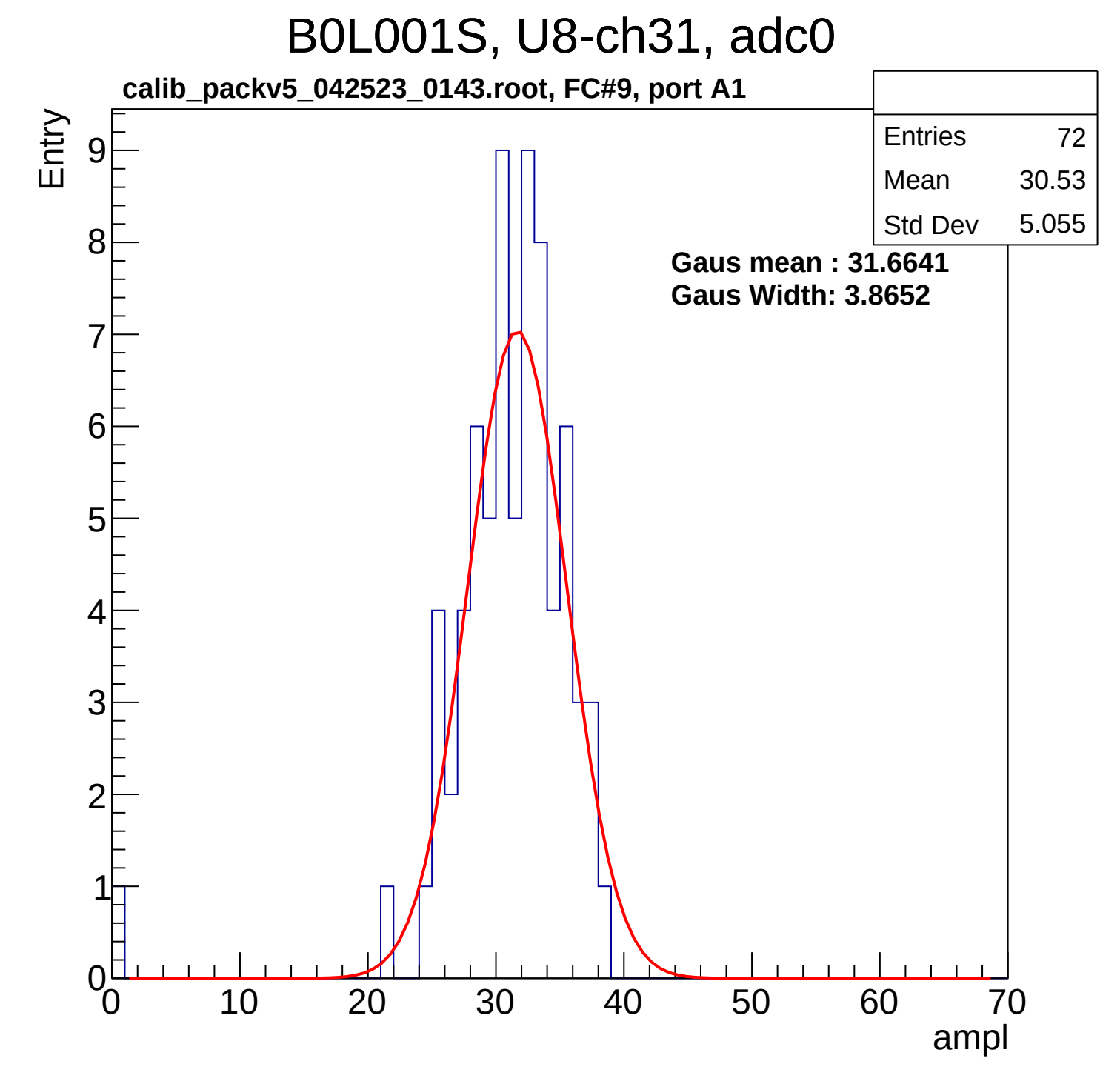
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.53
Std Dev 5.055
Gaus mean : 31.6641
Gaus Width: 3.8652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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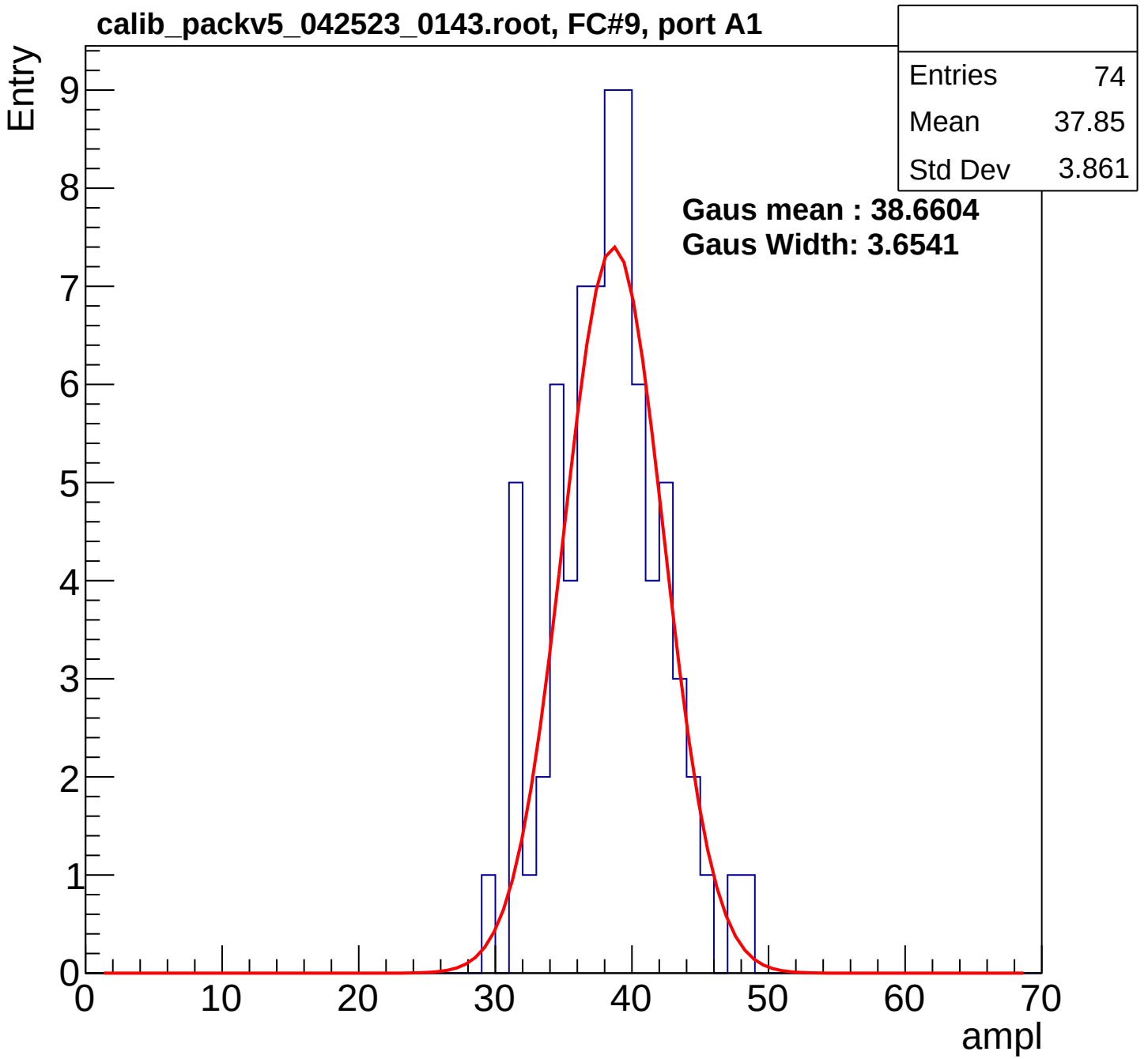
B0L001S, U8-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.85
Std Dev 3.861
Gaus mean : 38.6604
Gaus Width: 3.6541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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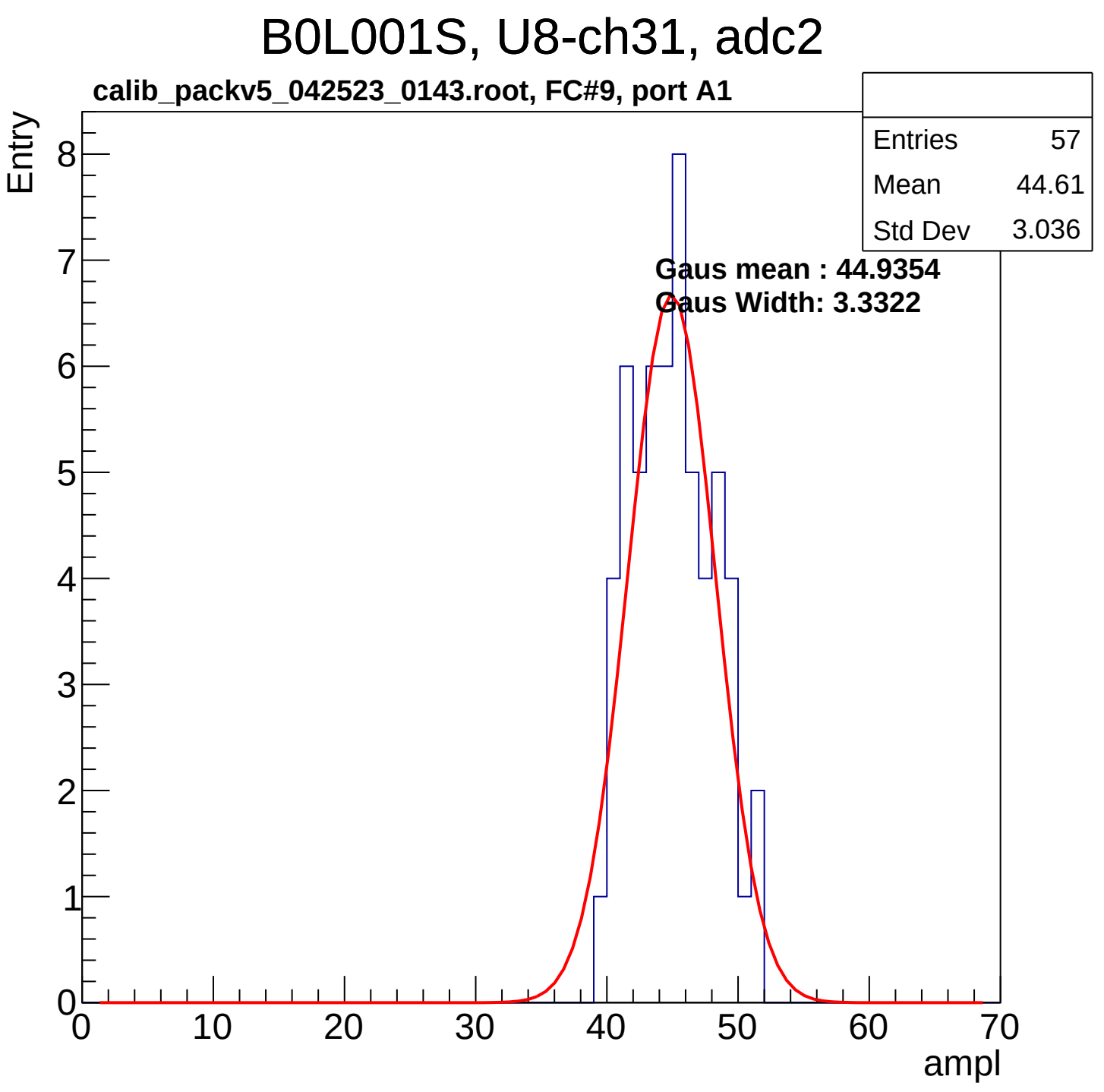
B0L001S, U8-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.61
Std Dev 3.036
Gaus mean : 44.9354
Gaus Width: 3.3322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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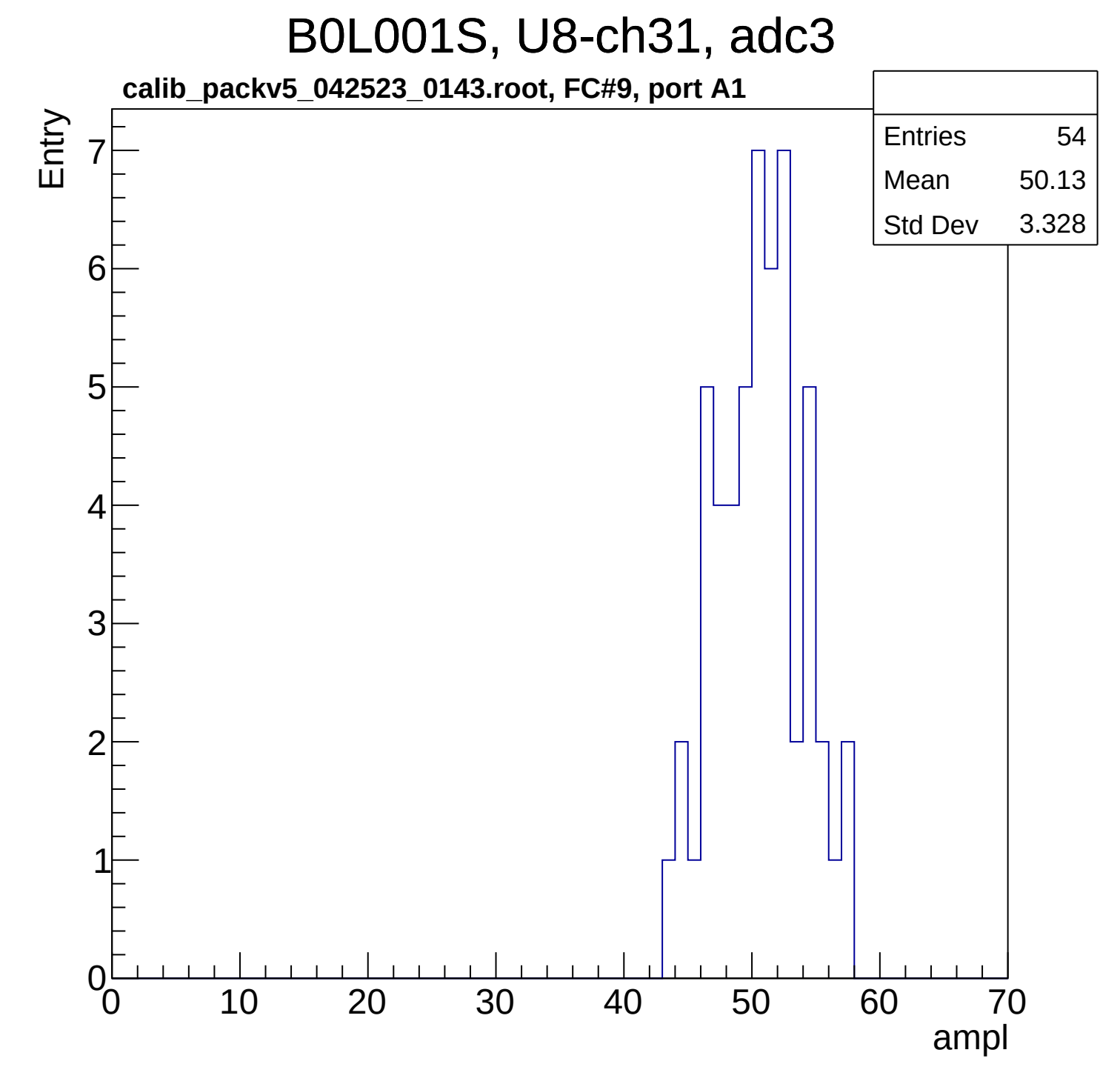
B0L001S, U8-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 50.13
Std Dev 3.328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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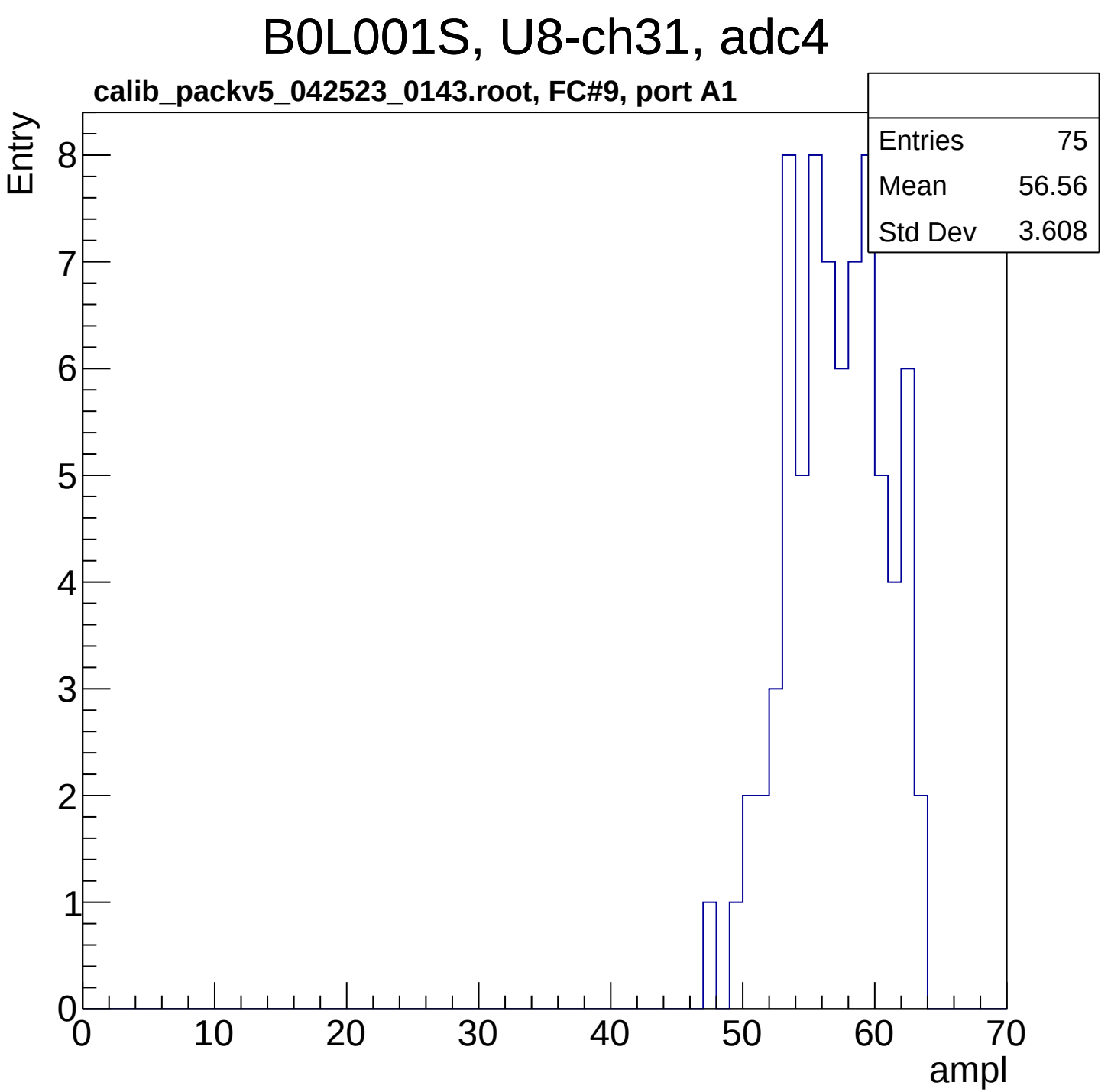
B0L001S, U8-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 56.56
Std Dev 3.608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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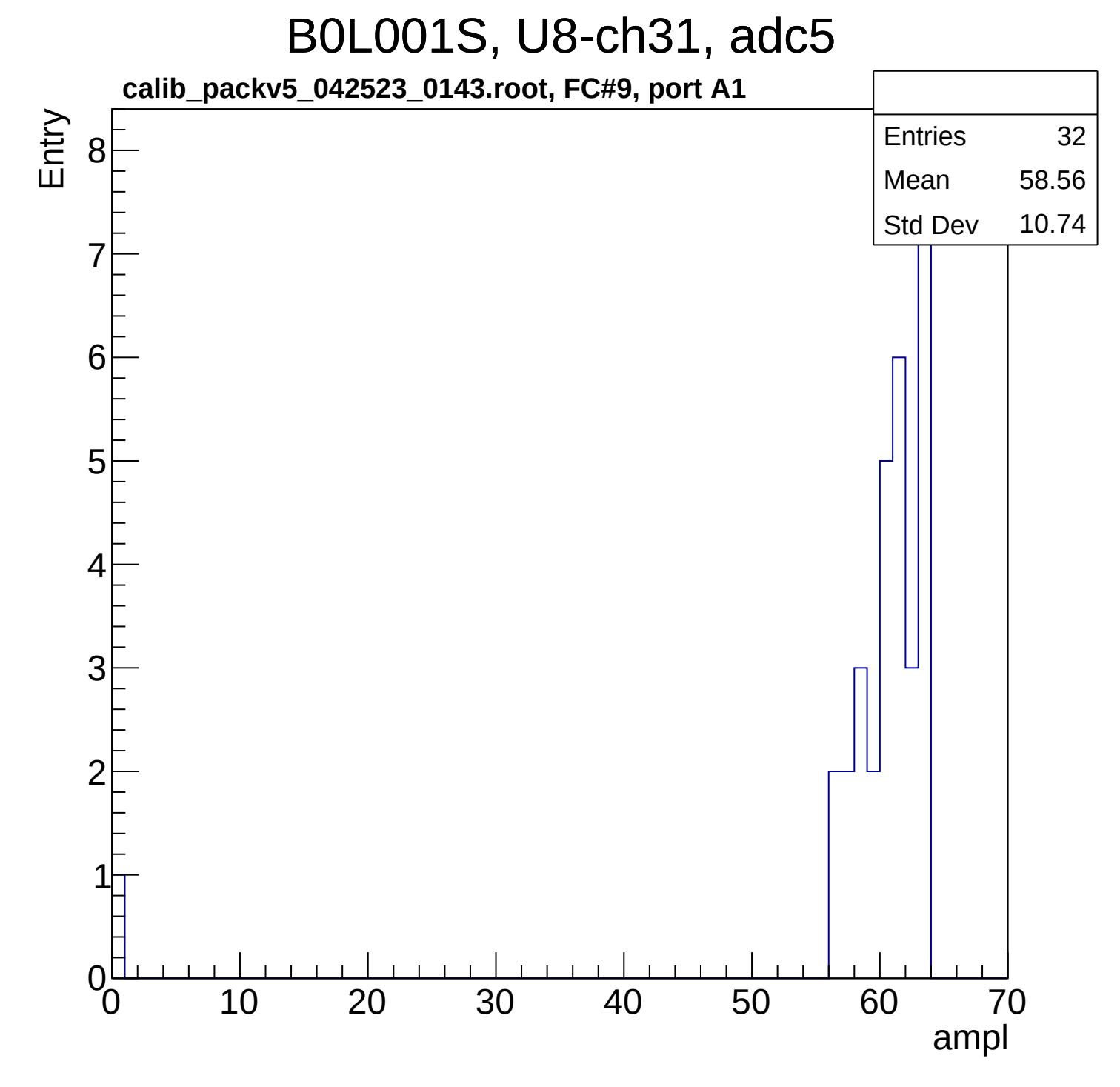
B0L001S, U8-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.56
Std Dev 10.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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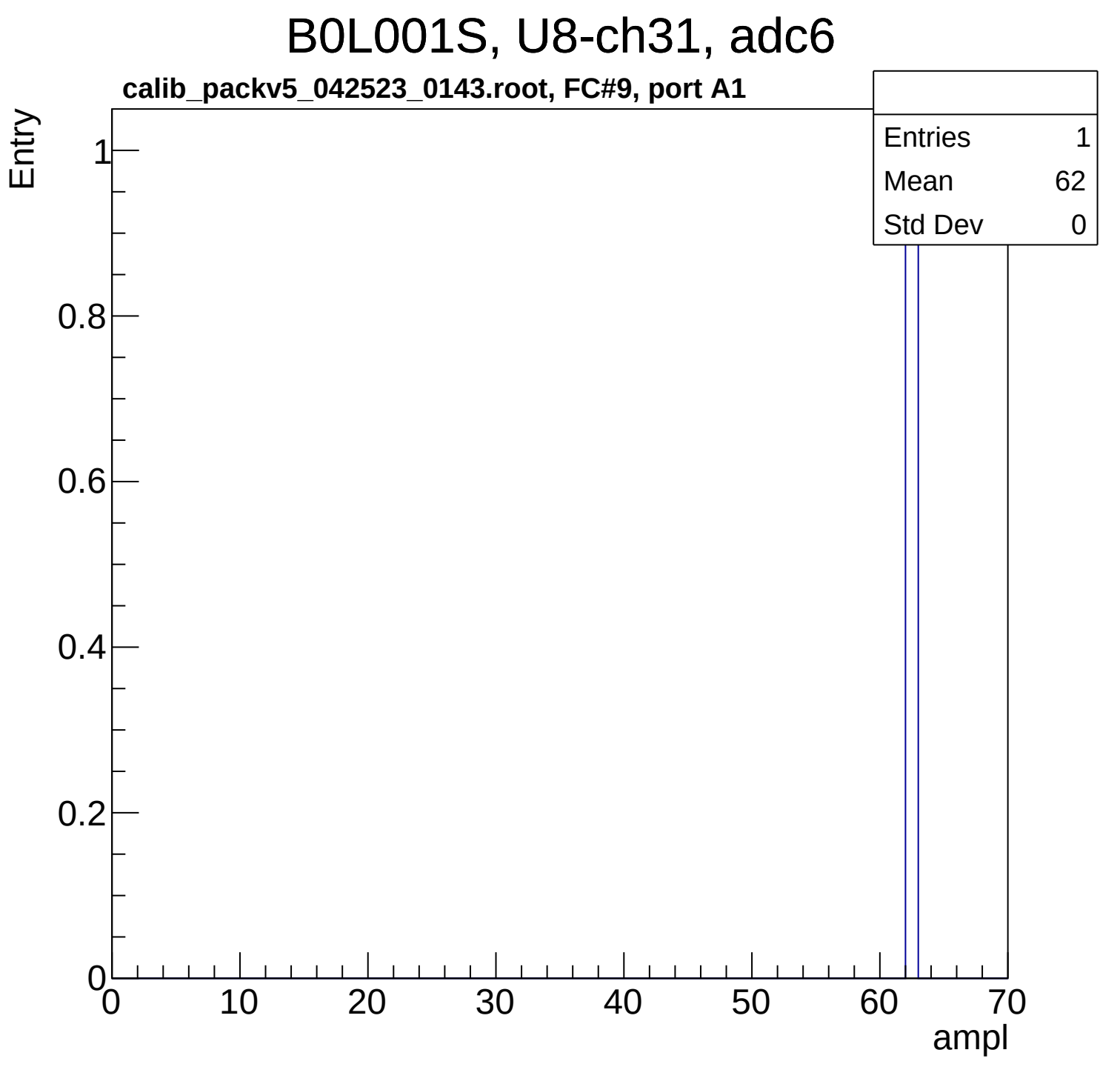
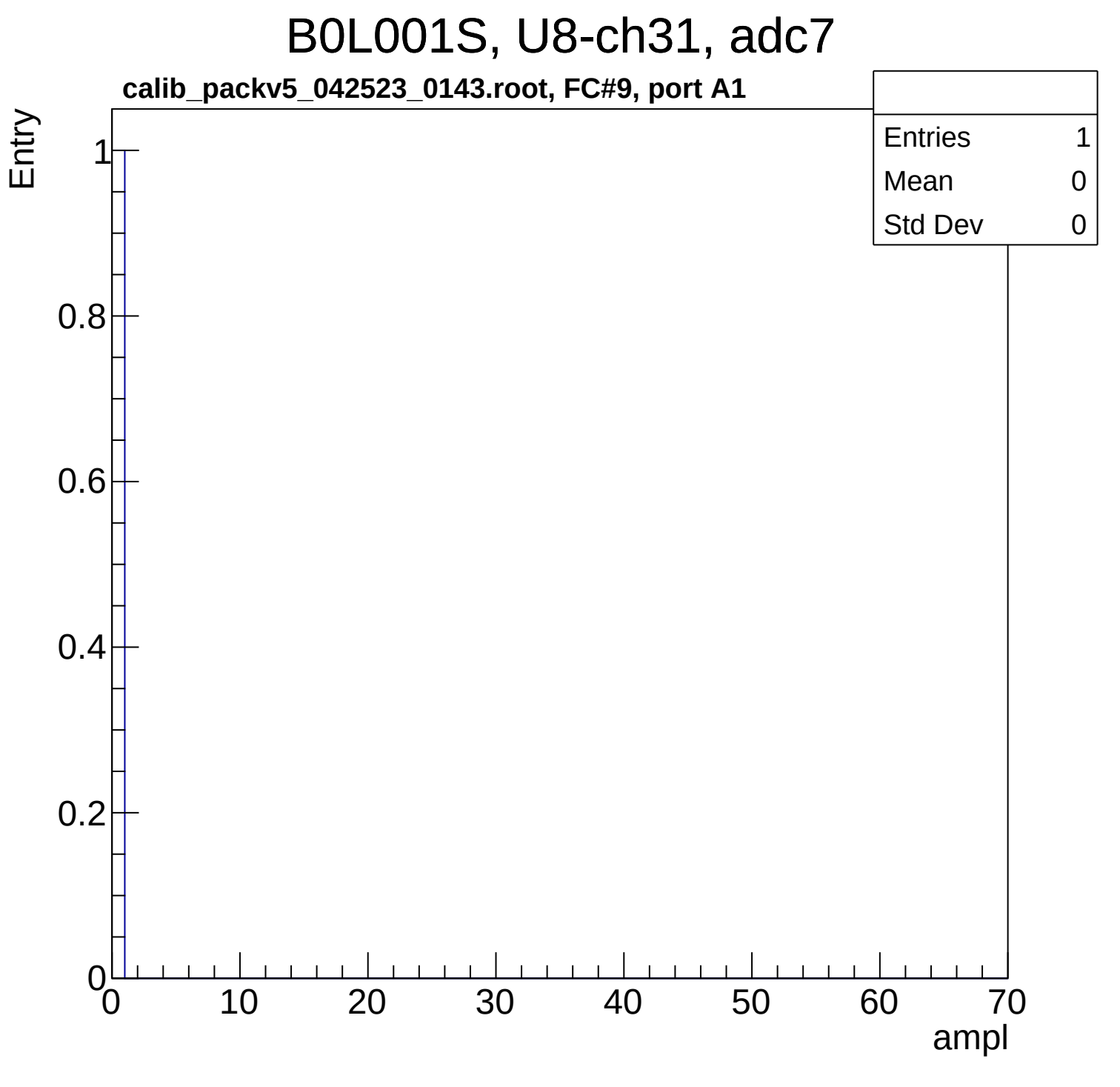
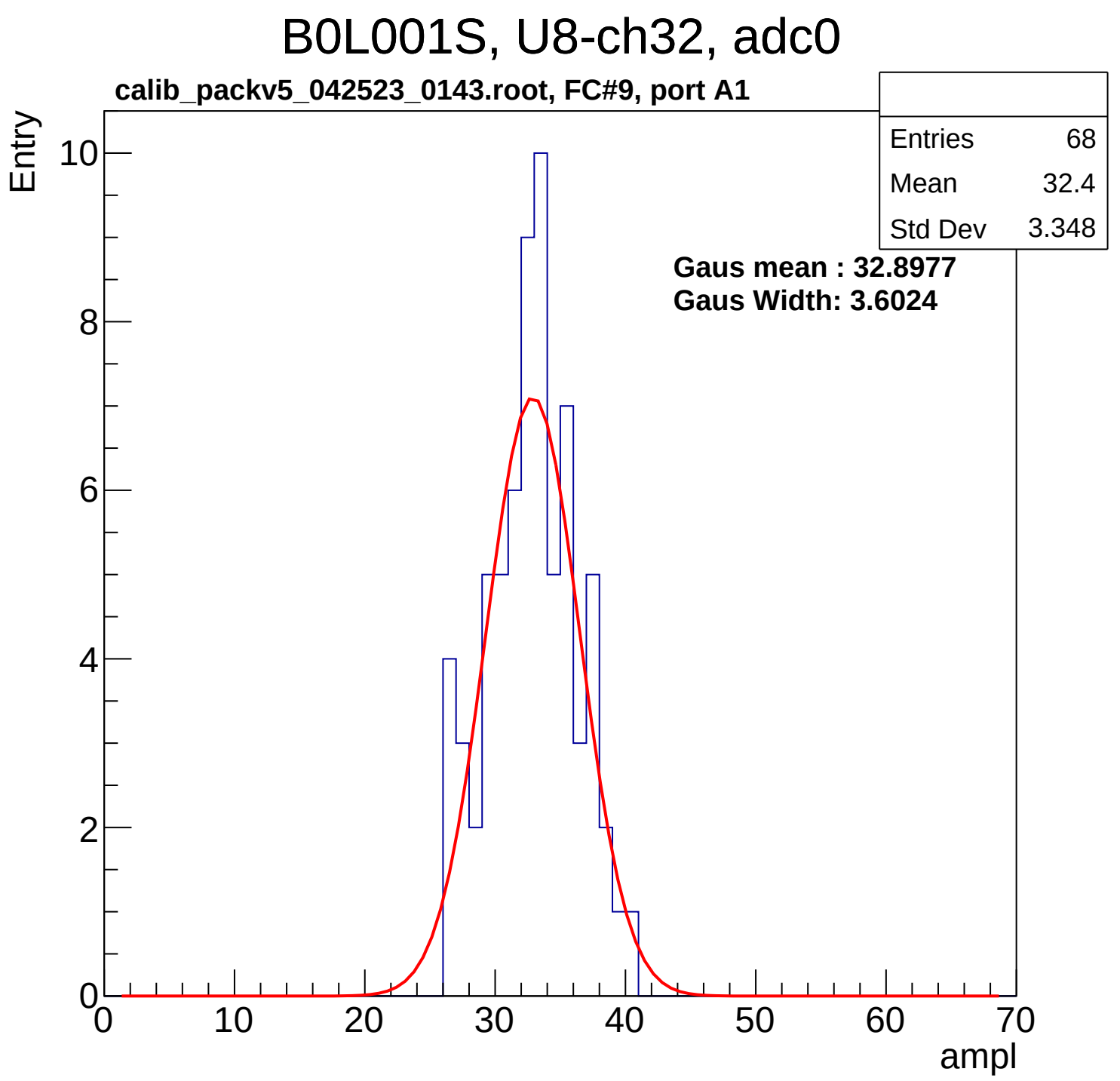
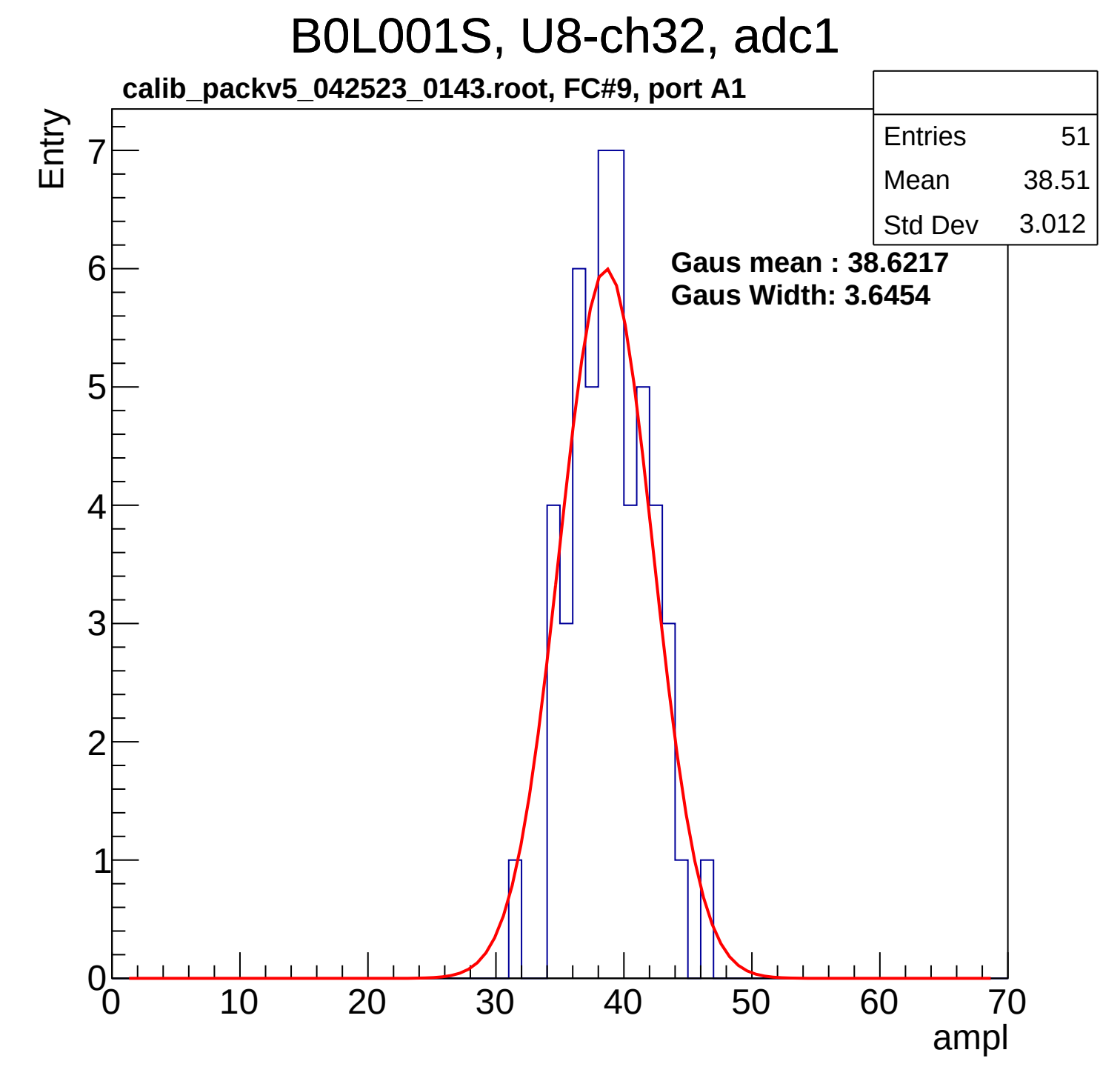
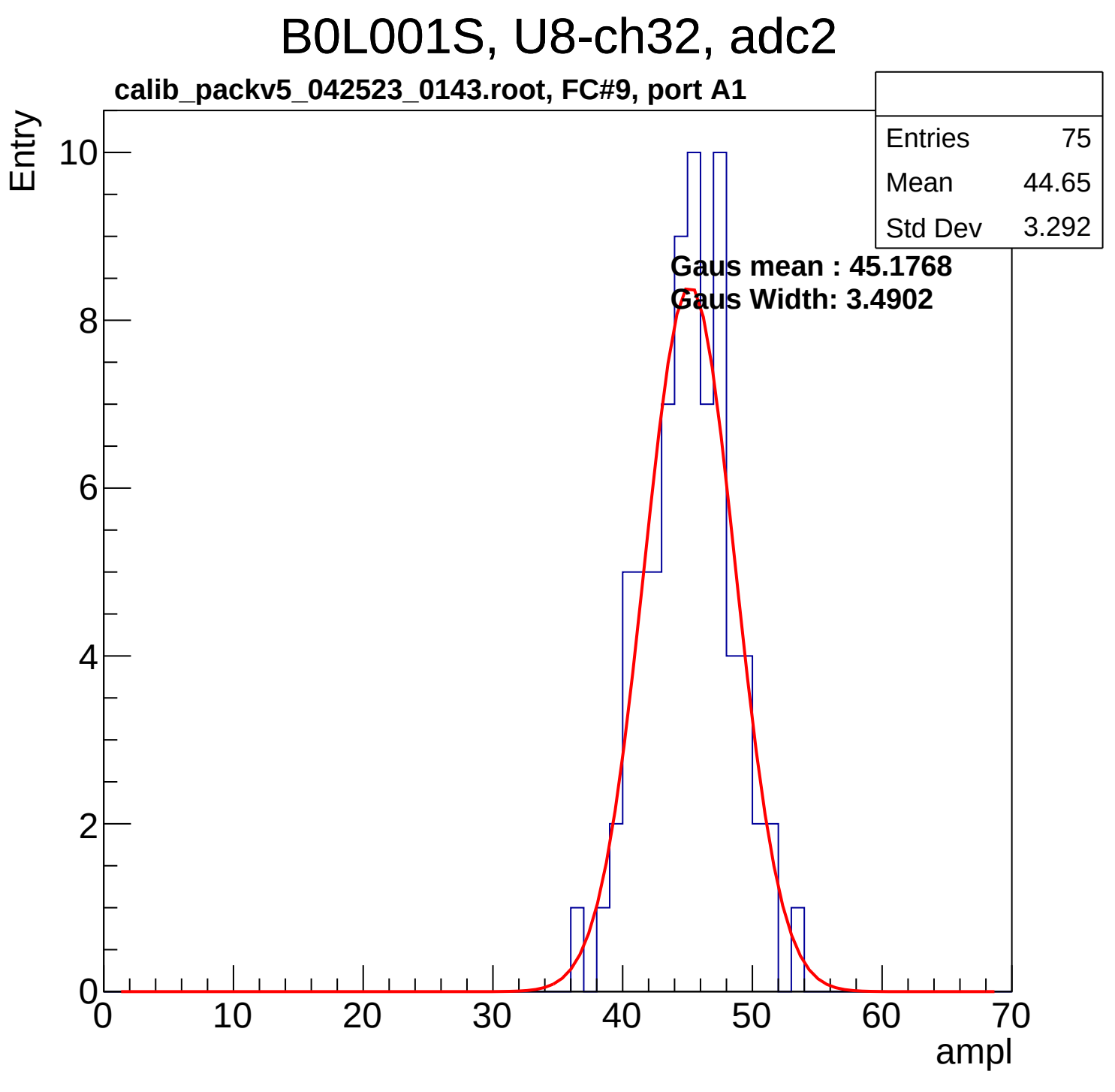

B0L001S, U8-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 32.4
Std Dev 3.348
Gaus mean : 32.8977
Gaus Width: 3.6024
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 38.51
Std Dev 3.012
Gaus mean : 38.6217
Gaus Width: 3.6454
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 44.65
Std Dev 3.292
Gaus mean : 45.1768
Gaus Width: 3.4902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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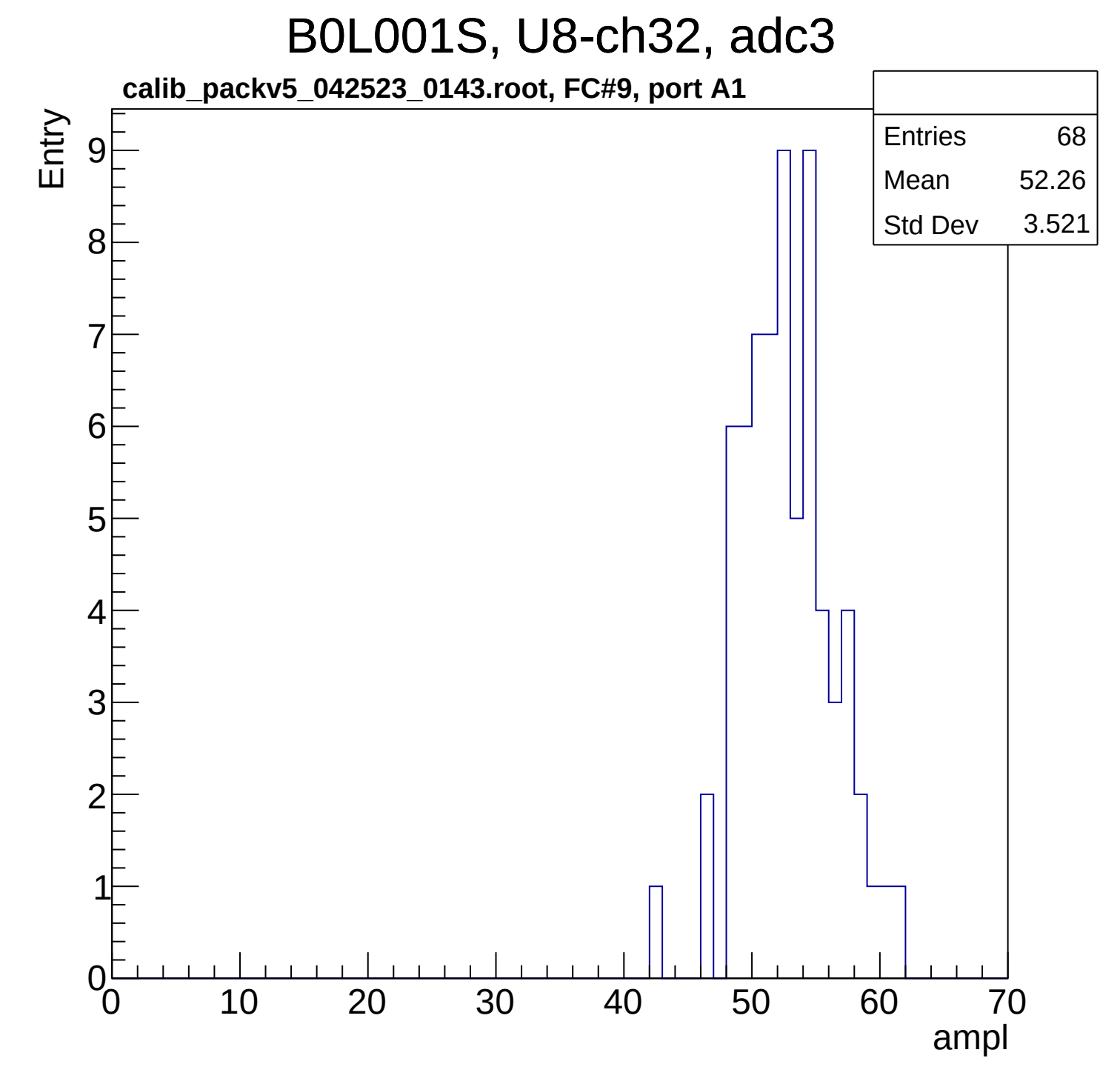
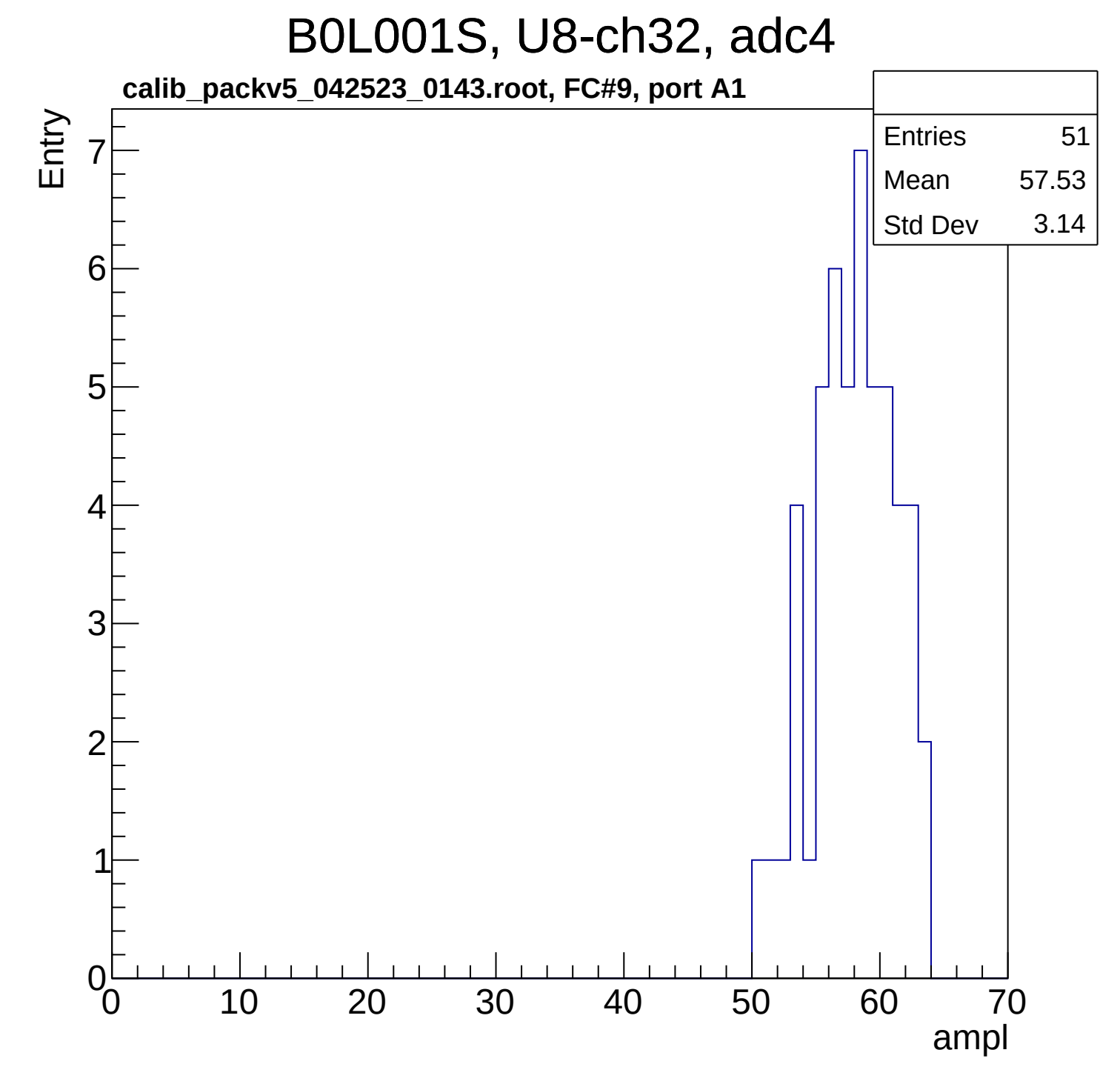
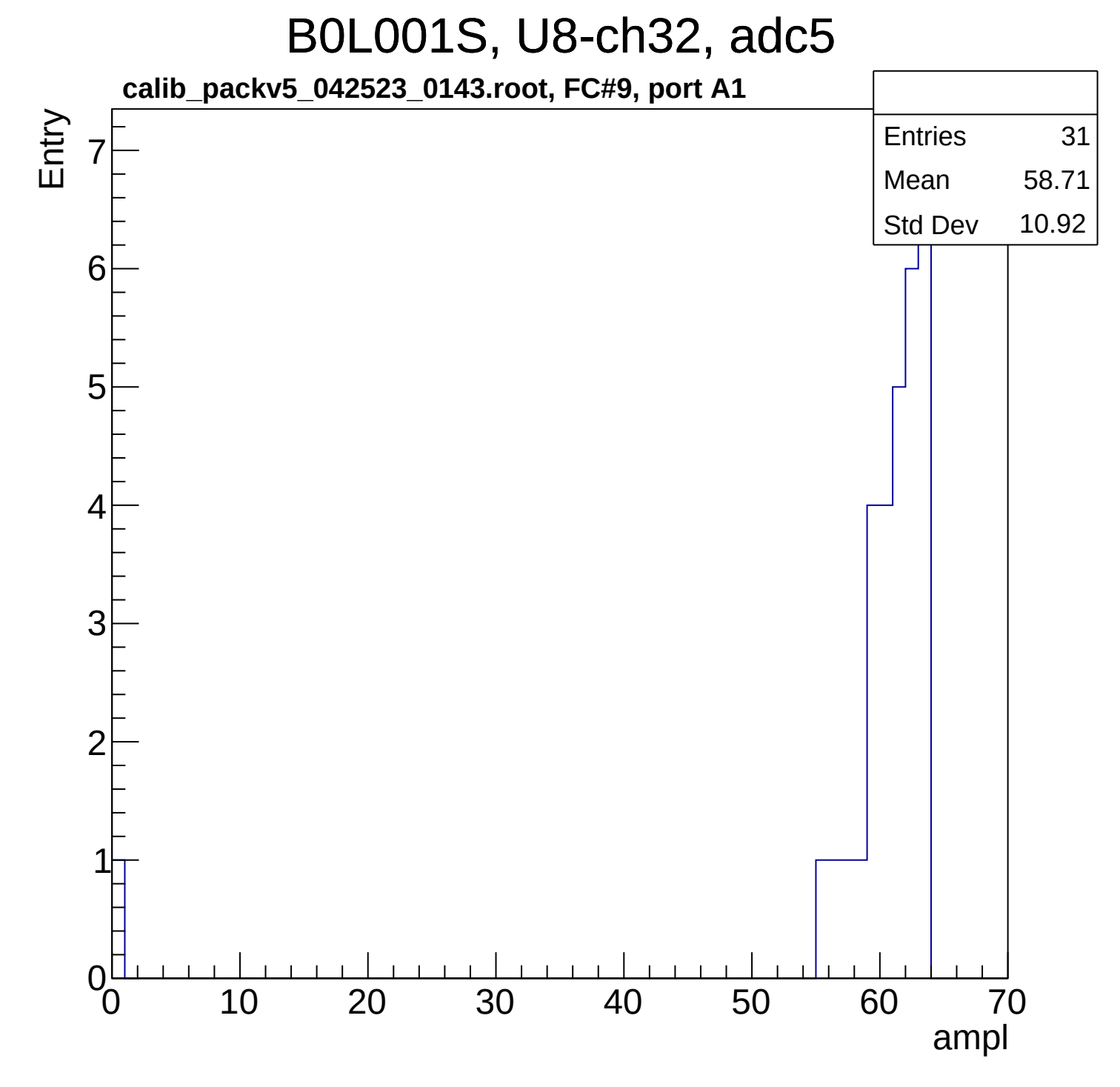

B0L001S, U8-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.26
Std Dev 3.521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.53
Std Dev 3.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.71
Std Dev 10.92
Entry
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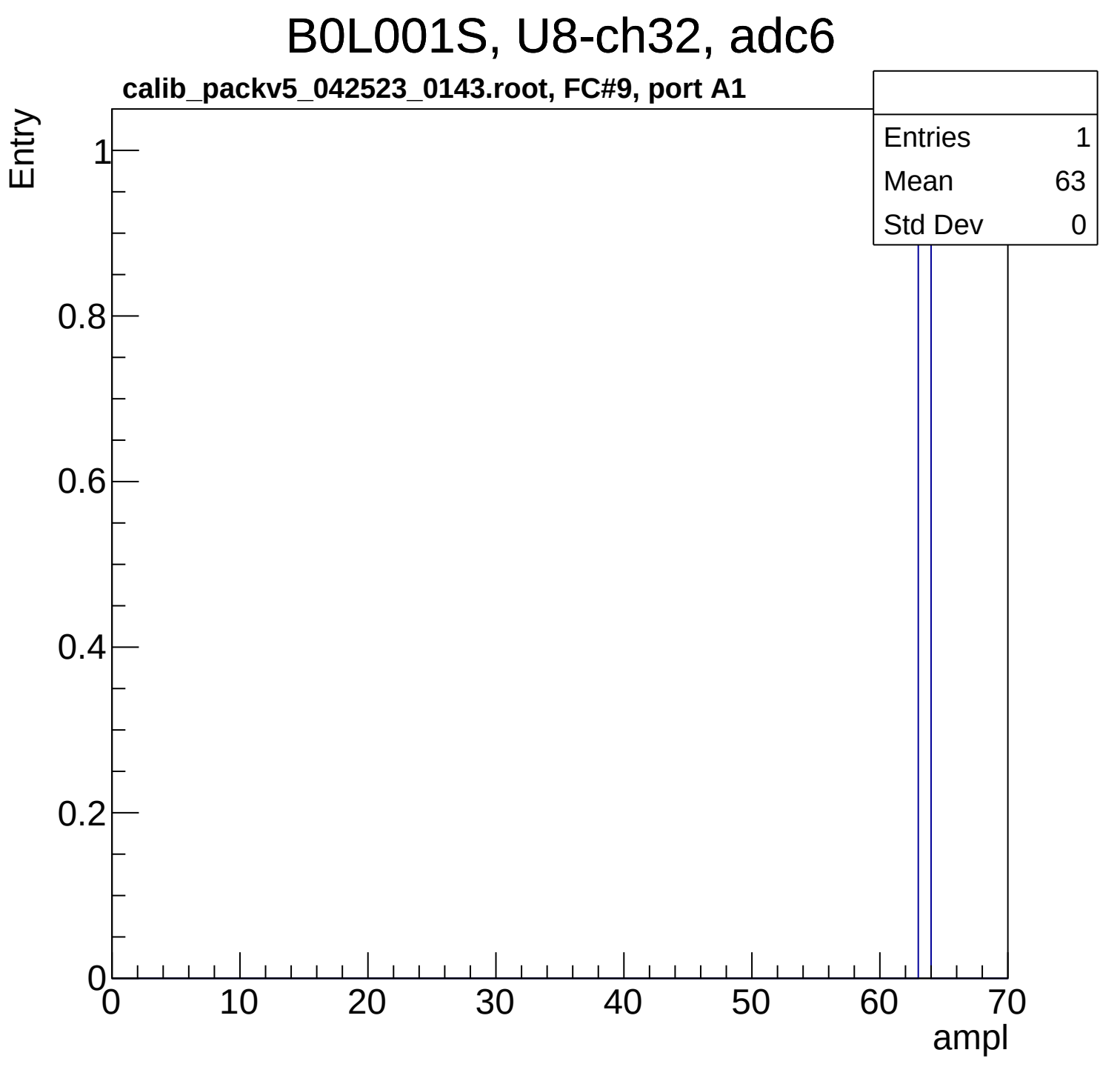
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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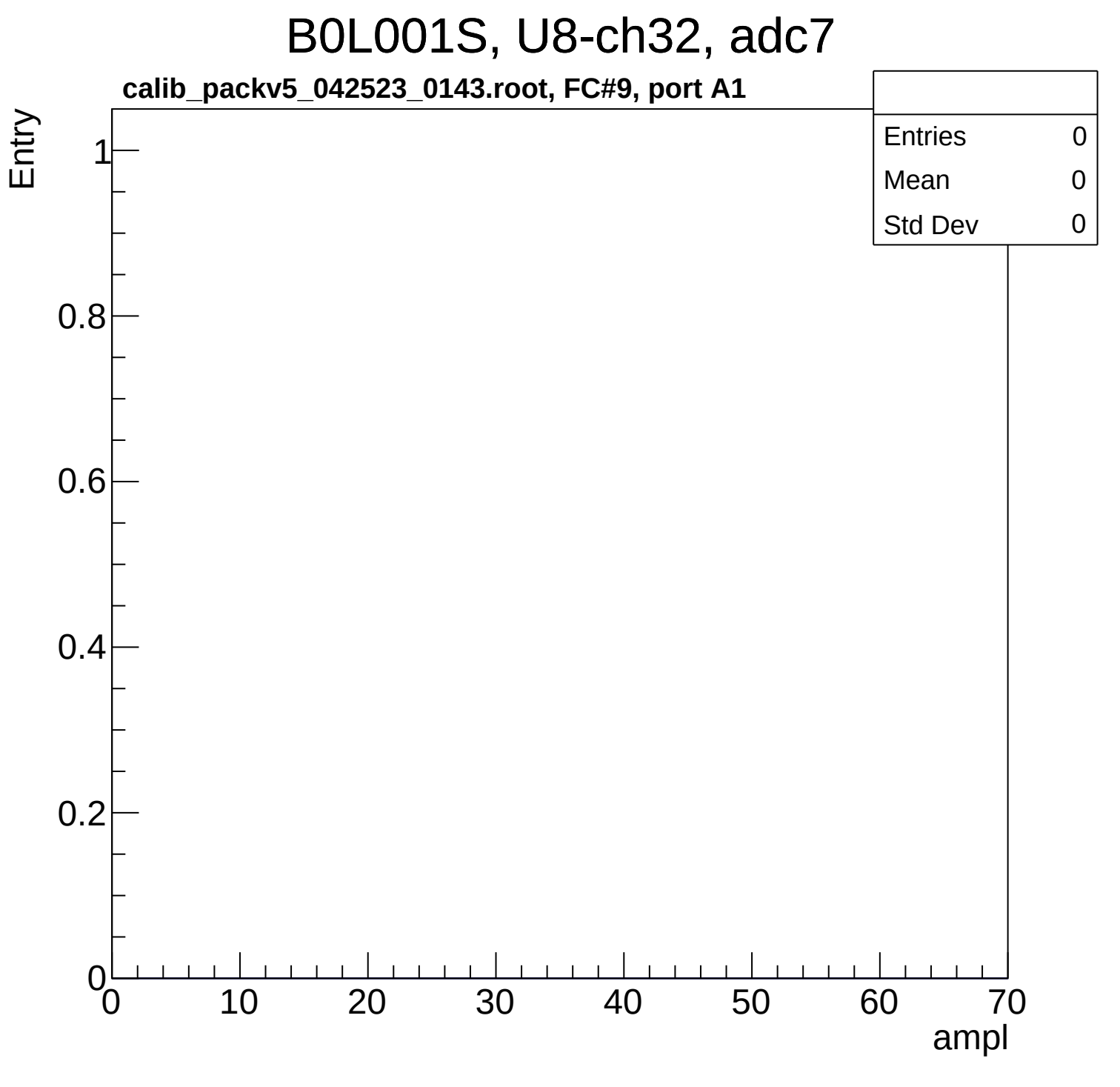
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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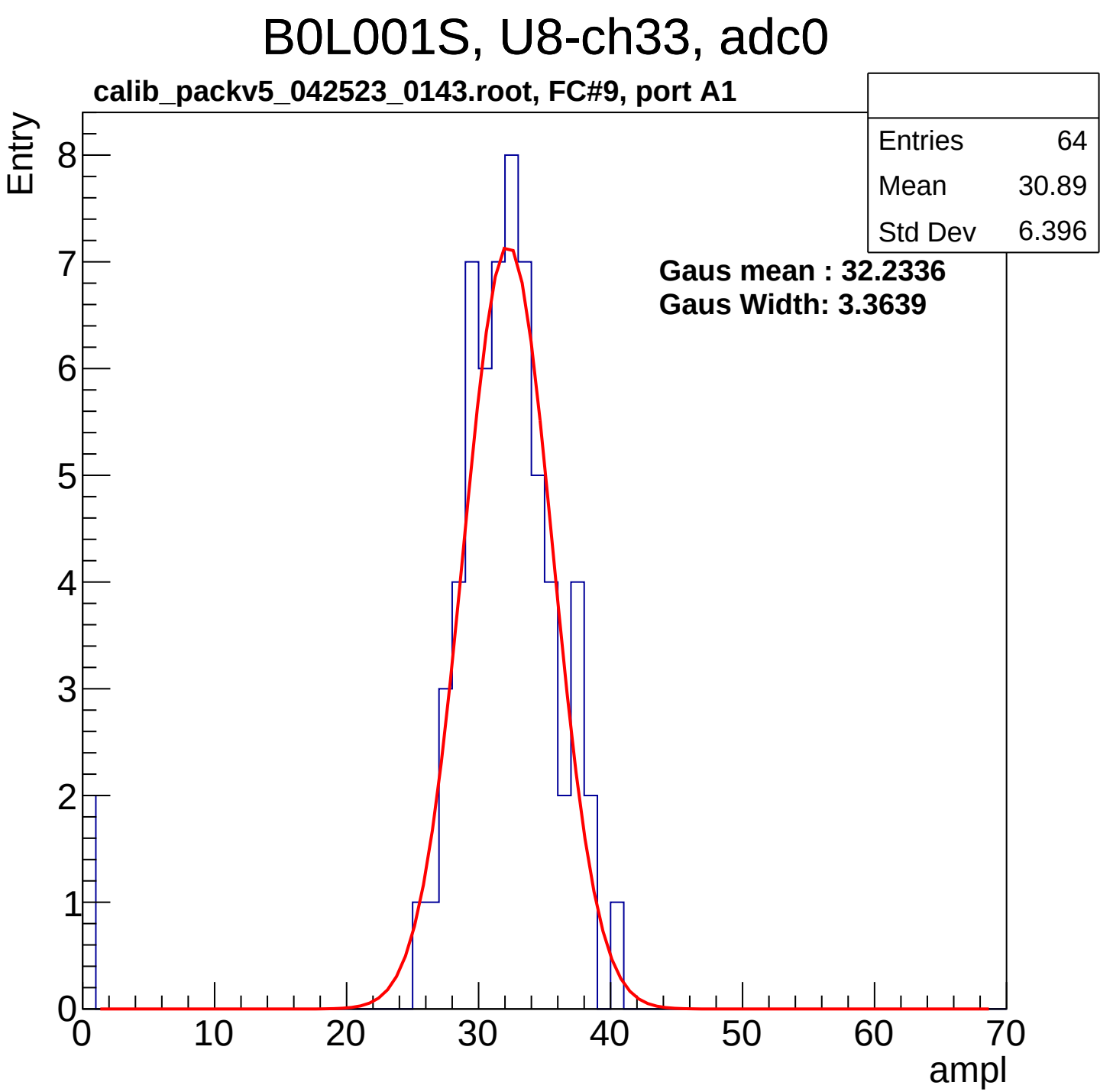
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.89
Std Dev 6.396
Gaus mean : 32.2336
Gaus Width: 3.3639
Entry
ampl
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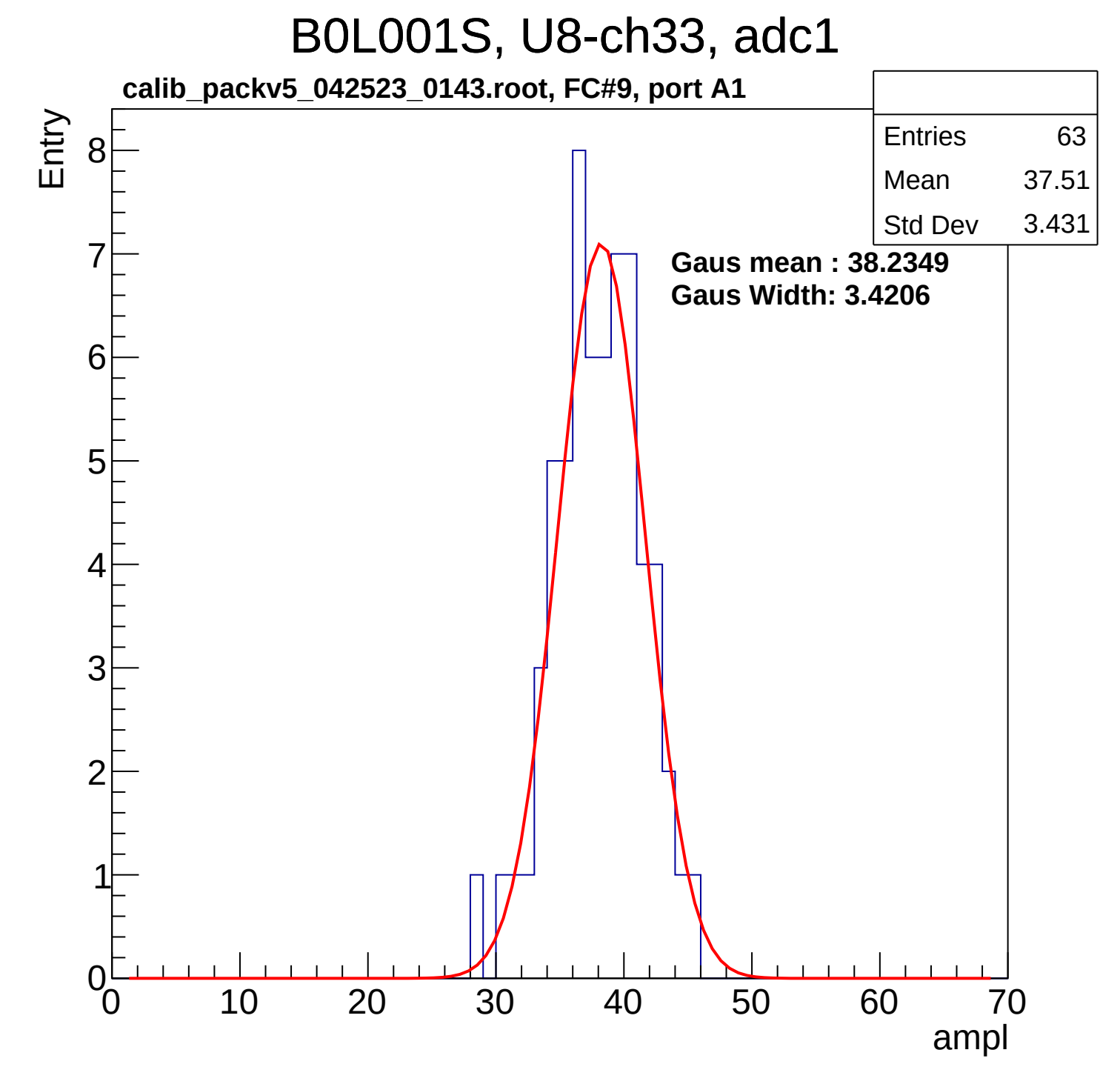
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.51
Std Dev 3.431
Gaus mean : 38.2349
Gaus Width: 3.4206
Entry
ampl
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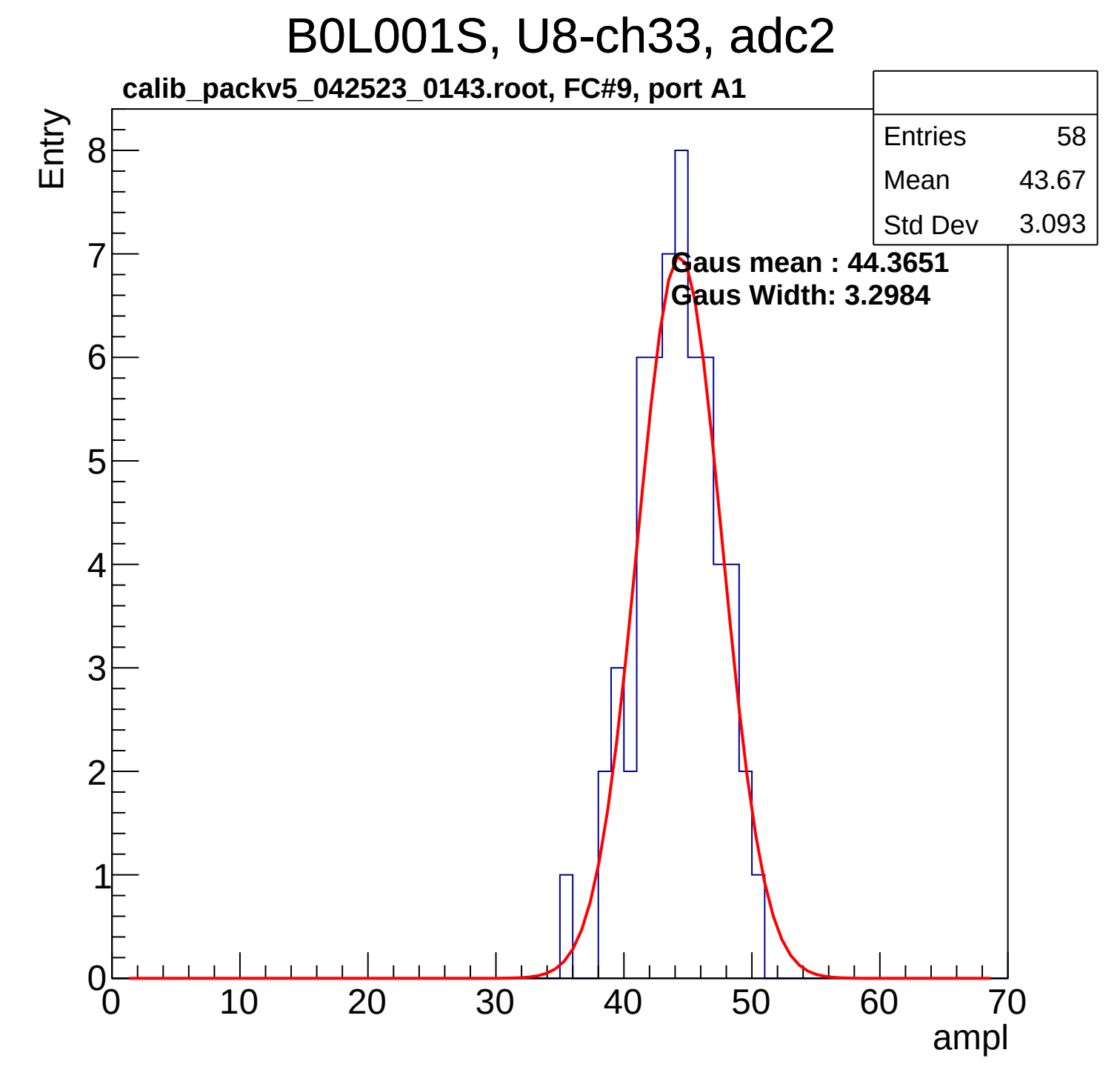
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.67
Std Dev 3.093
Gaus mean : 44.3651
Gaus Width: 3.2984
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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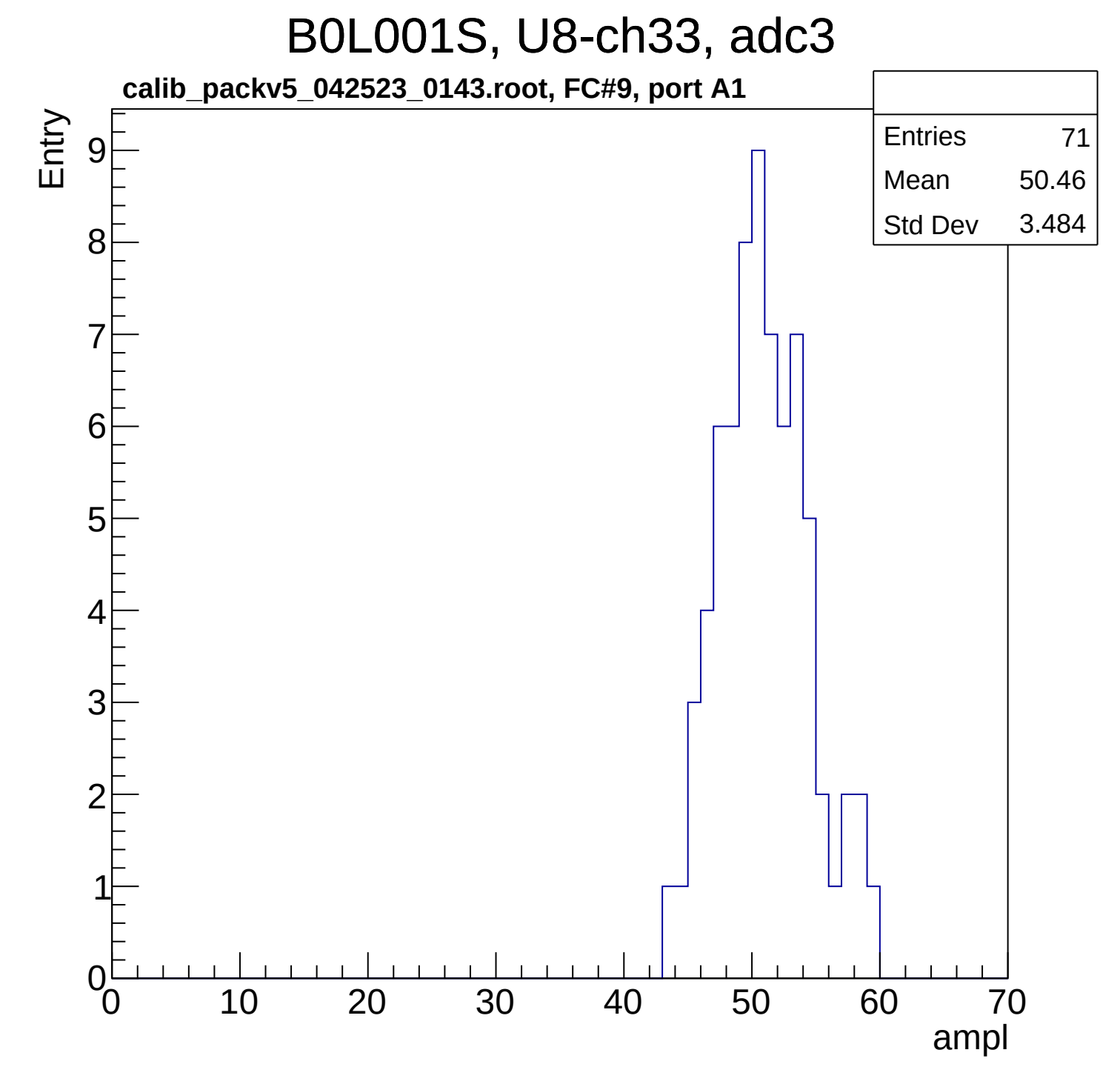
B0L001S, U8-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.46
Std Dev 3.484
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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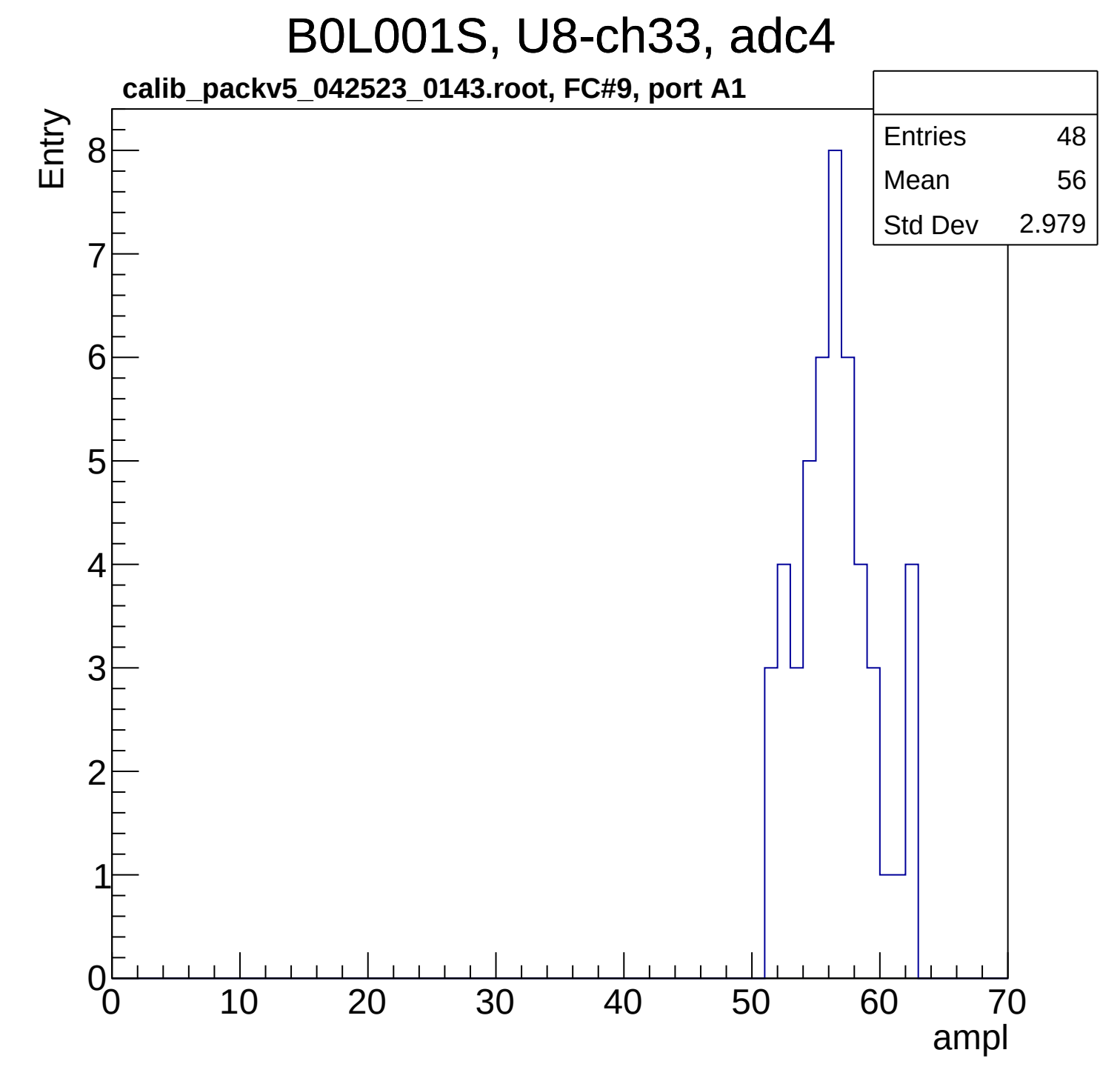
B0L001S, U8-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56
Std Dev 2.979
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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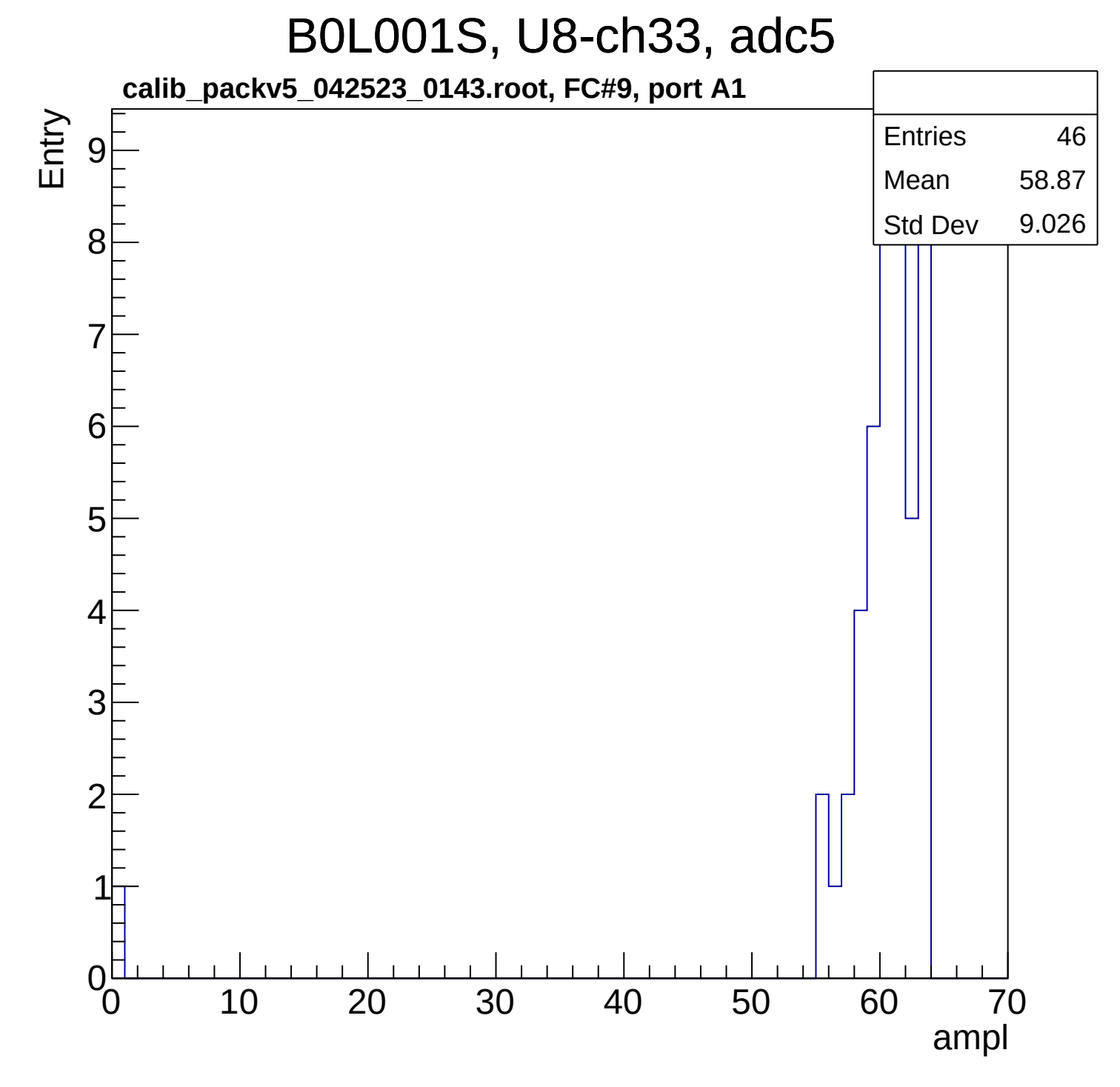
B0L001S, U8-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.87
Std Dev 9.026
Entry
ampl
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3
4
5
6
7
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0
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60
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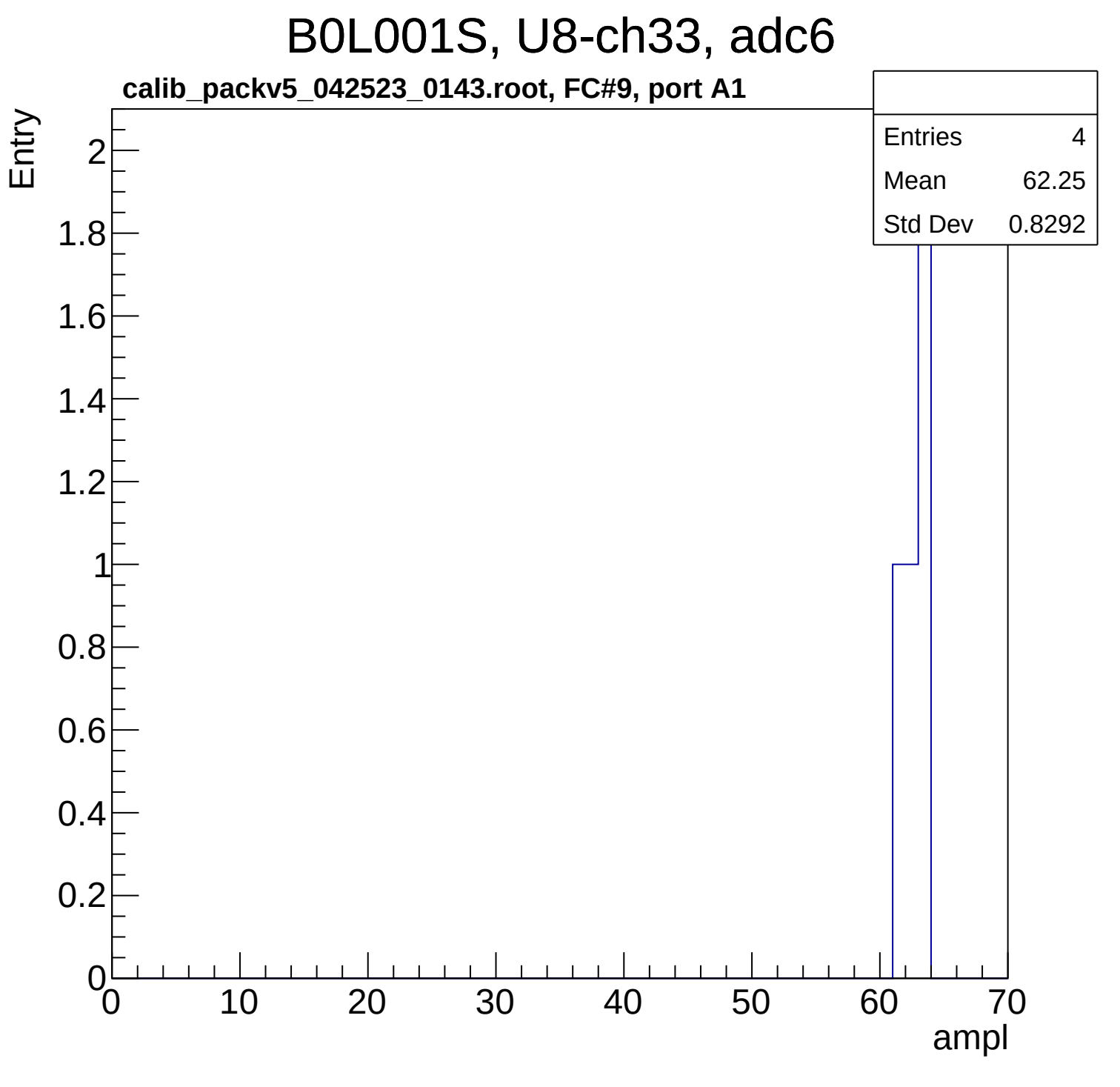
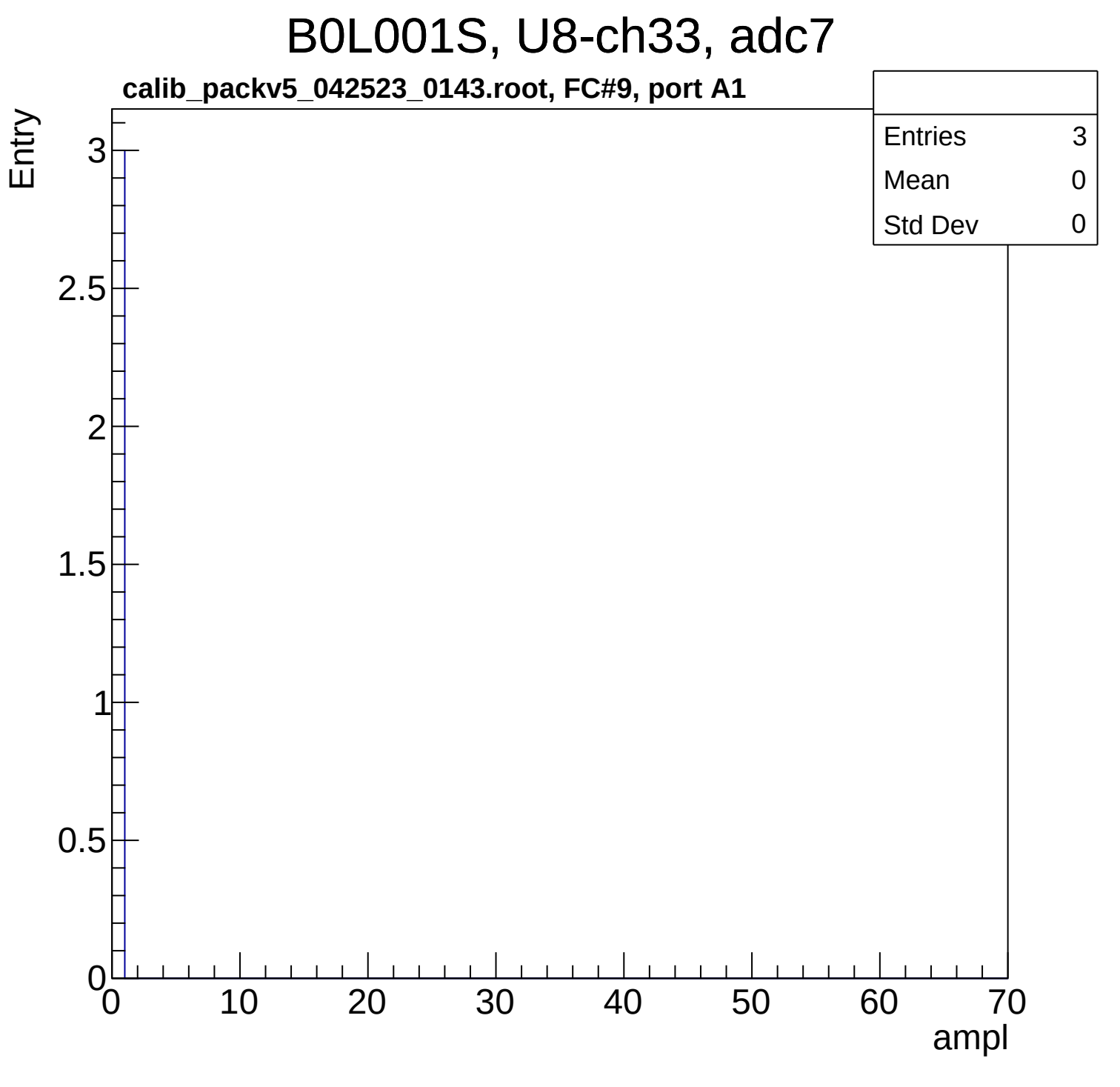

B0L001S, U8-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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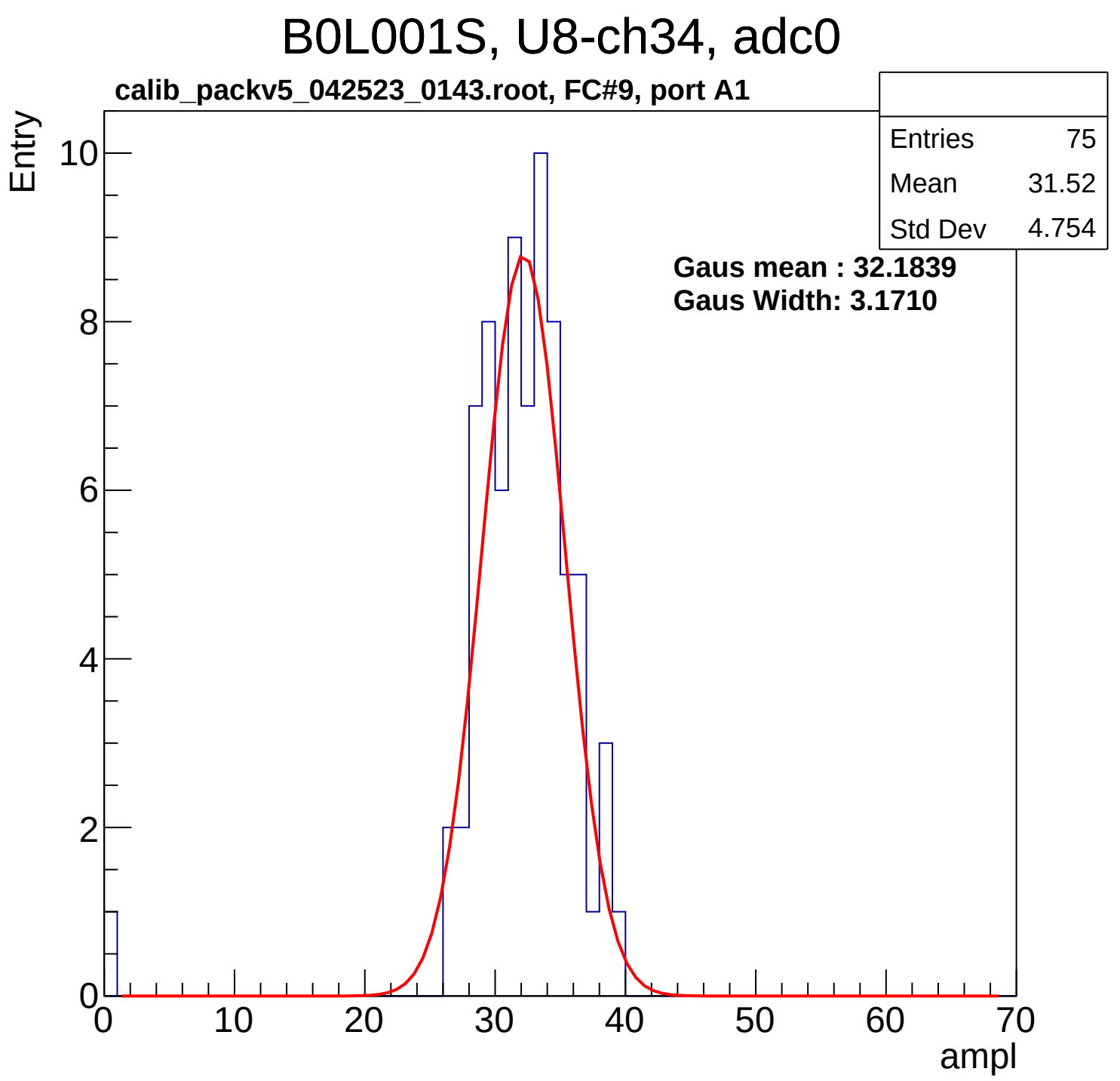
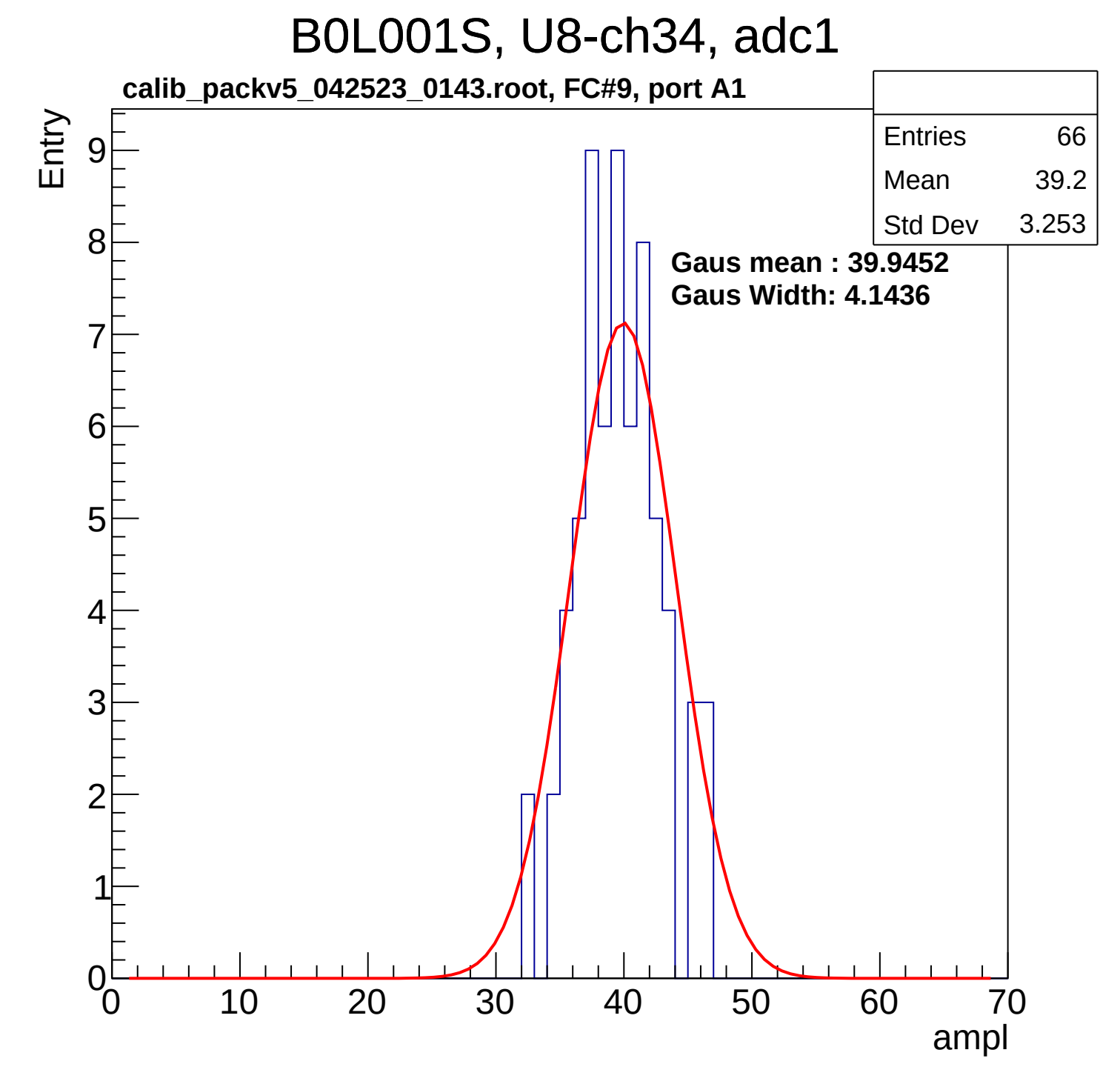
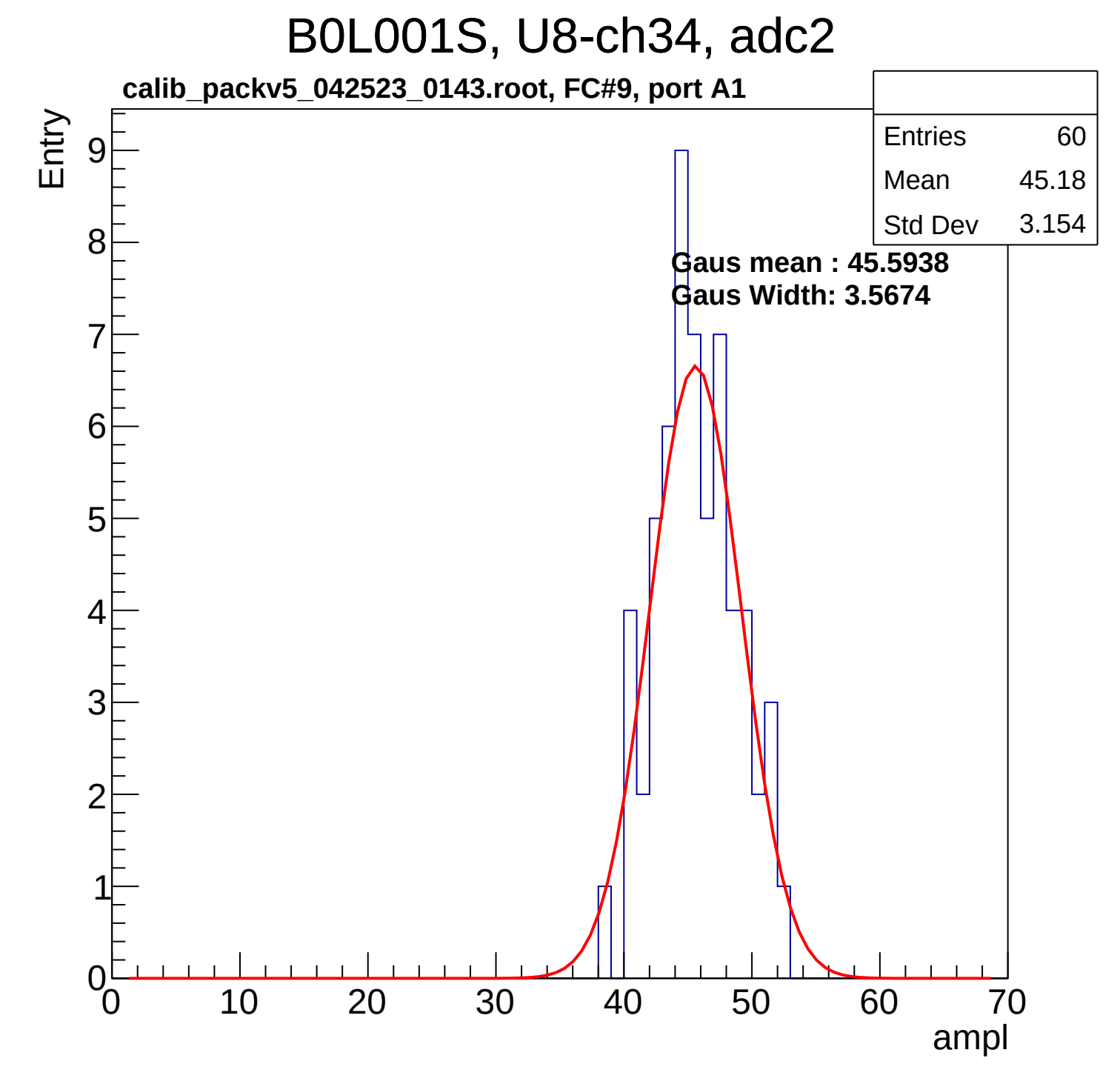
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 31.52
Std Dev 4.754
Gaus mean : 32.1839
Gaus Width: 3.1710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 39.2
Std Dev 3.253
Gaus mean : 39.9452
Gaus Width: 4.1436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.18
Std Dev 3.154
Gaus mean : 45.5938
Gaus Width: 3.5674
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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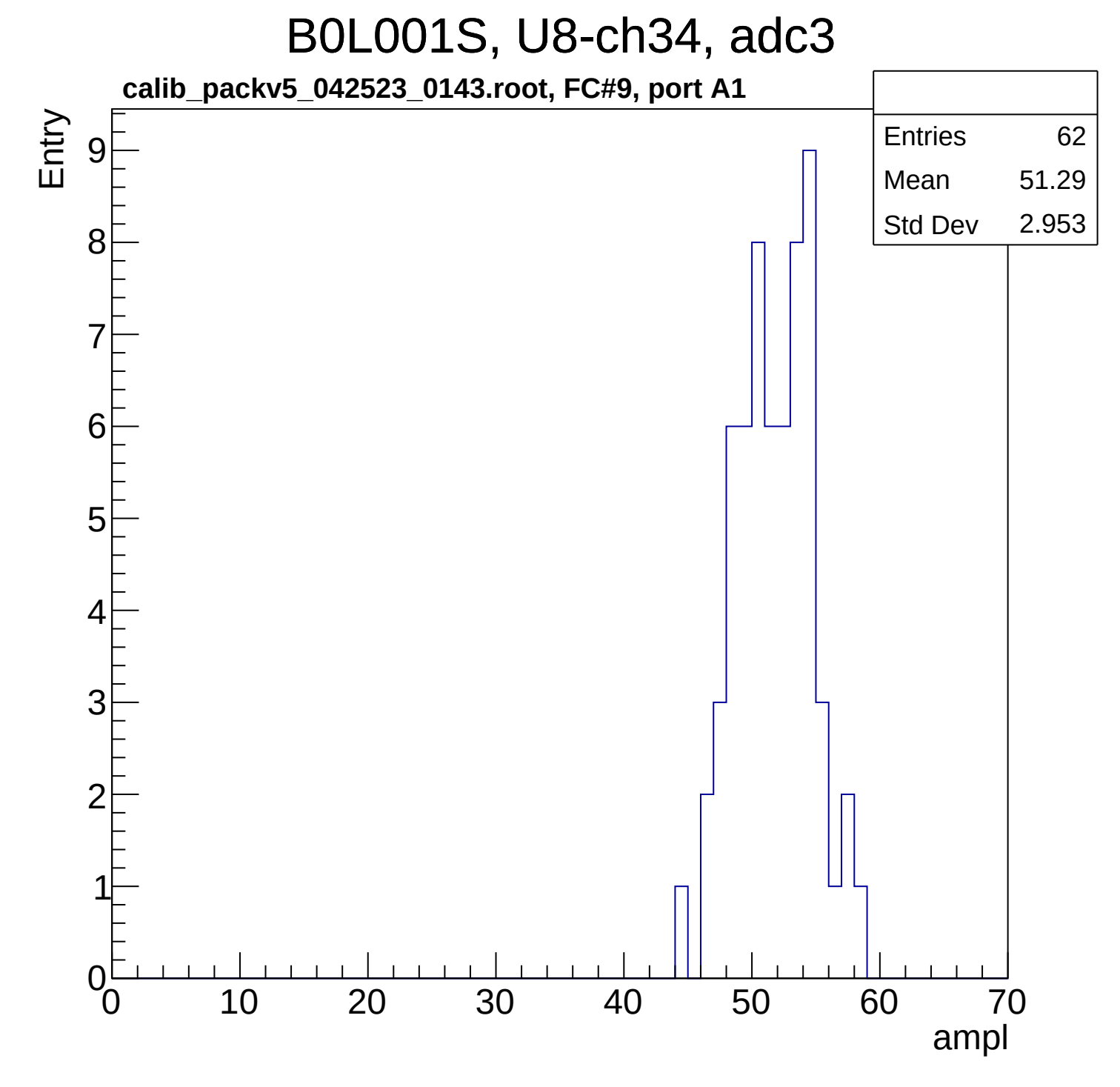
B0L001S, U8-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.29
Std Dev 2.953
Entry
ampl
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2
3
4
5
6
7
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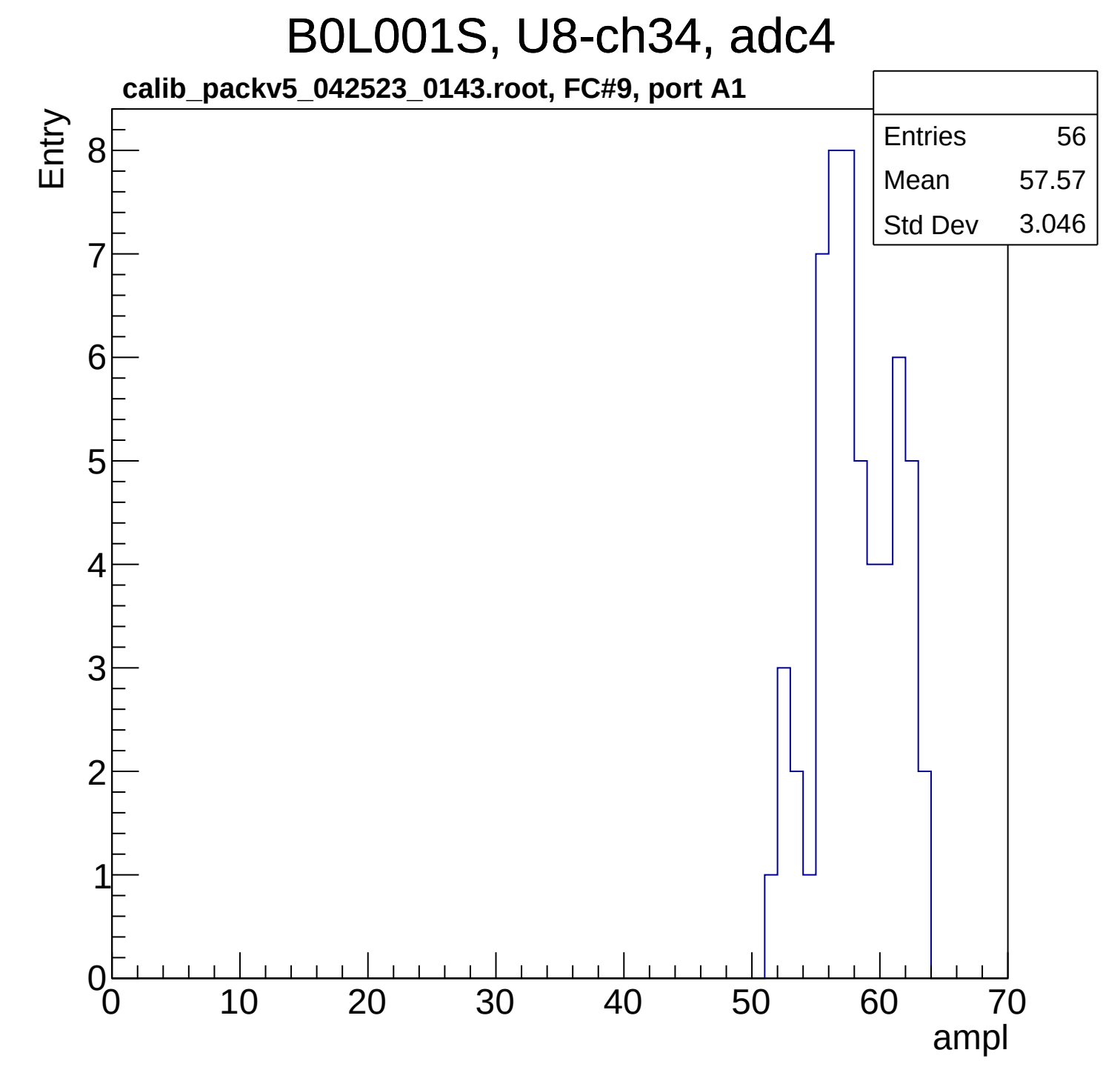
B0L001S, U8-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.57
Std Dev 3.046
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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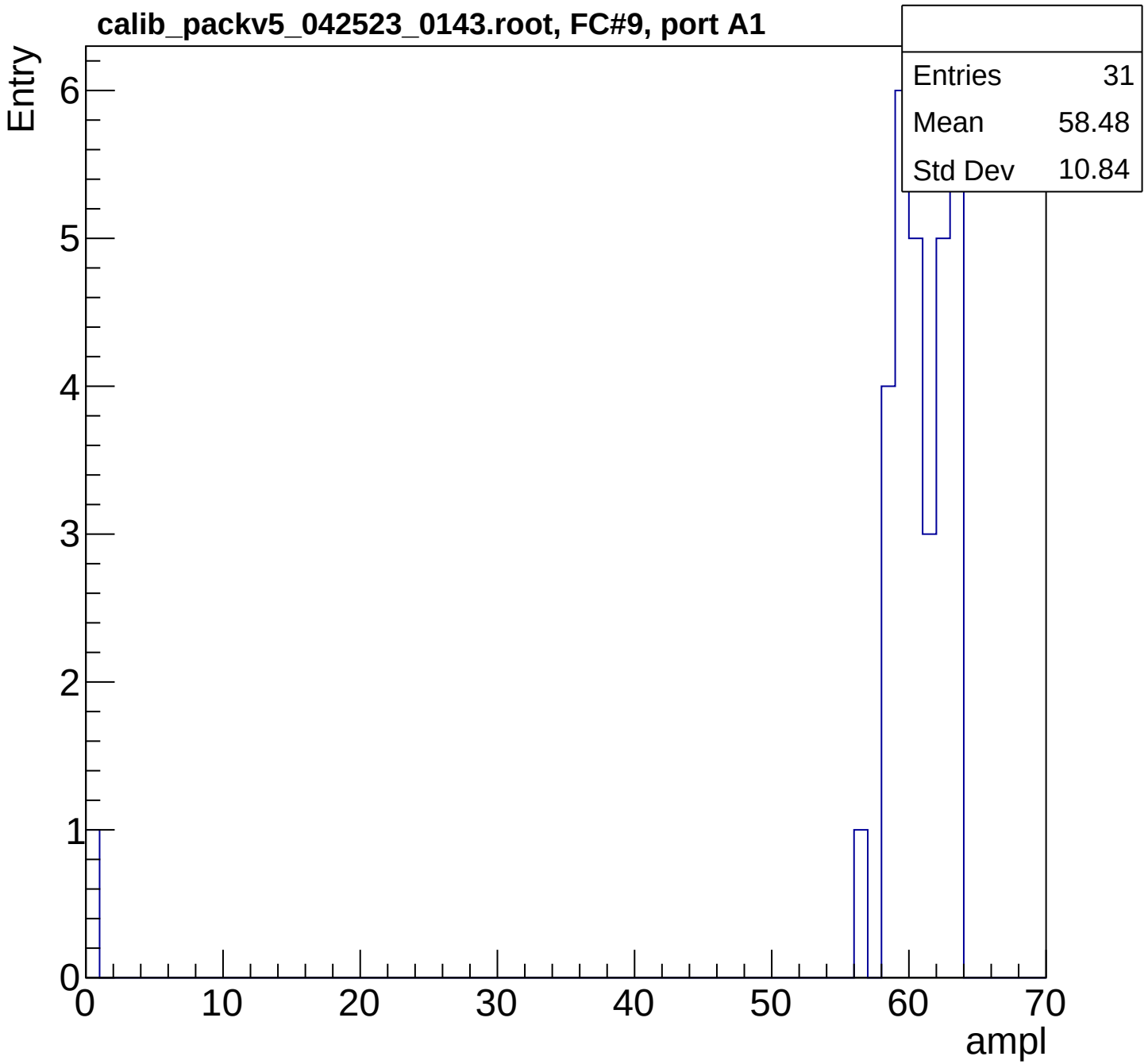
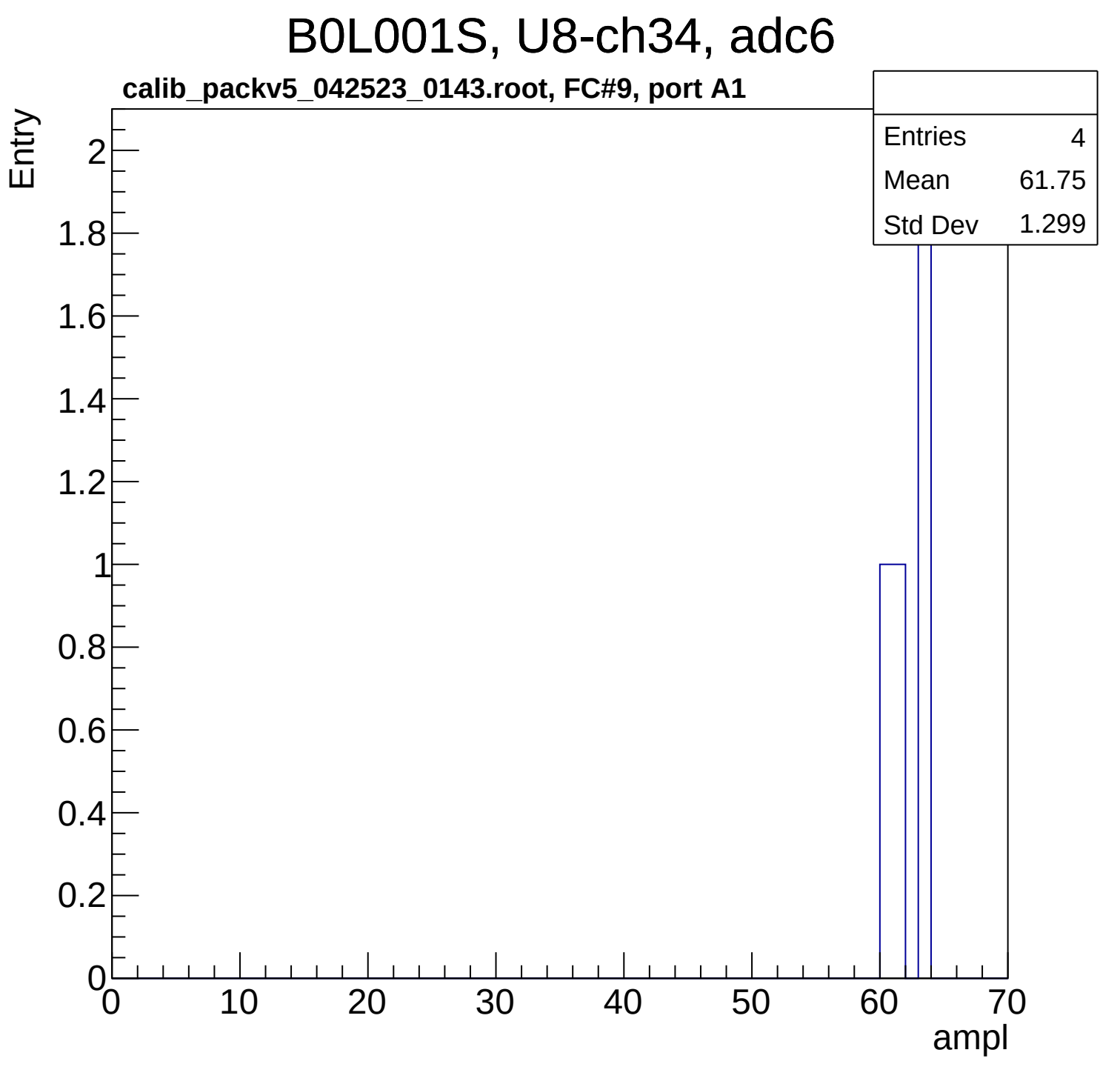

B0L001S, U8-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.48
Std Dev 10.84
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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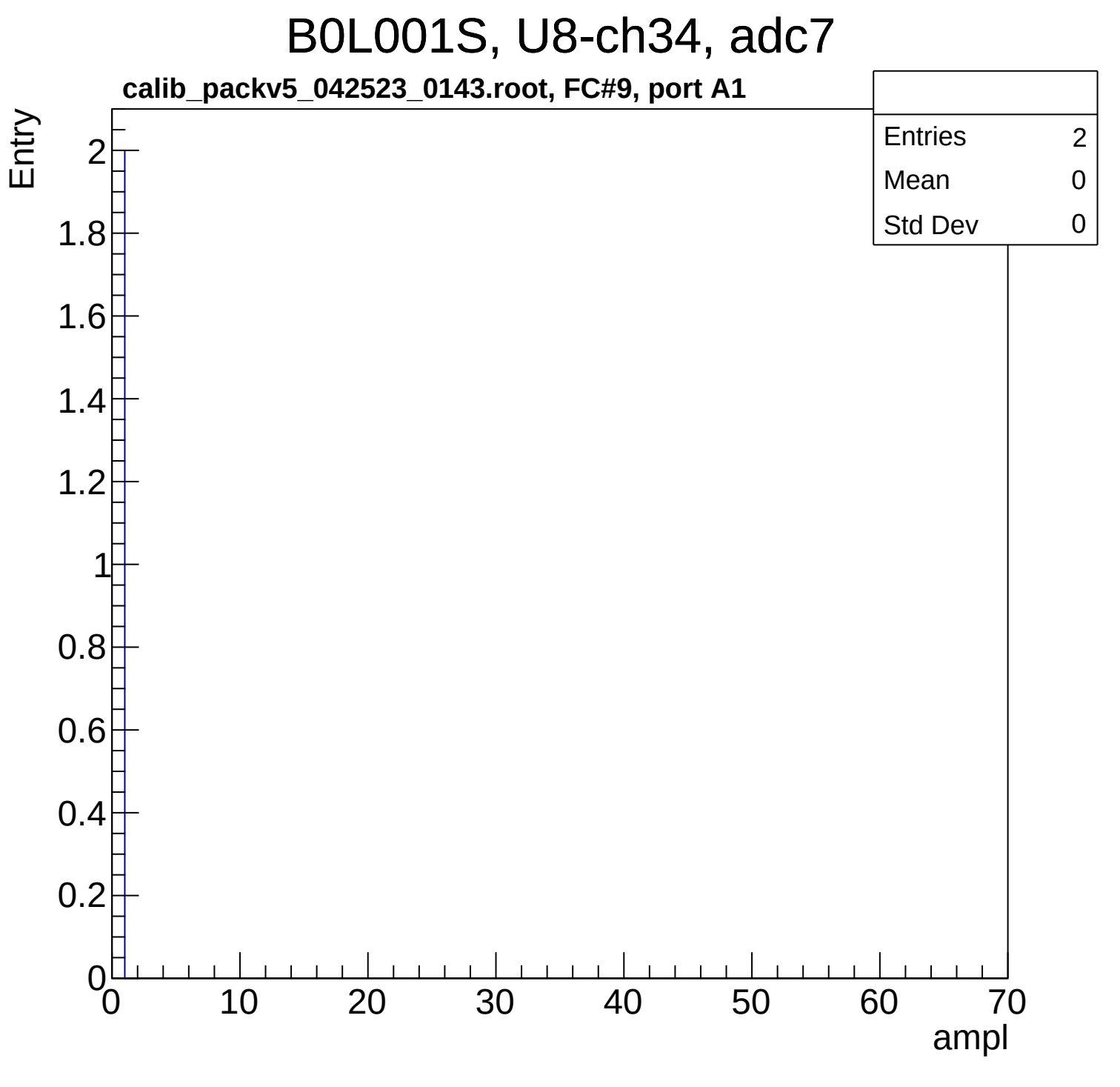
40
50
60
70


B0L001S, U8-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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0.6
0.4
0.2
0
0
10
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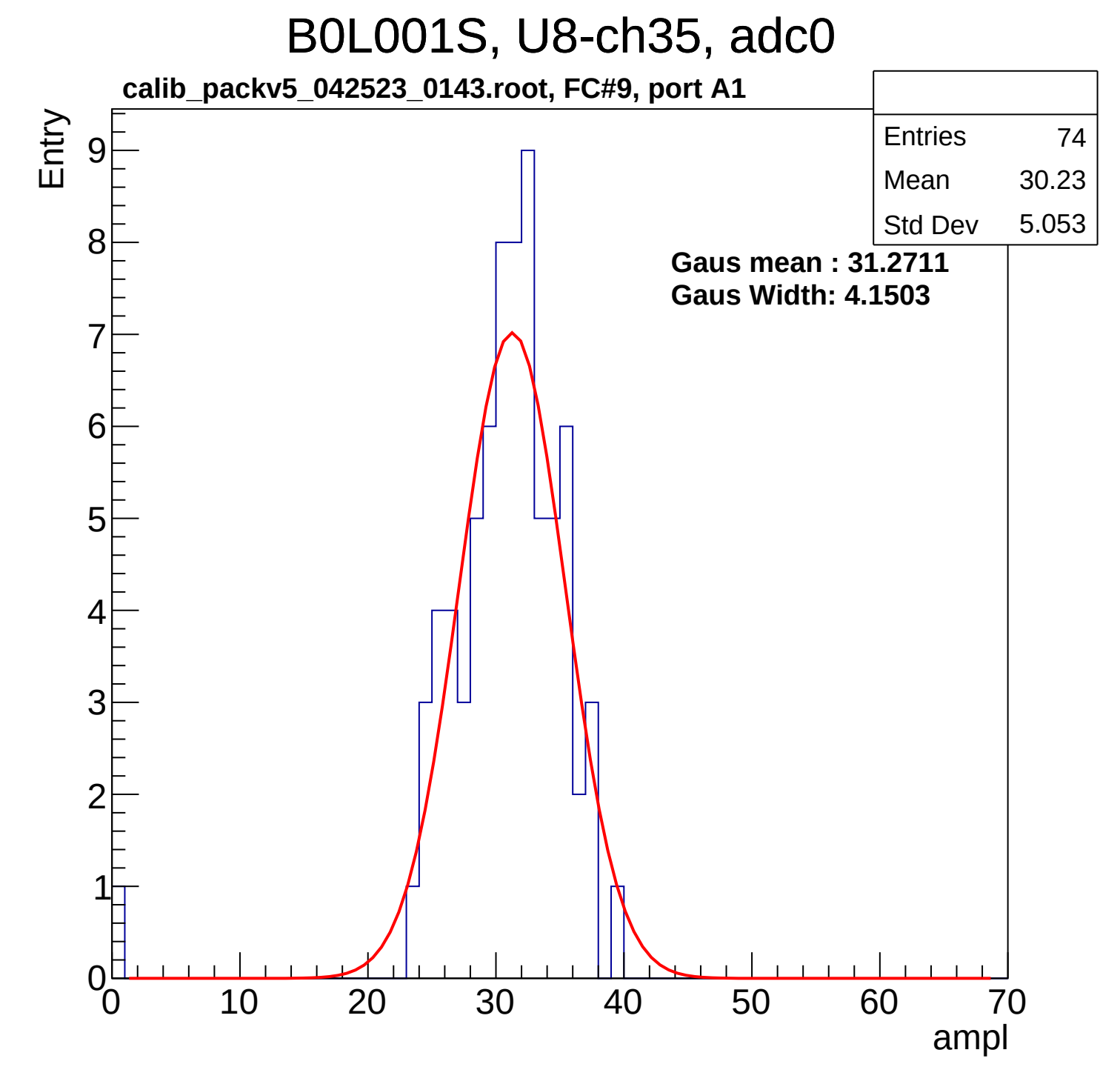
60
70
ampl


B0L001S, U8-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.23
Std Dev 5.053
Gaus mean : 31.2711
Gaus Width: 4.1503
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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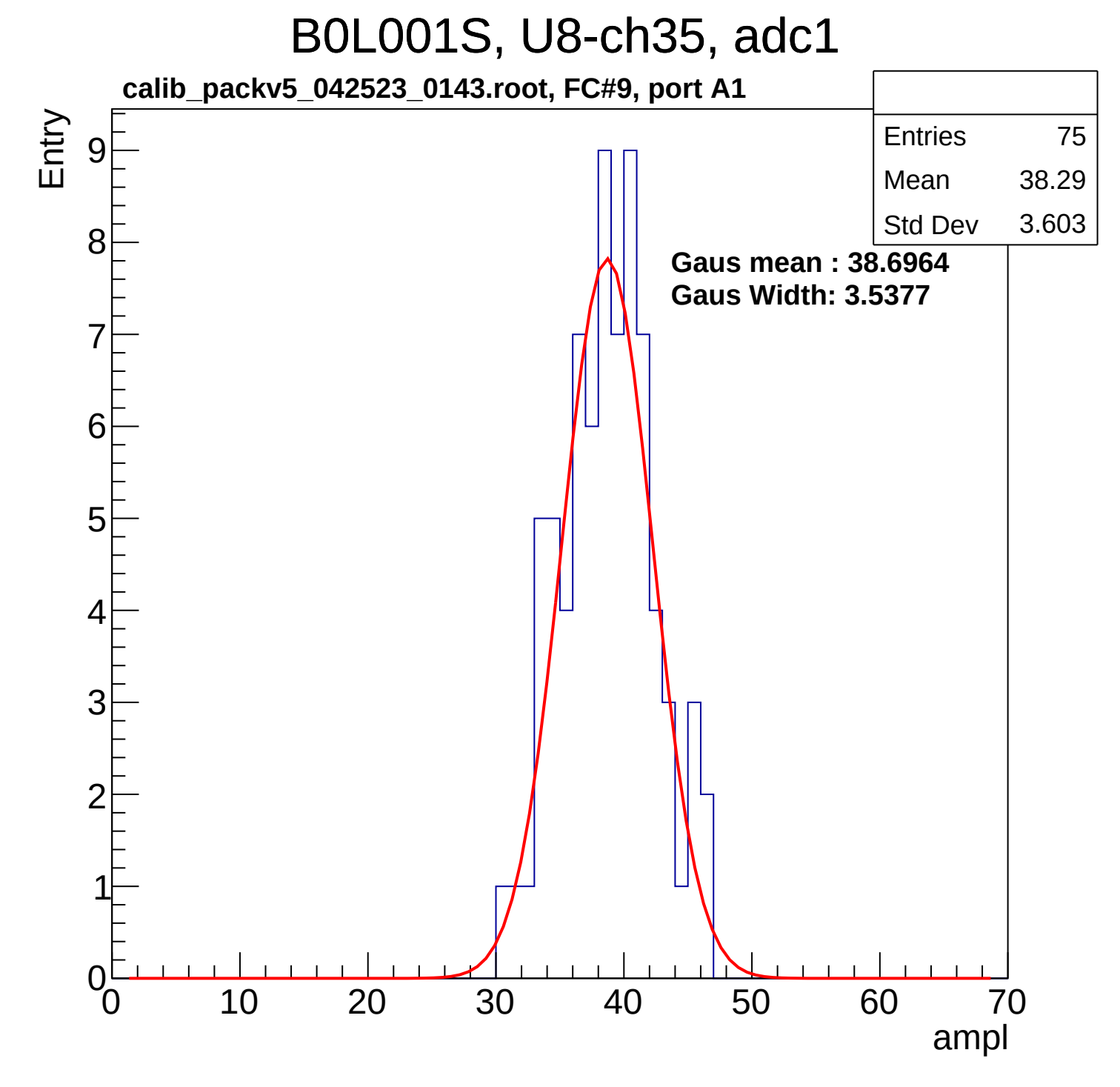
B0L001S, U8-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.29
Std Dev 3.603
Gaus mean : 38.6964
Gaus Width: 3.5377
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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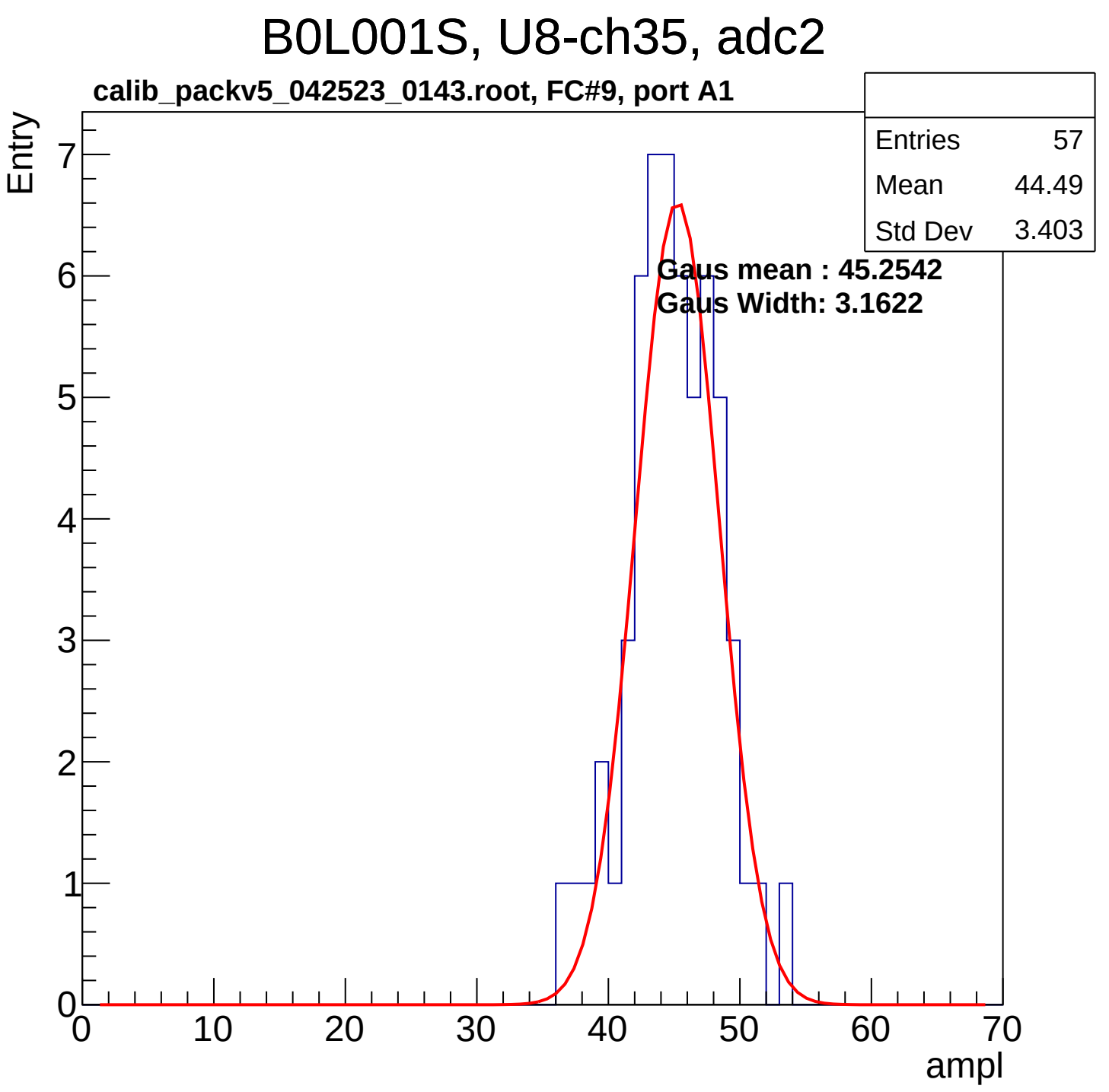
B0L001S, U8-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.49
Std Dev 3.403
Gaus mean : 45.2542
Gaus Width: 3.1622
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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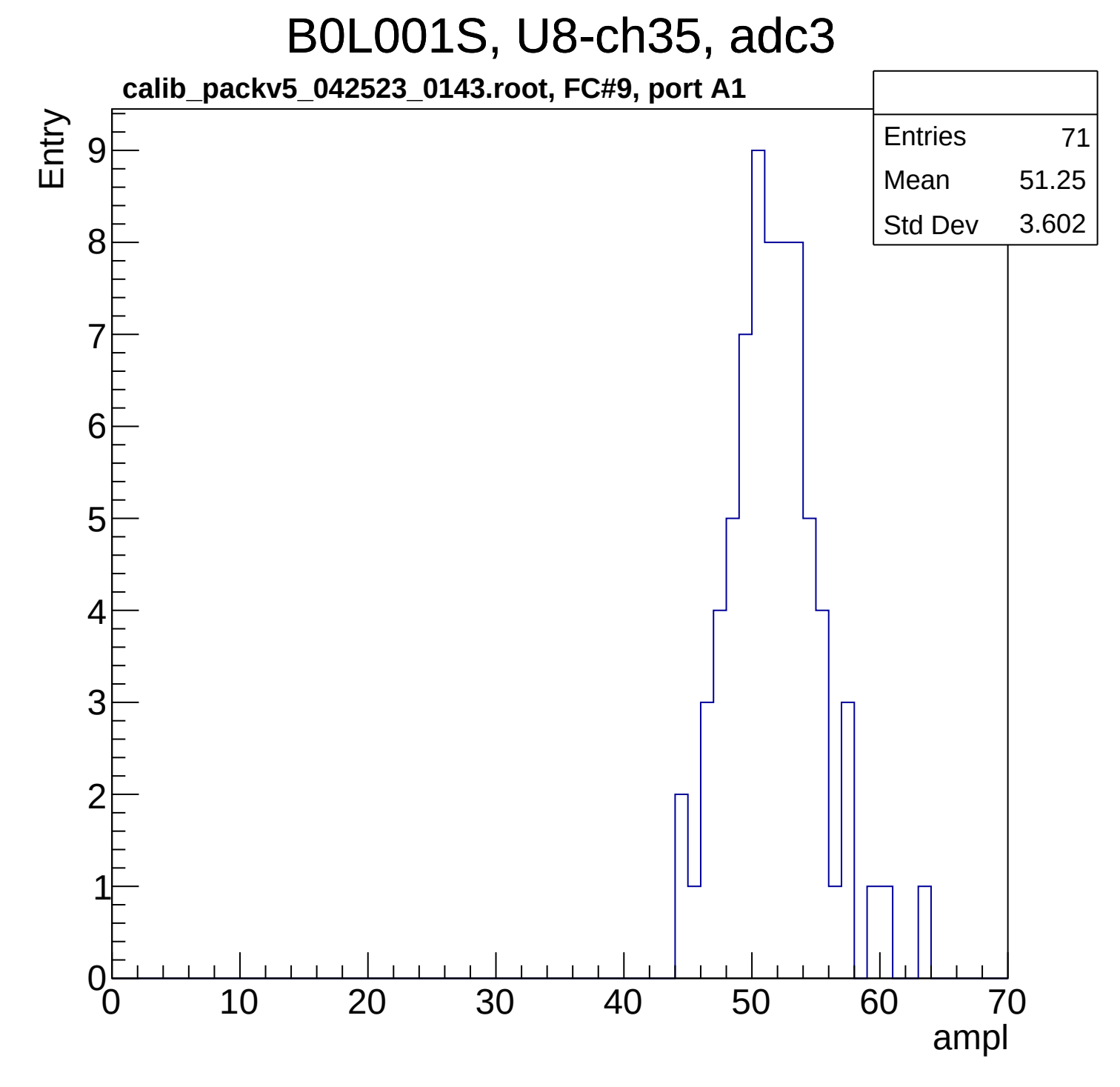
B0L001S, U8-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.25
Std Dev 3.602
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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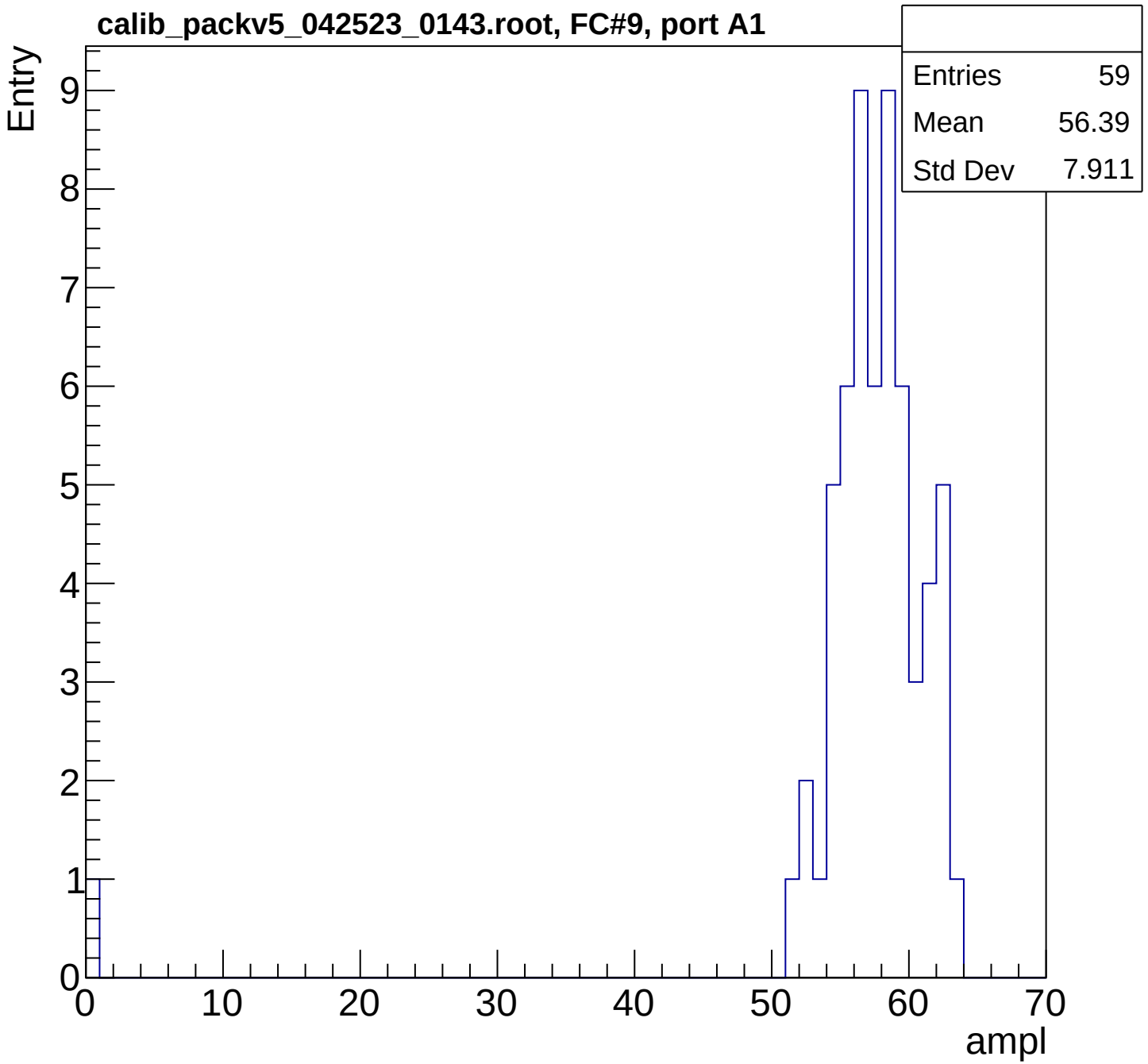
B0L001S, U8-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.39
Std Dev 7.911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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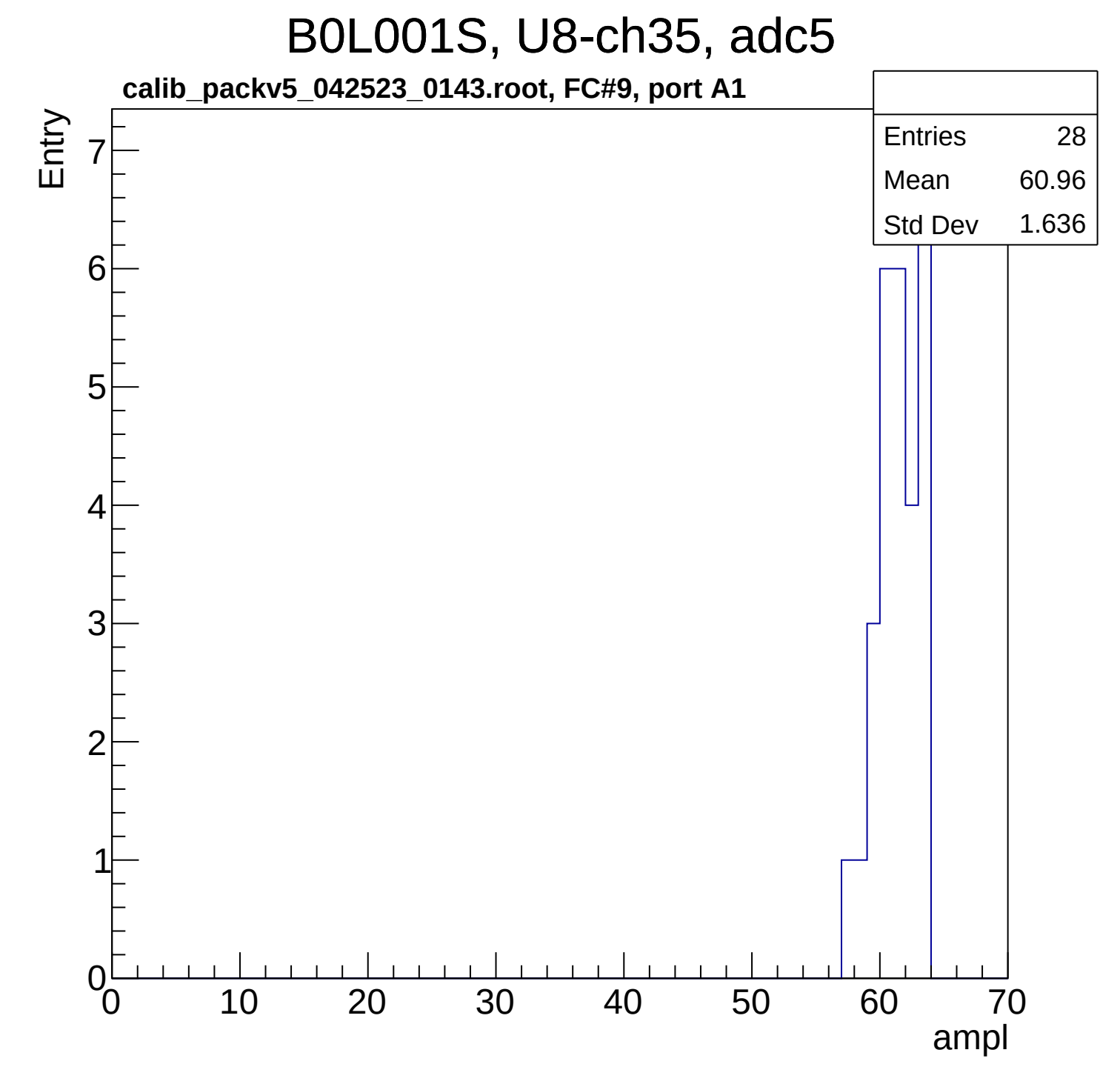
B0L001S, U8-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.96
Std Dev 1.636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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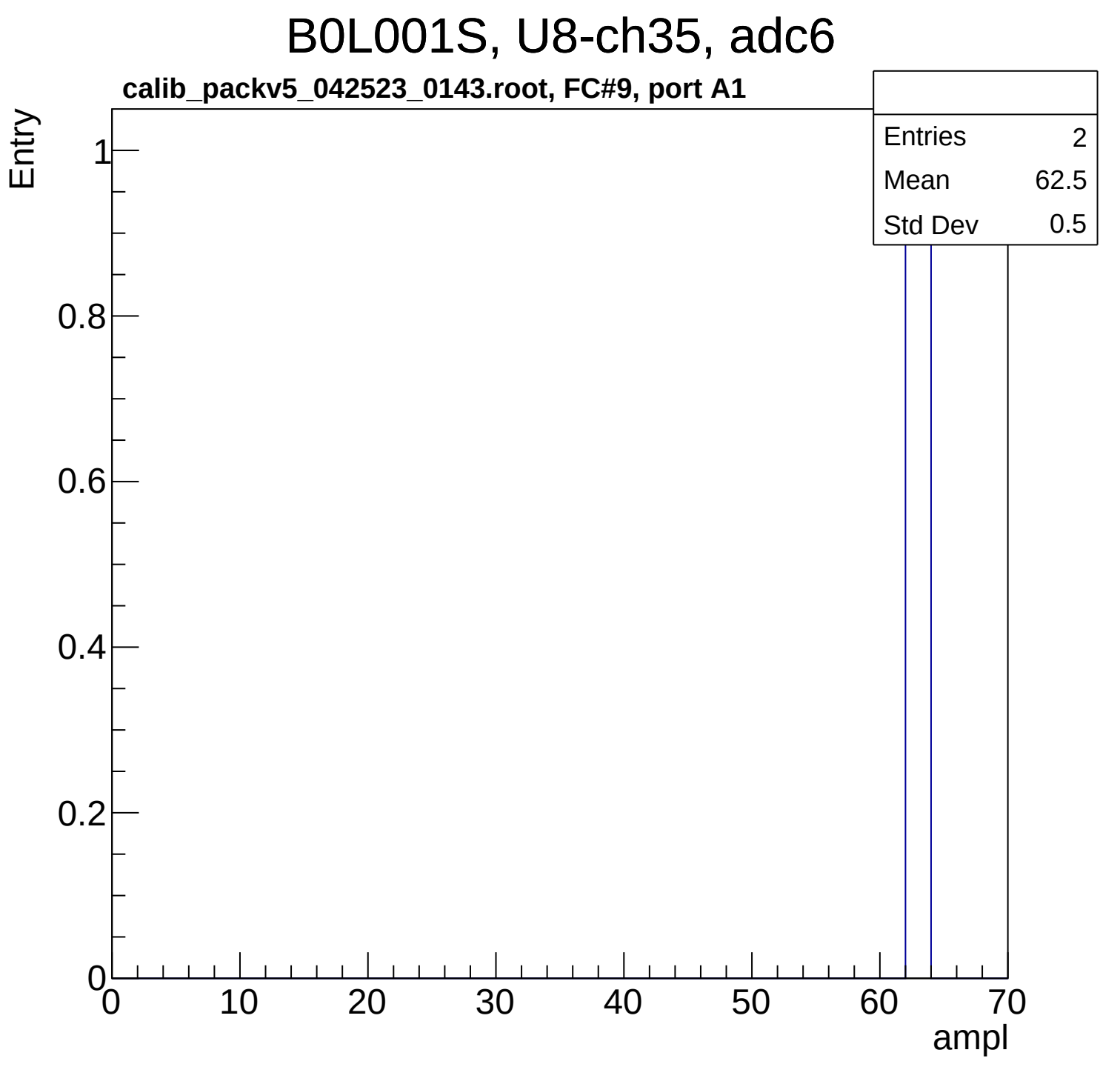
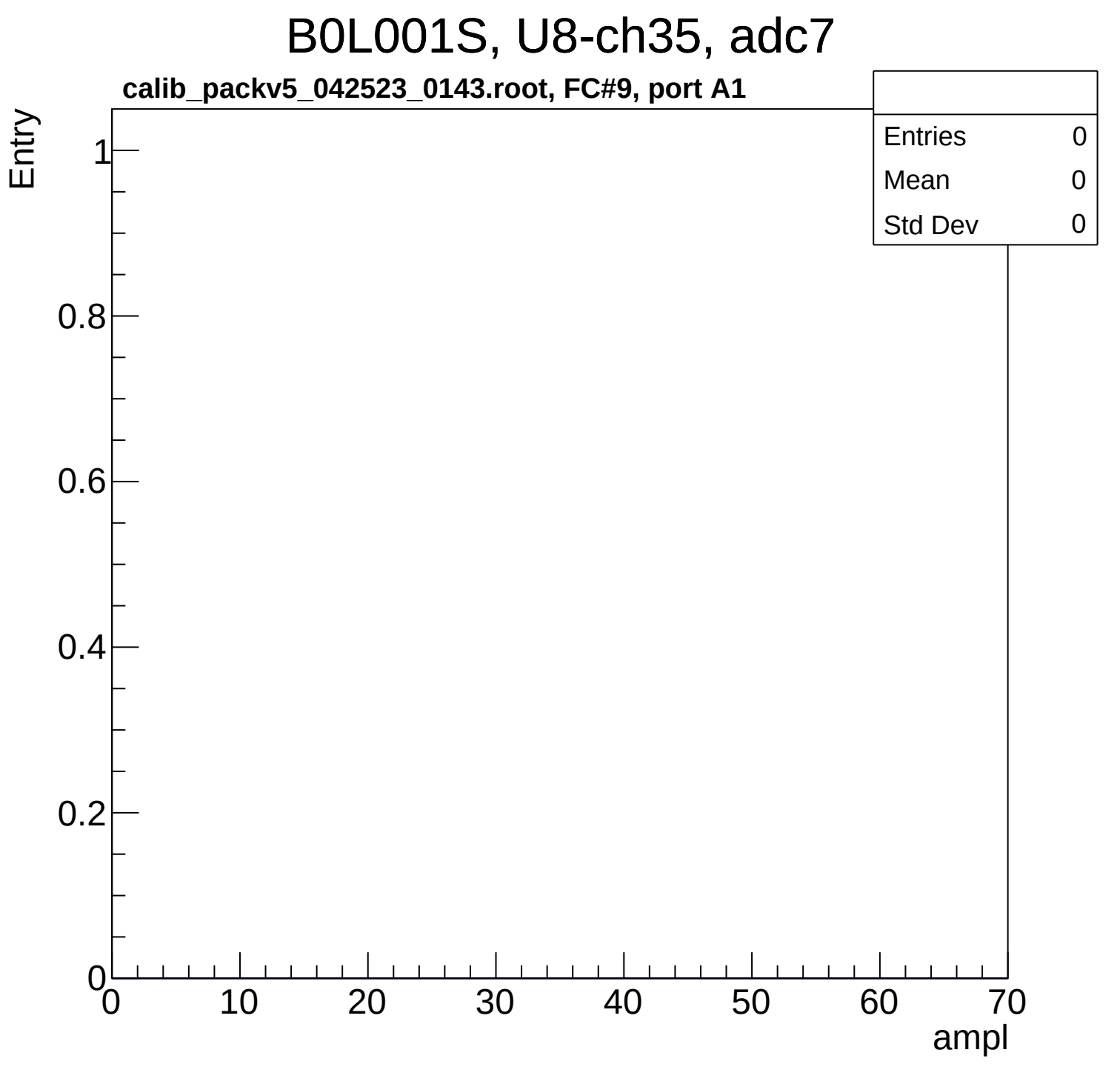

B0L001S, U8-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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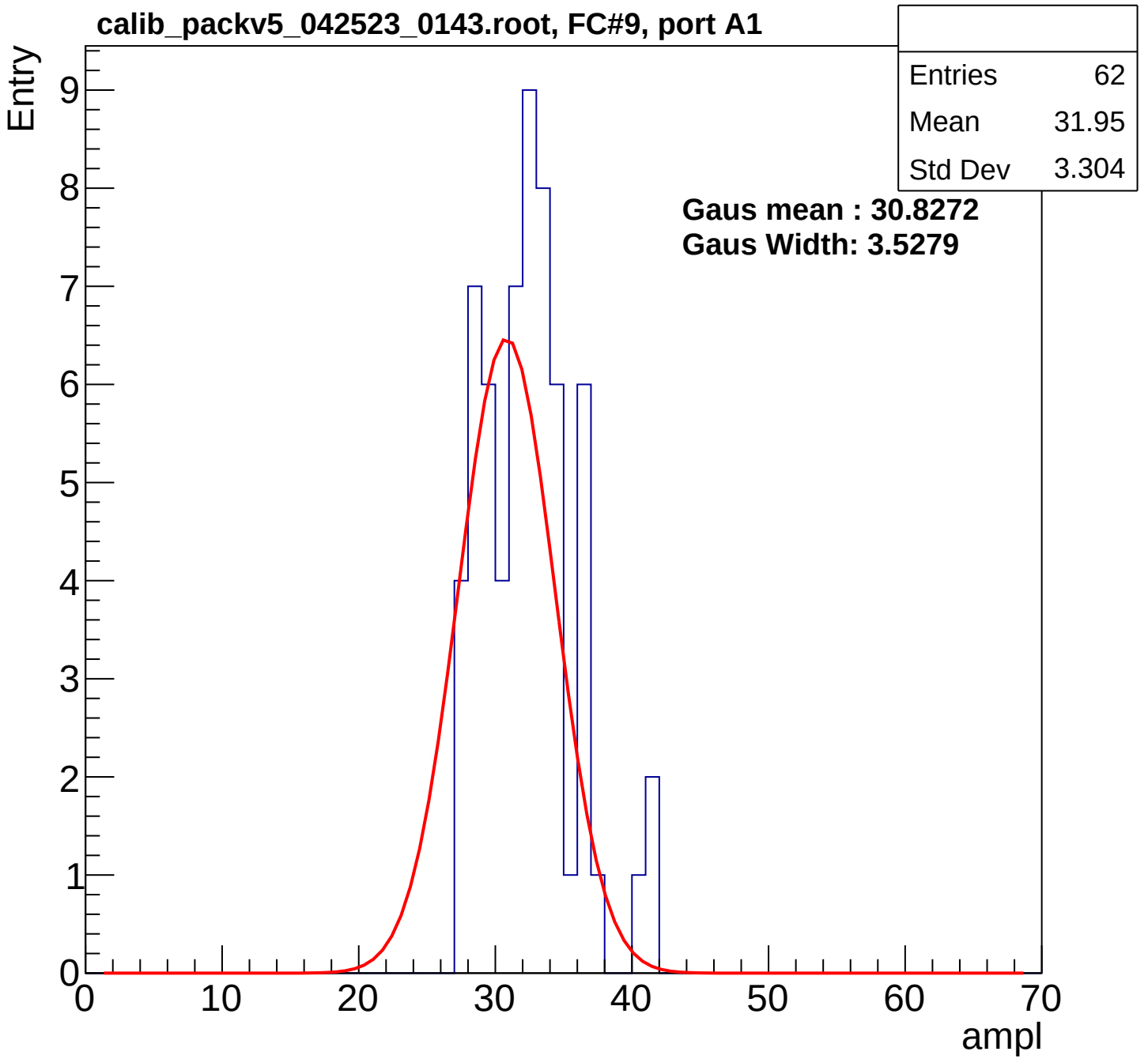
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.95
Std Dev 3.304
Gaus mean : 30.8272
Gaus Width: 3.5279
Entry
ampl
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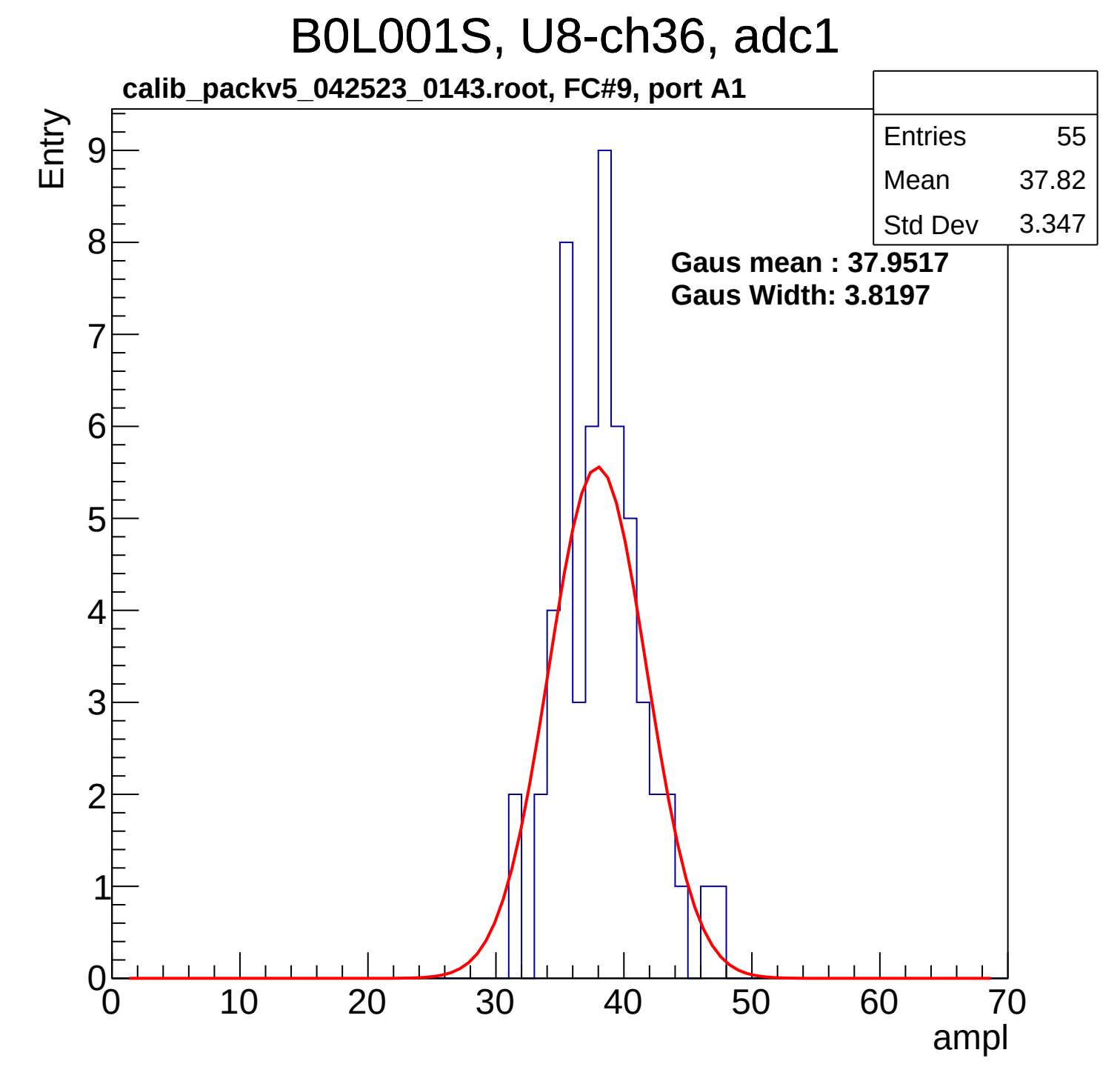
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 37.82
Std Dev 3.347
Gaus mean : 37.9517
Gaus Width: 3.8197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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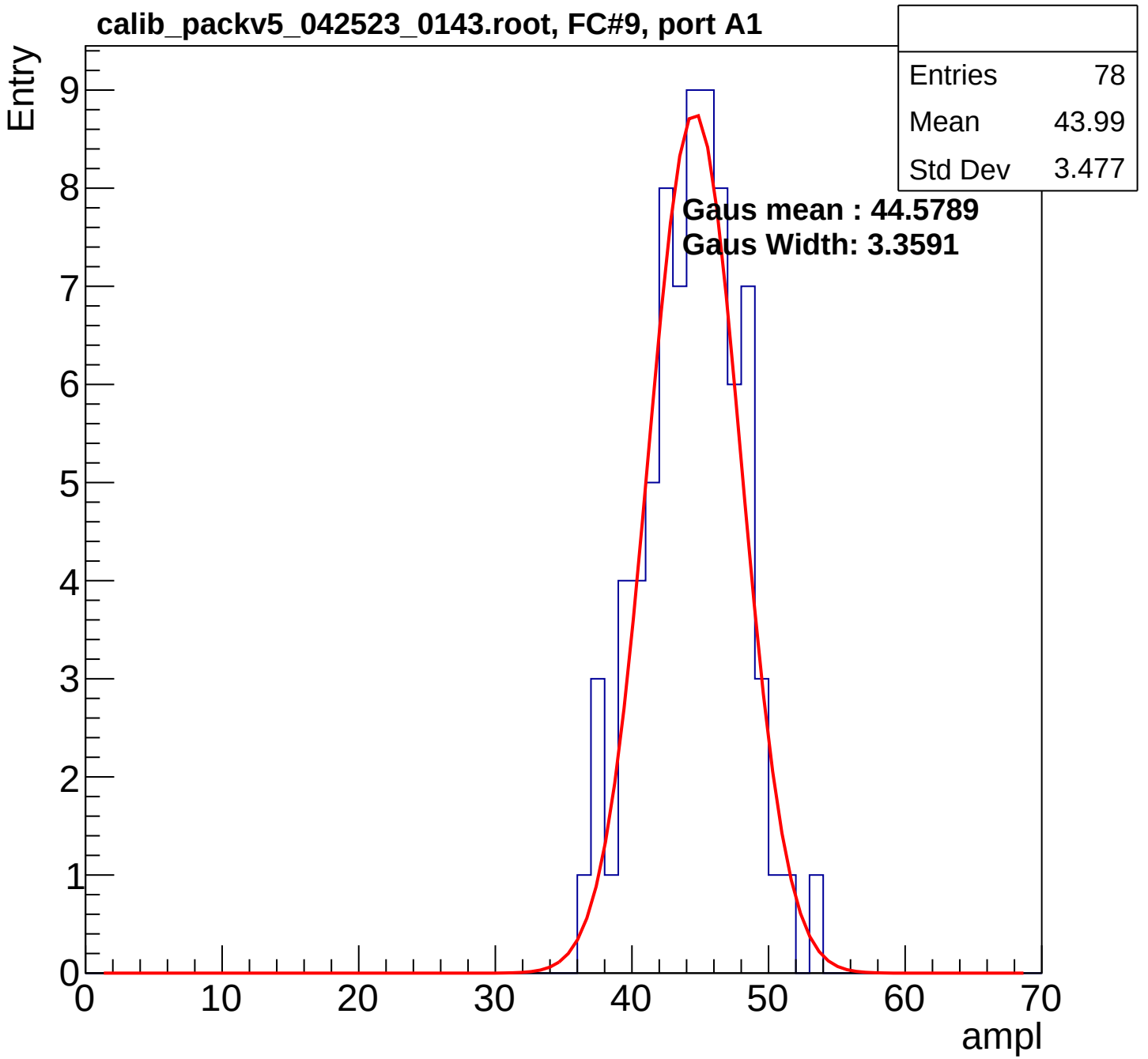
B0L001S, U8-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.99
Std Dev 3.477
Gaus mean : 44.5789
Gaus Width: 3.3591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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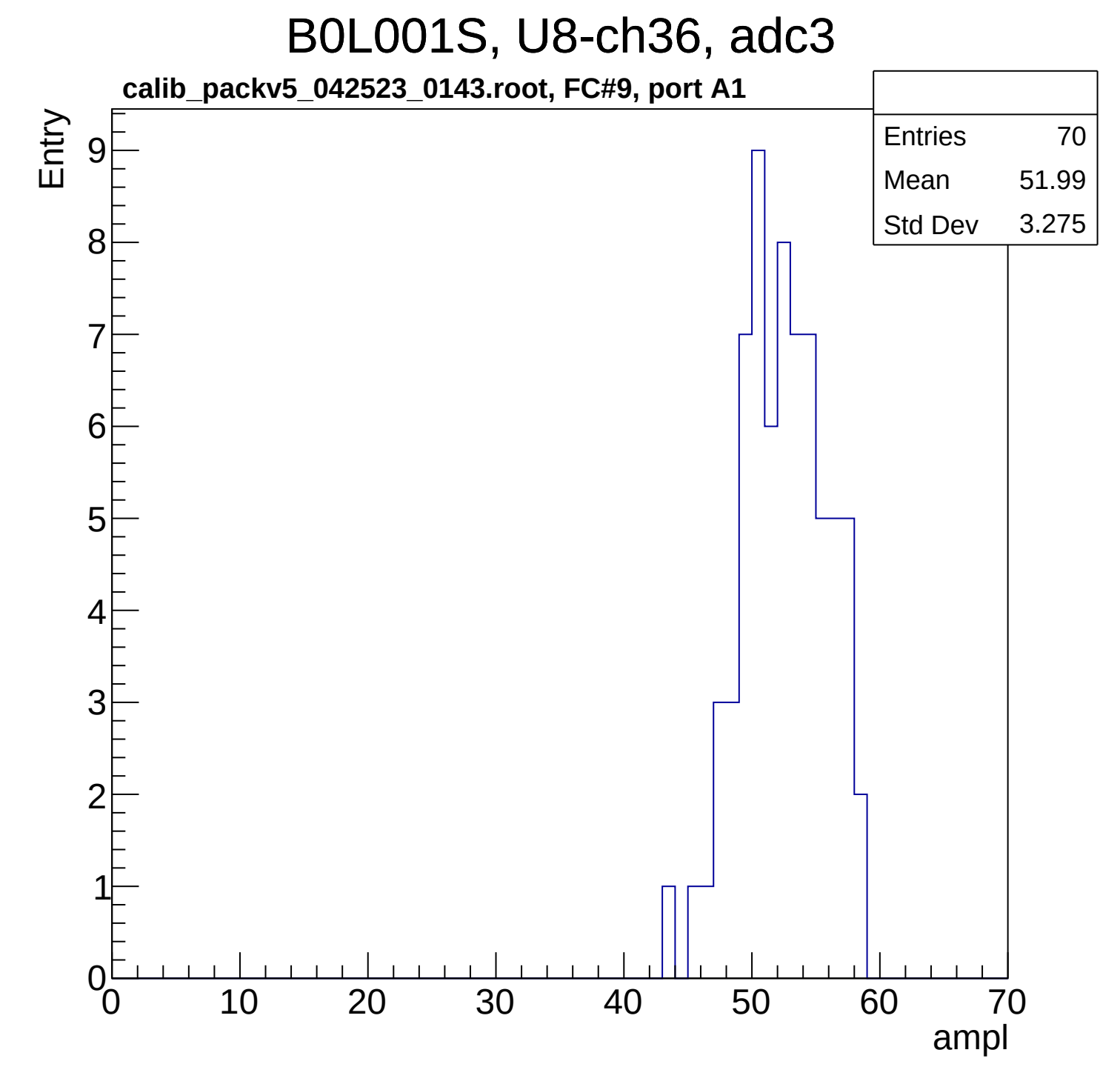
B0L001S, U8-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.99
Std Dev 3.275
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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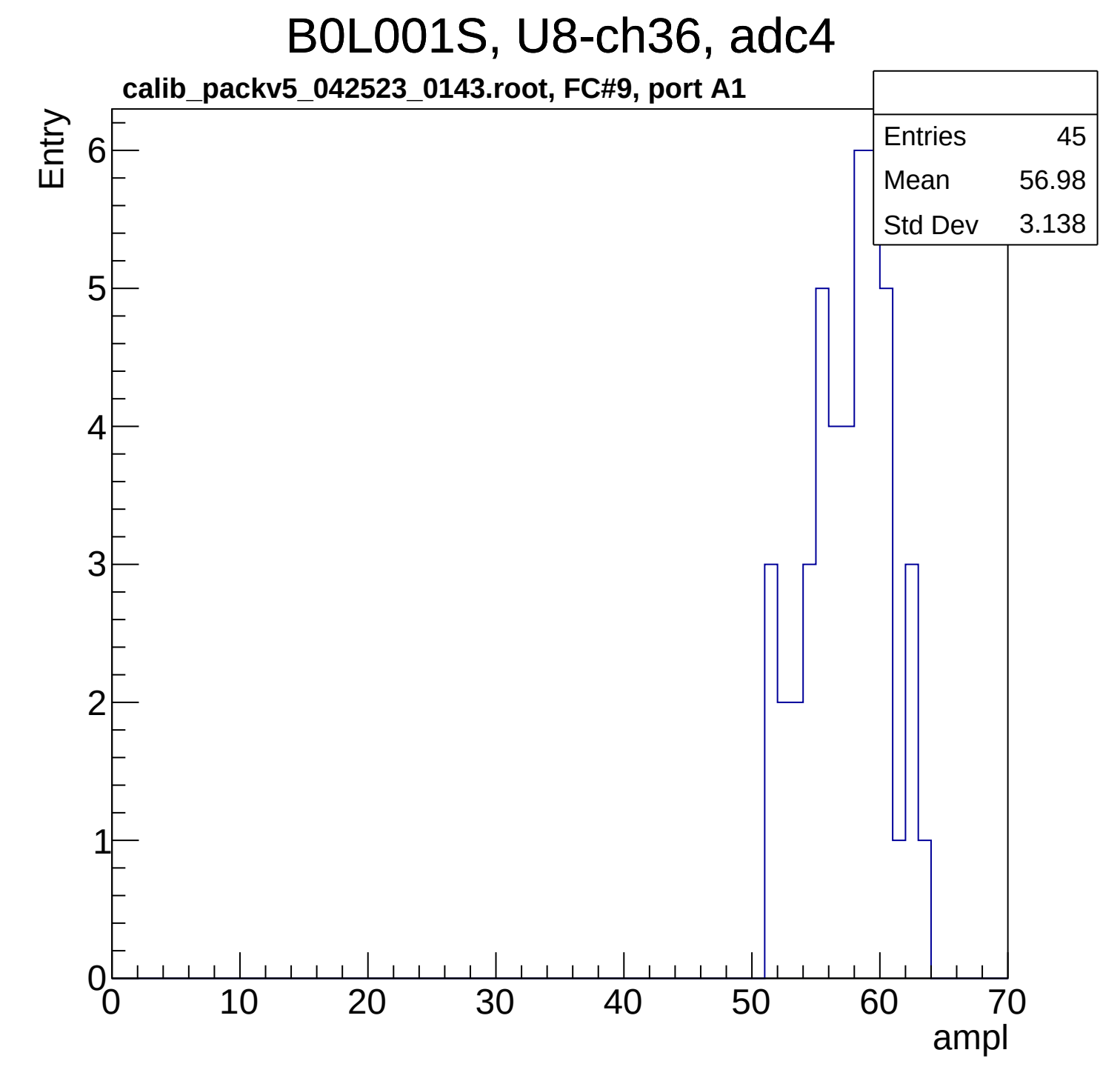
B0L001S, U8-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.98
Std Dev 3.138
Entry
ampl
0
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2
3
4
5
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0
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20
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40
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60
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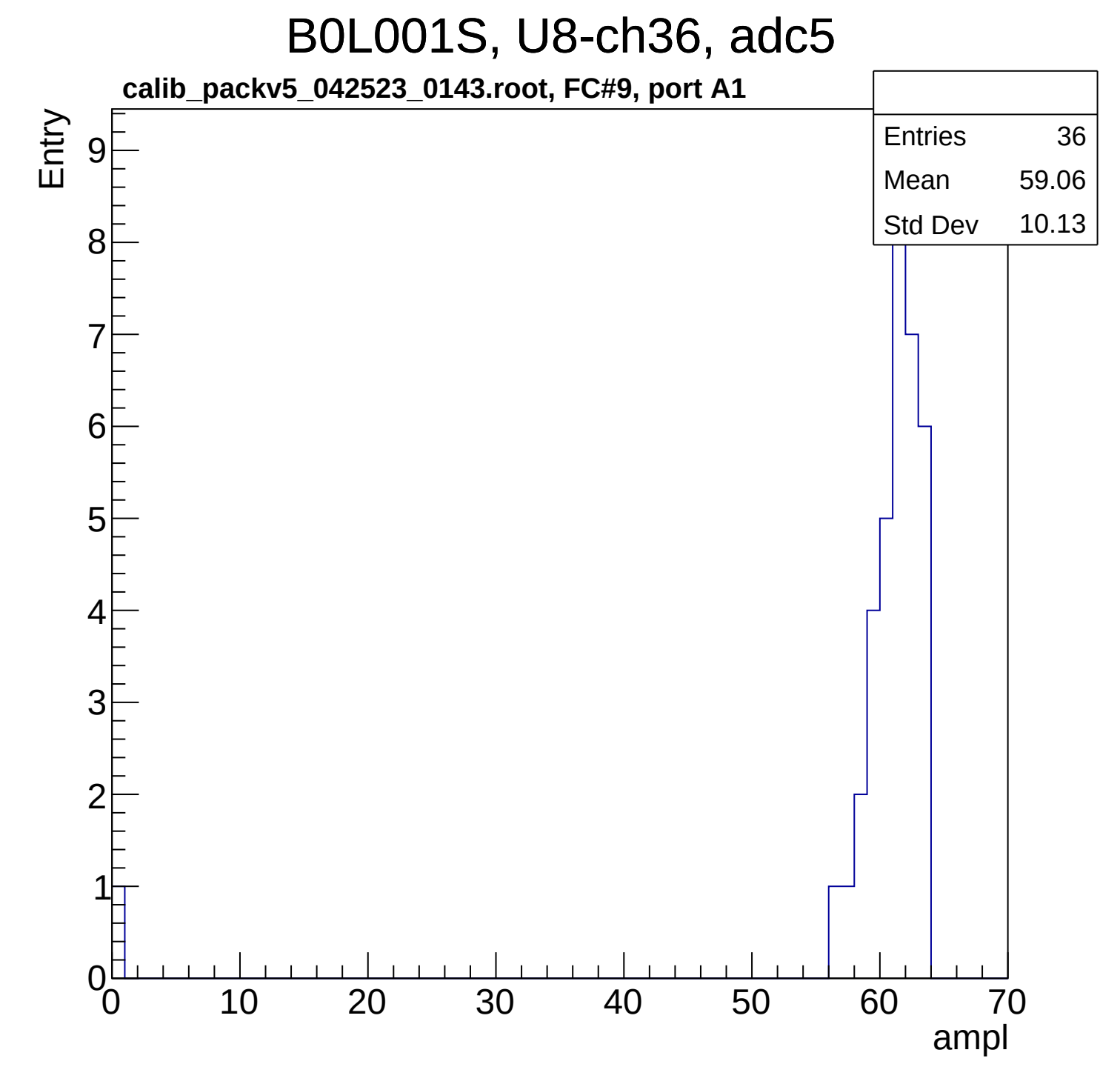
B0L001S, U8-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.06
Std Dev 10.13
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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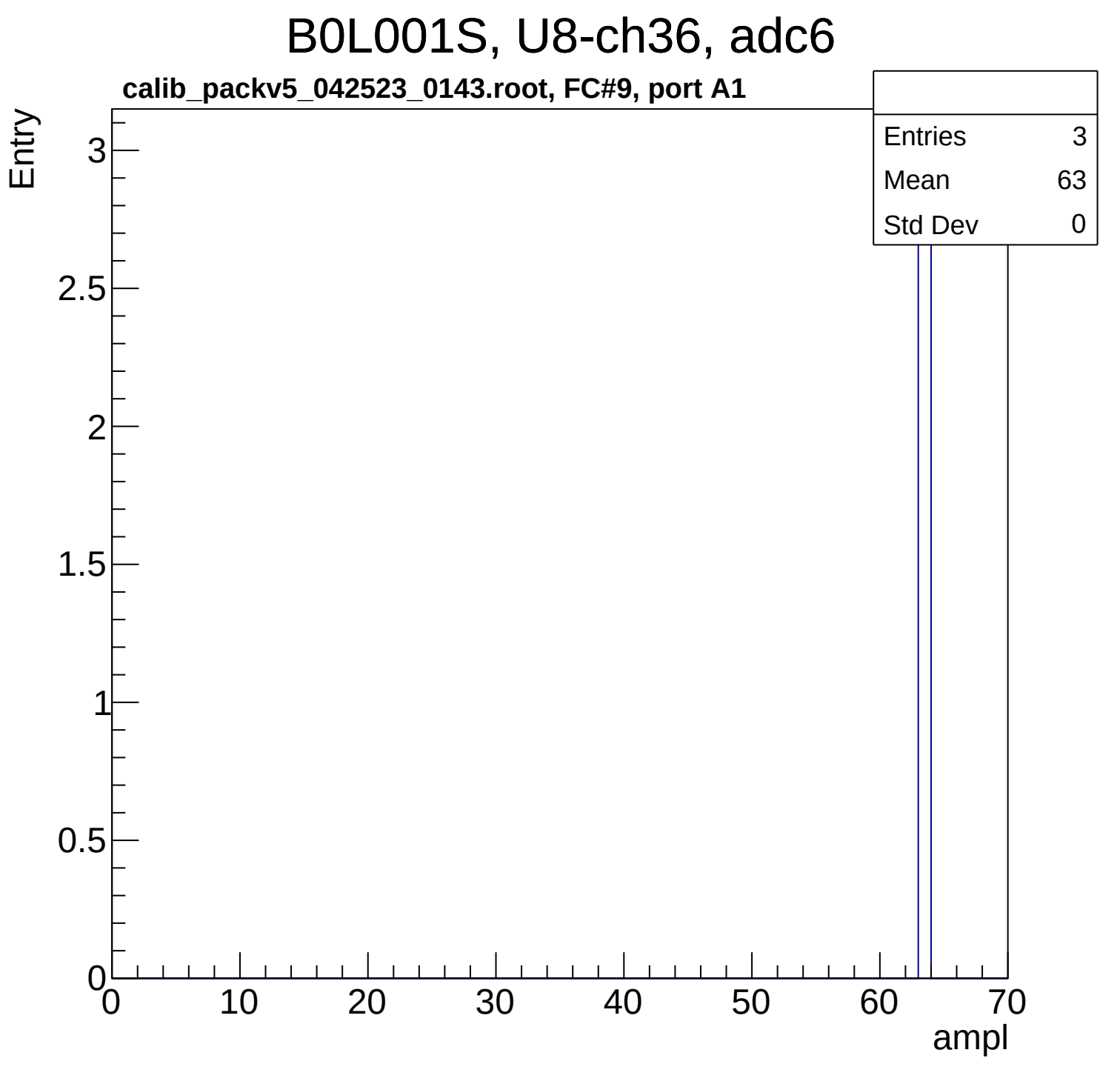
40
50
60
70


B0L001S, U8-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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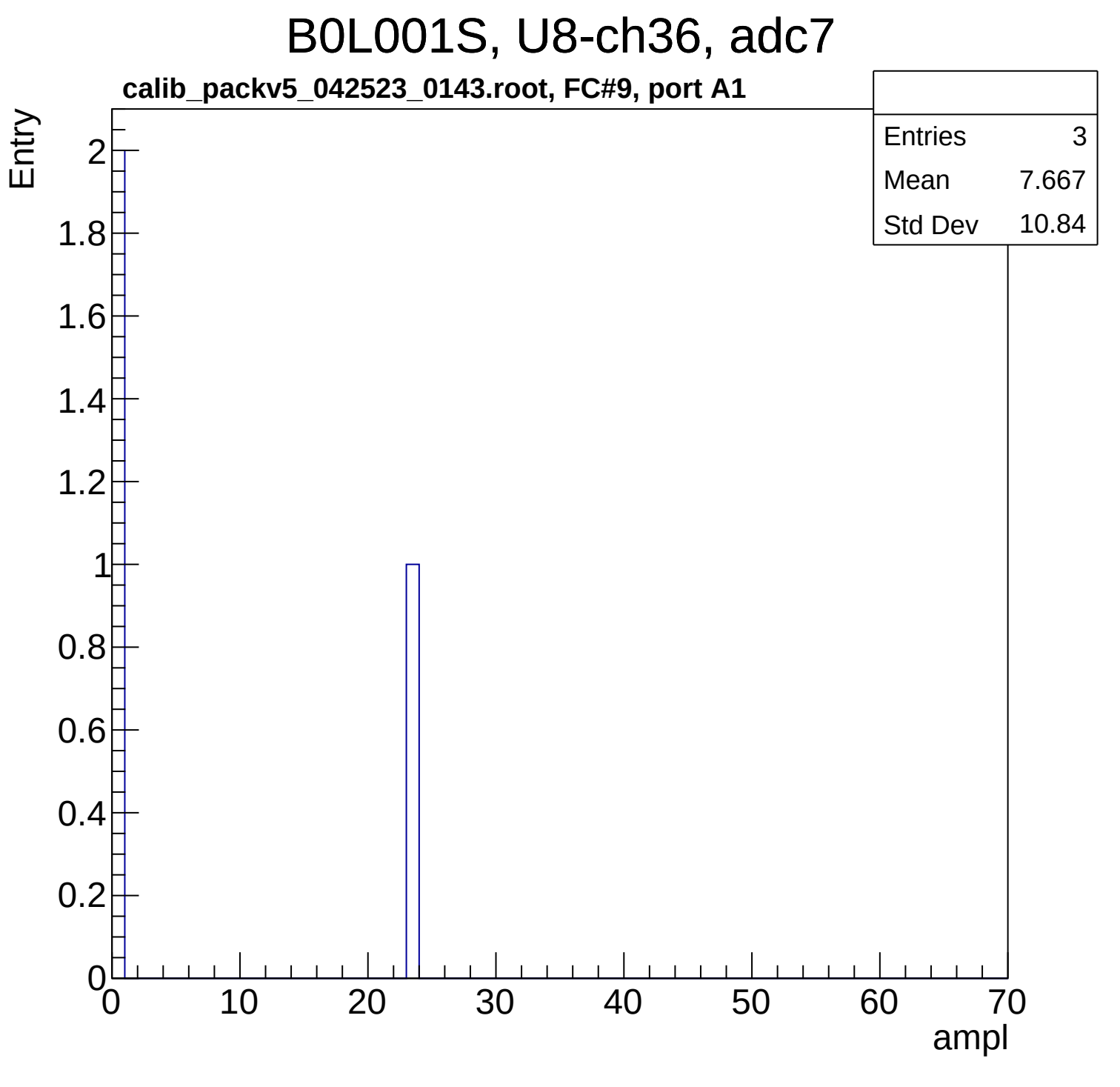
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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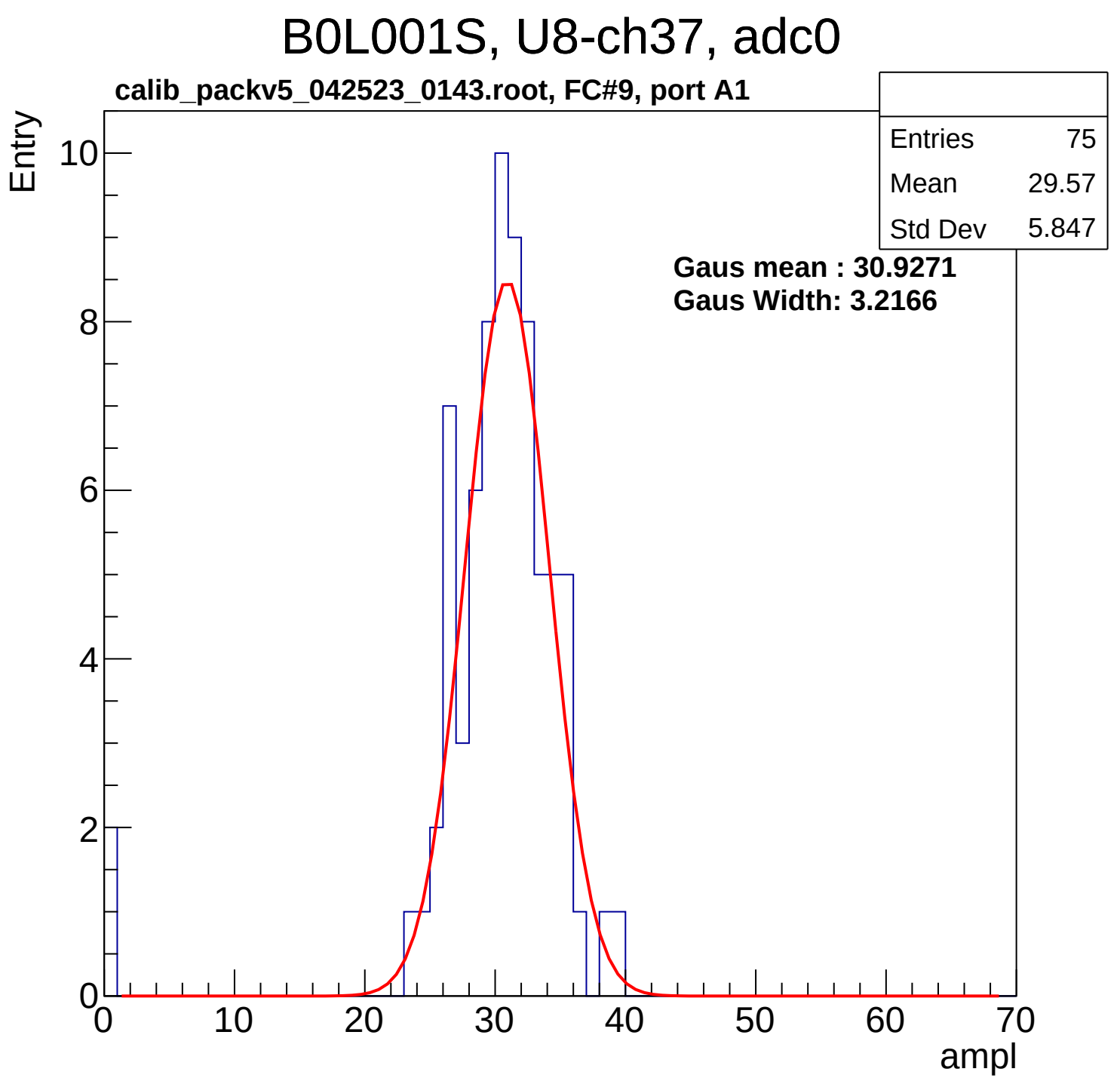
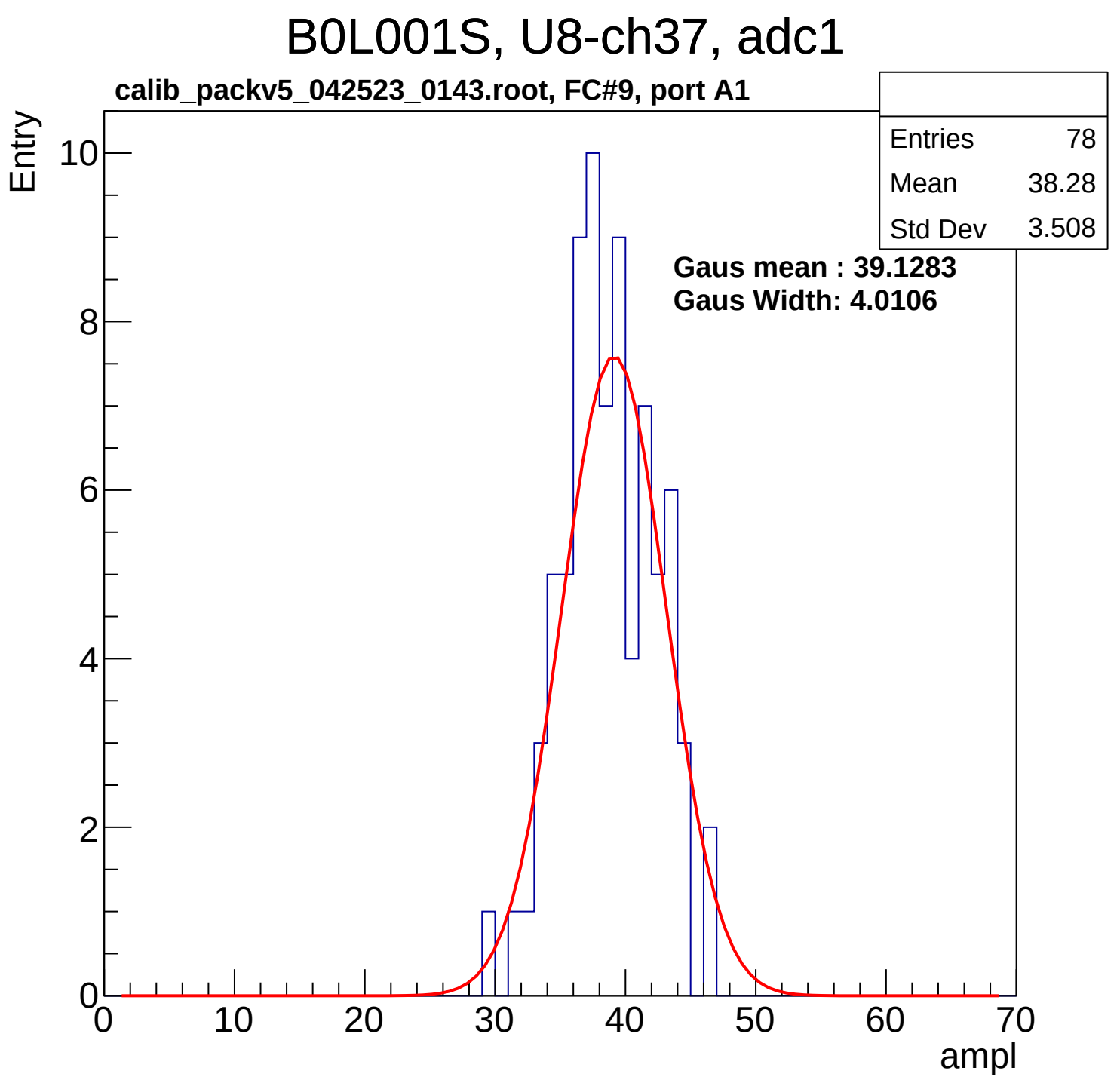
40
50
60
70


B0L001S, U8-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.57
Std Dev 5.847
Gaus mean : 30.9271
Gaus Width: 3.2166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.28
Std Dev 3.508
Gaus mean : 39.1283
Gaus Width: 4.0106
Entry
ampl
0
2
4
6
8
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0
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20
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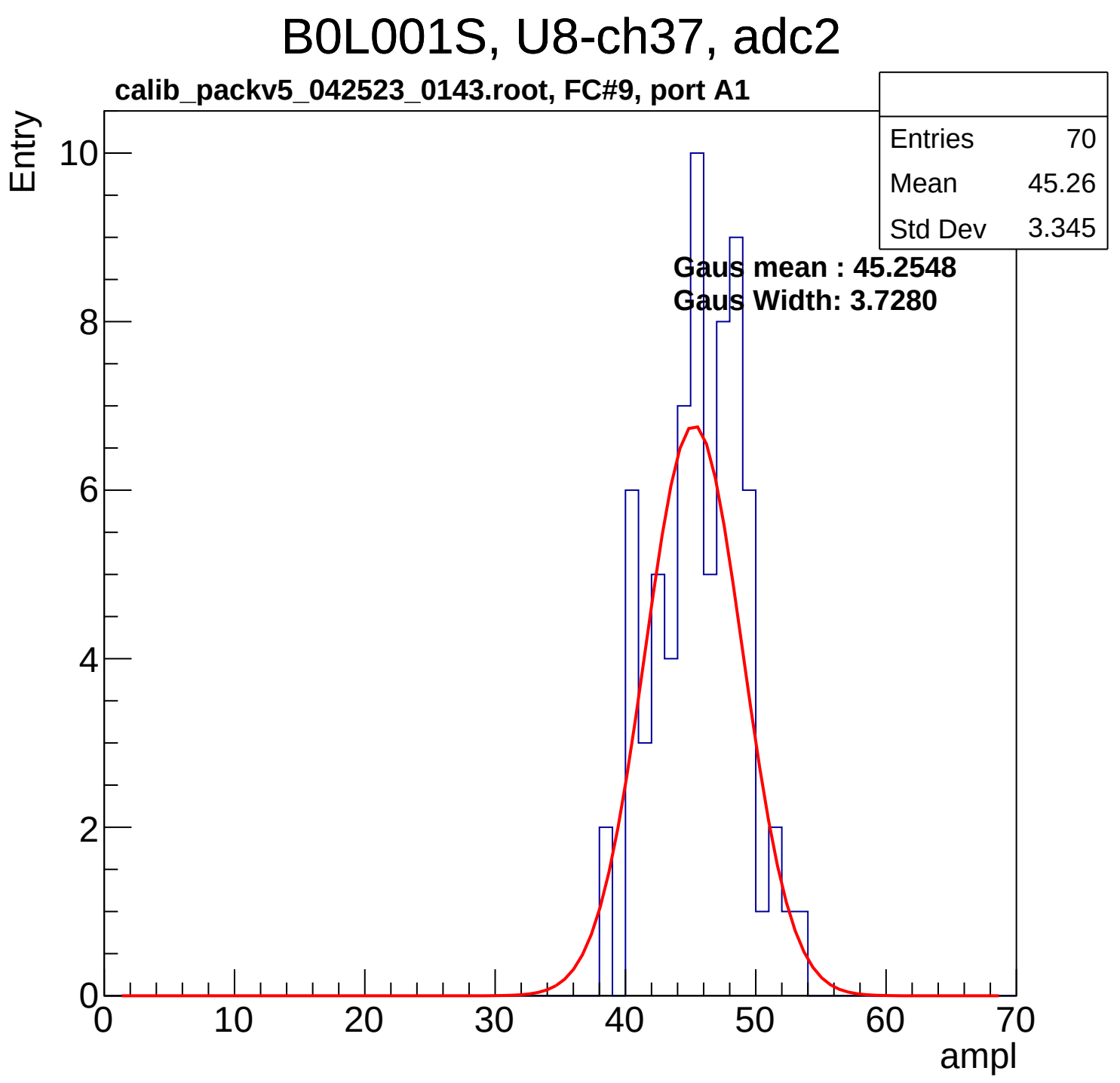
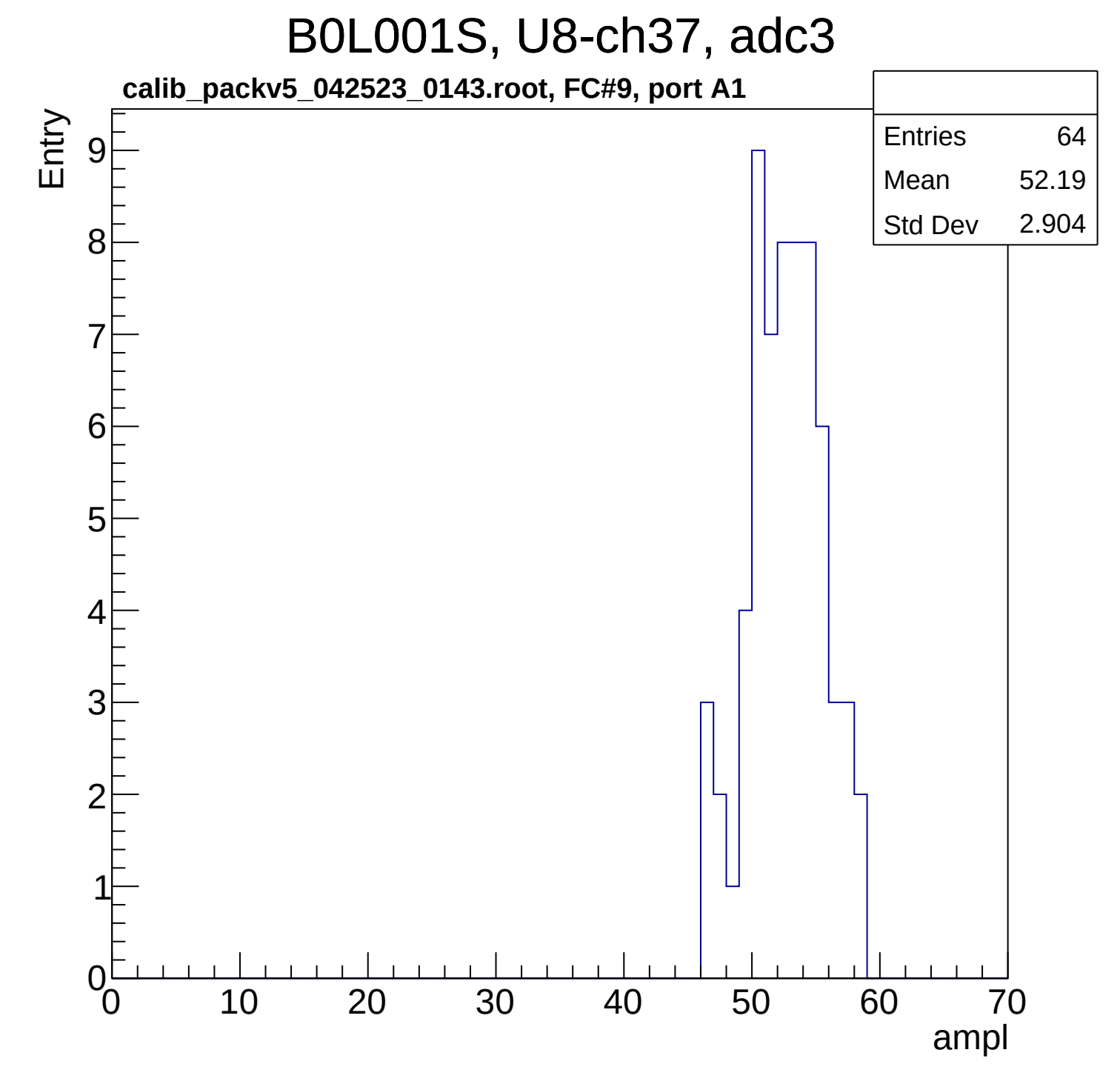
60
70


B0L001S, U8-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 45.26
Std Dev 3.345
Gaus mean : 45.2548
Gaus Width: 3.7280
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.19
Std Dev 2.904
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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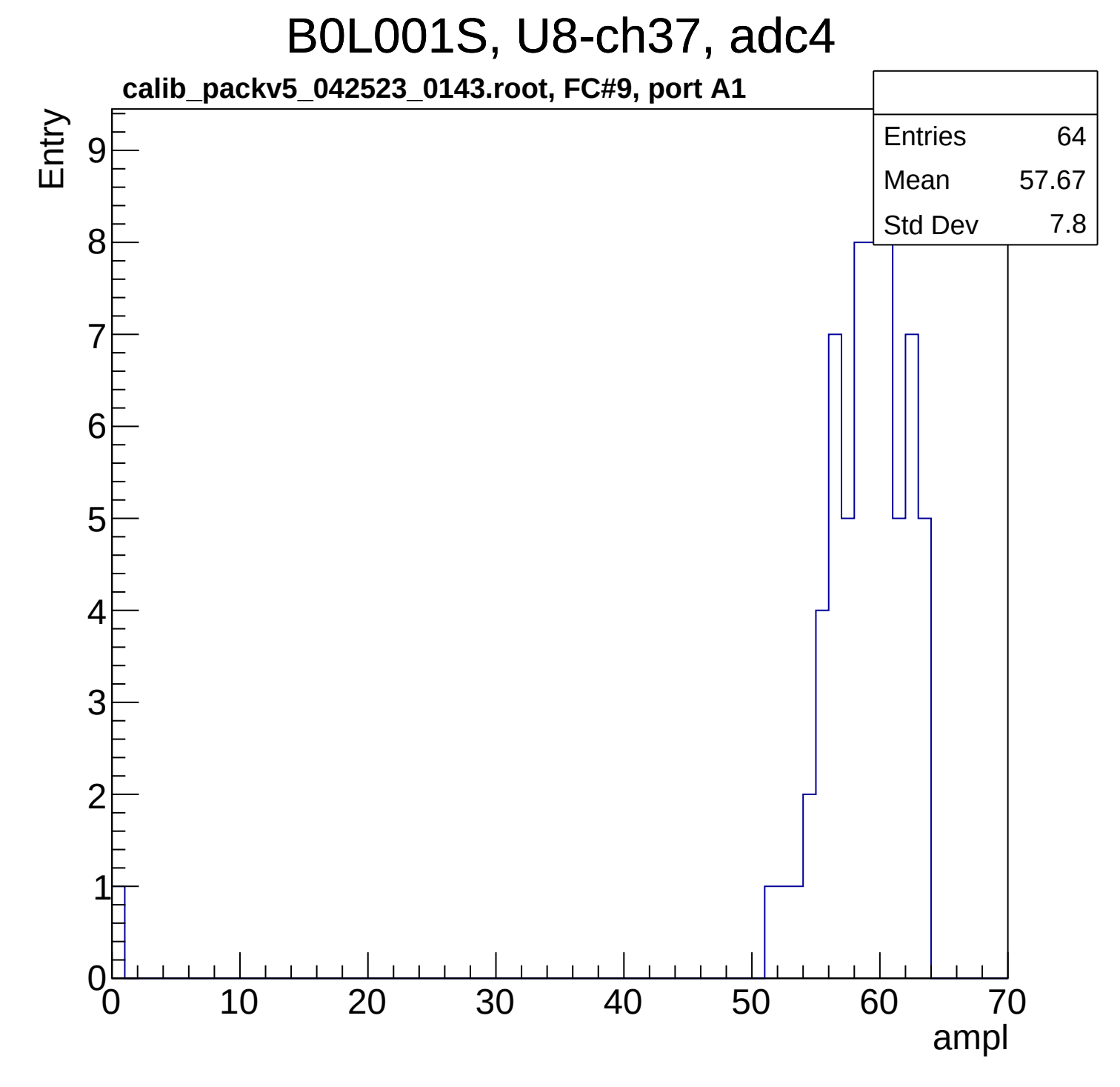
60
70


B0L001S, U8-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.67
Std Dev 7.8
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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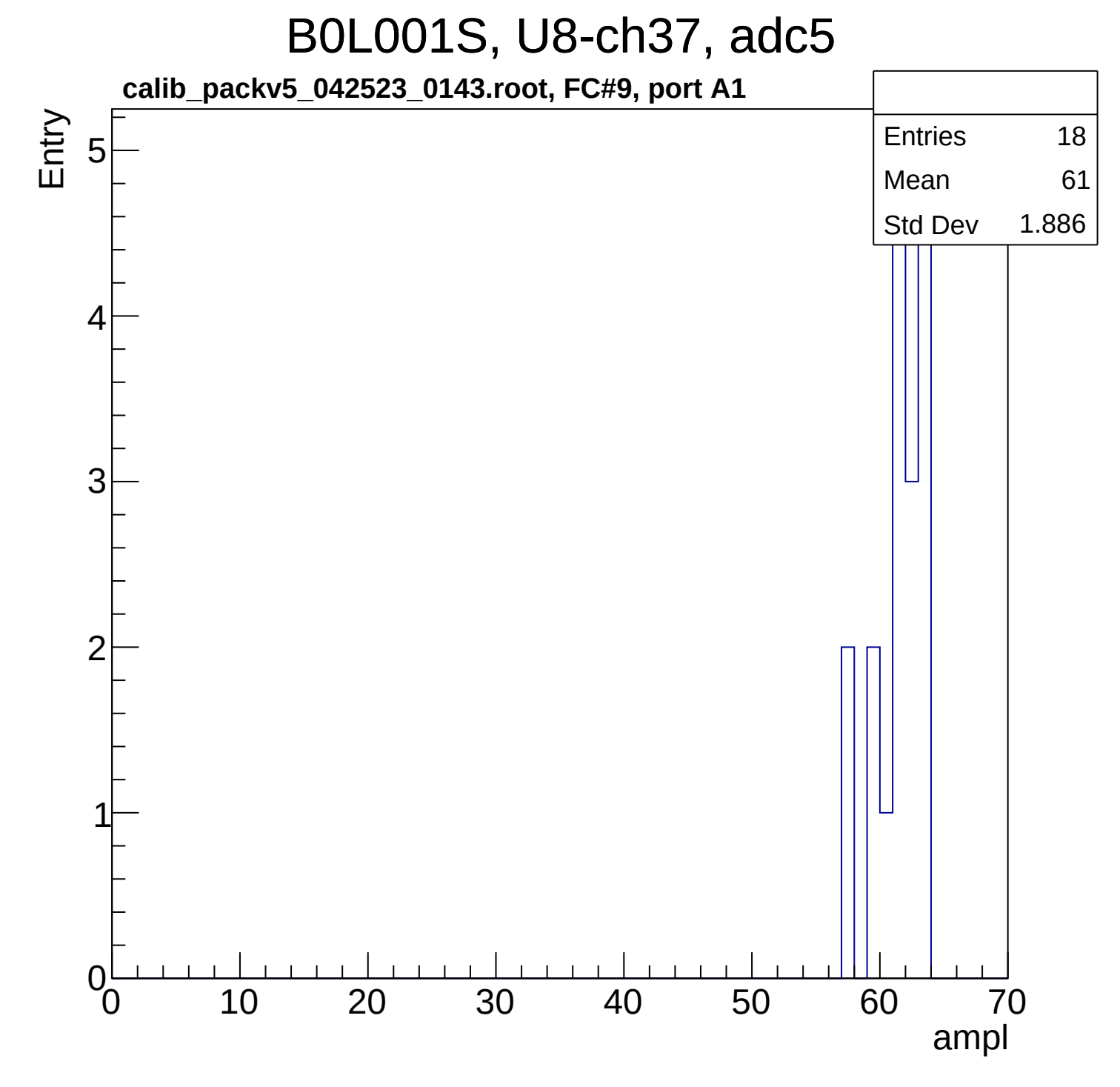
B0L001S, U8-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 61
Std Dev 1.886
Entry
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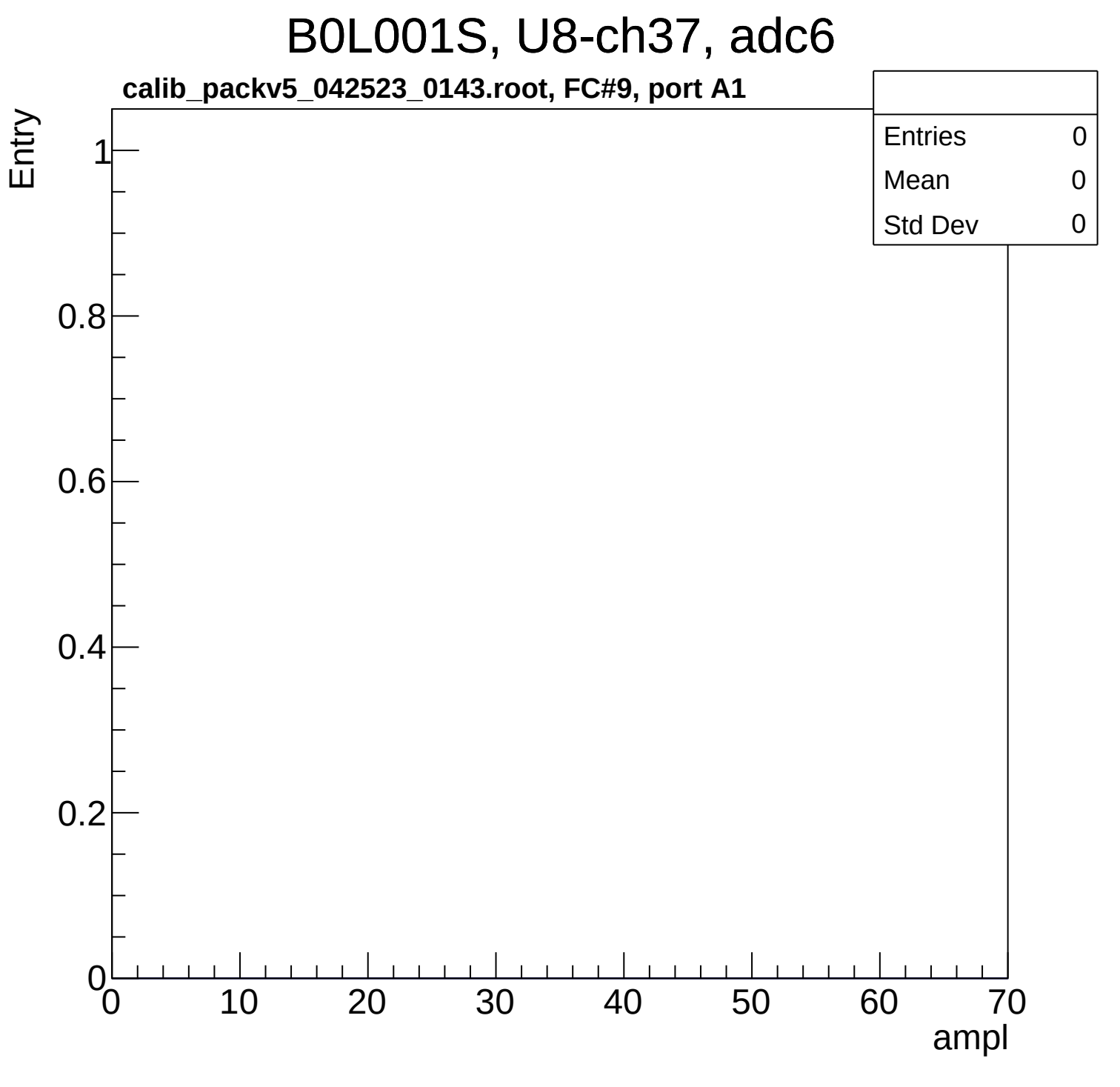
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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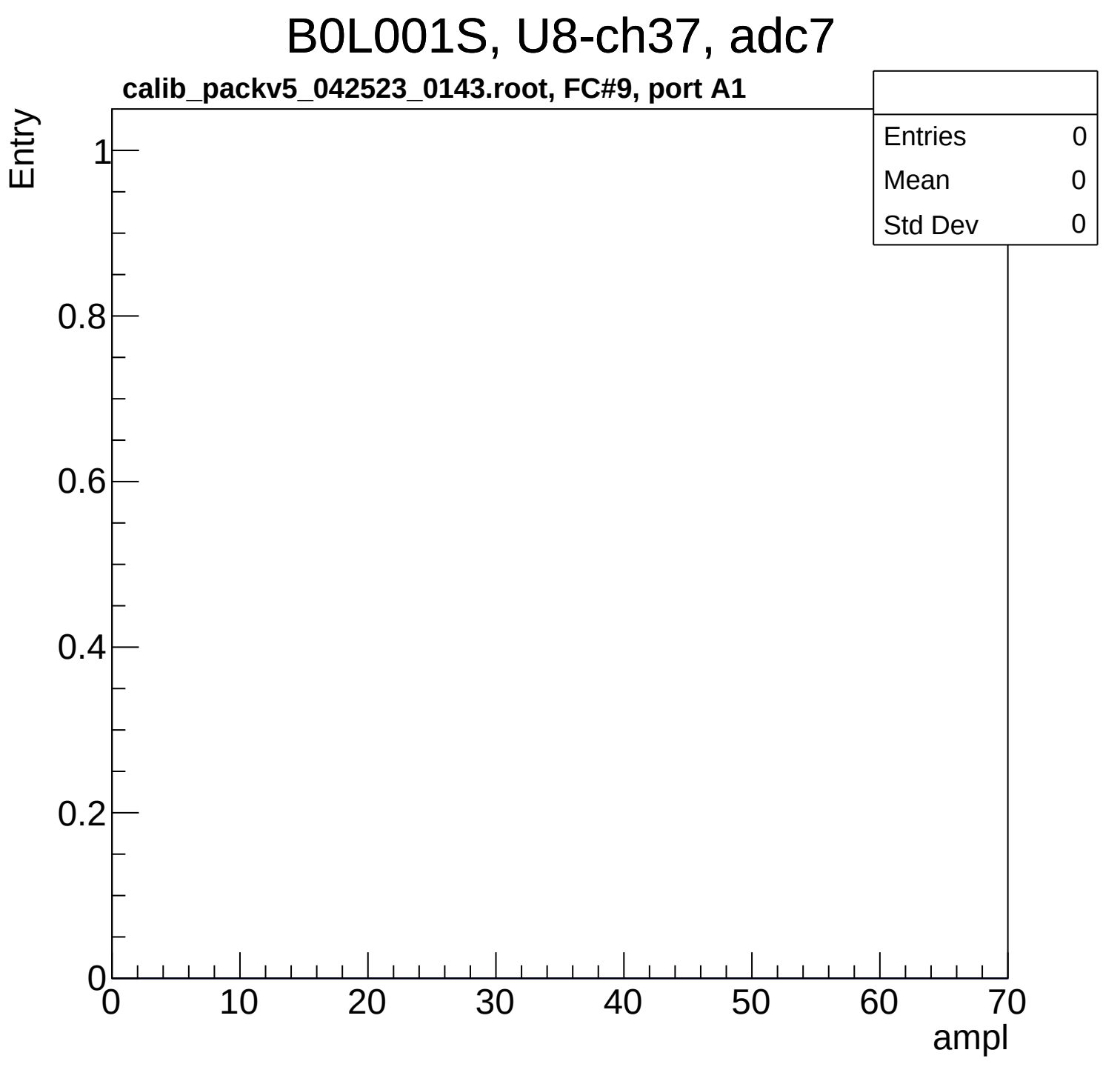
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
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10
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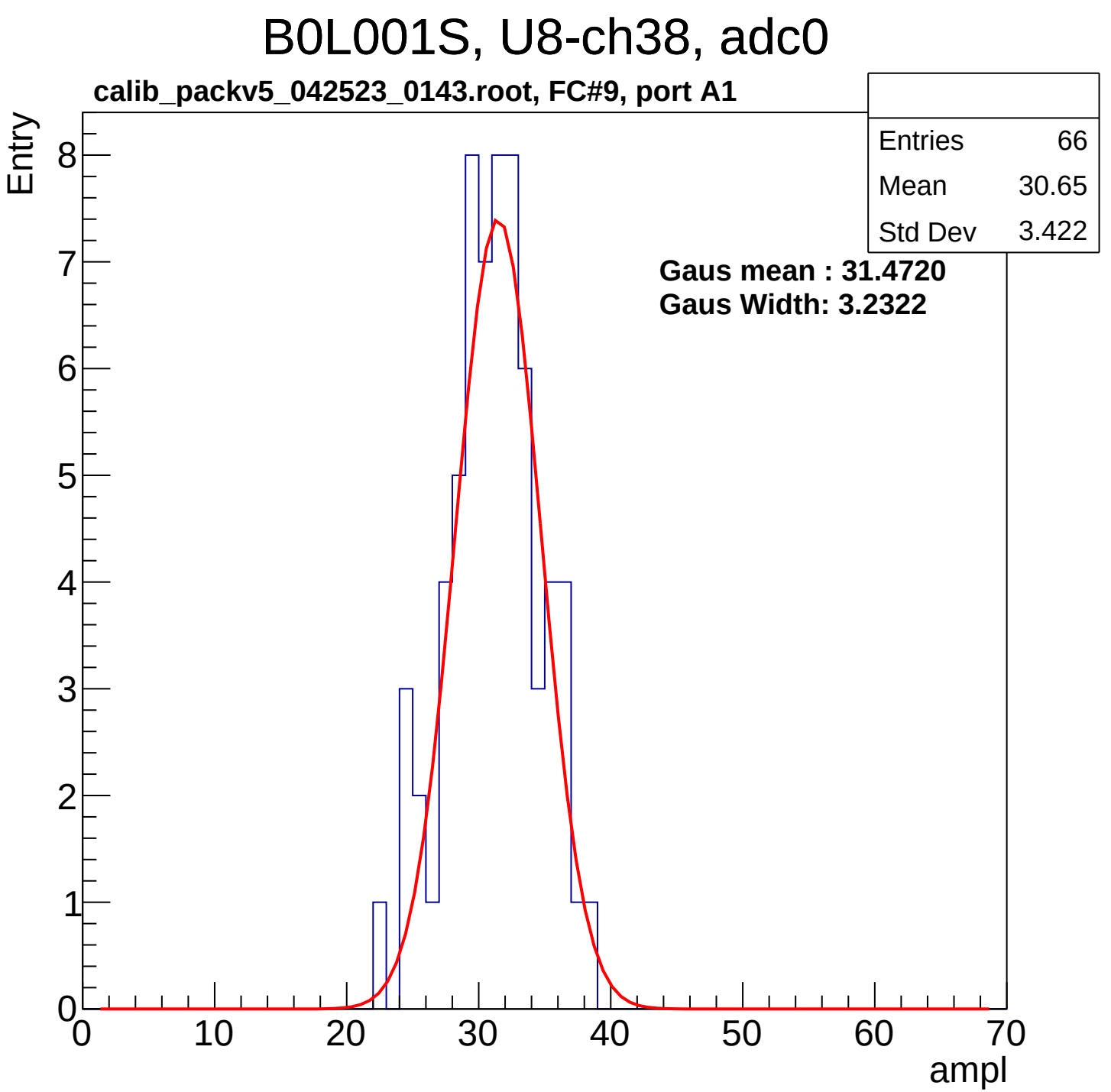
70
ampl


B0L001S, U8-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.65
Std Dev 3.422
Gaus mean : 31.4720
Gaus Width: 3.2322
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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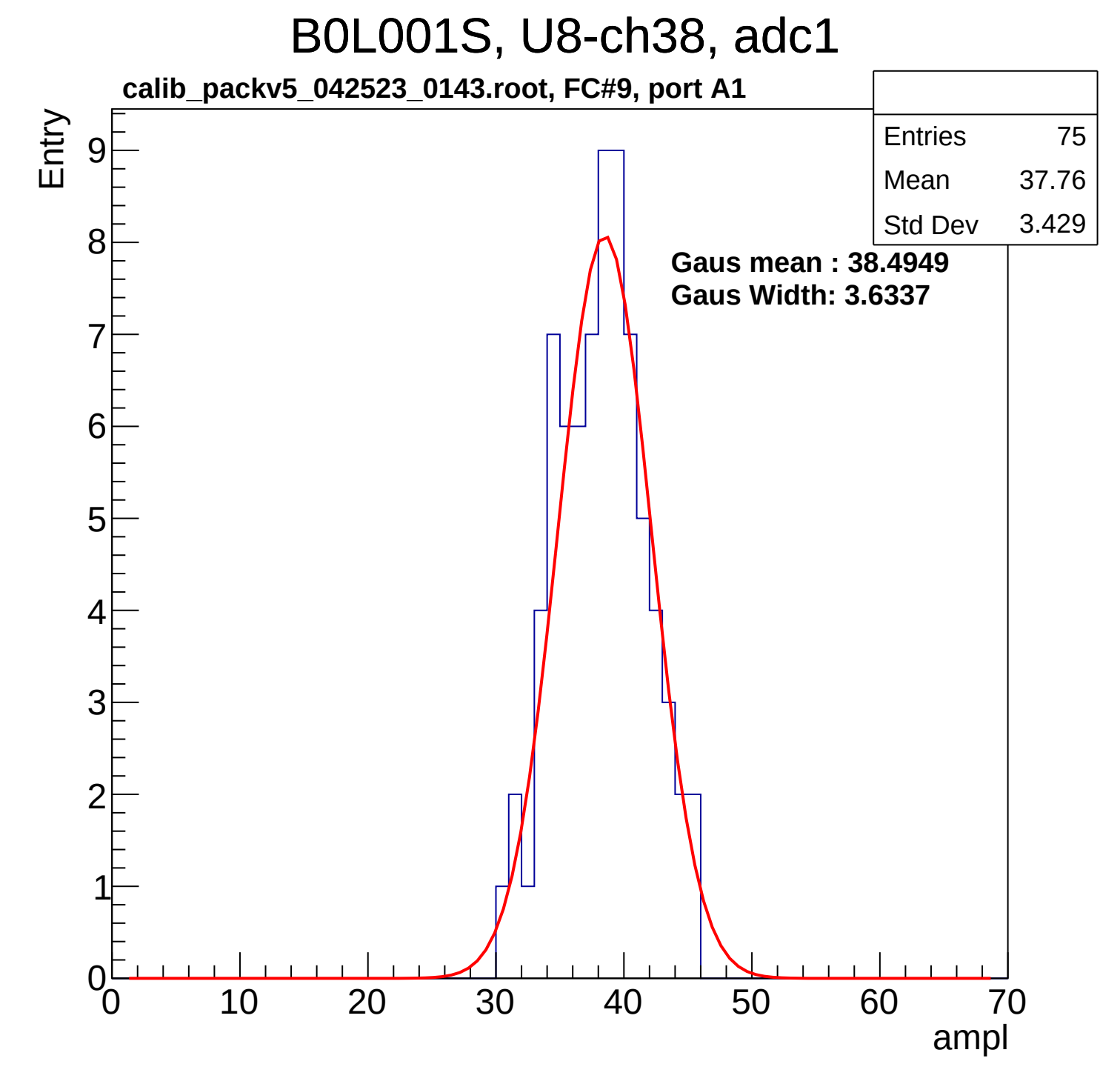
40
50
60
70


B0L001S, U8-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.76
Std Dev 3.429
Gaus mean : 38.4949
Gaus Width: 3.6337
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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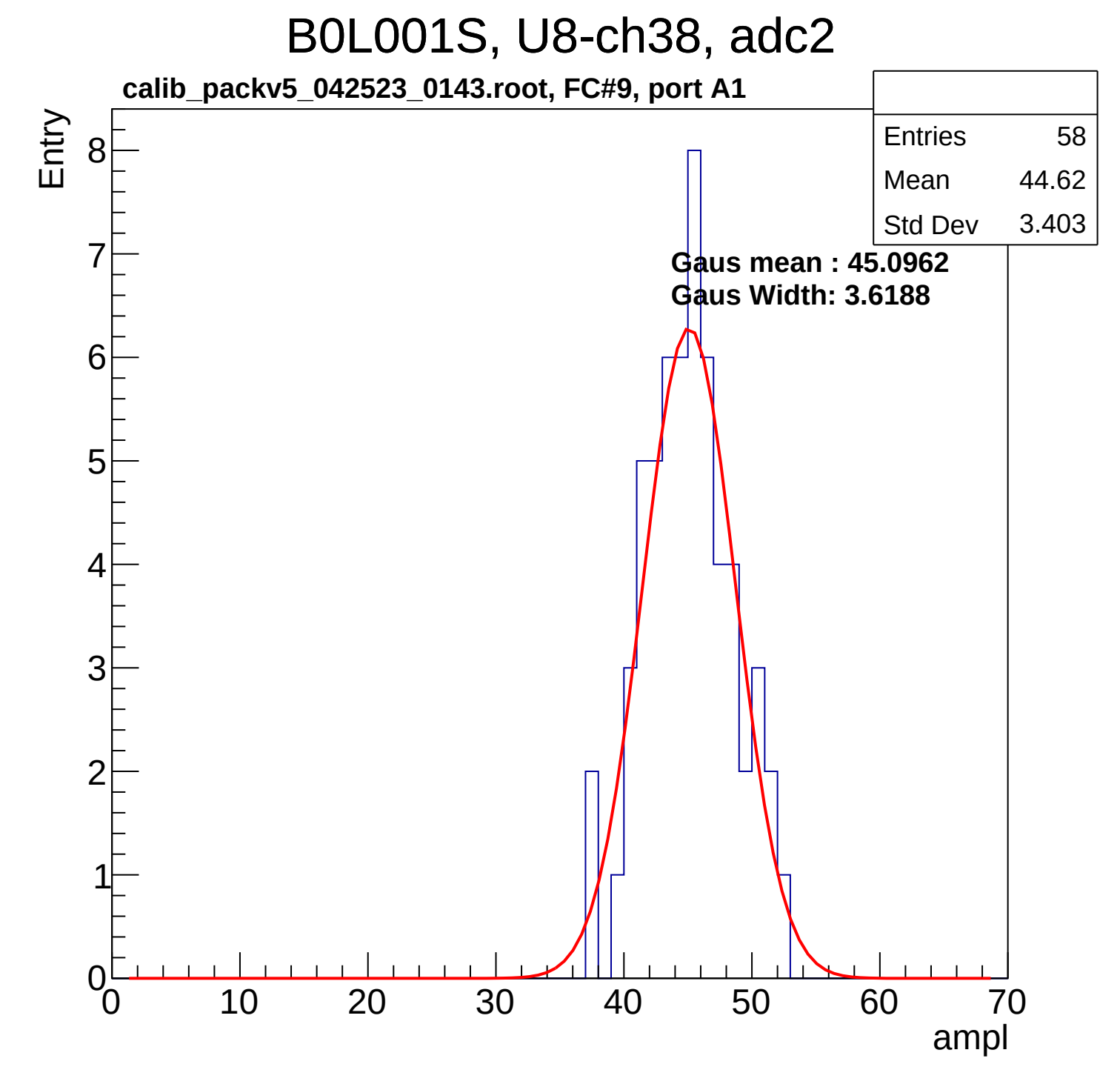
B0L001S, U8-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.62
Std Dev 3.403
Gaus mean : 45.0962
Gaus Width: 3.6188
Entry
ampl
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3
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6
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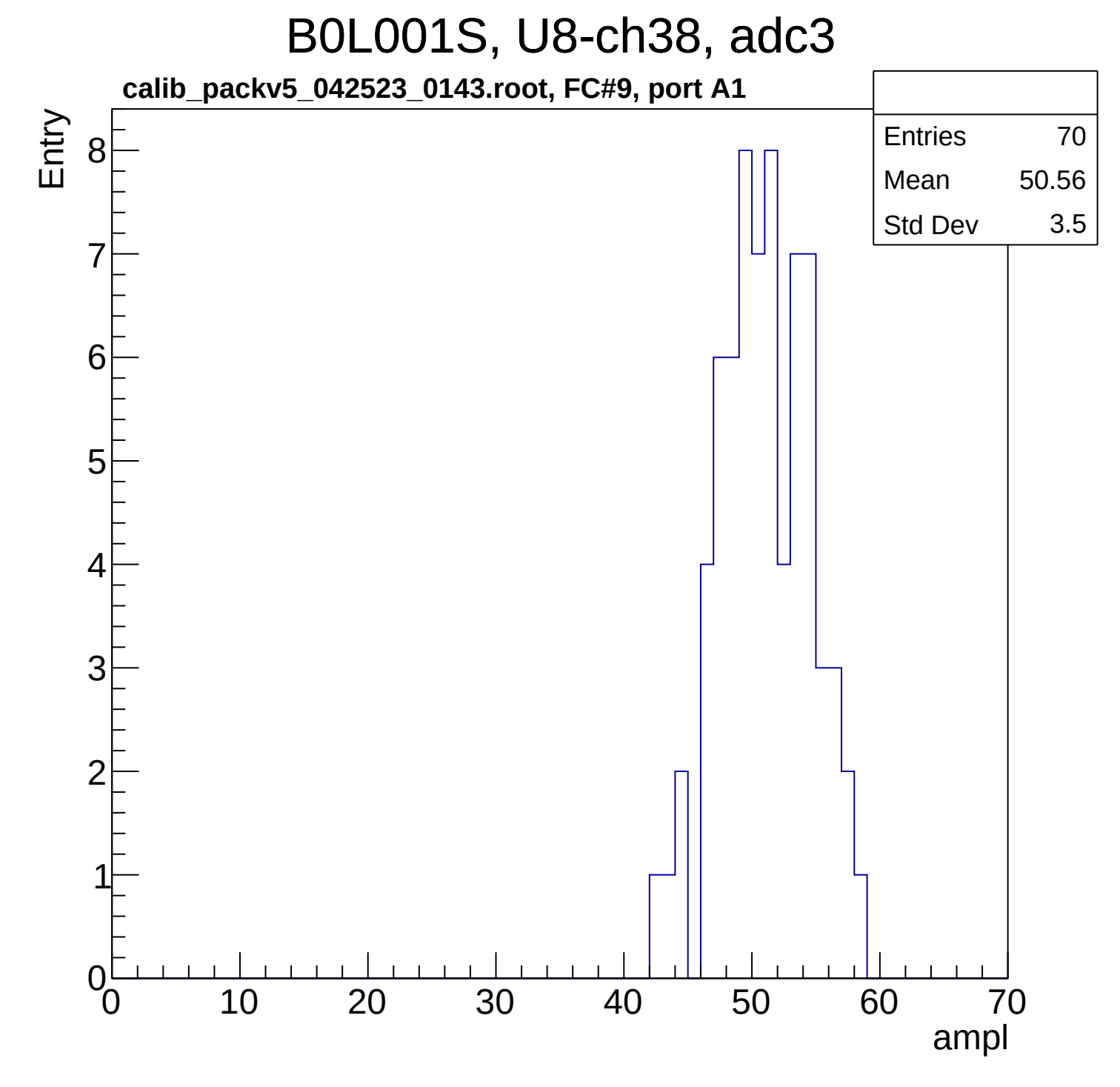
B0L001S, U8-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.56
Std Dev 3.5
Entry
ampl
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2
3
4
5
6
7
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40
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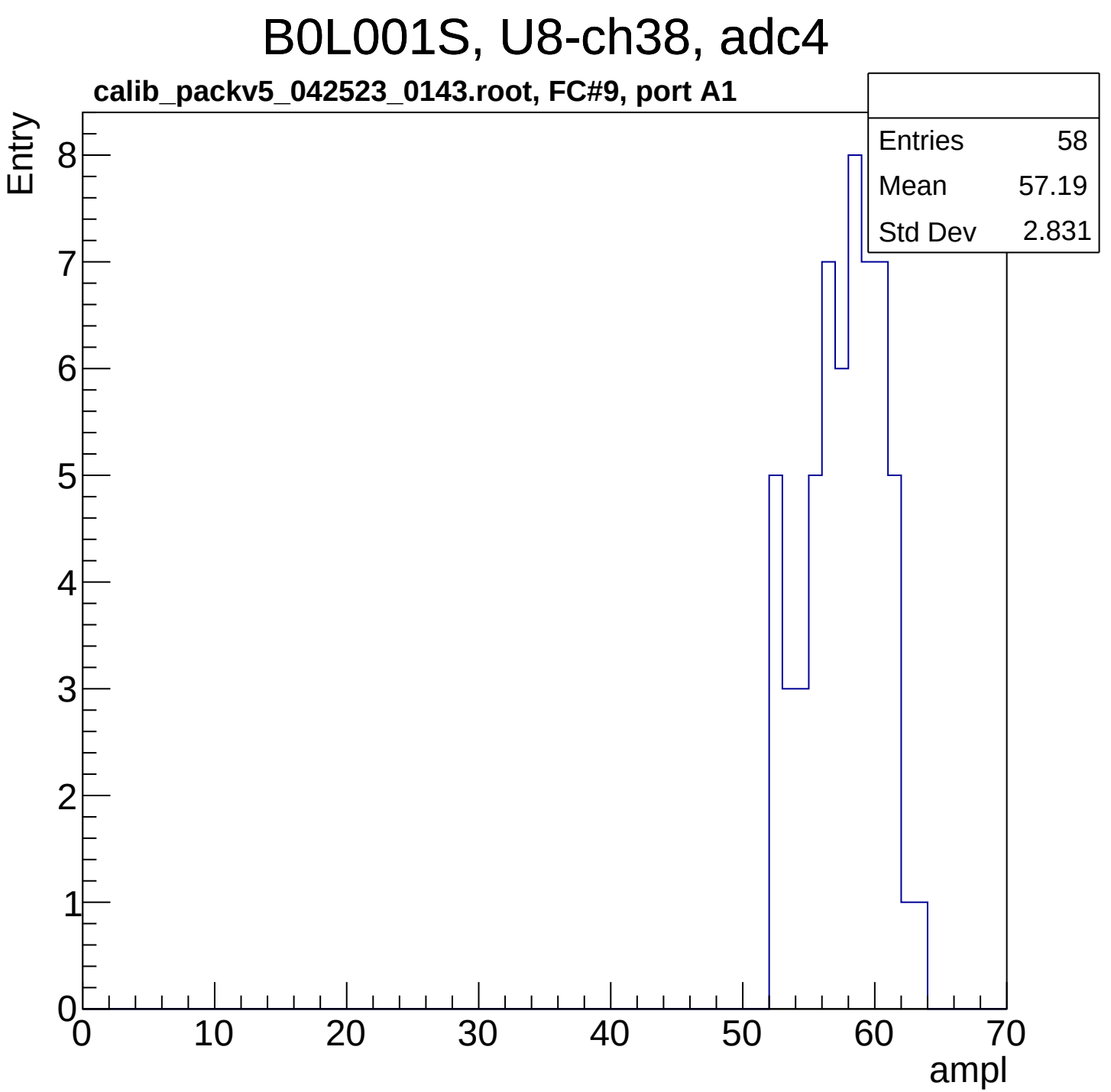
B0L001S, U8-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.19
Std Dev 2.831
Entry
ampl
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1
2
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5
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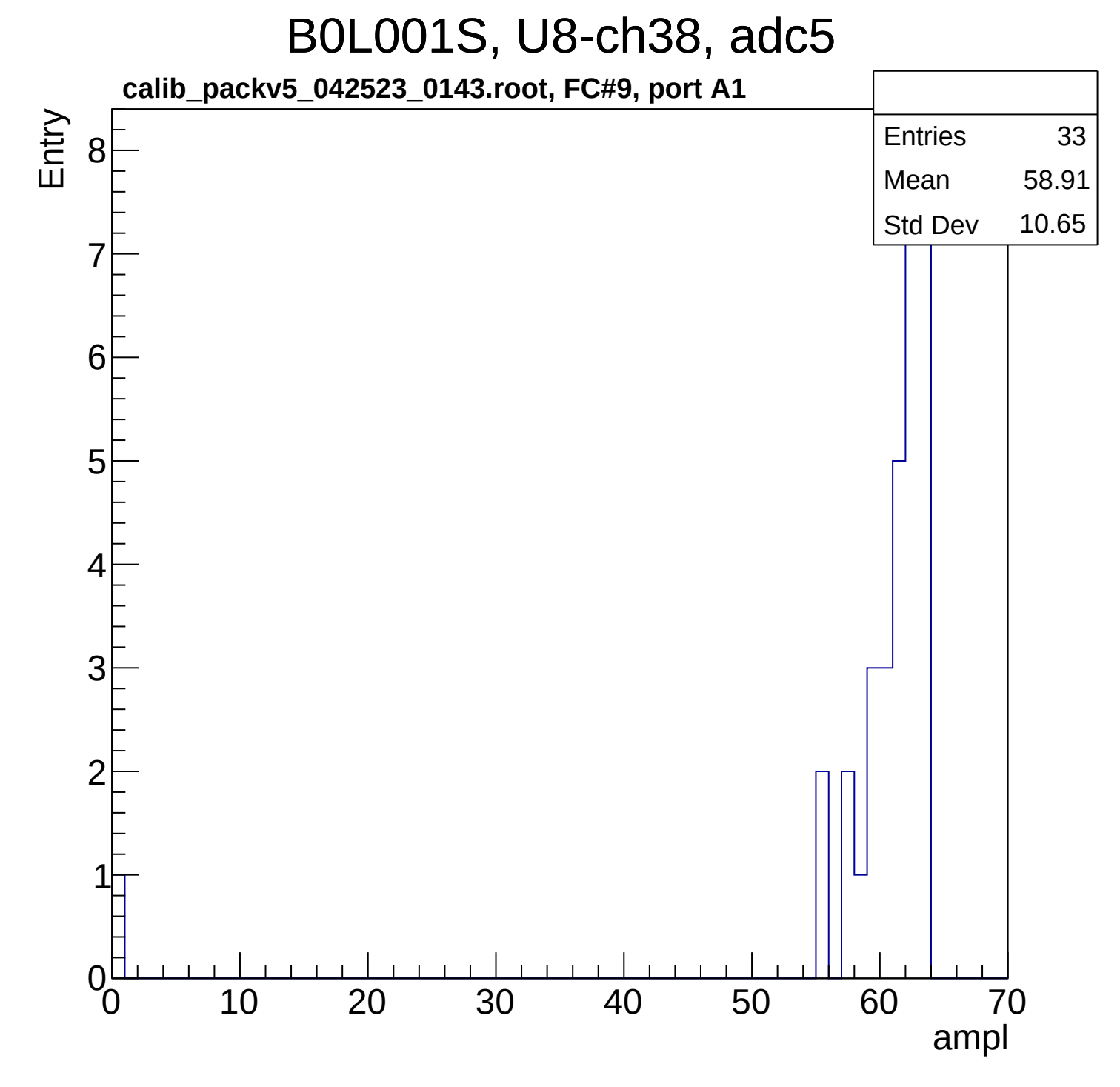
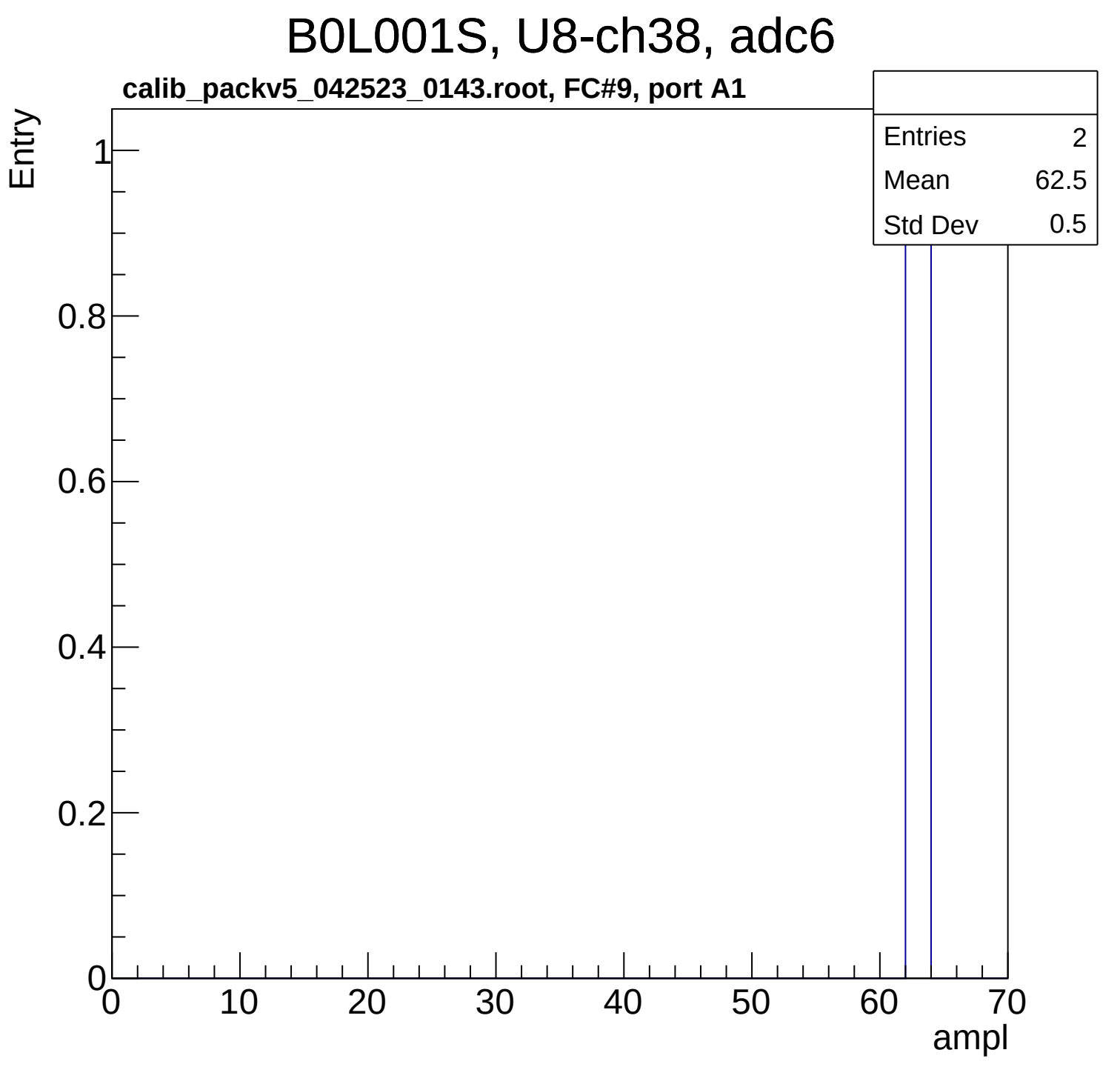
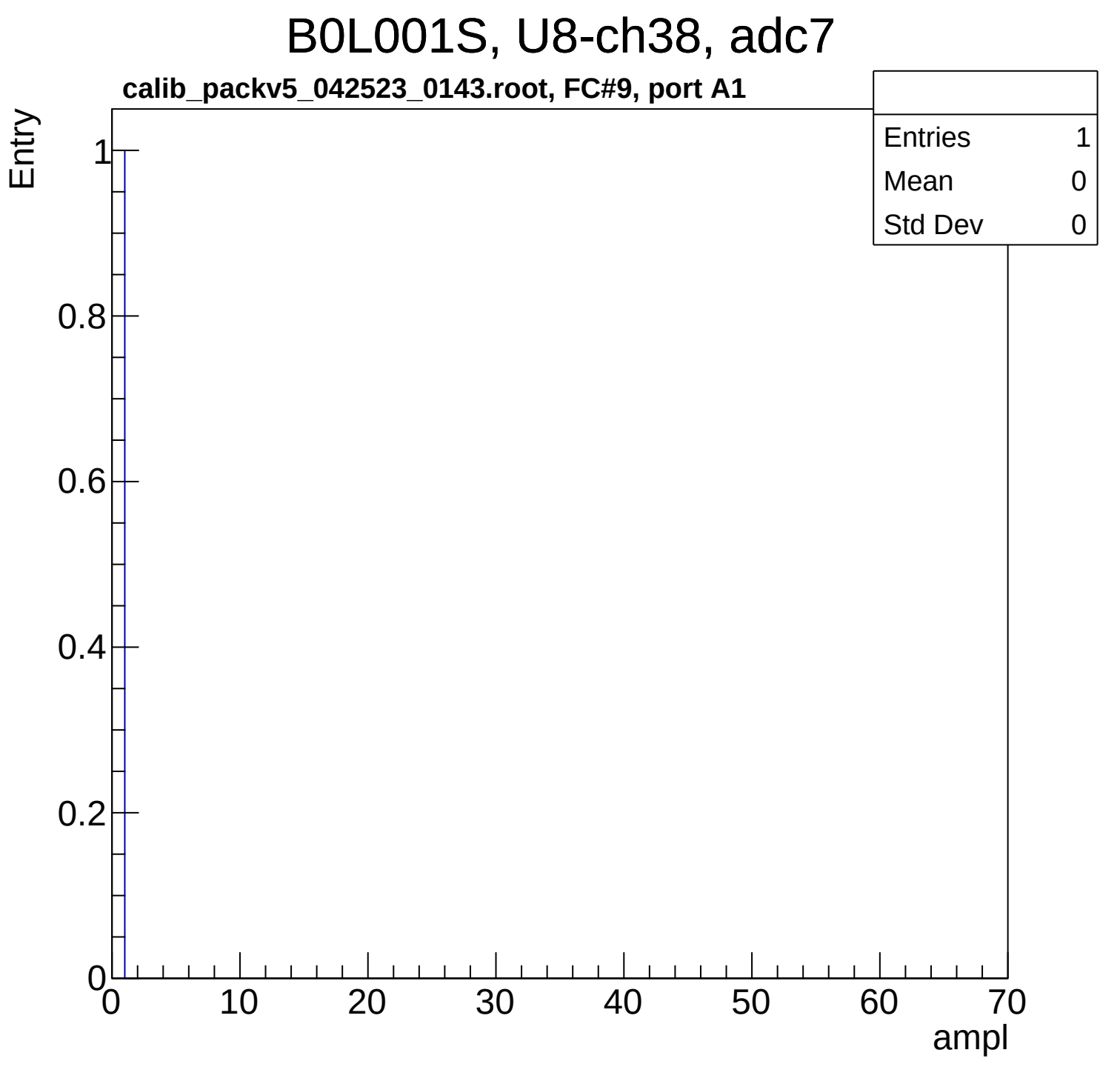

B0L001S, U8-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.91
Std Dev 10.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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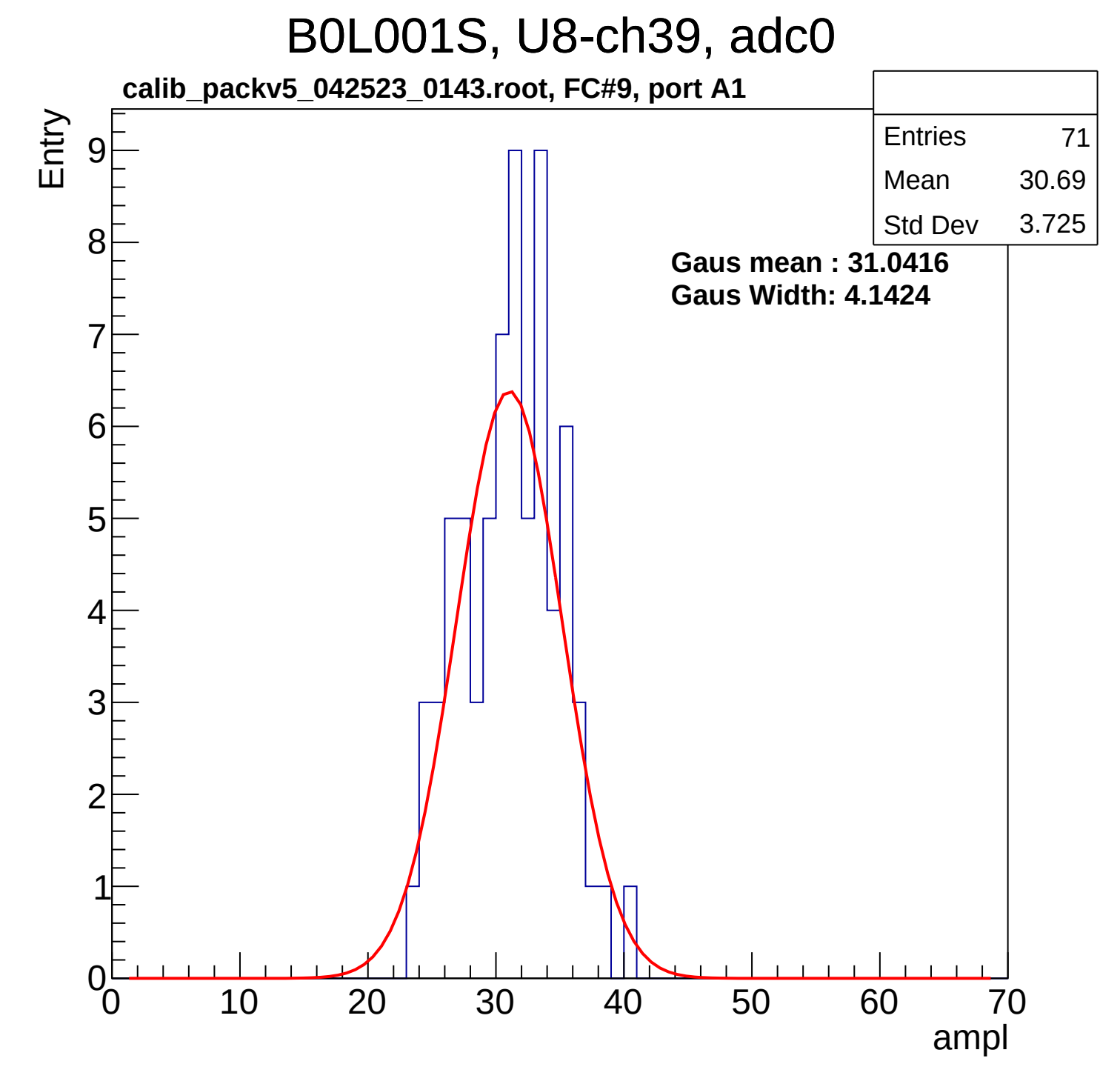
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.69
Std Dev 3.725
Gaus mean : 31.0416
Gaus Width: 4.1424
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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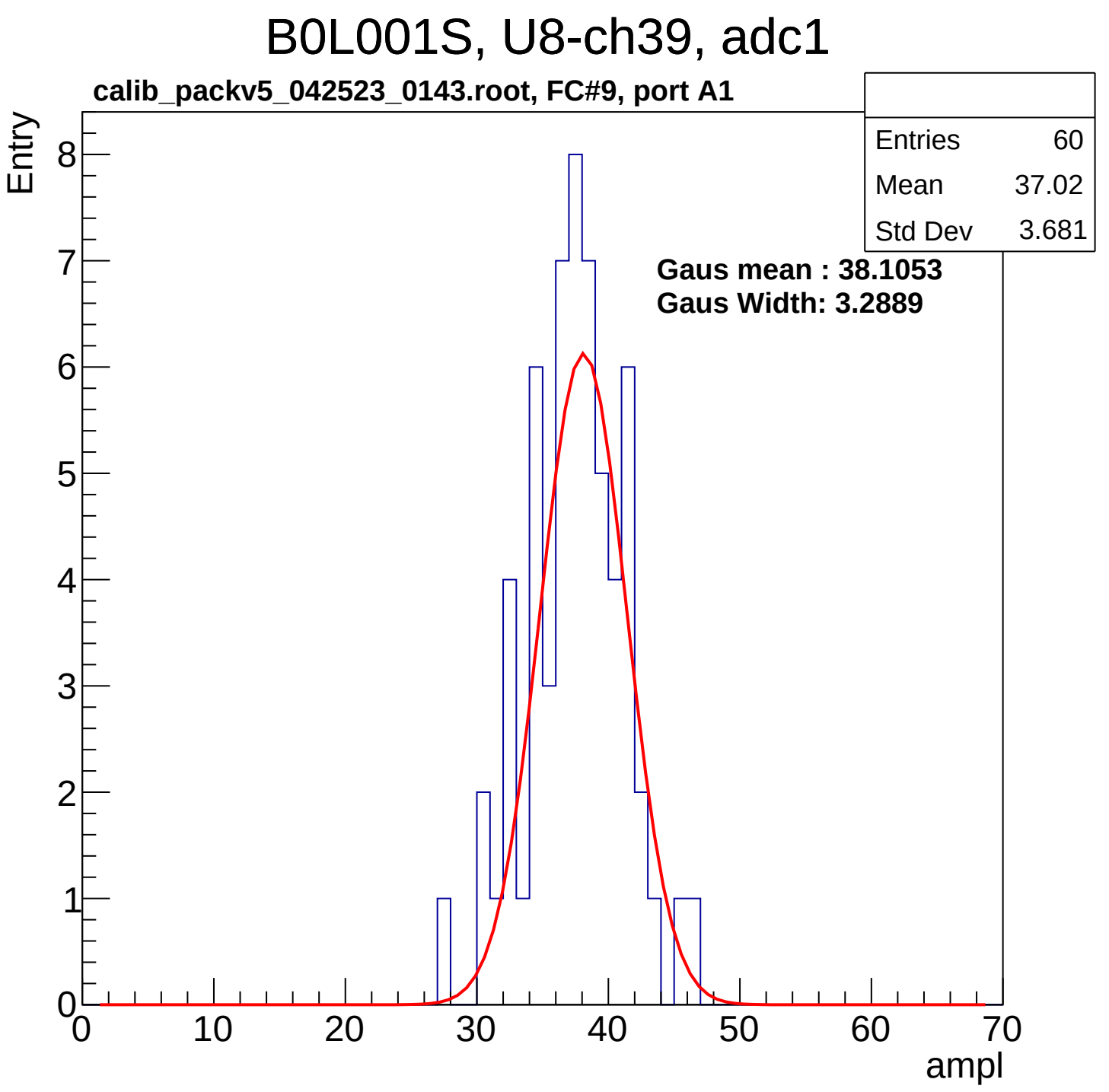
B0L001S, U8-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.02
Std Dev 3.681
Gaus mean : 38.1053
Gaus Width: 3.2889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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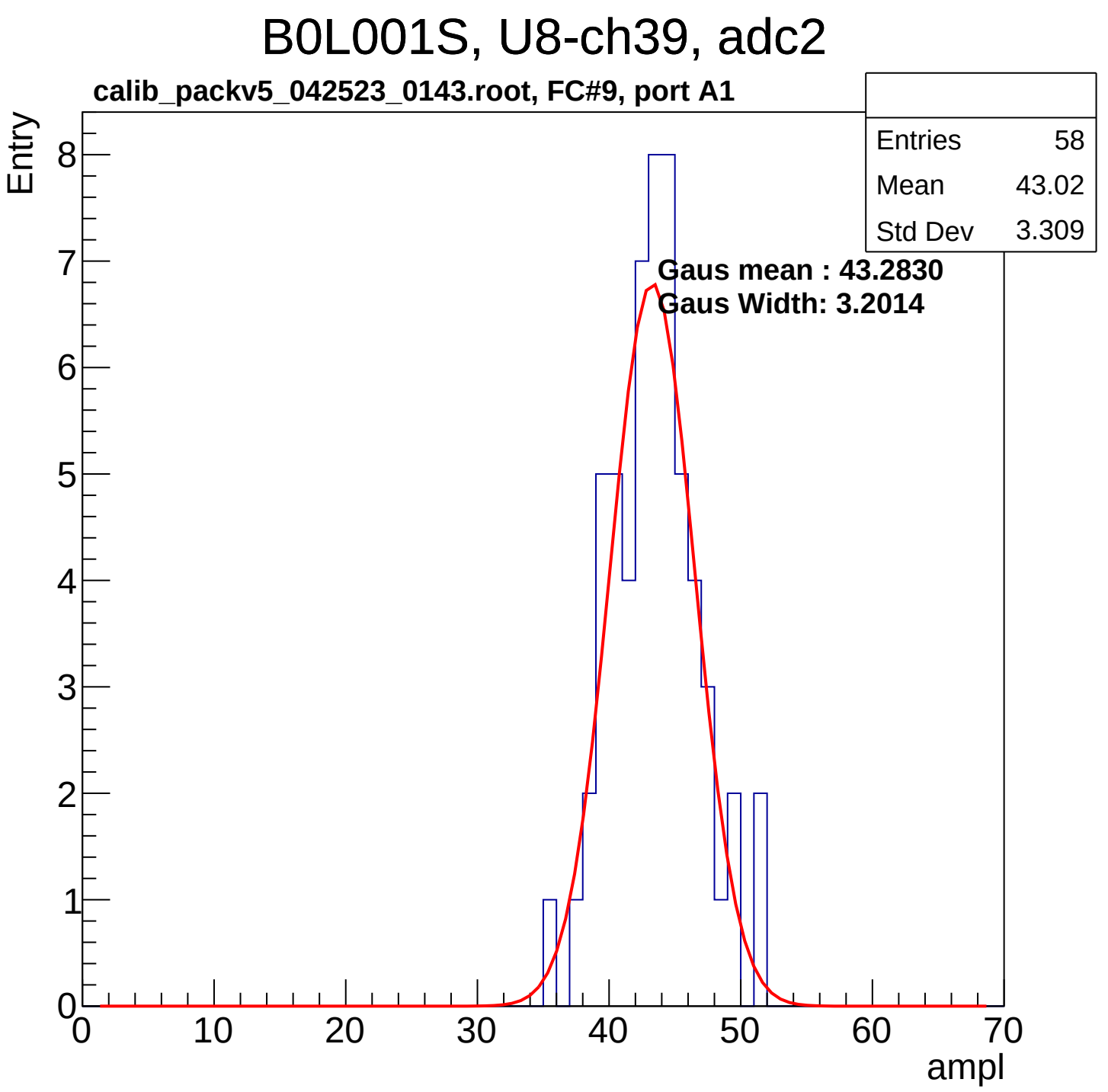
B0L001S, U8-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.02
Std Dev 3.309
Gaus mean : 43.2830
Gaus Width: 3.2014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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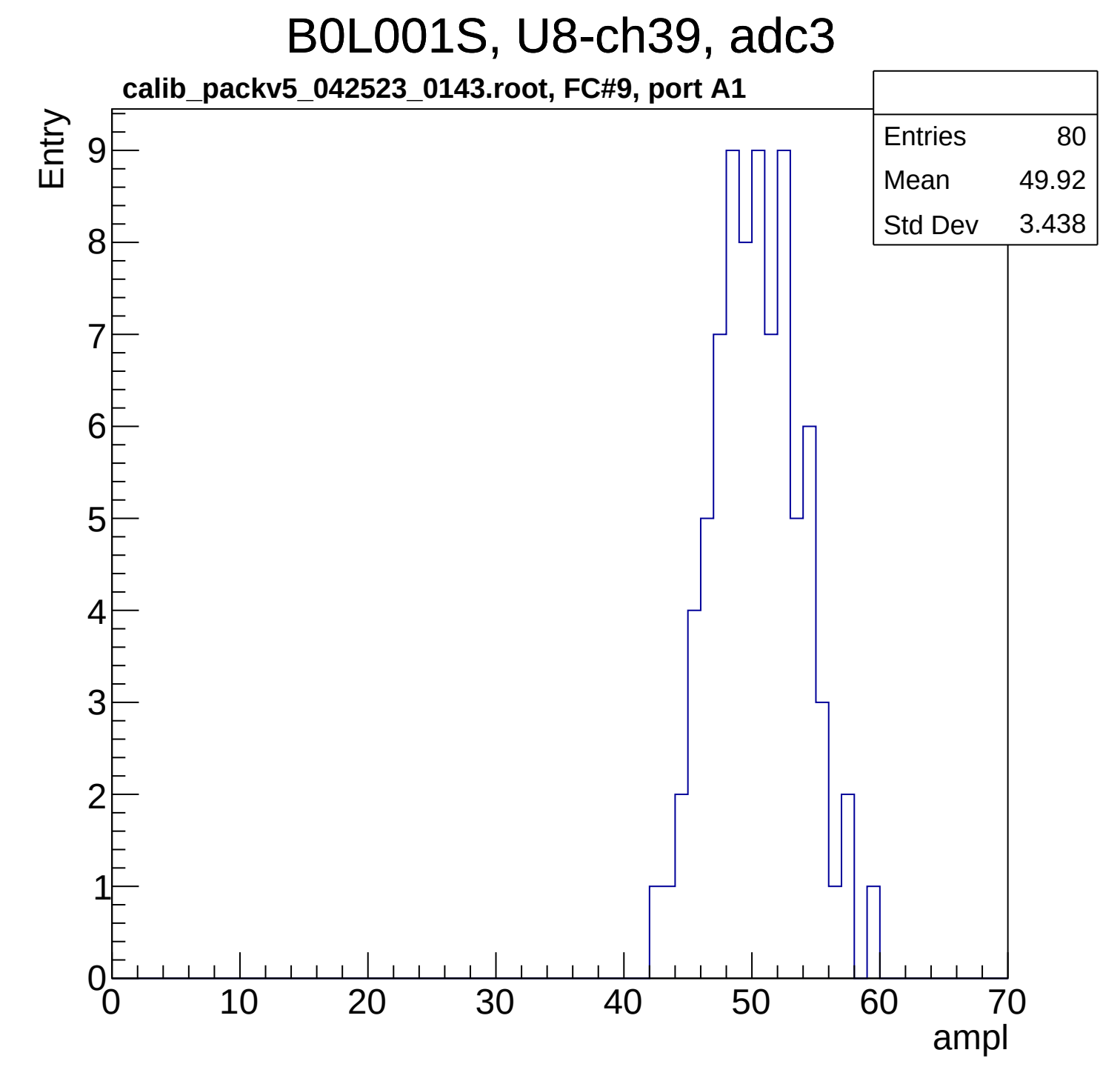
B0L001S, U8-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.92
Std Dev 3.438
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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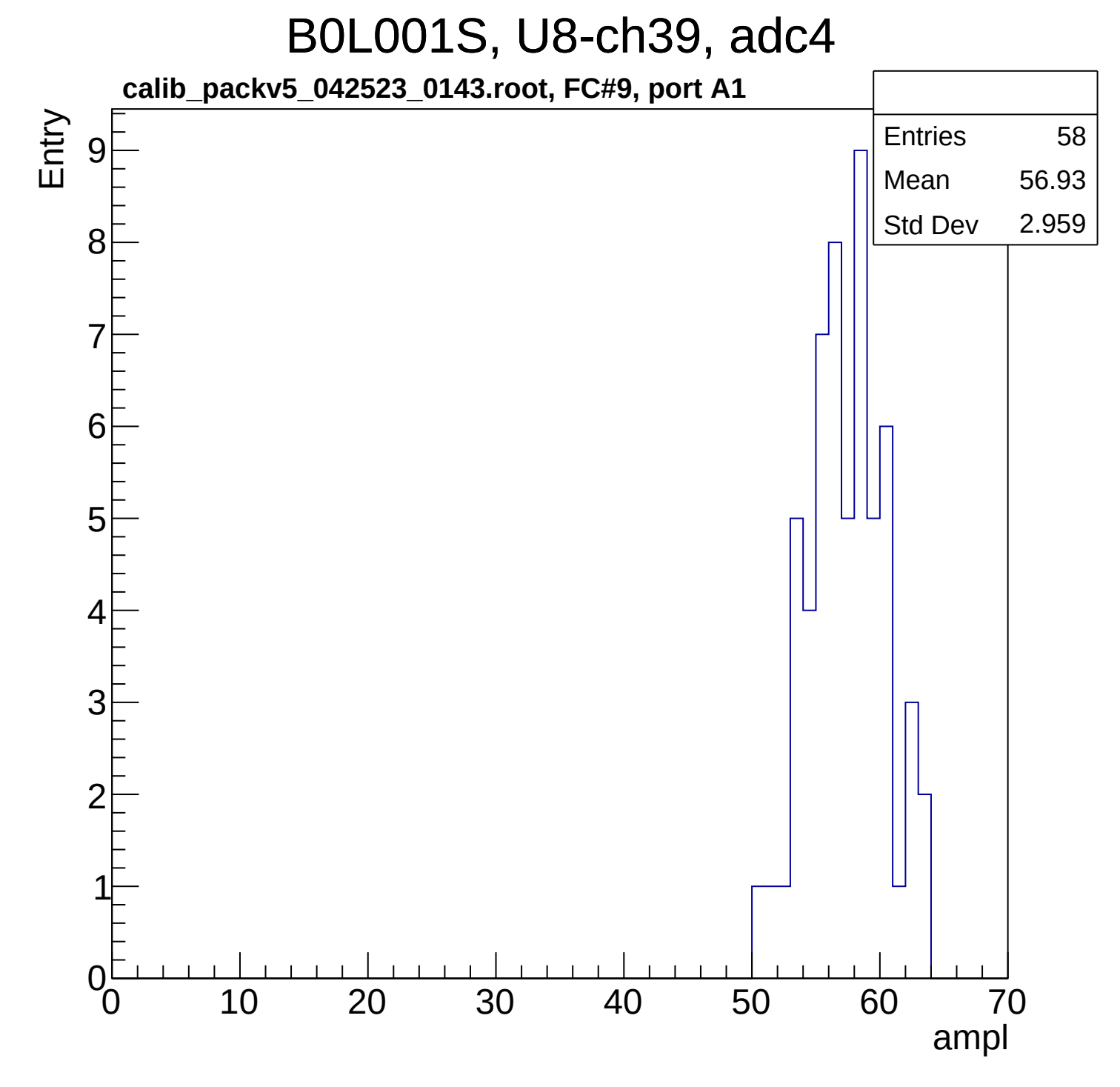
B0L001S, U8-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.93
Std Dev 2.959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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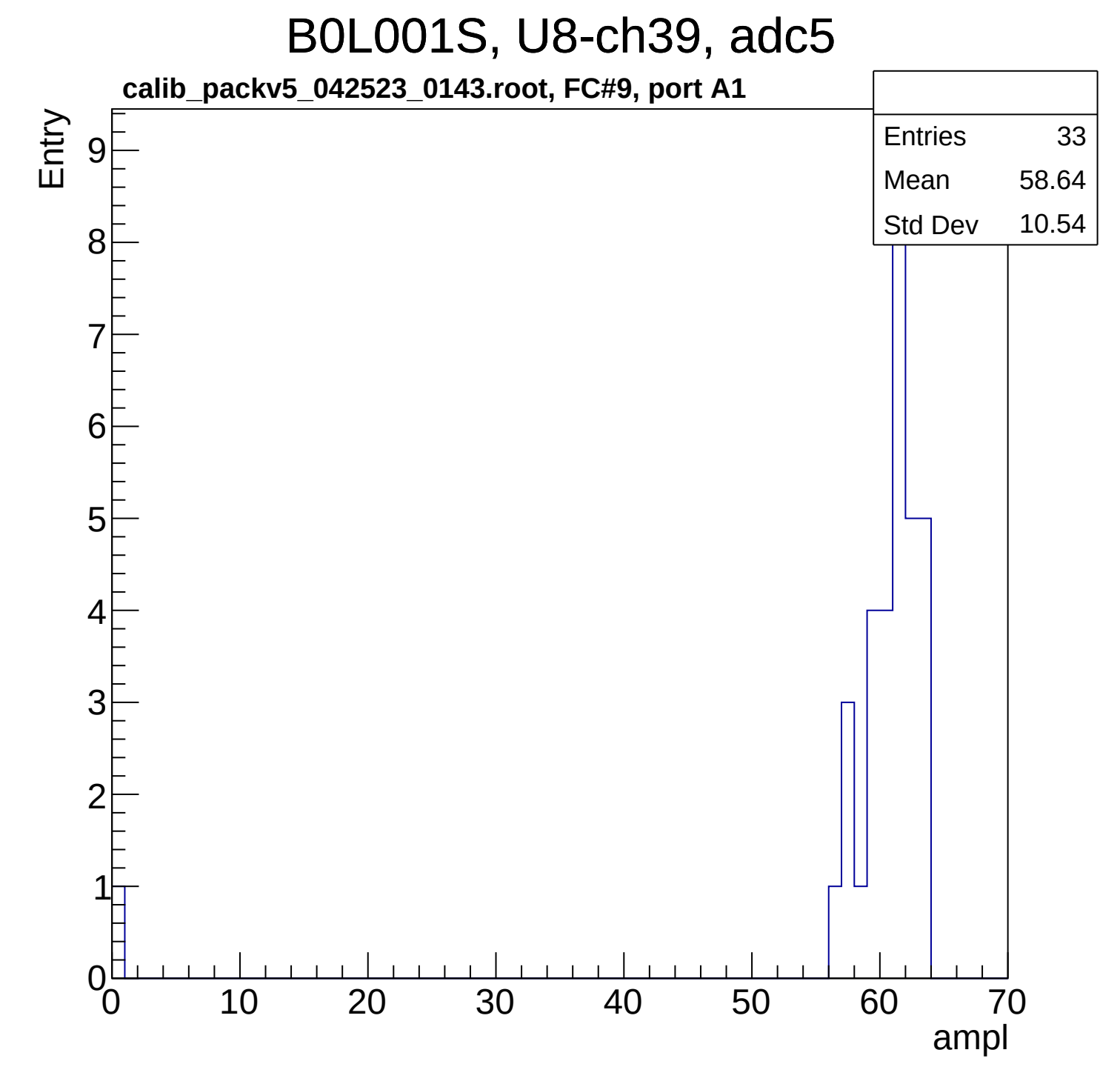
B0L001S, U8-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.64
Std Dev 10.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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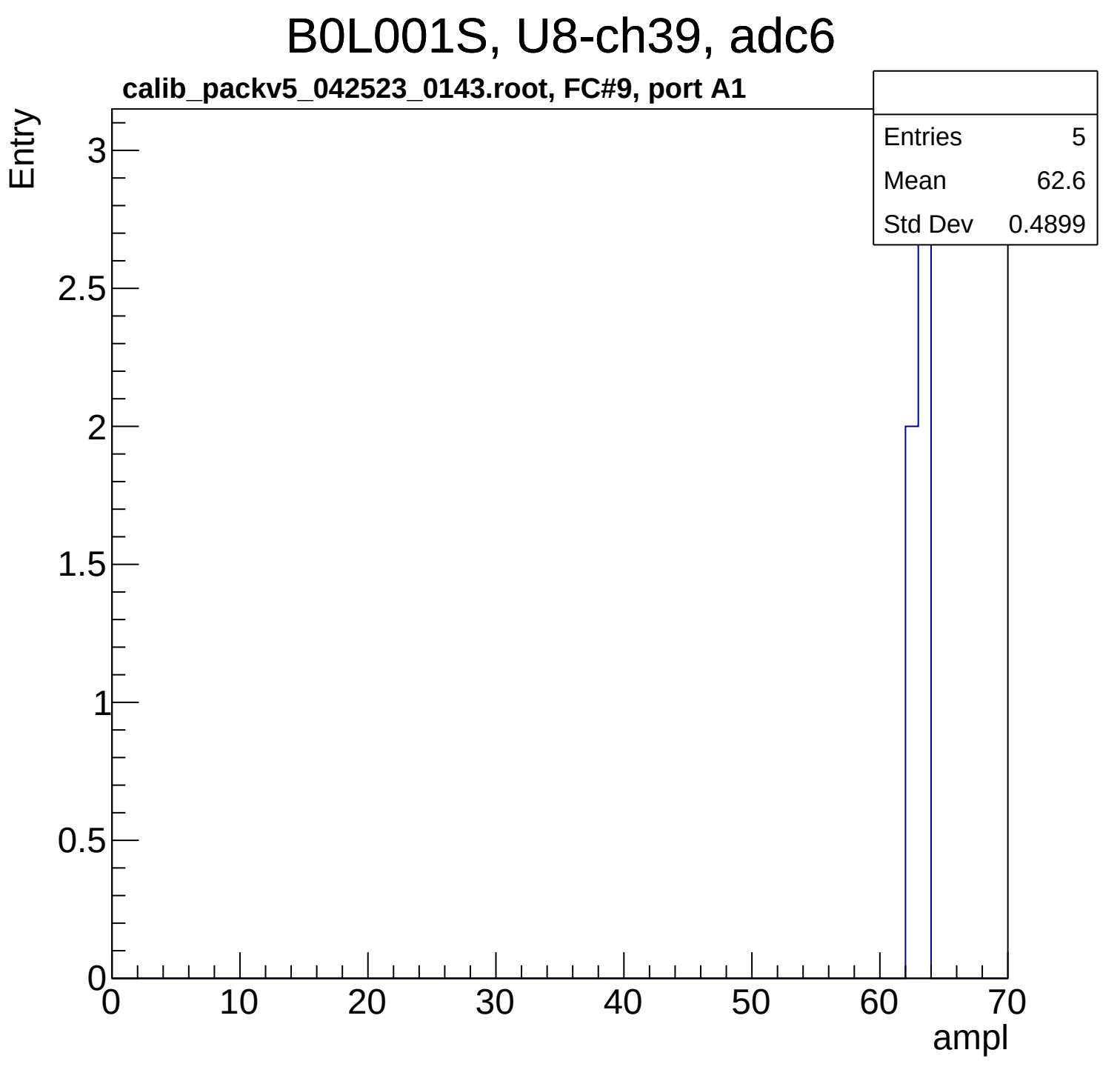
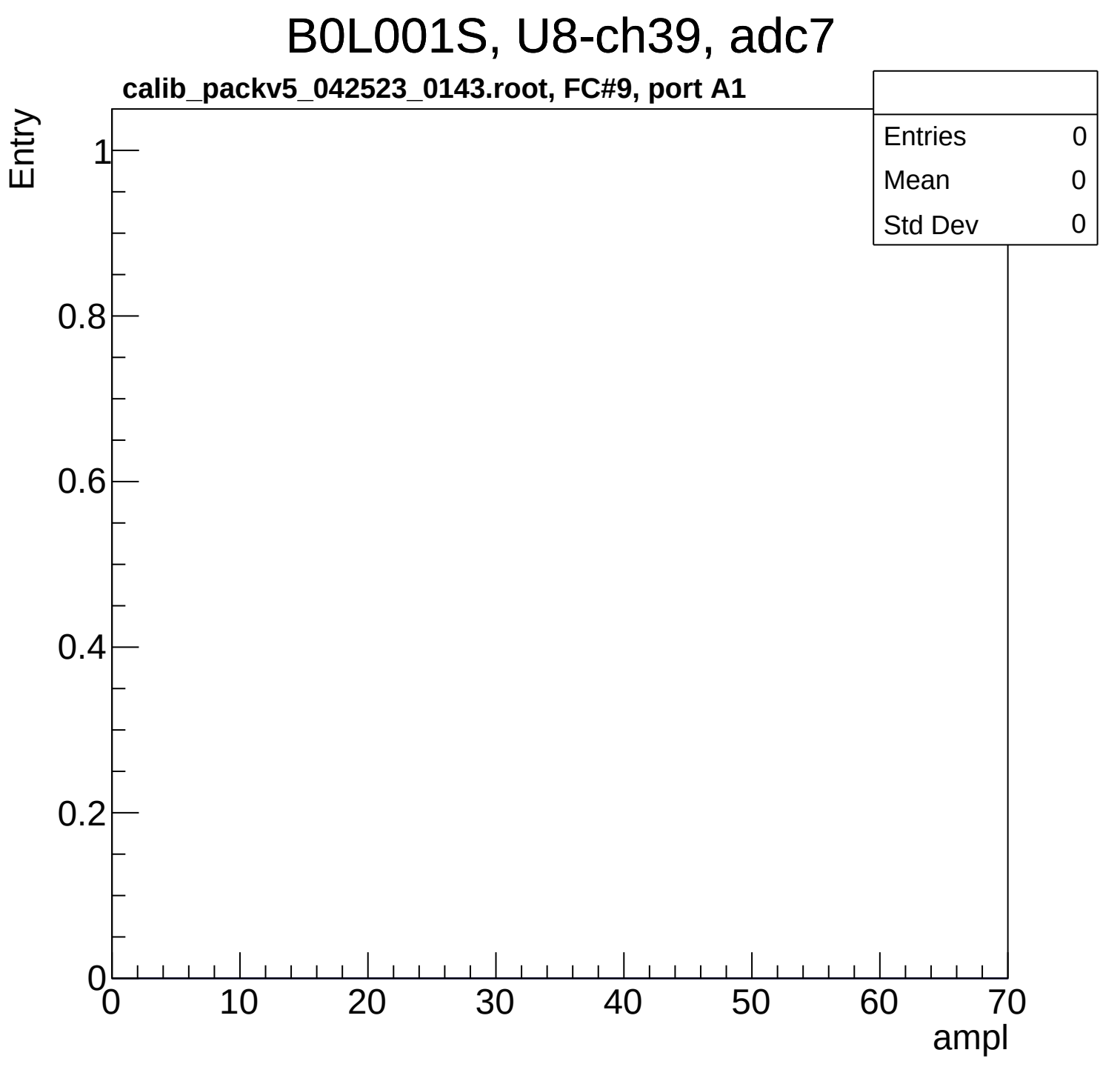
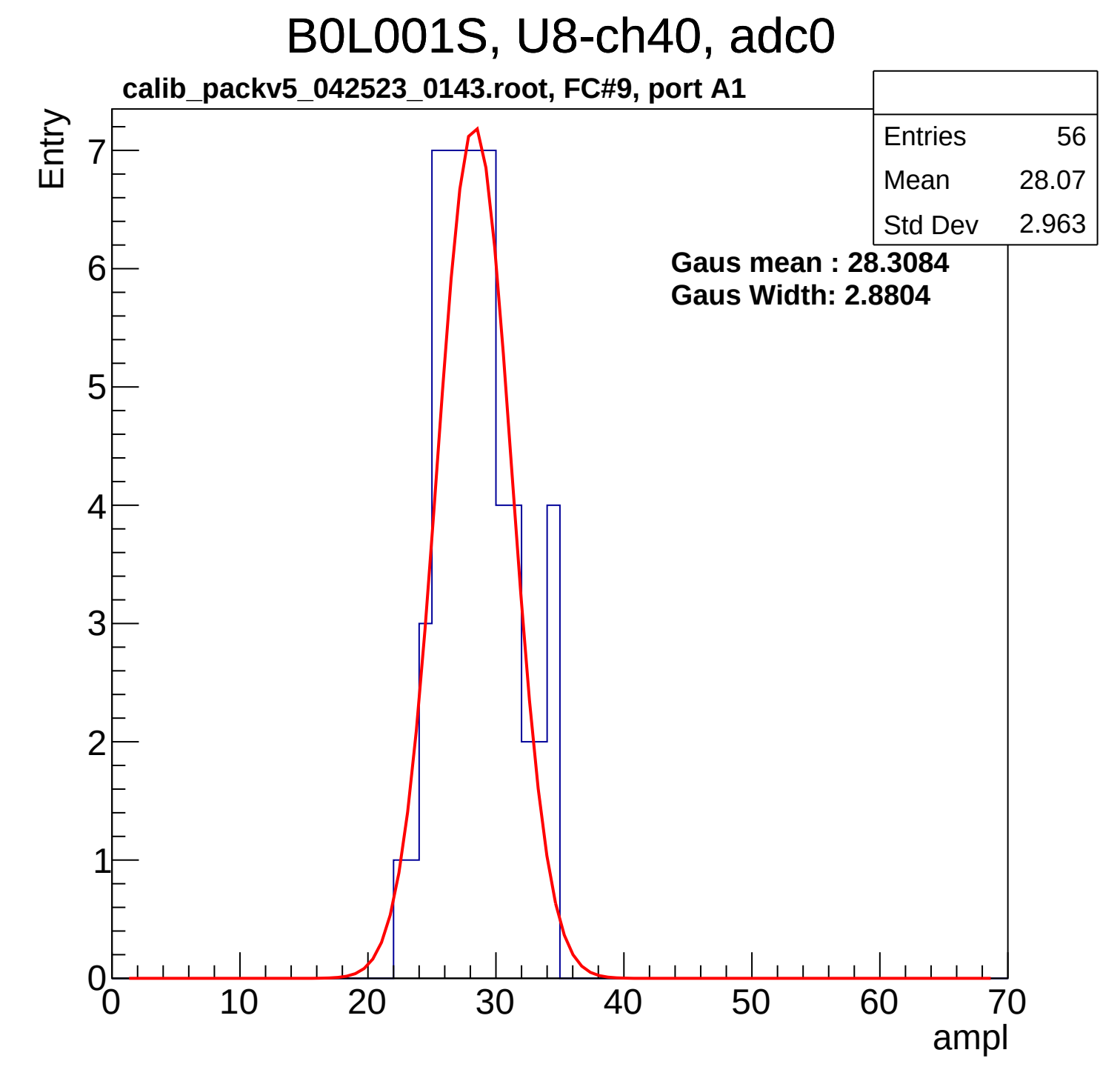
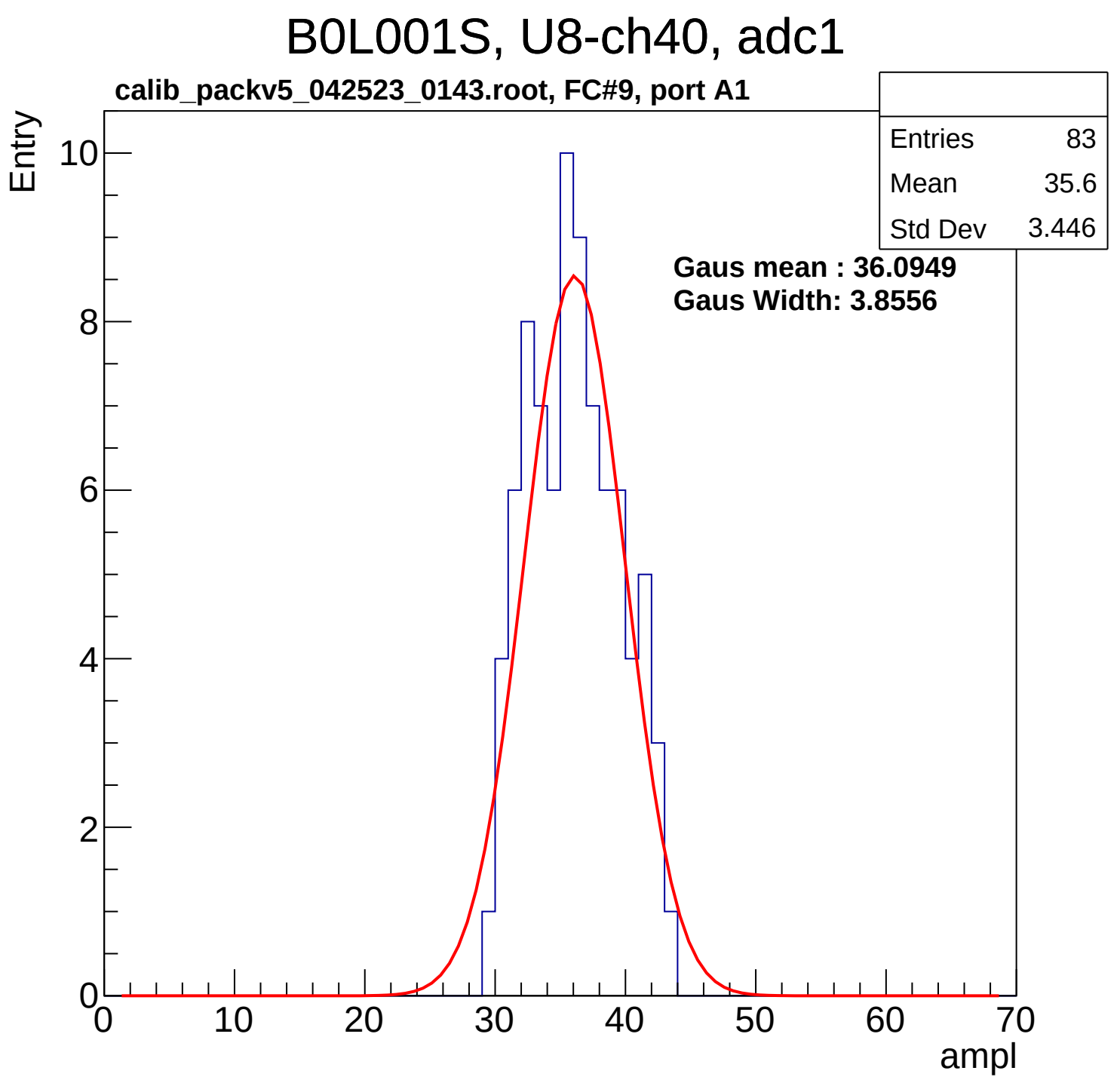
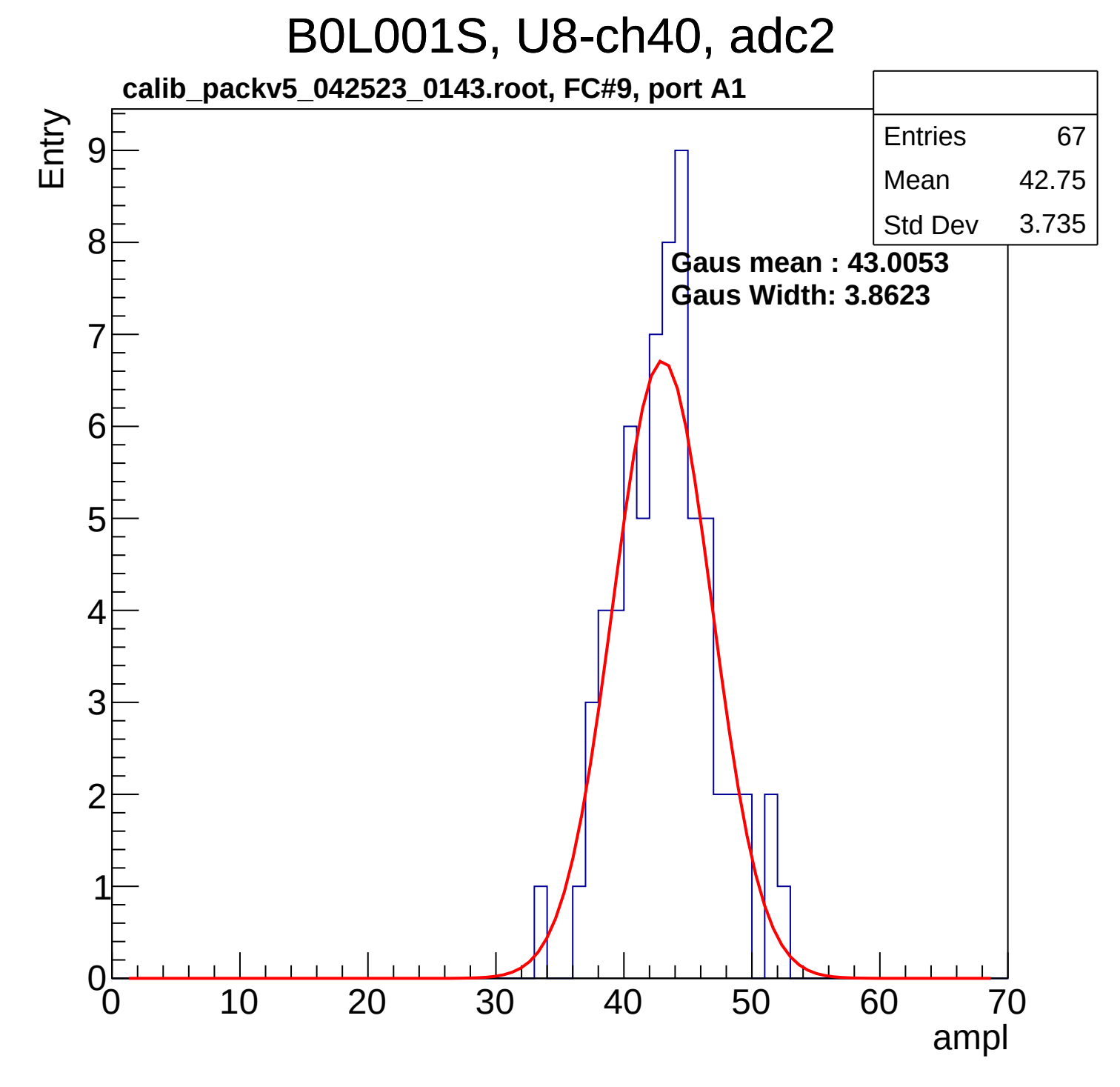

B0L001S, U8-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 28.07
Std Dev 2.963
Gaus mean : 28.3084
Gaus Width: 2.8804
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 35.6
Std Dev 3.446
Gaus mean : 36.0949
Gaus Width: 3.8556
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.75
Std Dev 3.735
Gaus mean : 43.0053
Gaus Width: 3.8623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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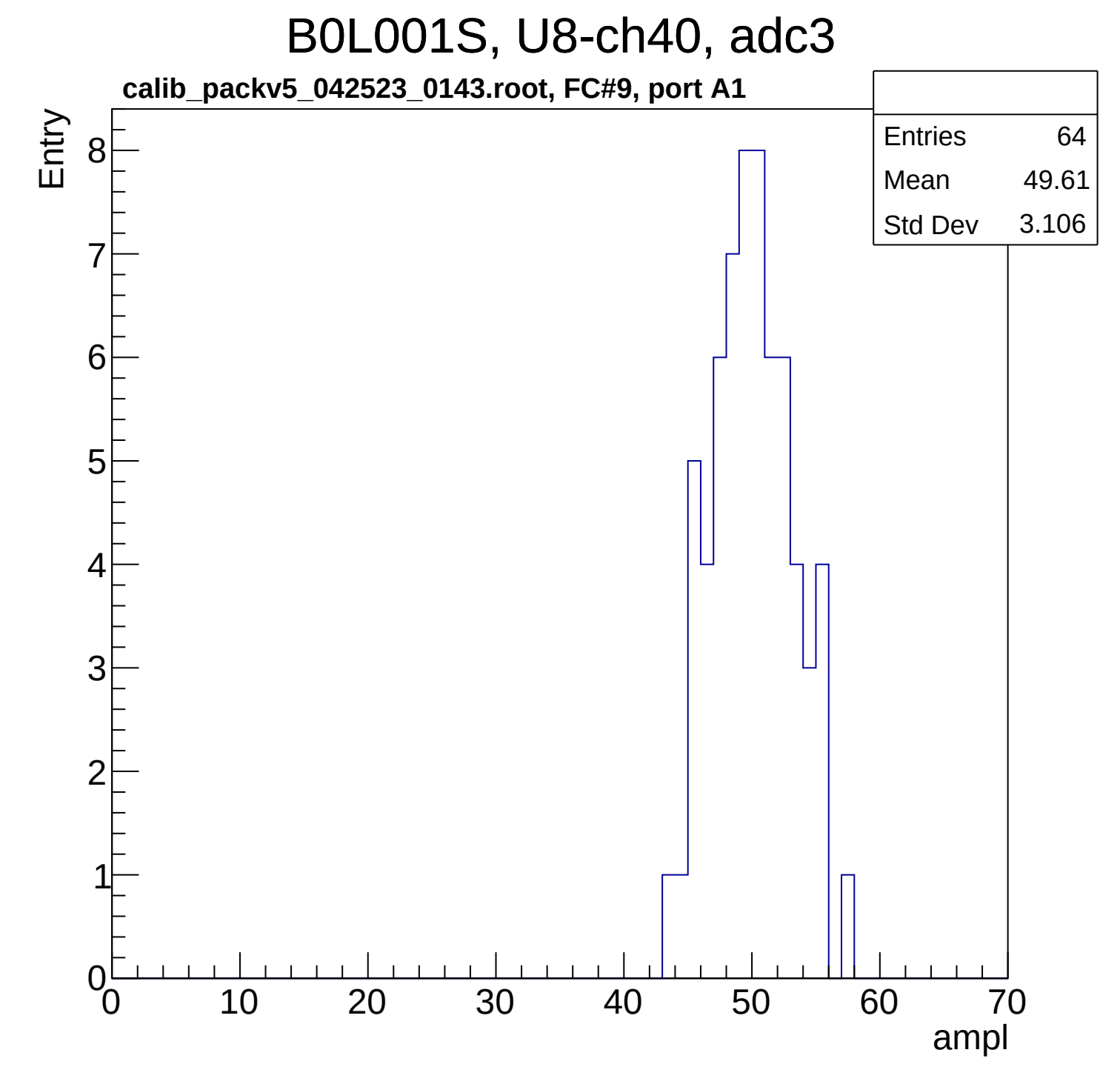
B0L001S, U8-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.61
Std Dev 3.106
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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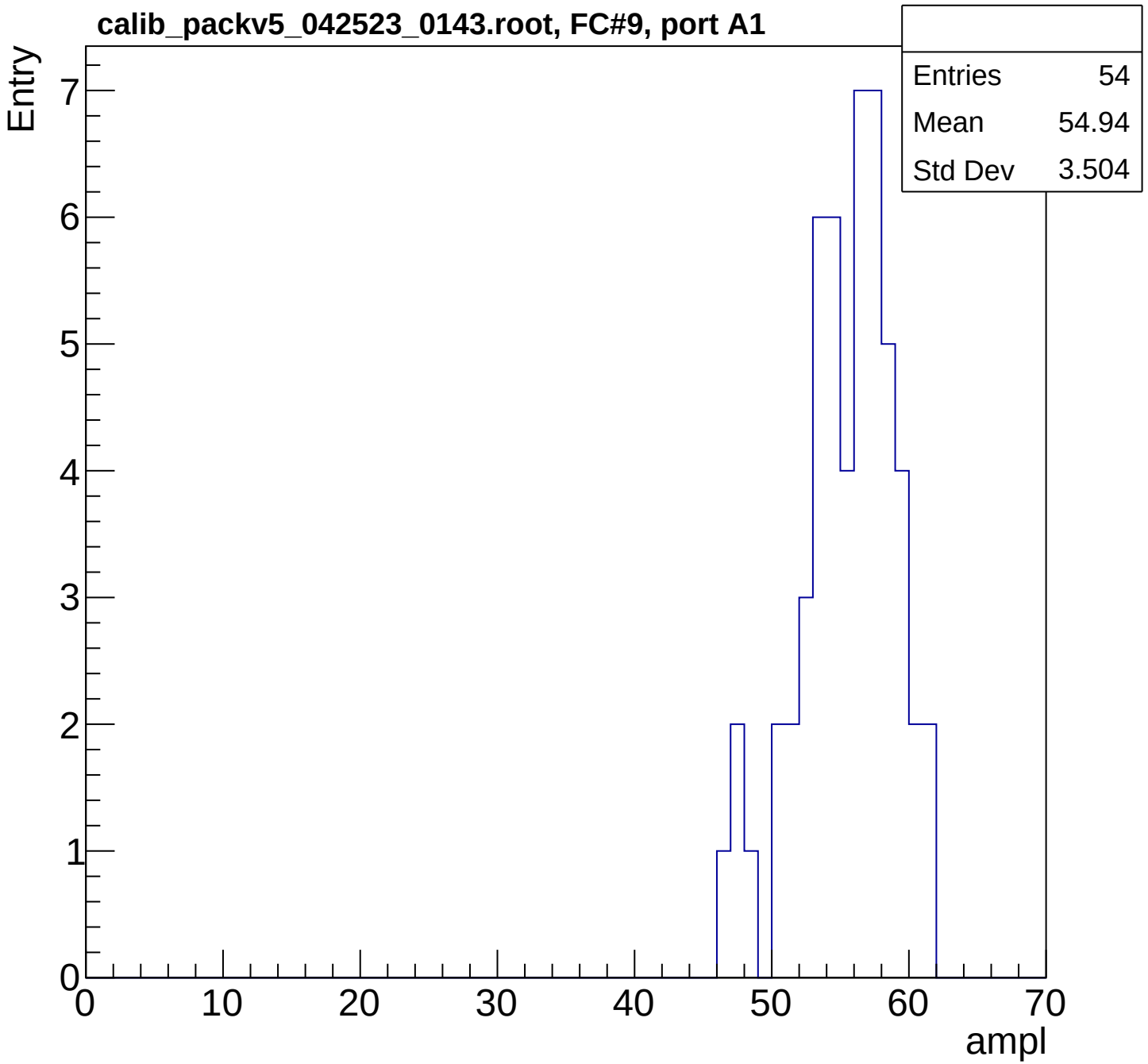
40
50
60
70


B0L001S, U8-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 54.94
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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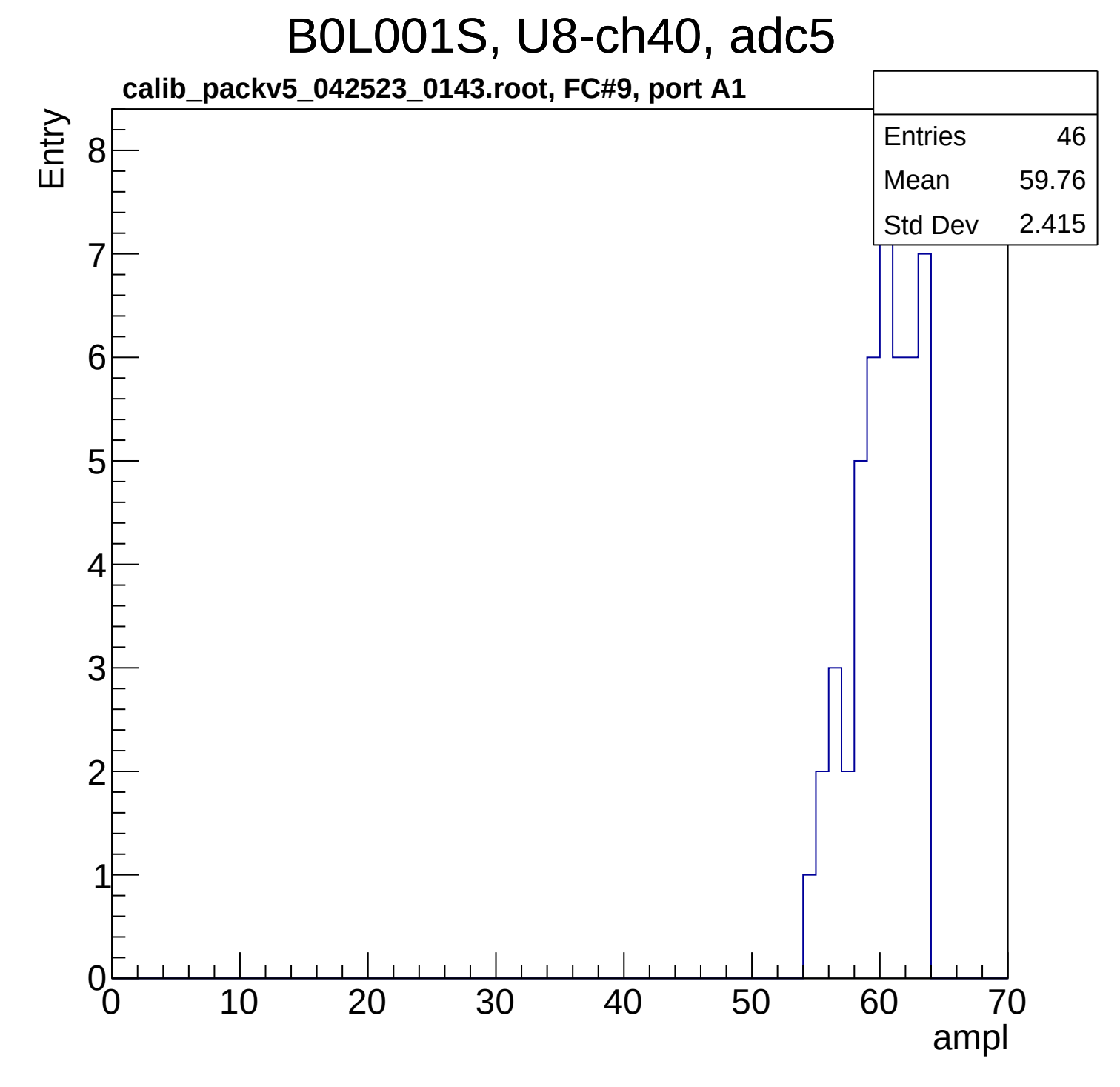
B0L001S, U8-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 59.76
Std Dev 2.415
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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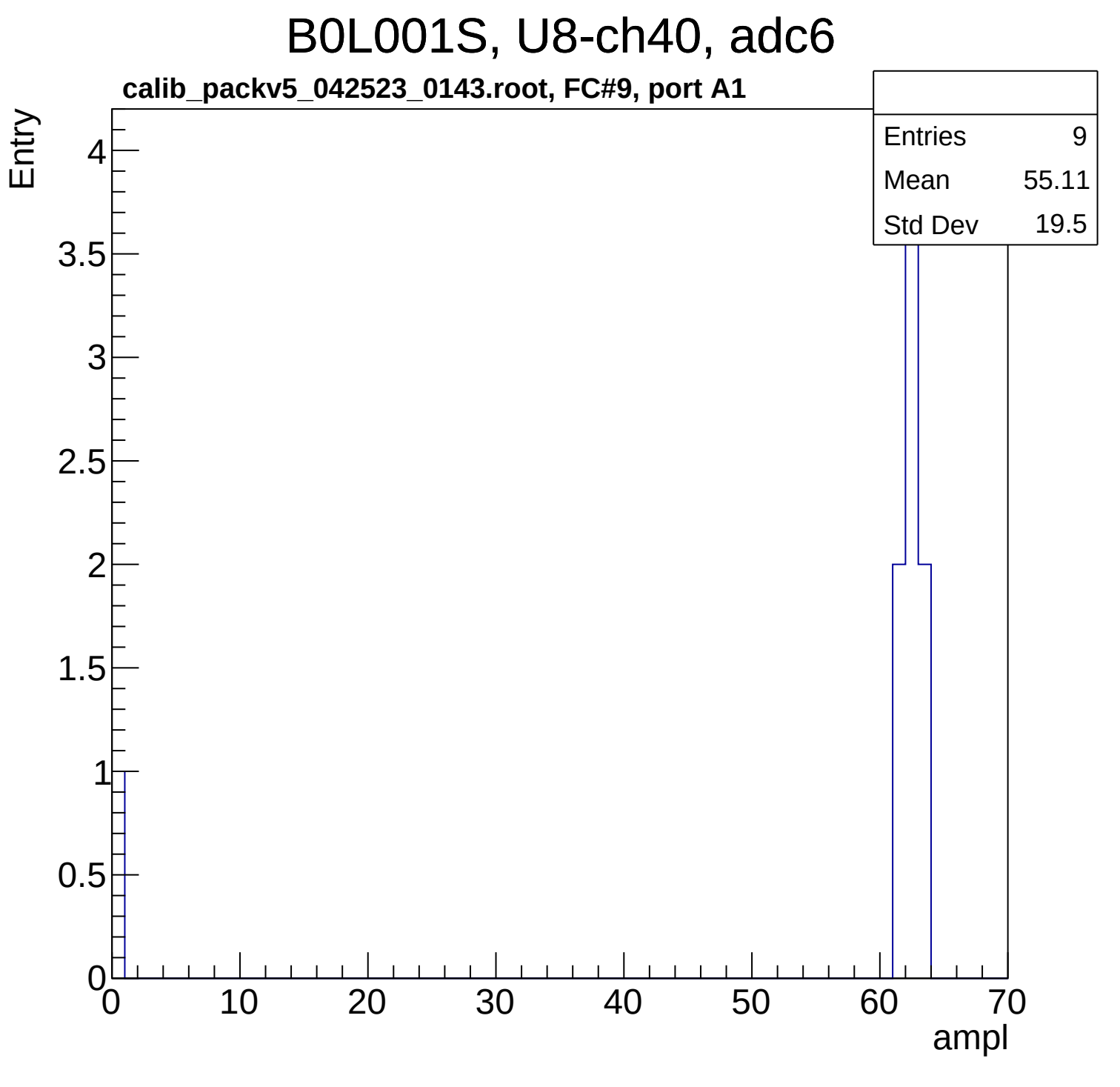
60
70


B0L001S, U8-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 55.11
Std Dev 19.5
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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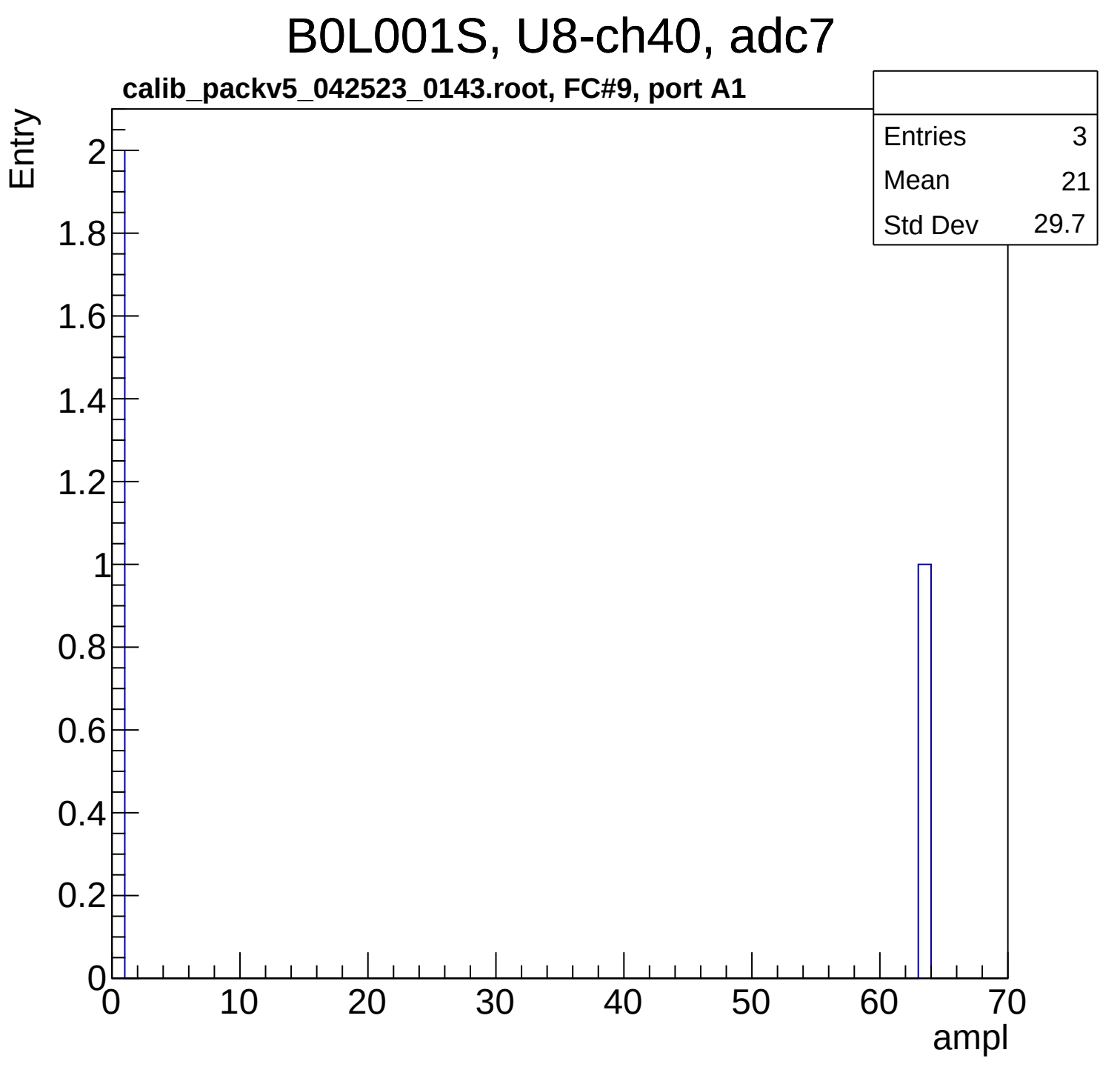
40
50
60
70


B0L001S, U8-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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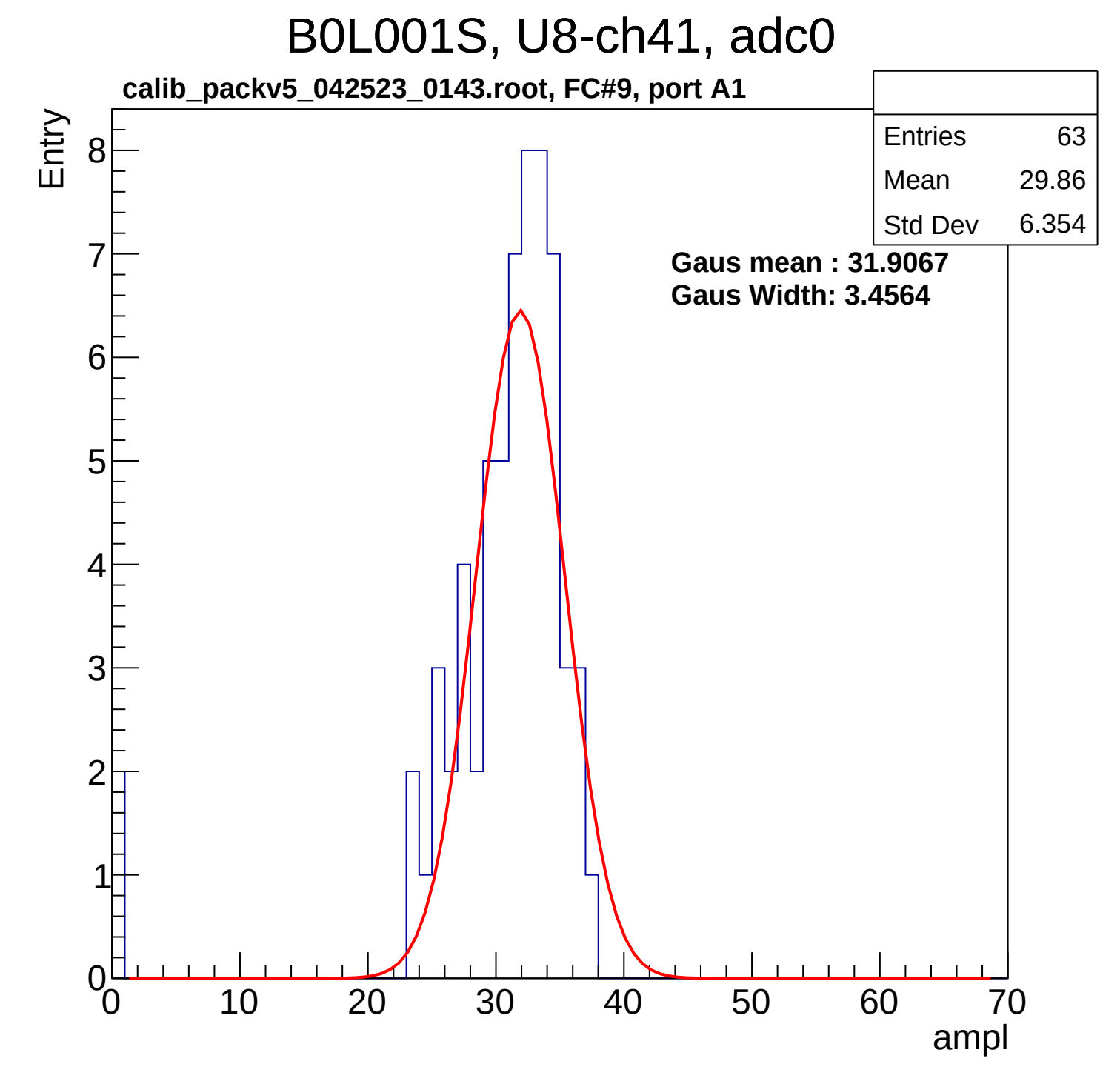
B0L001S, U8-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.86
Std Dev 6.354
Gaus mean : 31.9067
Gaus Width: 3.4564
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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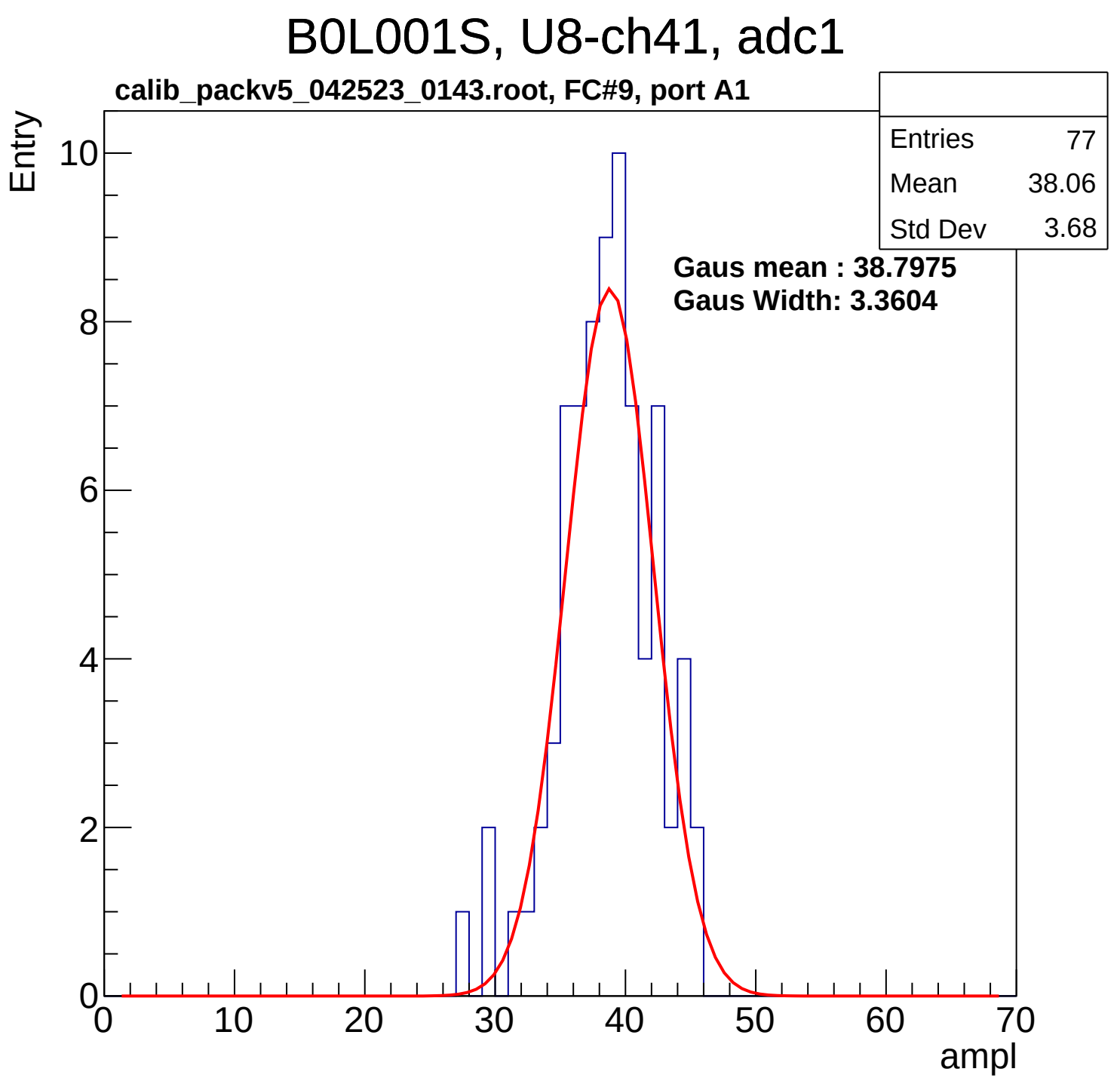
B0L001S, U8-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.06
Std Dev 3.68
Gaus mean : 38.7975
Gaus Width: 3.3604
Entry
ampl
0
2
4
6
8
10
0
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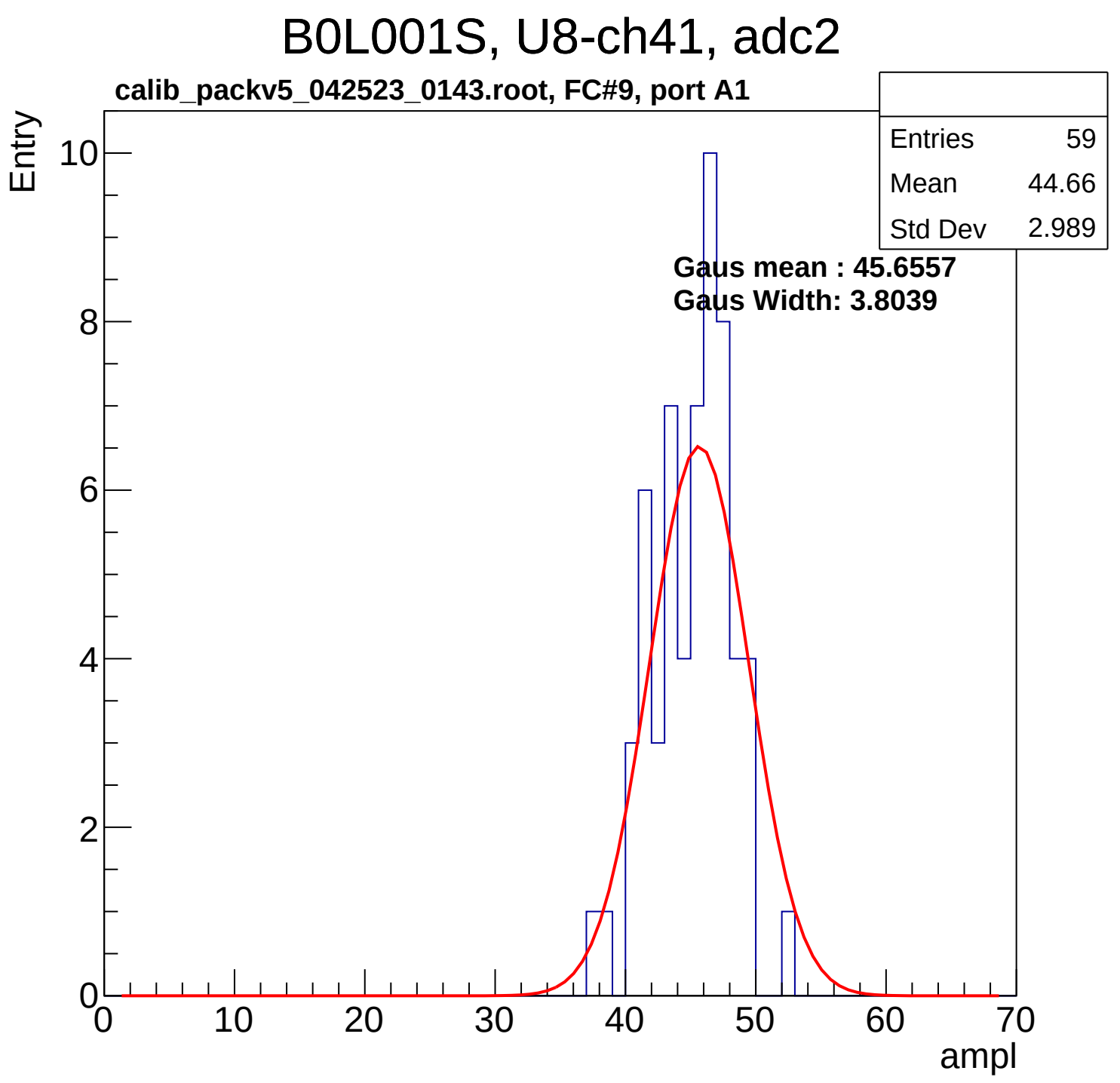
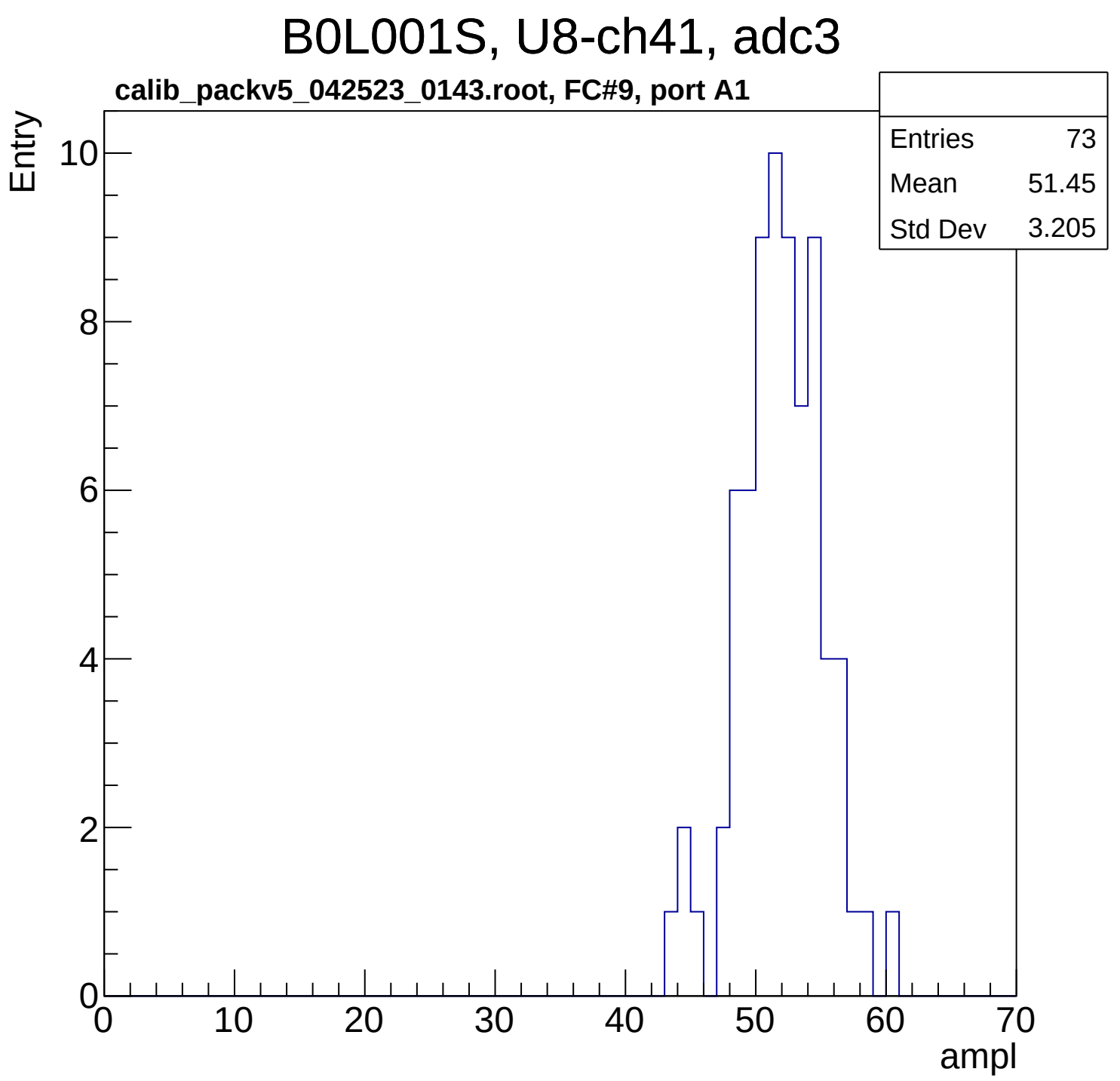
40
50
60
70


B0L001S, U8-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.66
Std Dev 2.989
Gaus mean : 45.6557
Gaus Width: 3.8039
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 51.45
Std Dev 3.205
Entry
ampl
0
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4
6
8
10
0
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20
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40
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60
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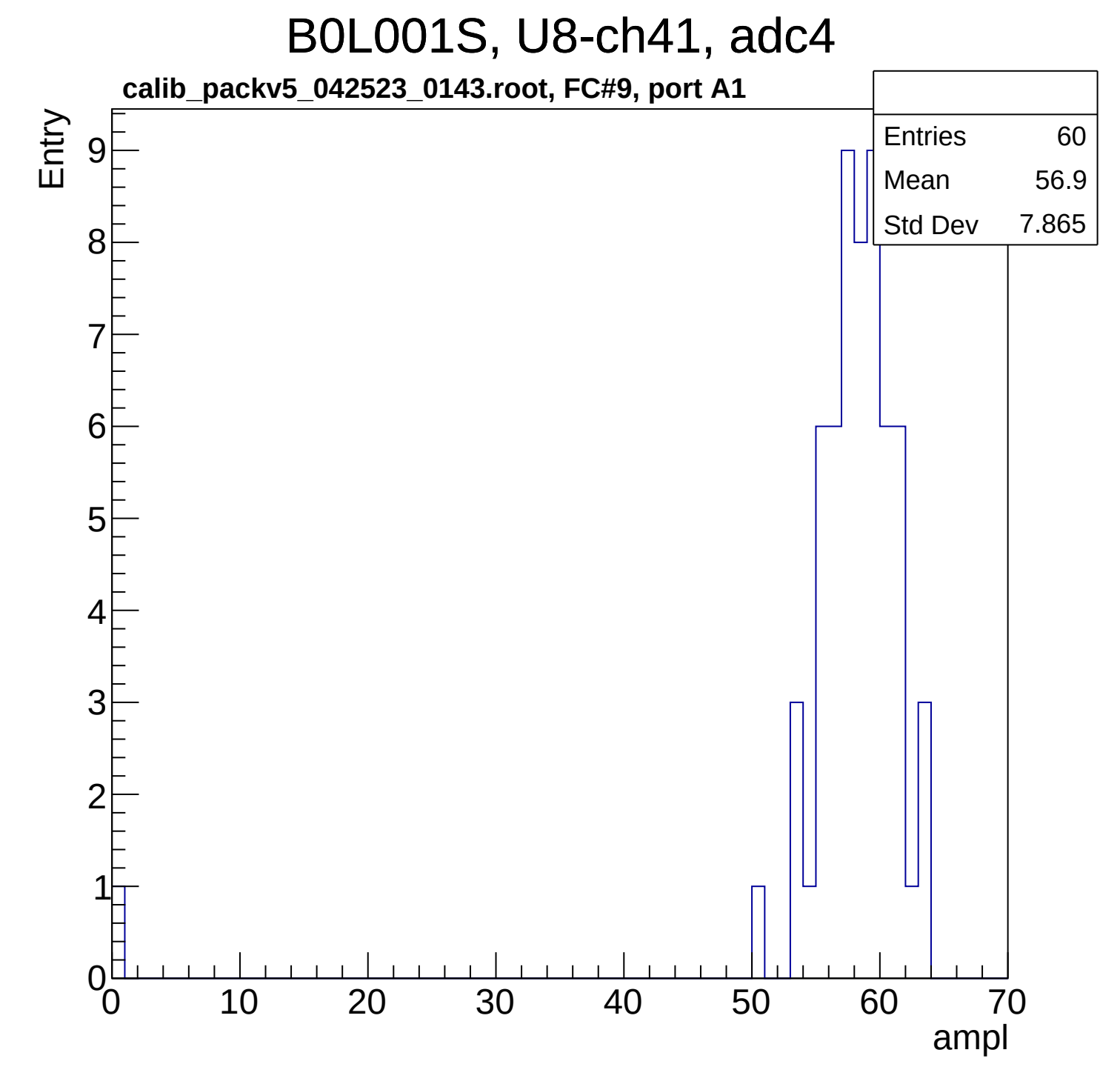
B0L001S, U8-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.9
Std Dev 7.865
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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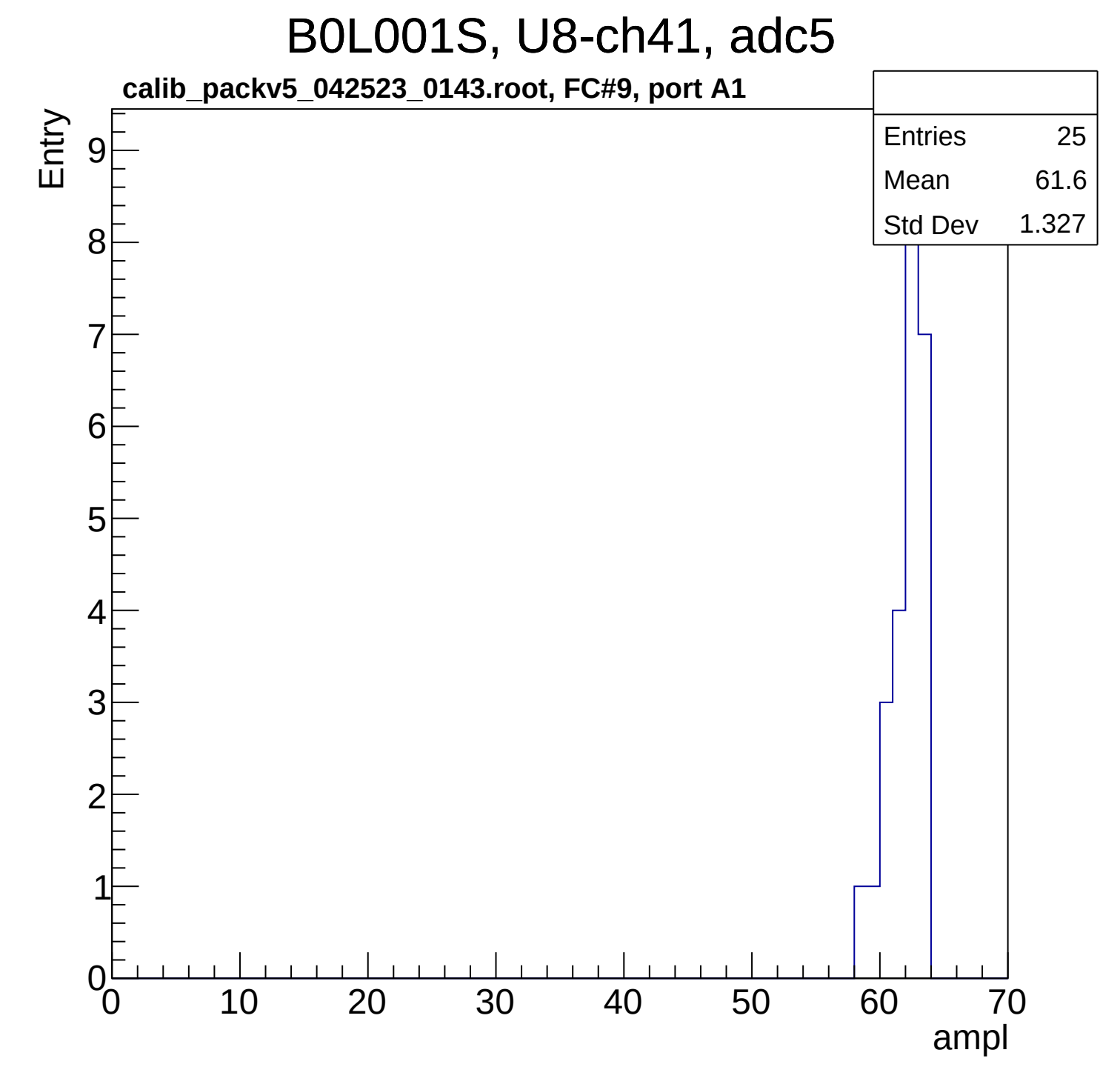
B0L001S, U8-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 61.6
Std Dev 1.327
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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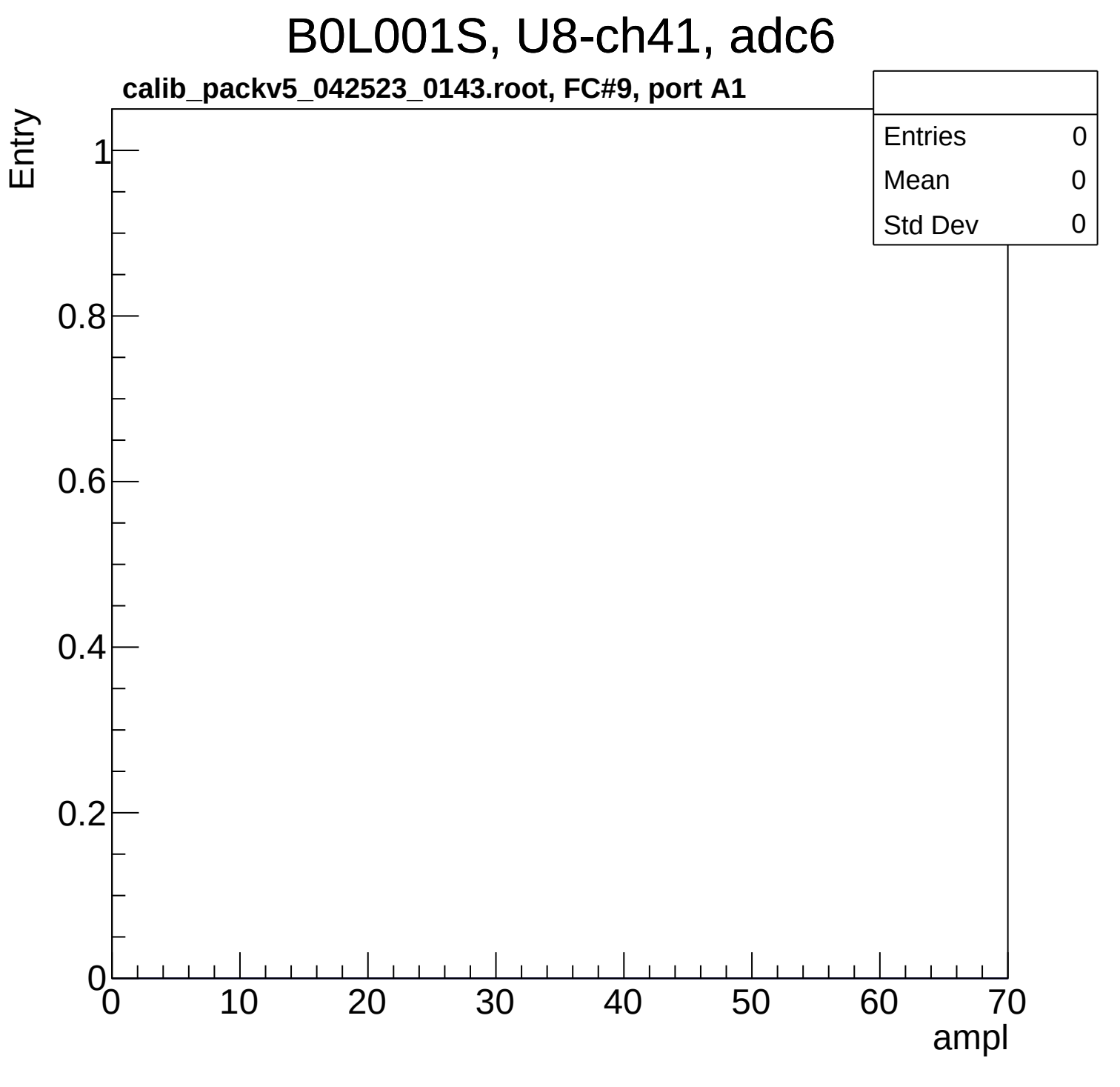
B0L001S, U8-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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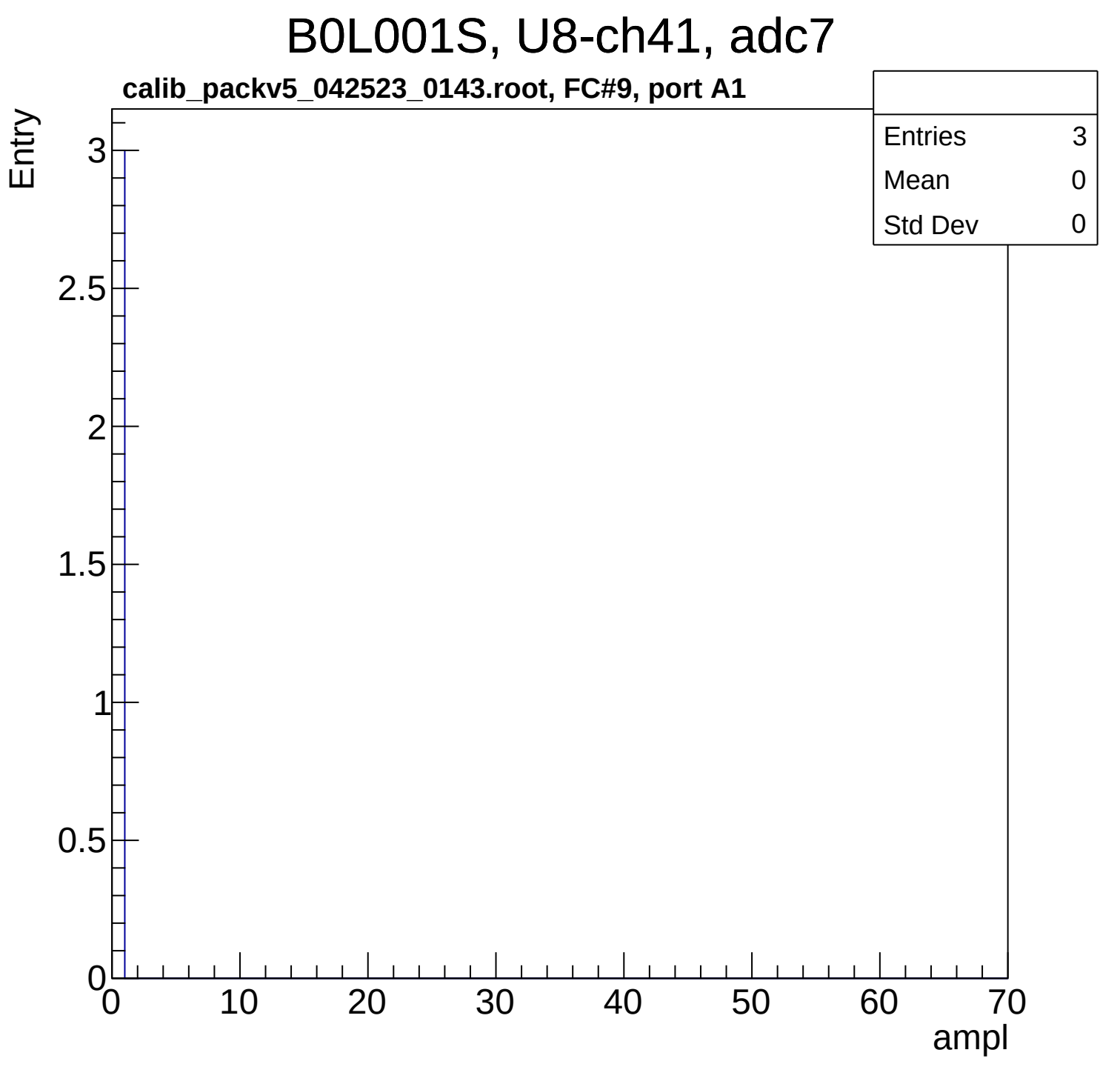
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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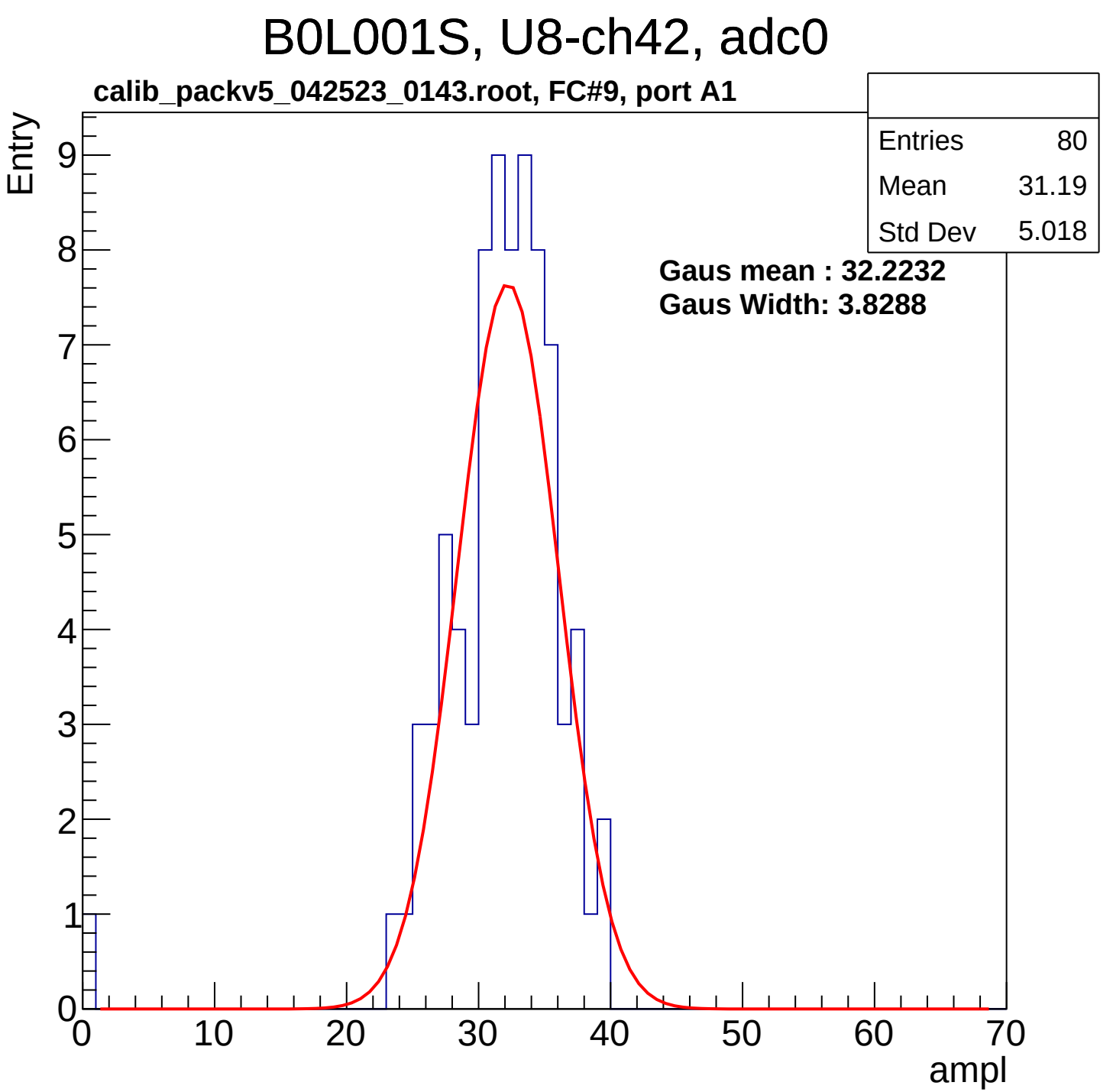
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 31.19
Std Dev 5.018
Gaus mean : 32.2232
Gaus Width: 3.8288
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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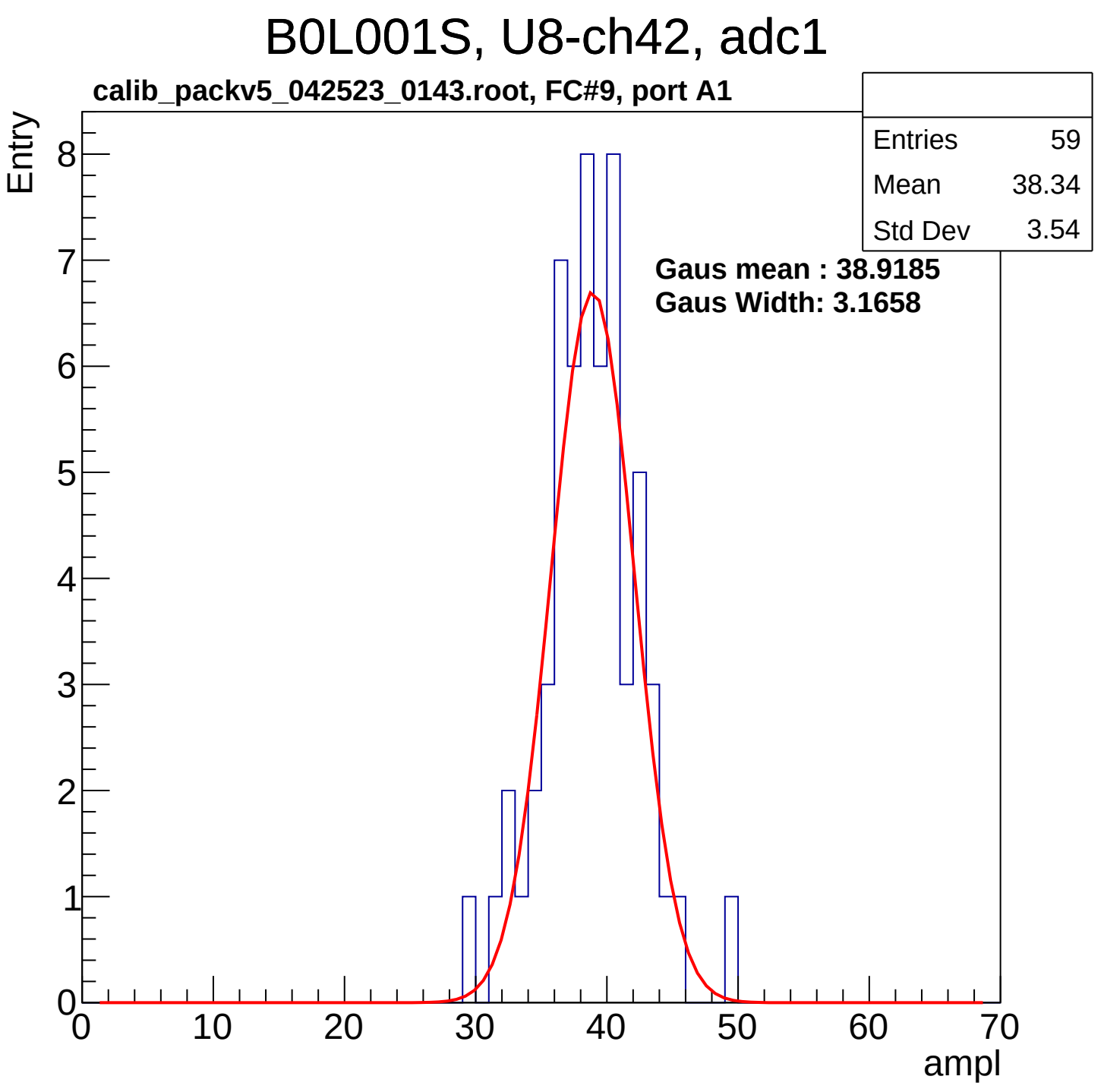
B0L001S, U8-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.34
Std Dev 3.54
Gaus mean : 38.9185
Gaus Width: 3.1658
Entry
ampl
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3
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6
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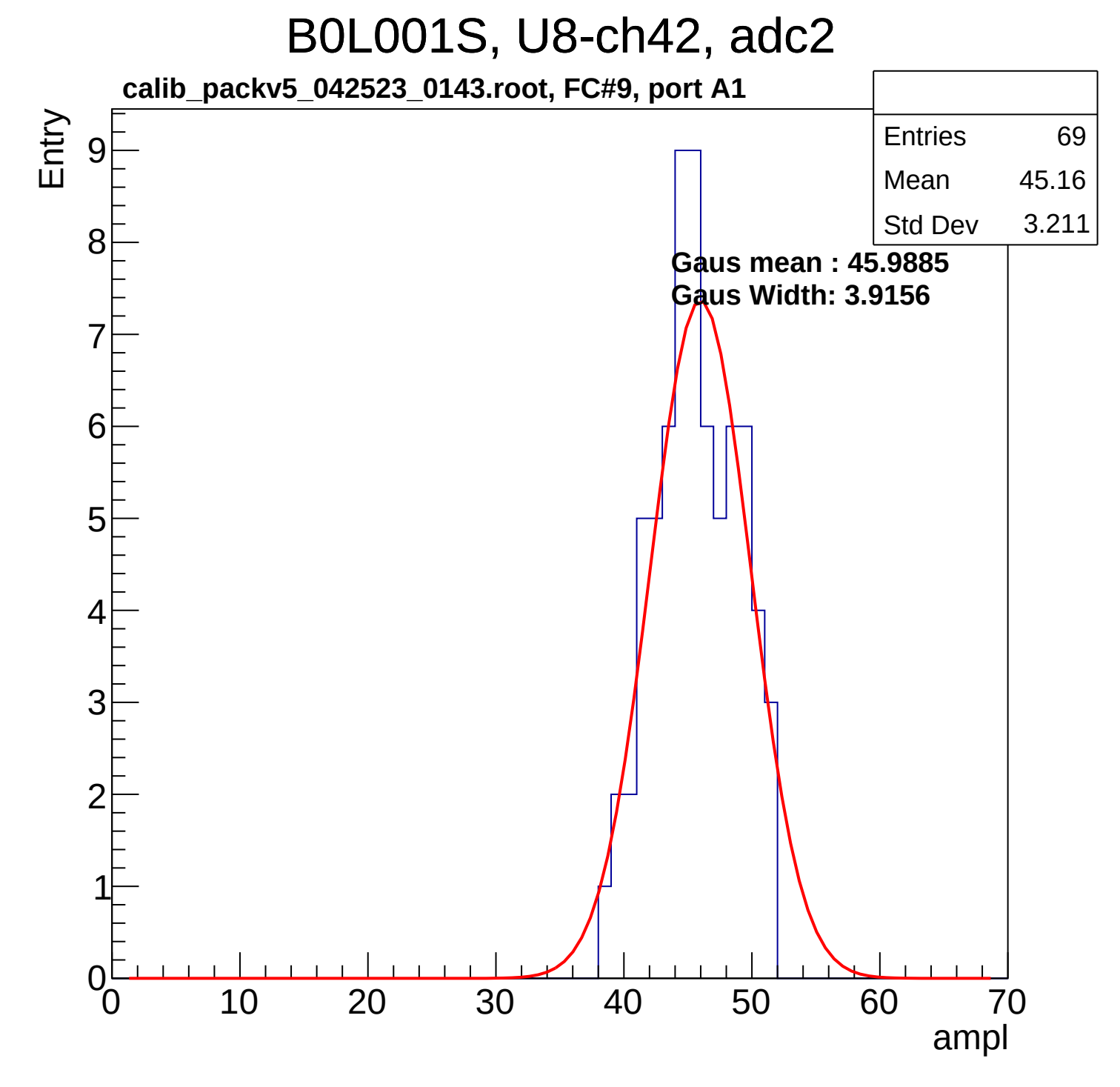
B0L001S, U8-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.16
Std Dev 3.211
Gaus mean : 45.9885
Gaus Width: 3.9156
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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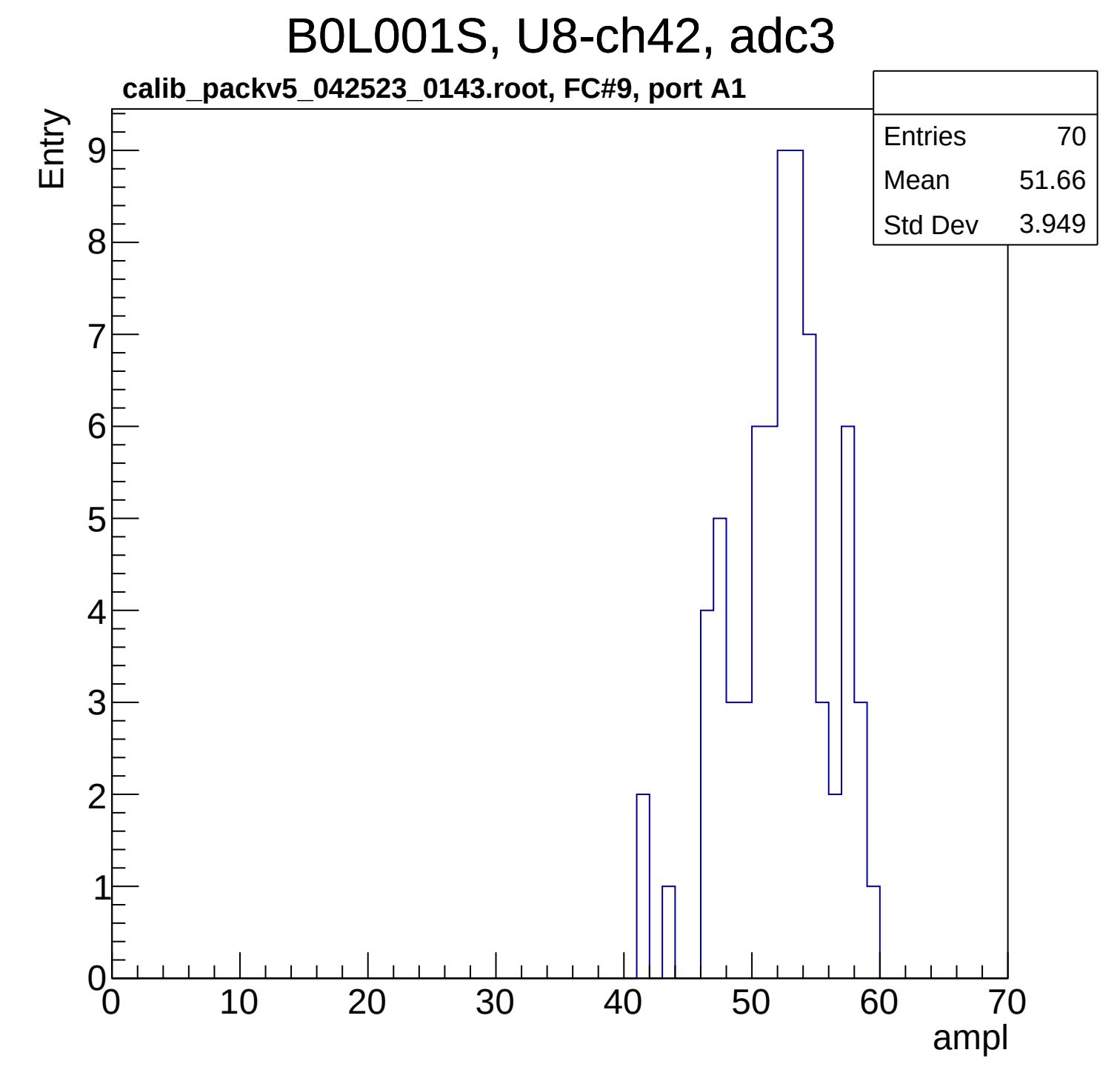
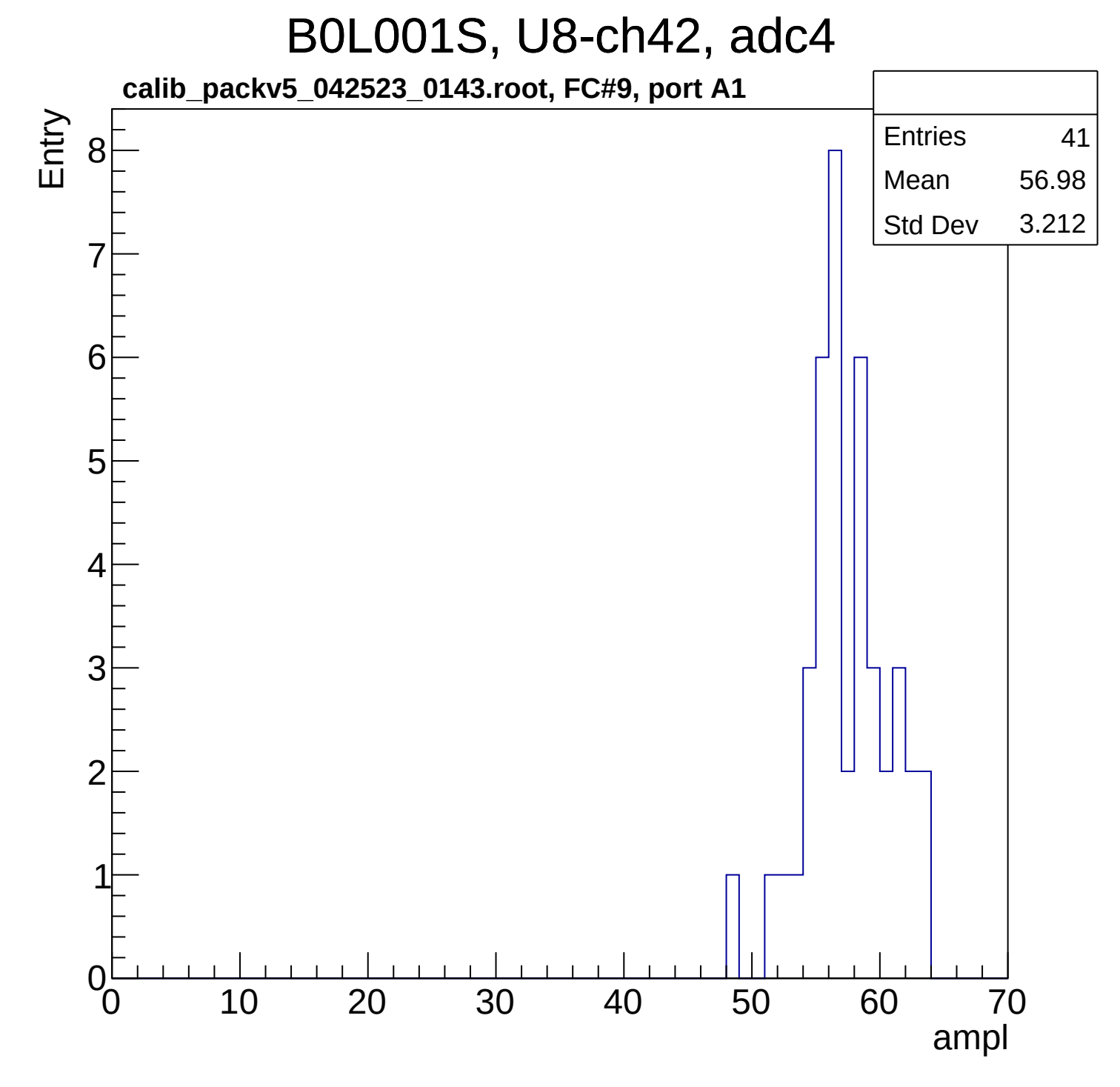

B0L001S, U8-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.66
Std Dev 3.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 56.98
Std Dev 3.212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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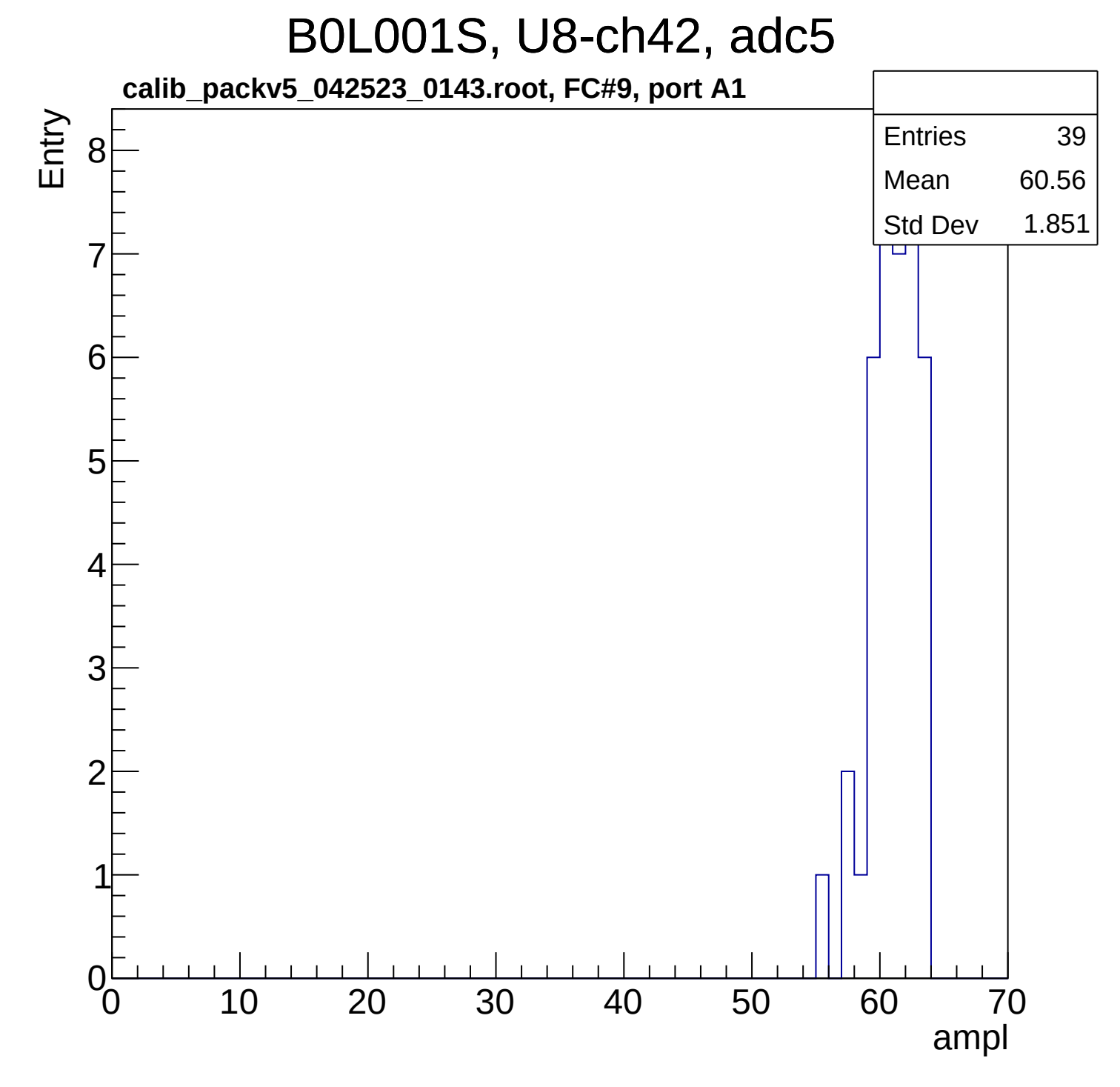
B0L001S, U8-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 60.56
Std Dev 1.851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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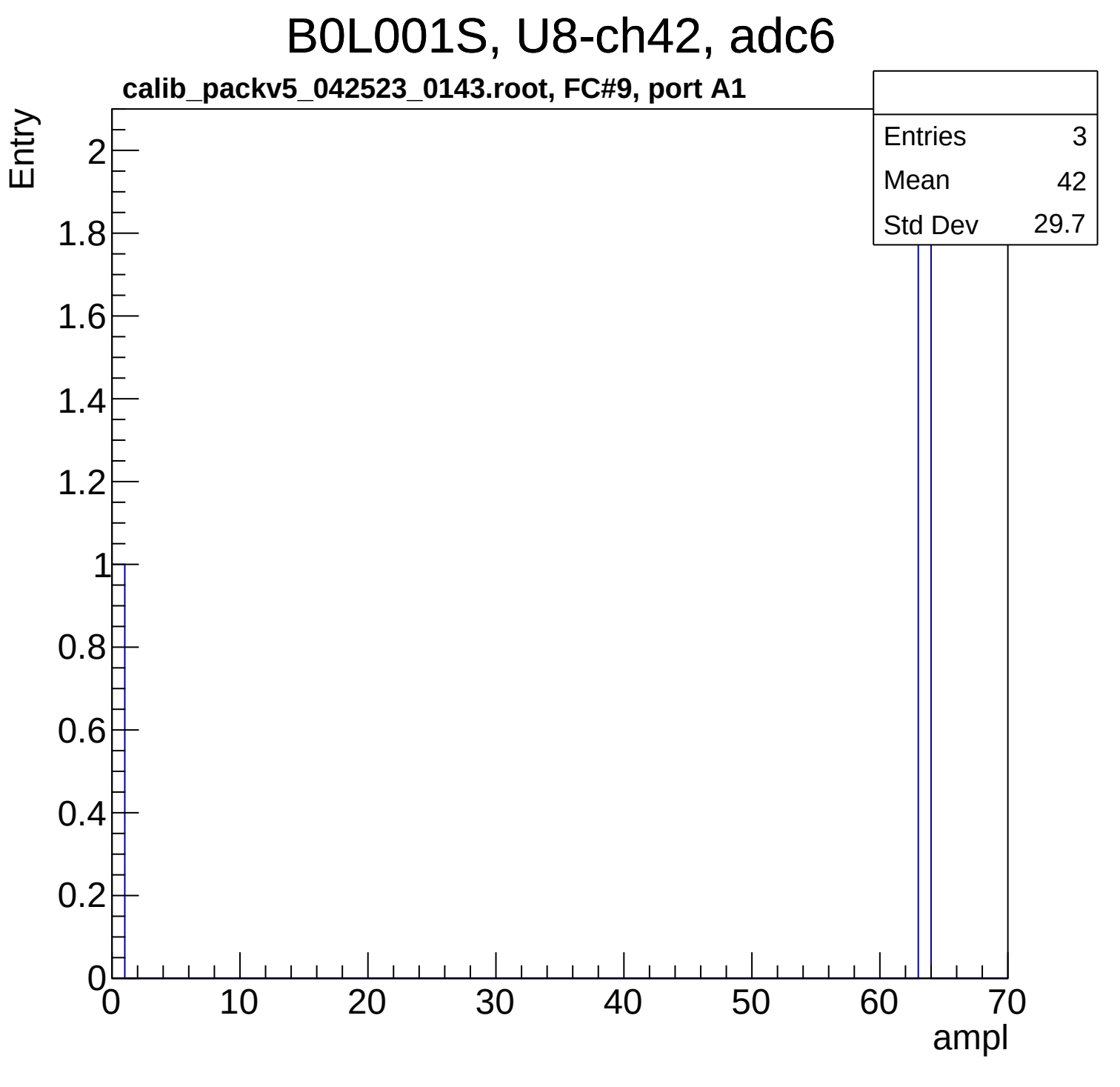
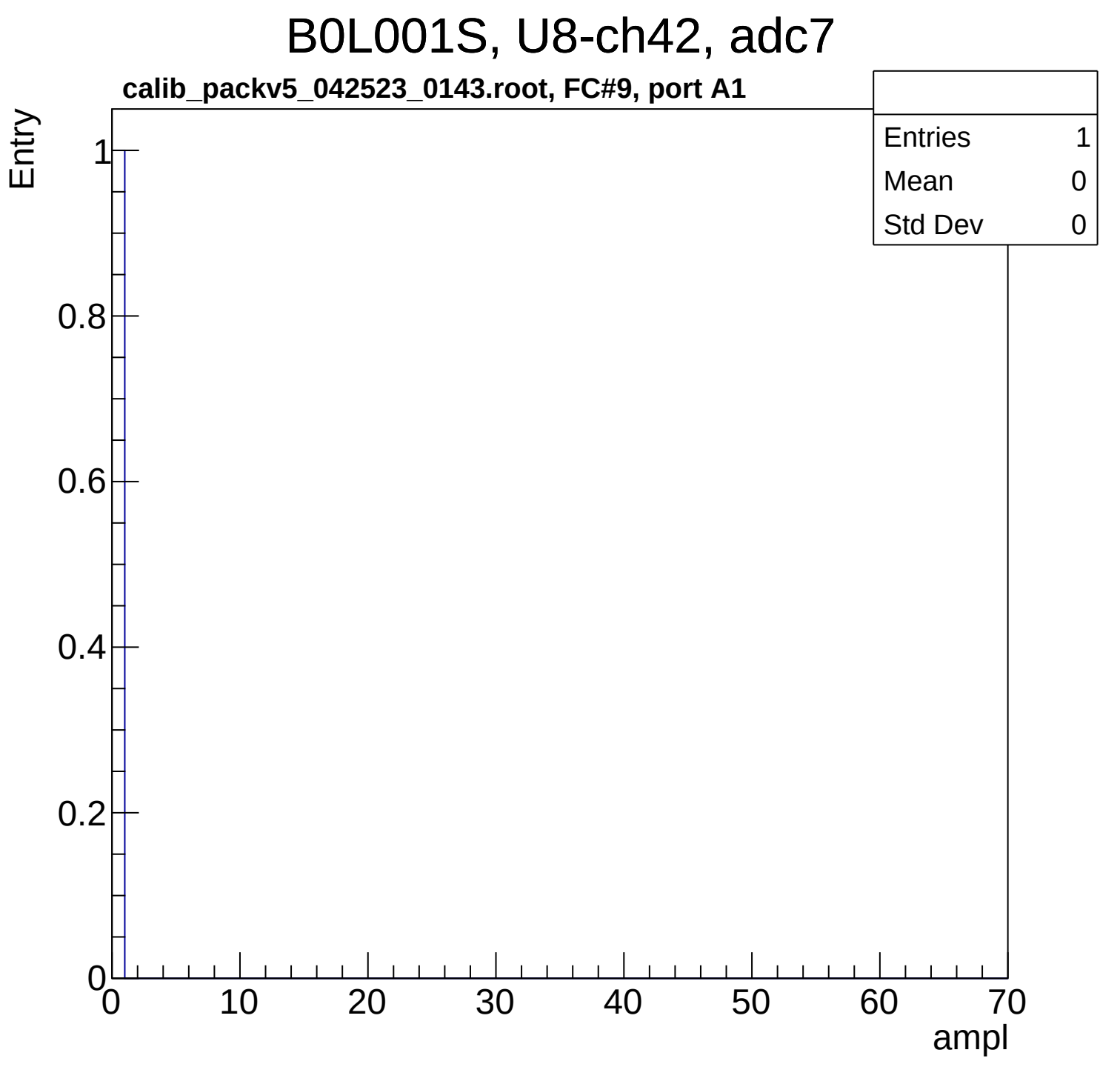
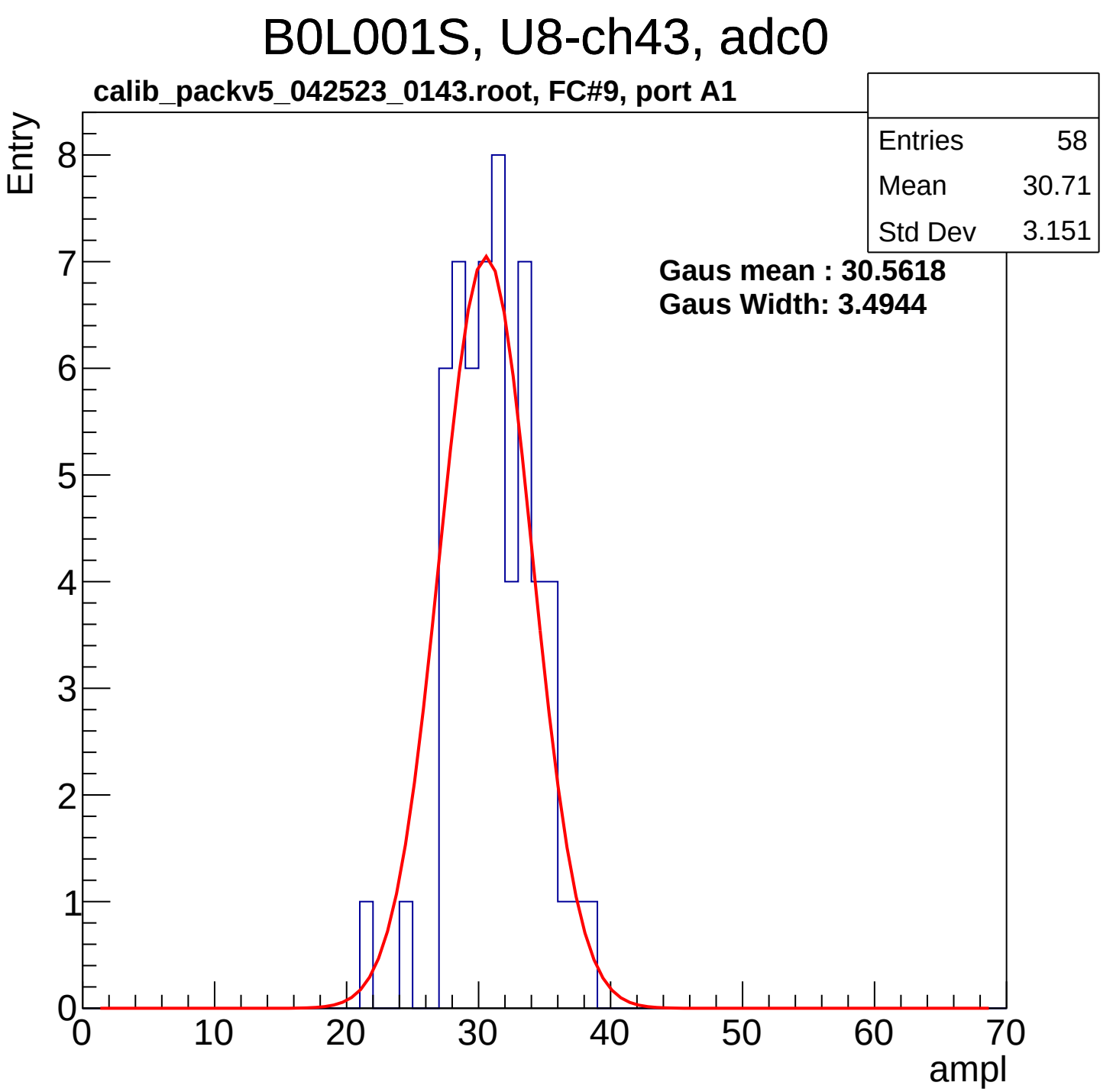
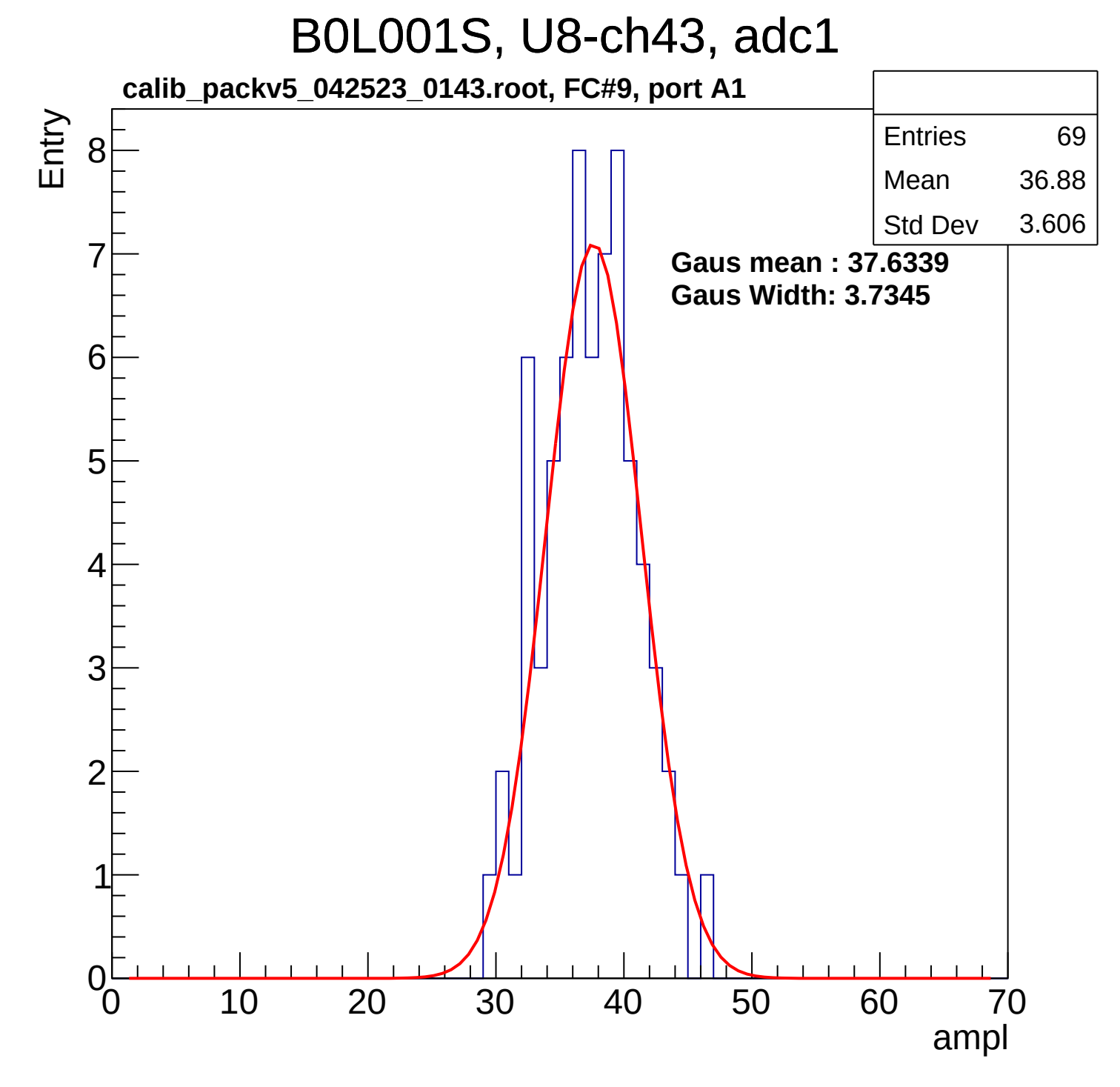
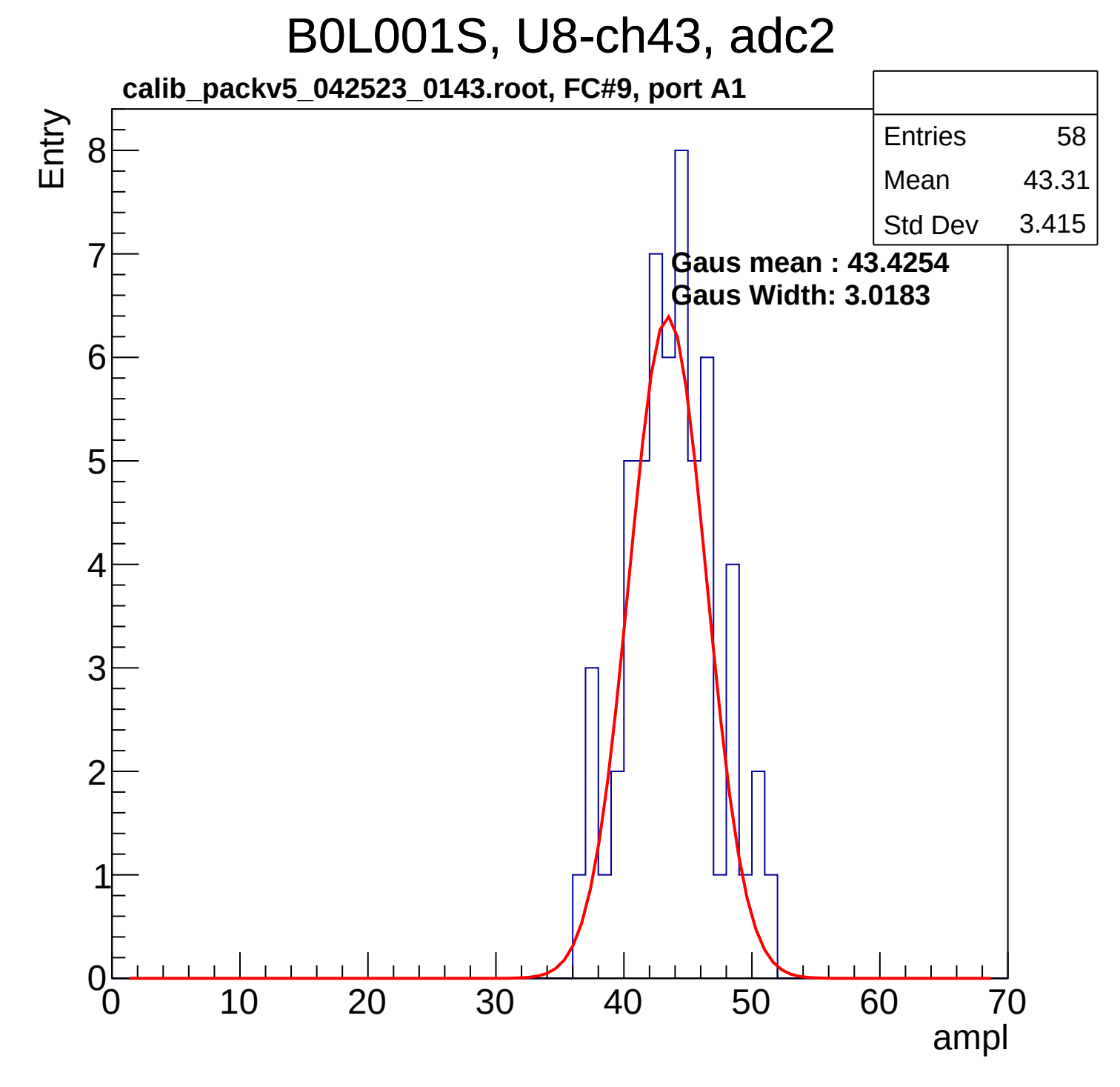
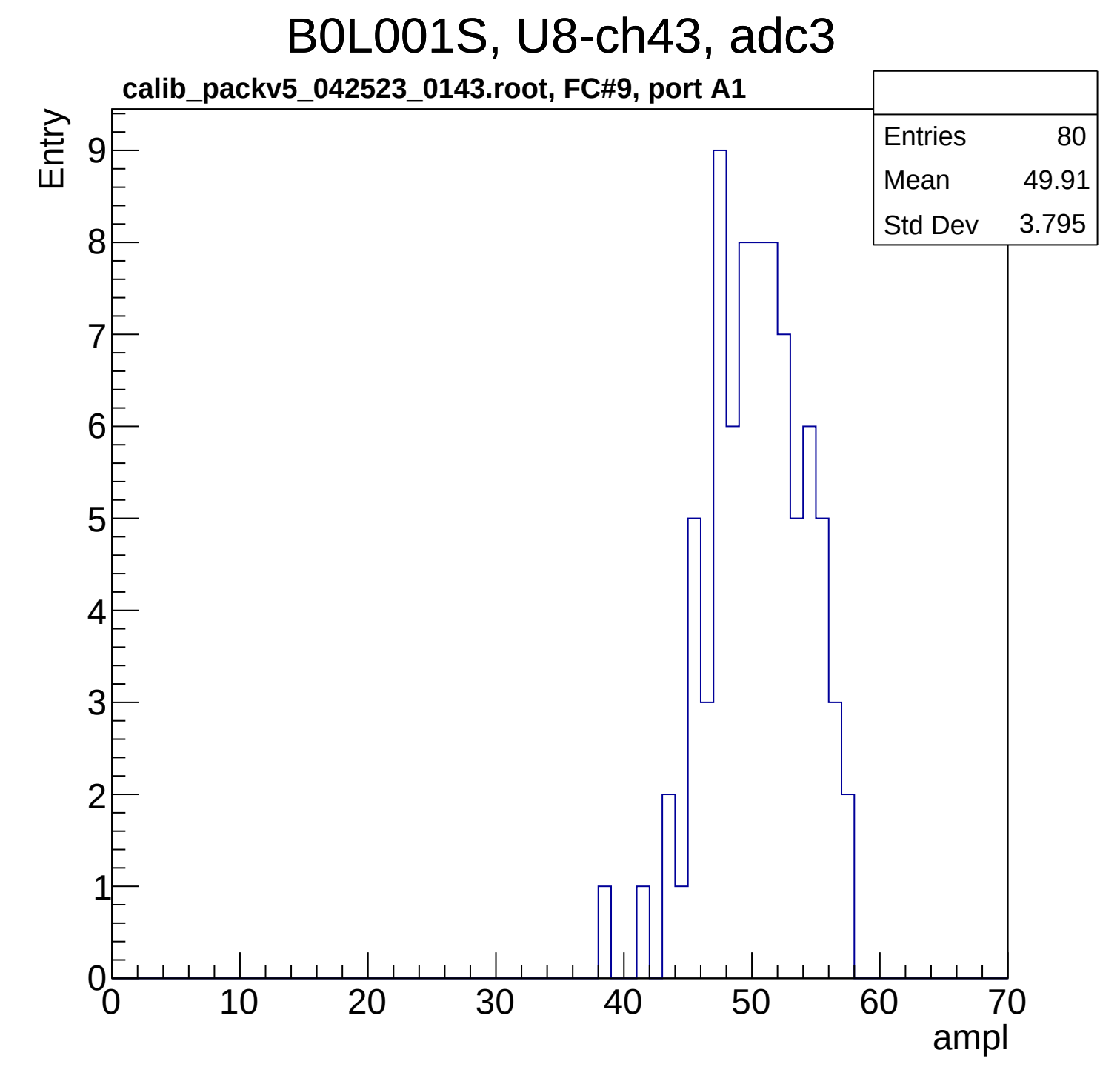

B0L001S, U8-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.71
Std Dev 3.151
Gaus mean : 30.5618
Gaus Width: 3.4944
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.88
Std Dev 3.606
Gaus mean : 37.6339
Gaus Width: 3.7345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.31
Std Dev 3.415
Gaus mean : 43.4254
Gaus Width: 3.0183
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.91
Std Dev 3.795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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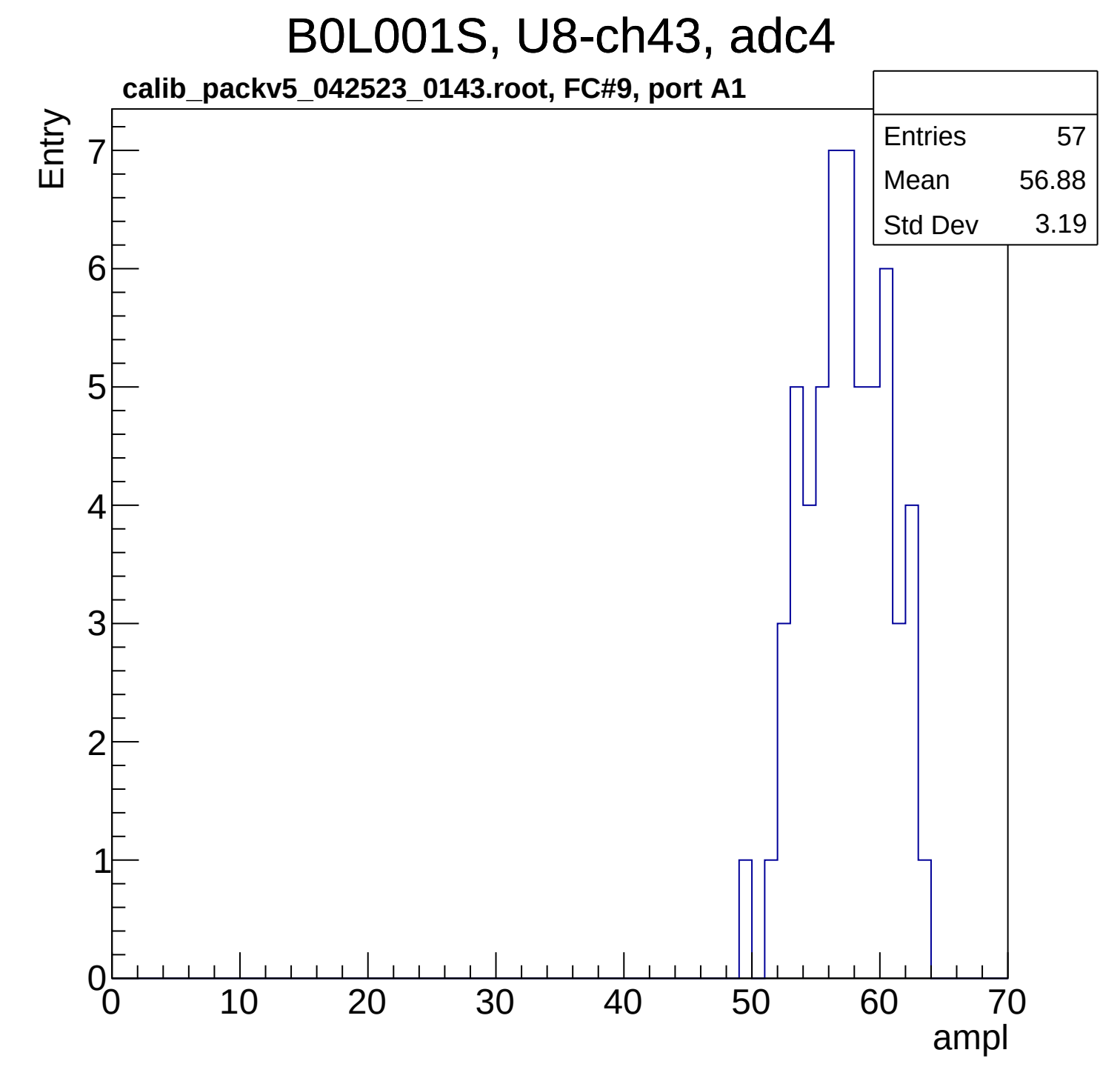
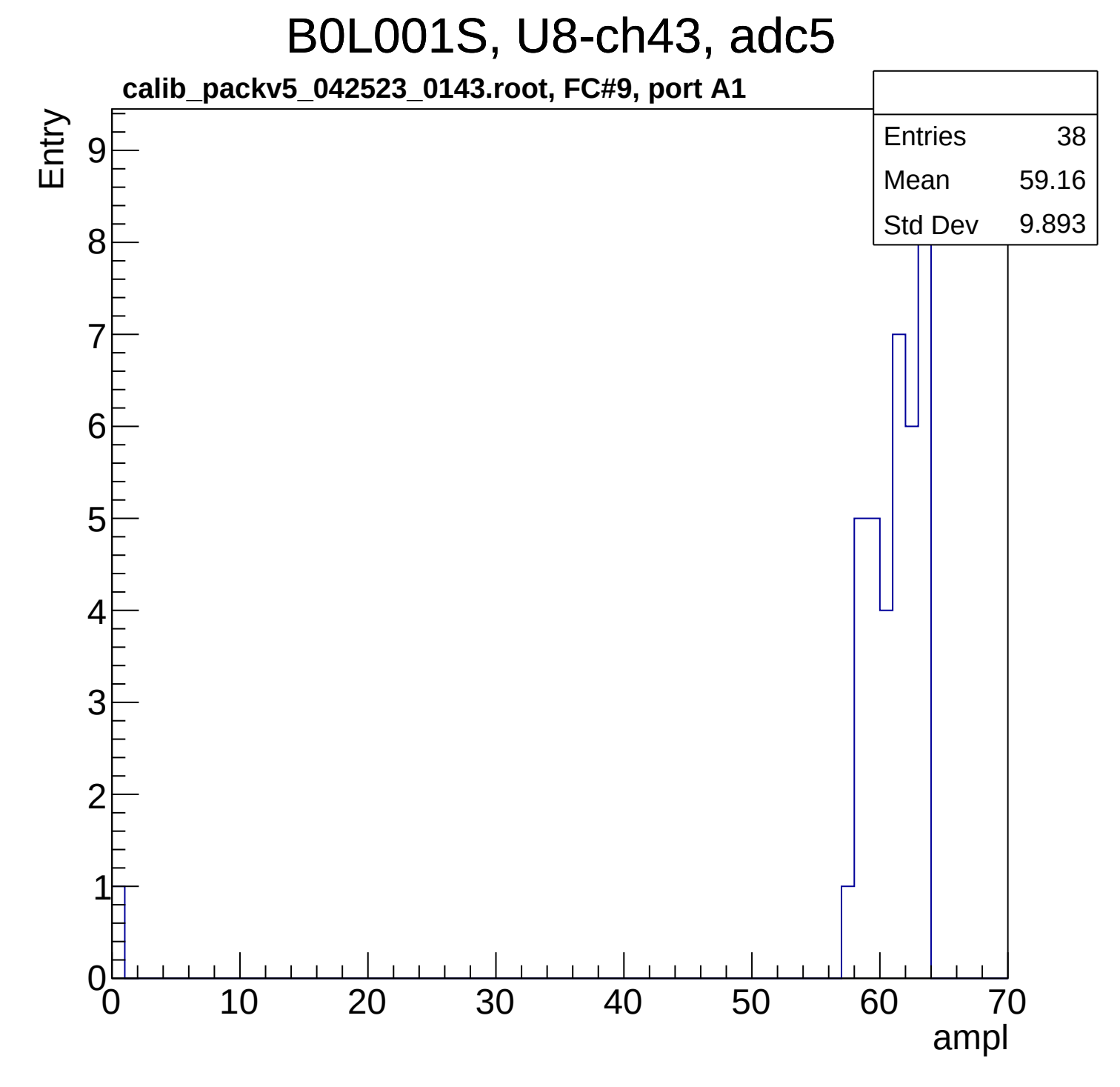
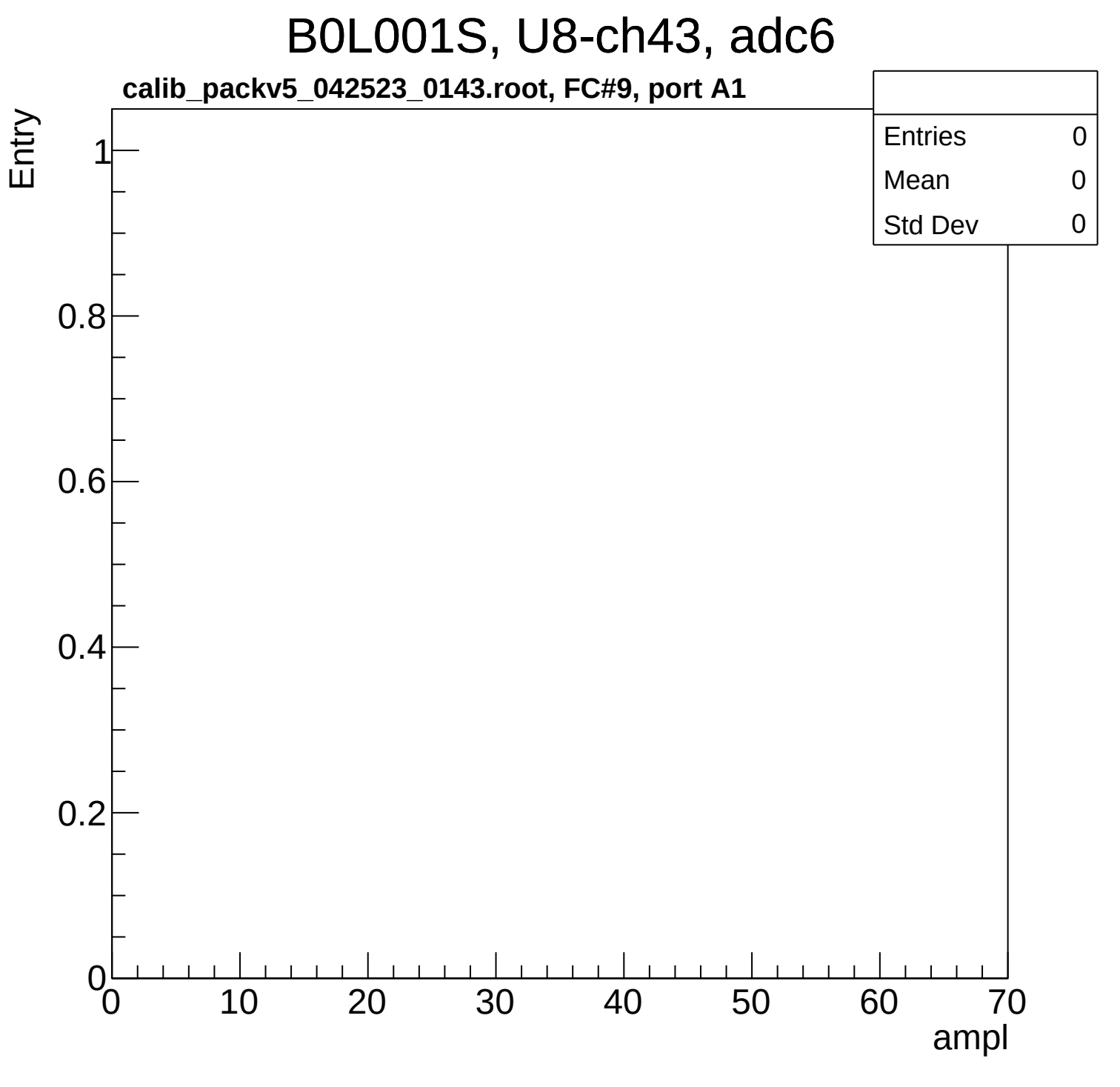
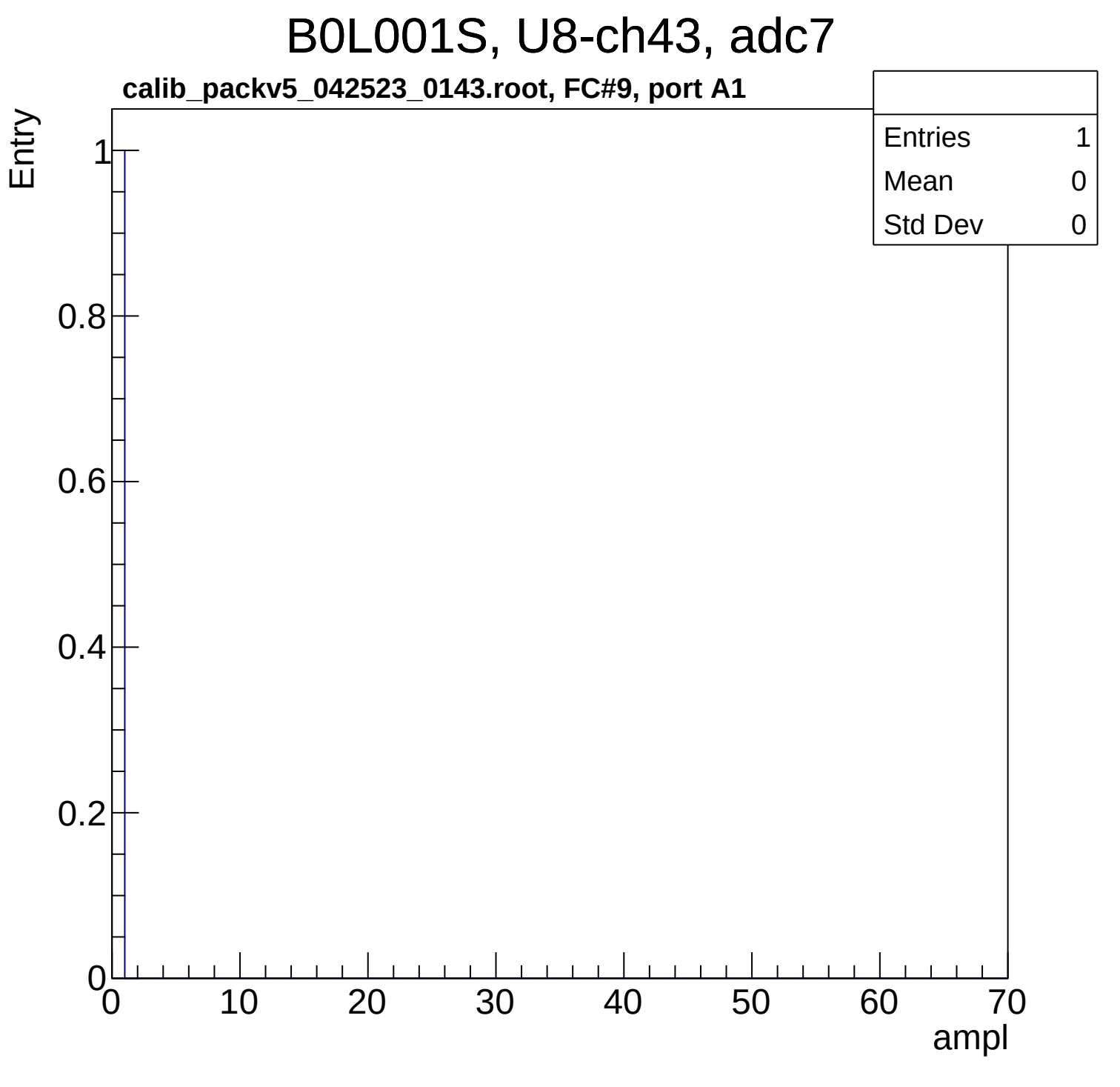
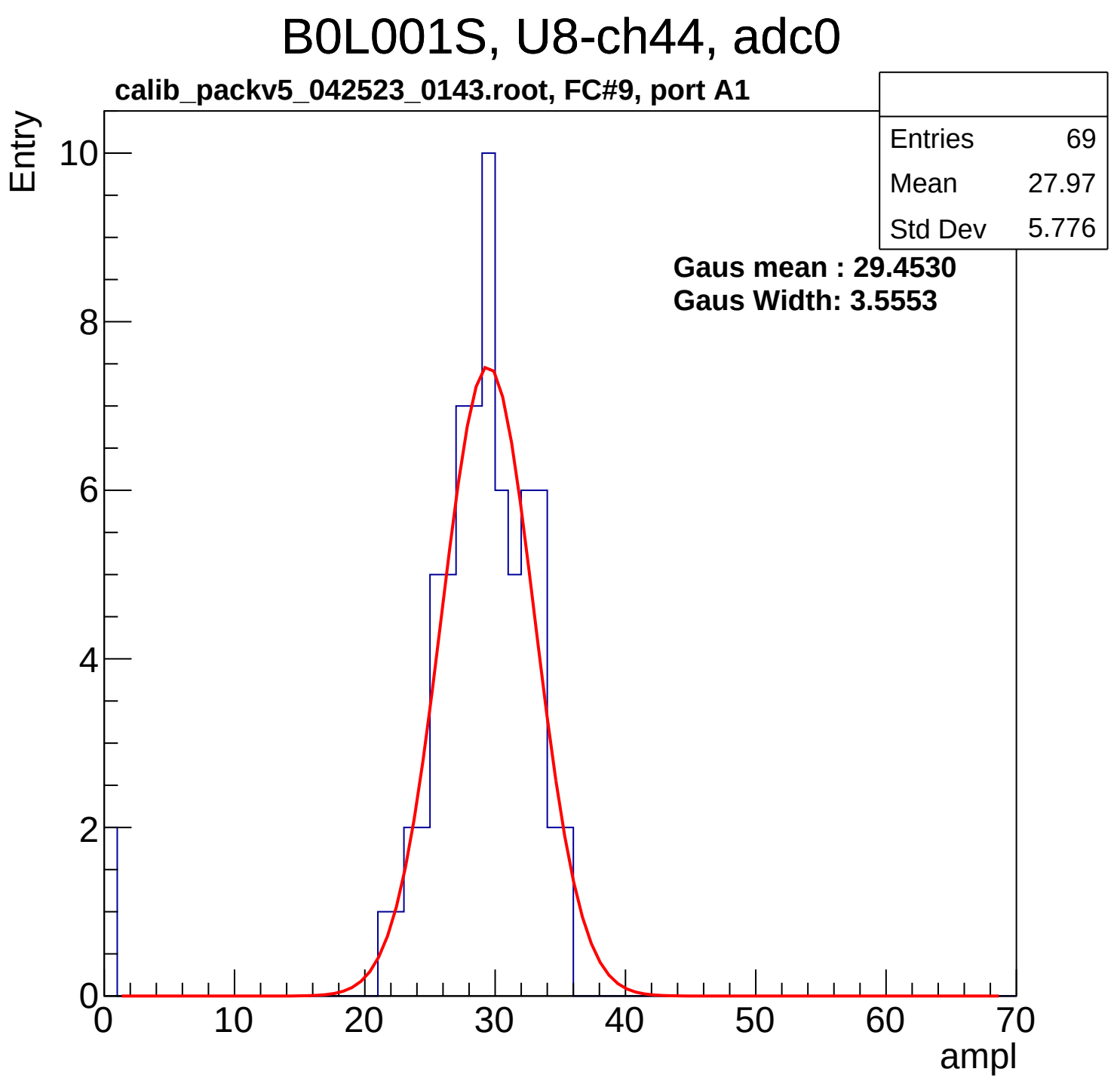
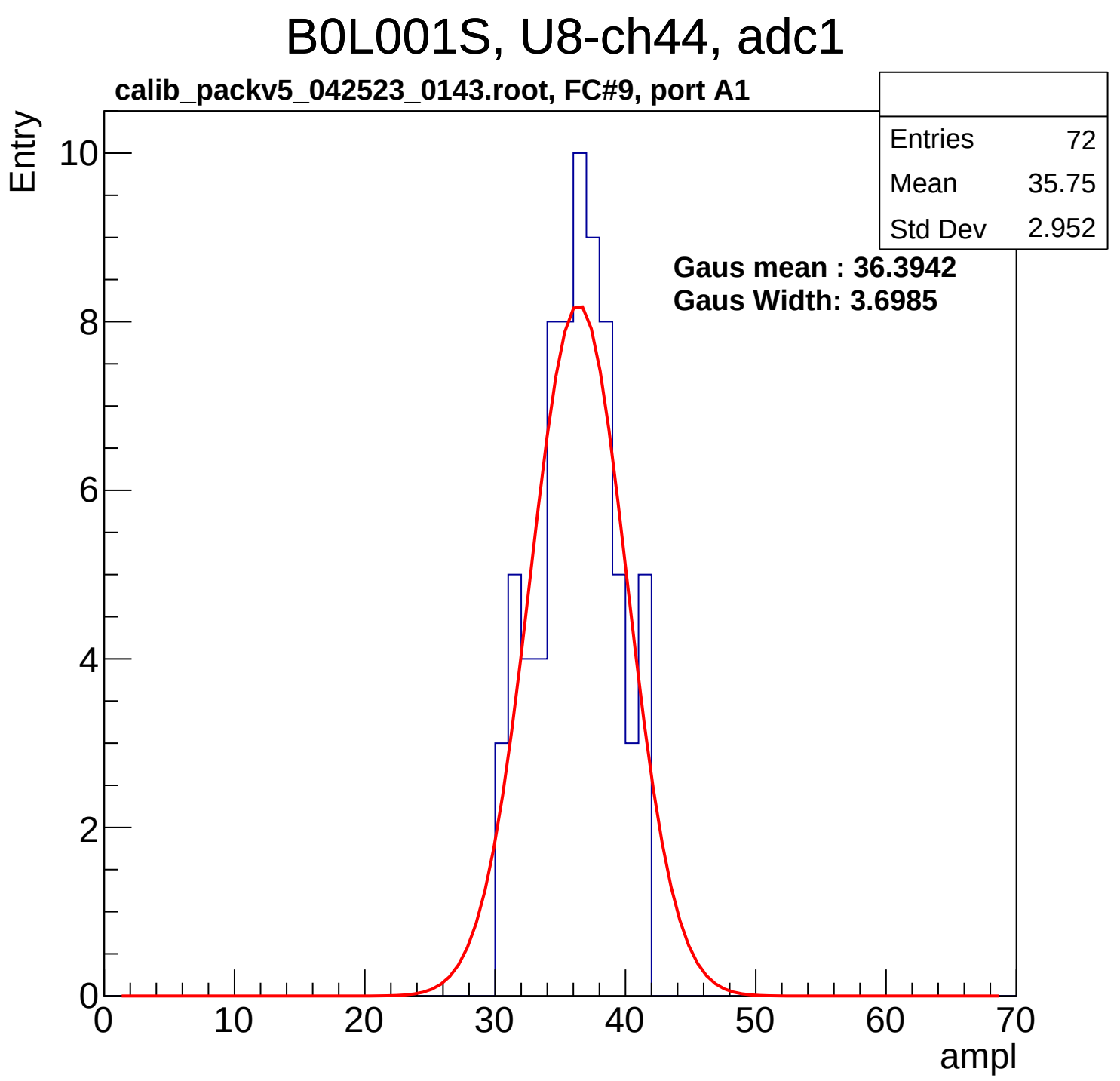

B0L001S, U8-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.88
Std Dev 3.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

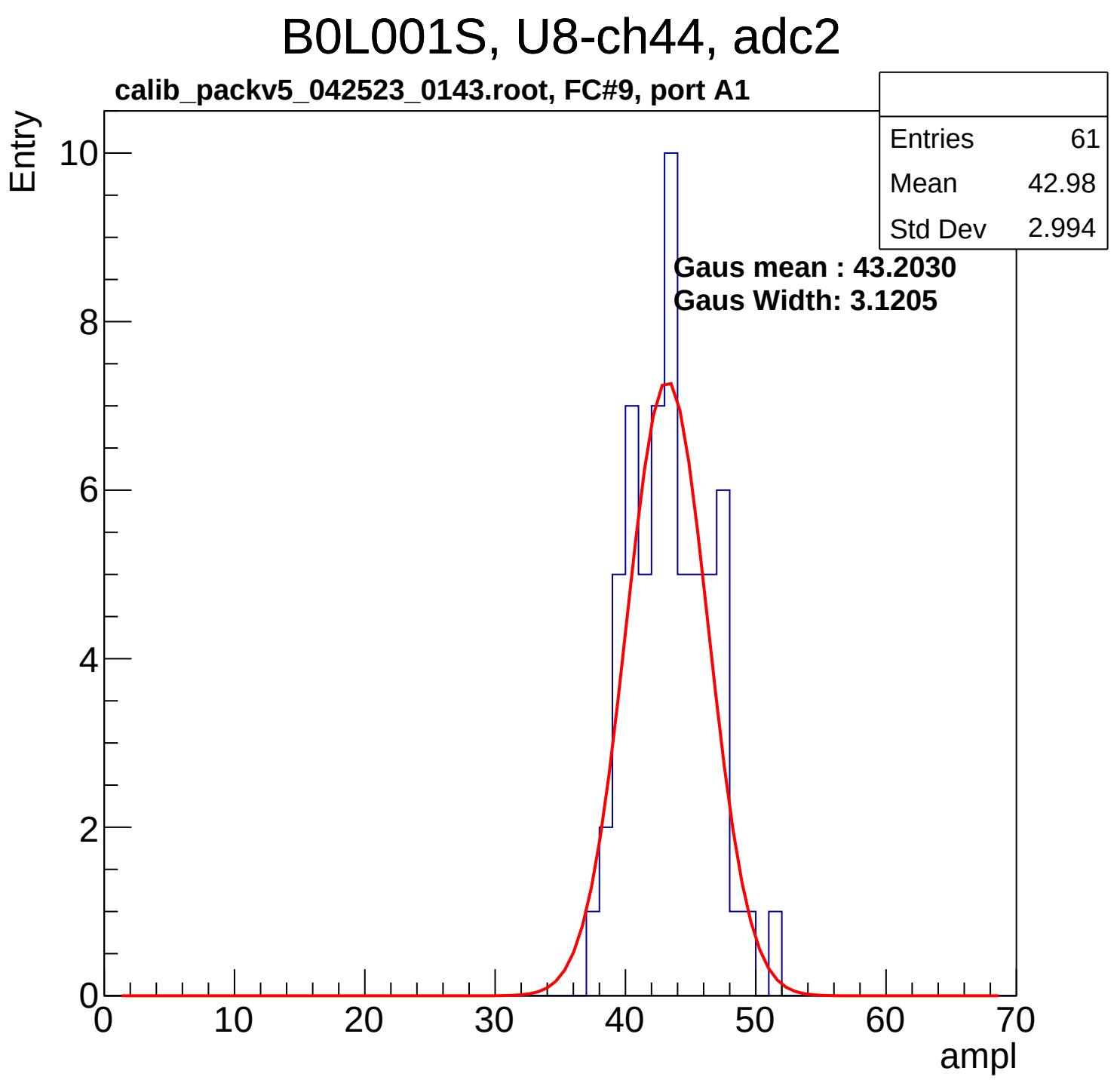

B0L001S, U8-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.16
Std Dev 9.893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

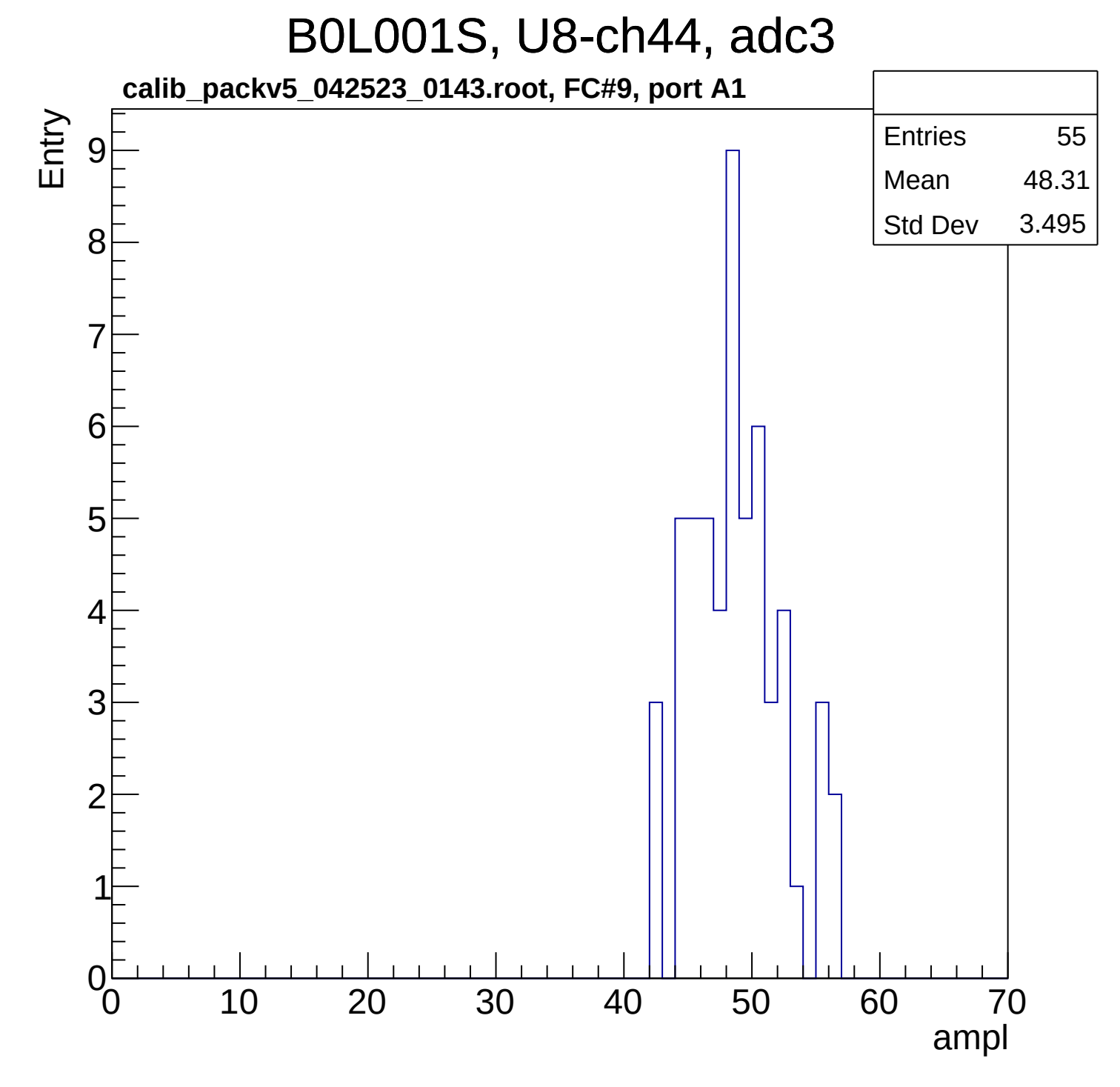

B0L001S, U8-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

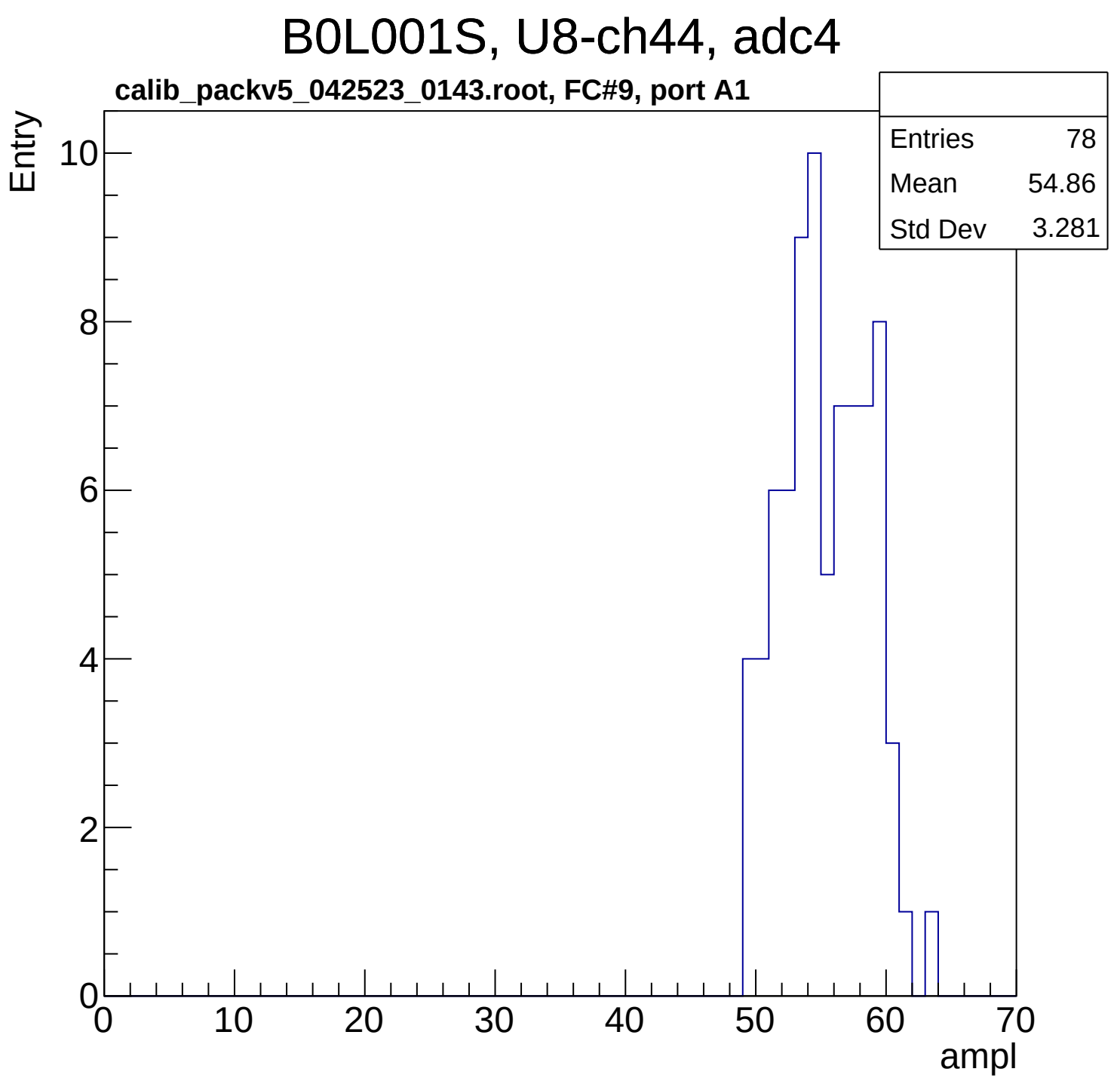

B0L001S, U8-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

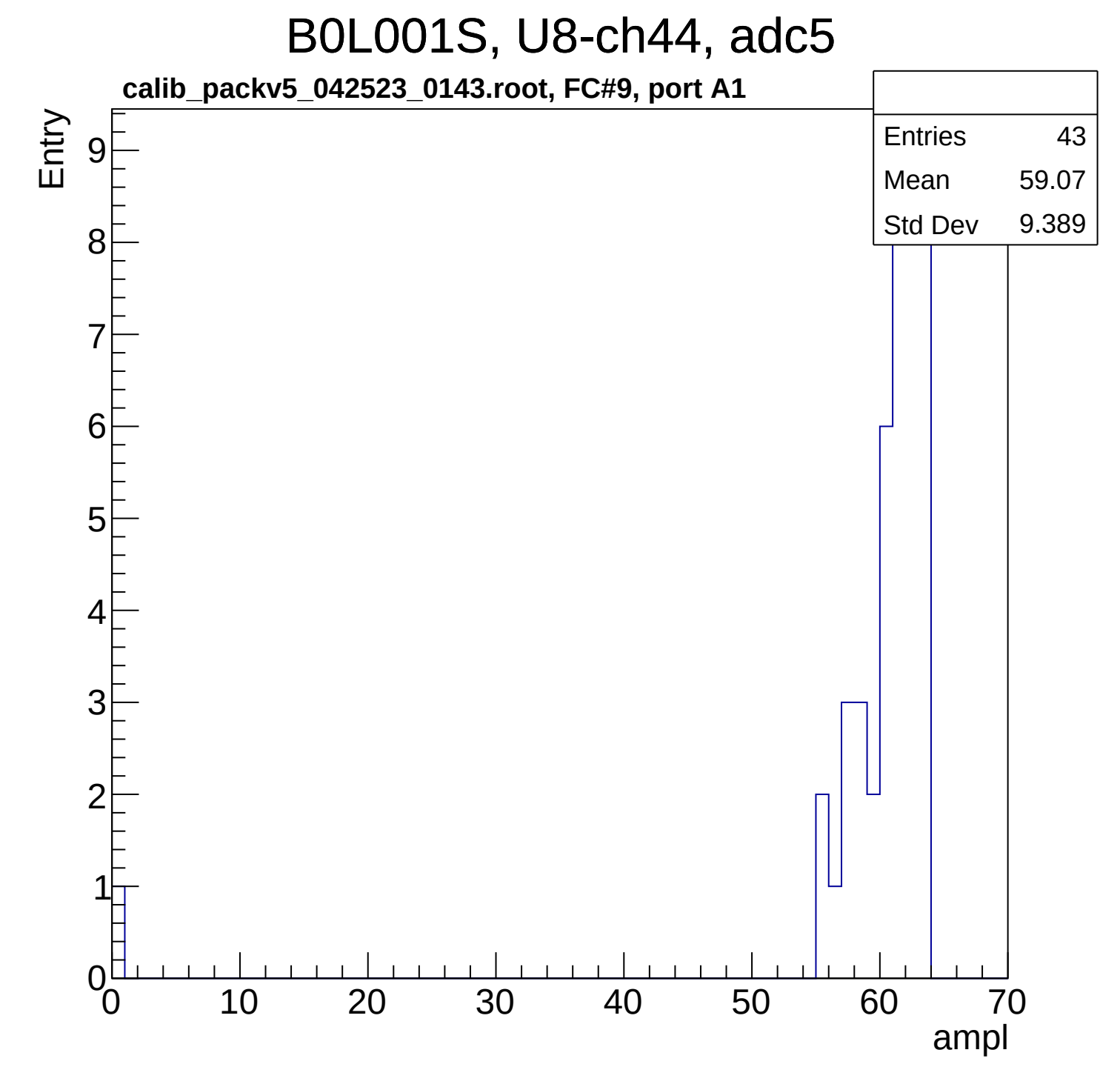

B0L001S, U8-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 27.97
Std Dev 5.776
Gaus mean : 29.4530
Gaus Width: 3.5553
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 35.75
Std Dev 2.952
Gaus mean : 36.3942
Gaus Width: 3.6985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 42.98
Std Dev 2.994
Gaus mean : 43.2030
Gaus Width: 3.1205
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 48.31
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 54.86
Std Dev 3.281
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.07
Std Dev 9.389
Entry
ampl
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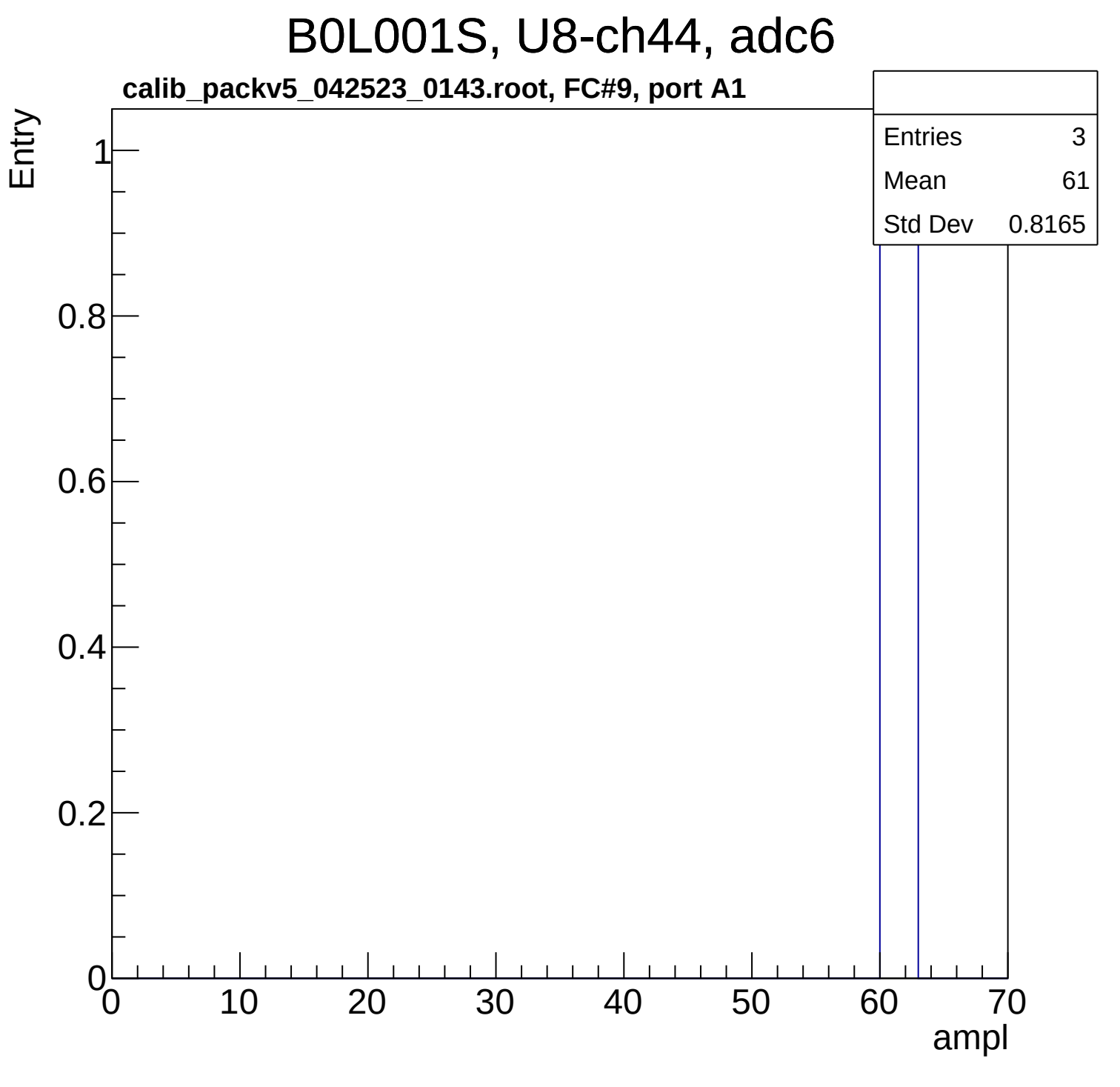
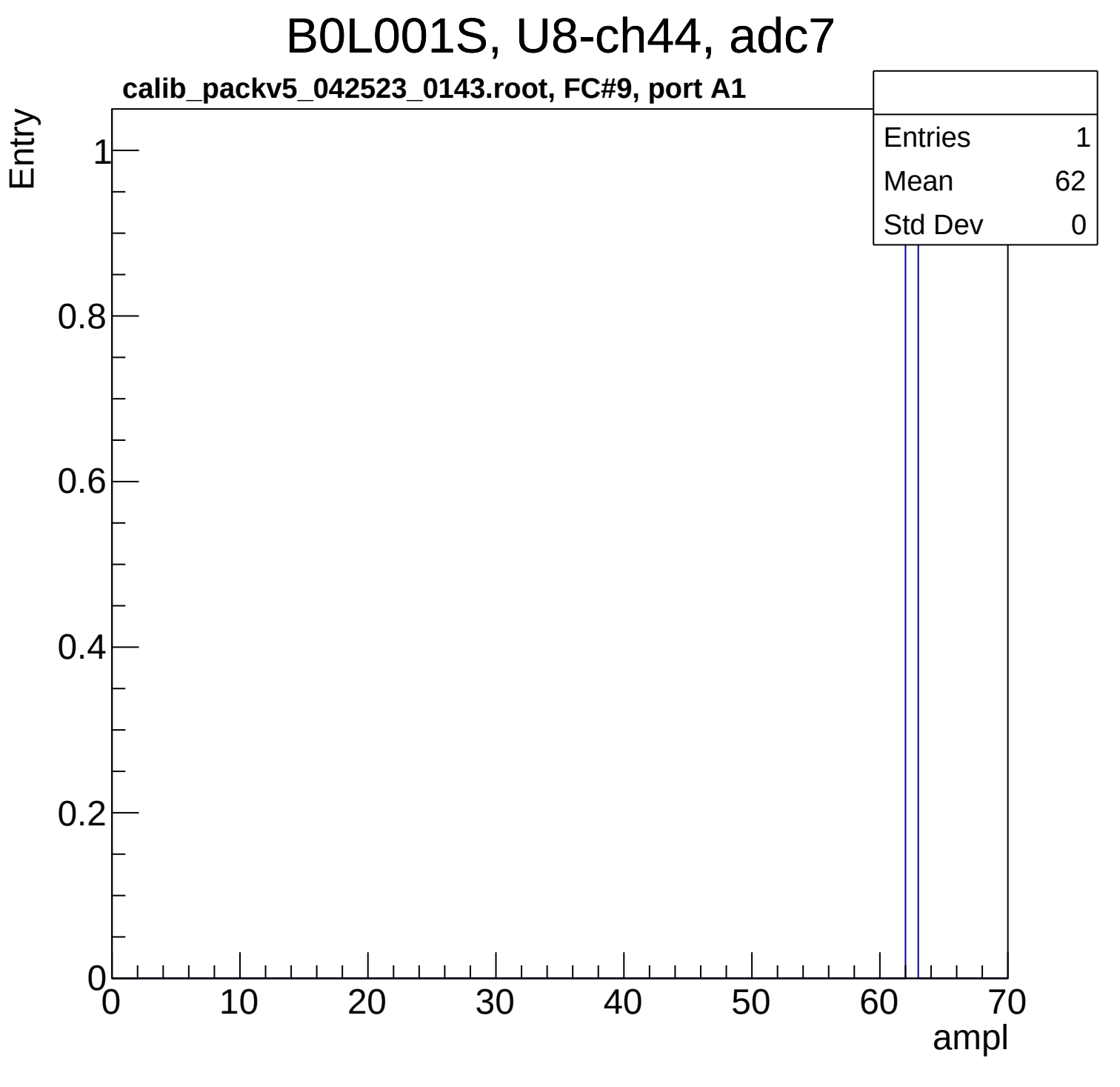
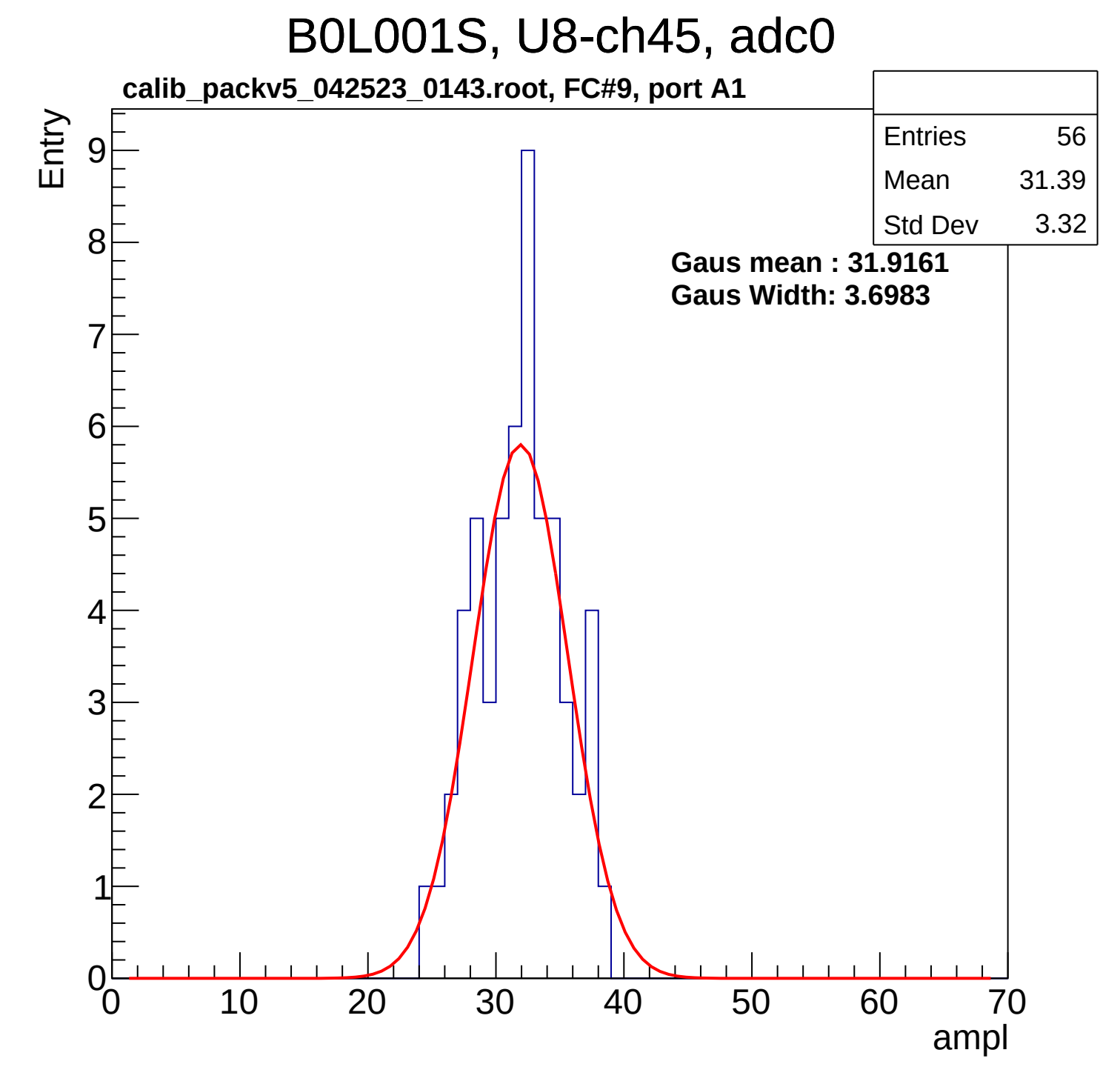
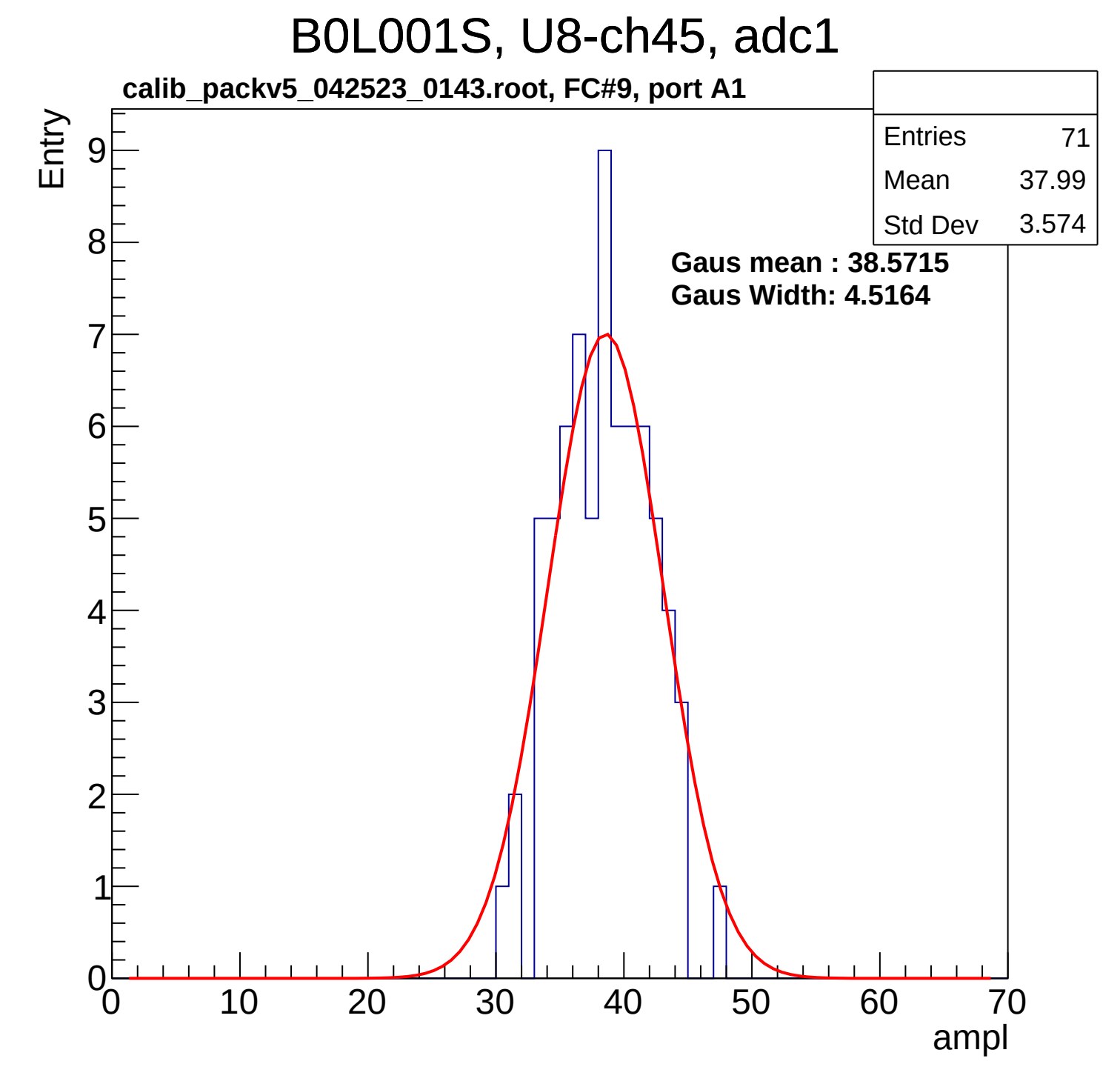
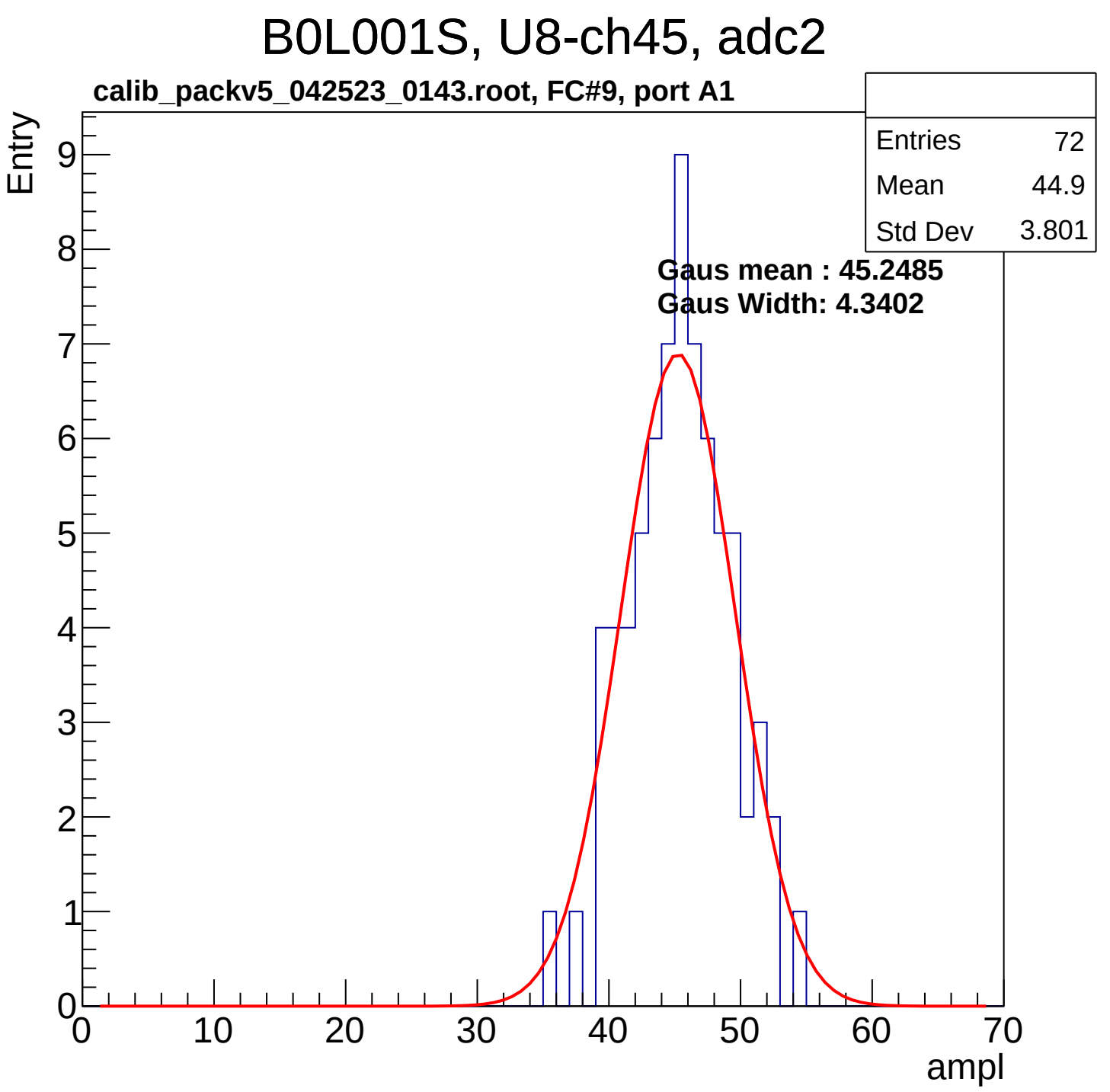
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

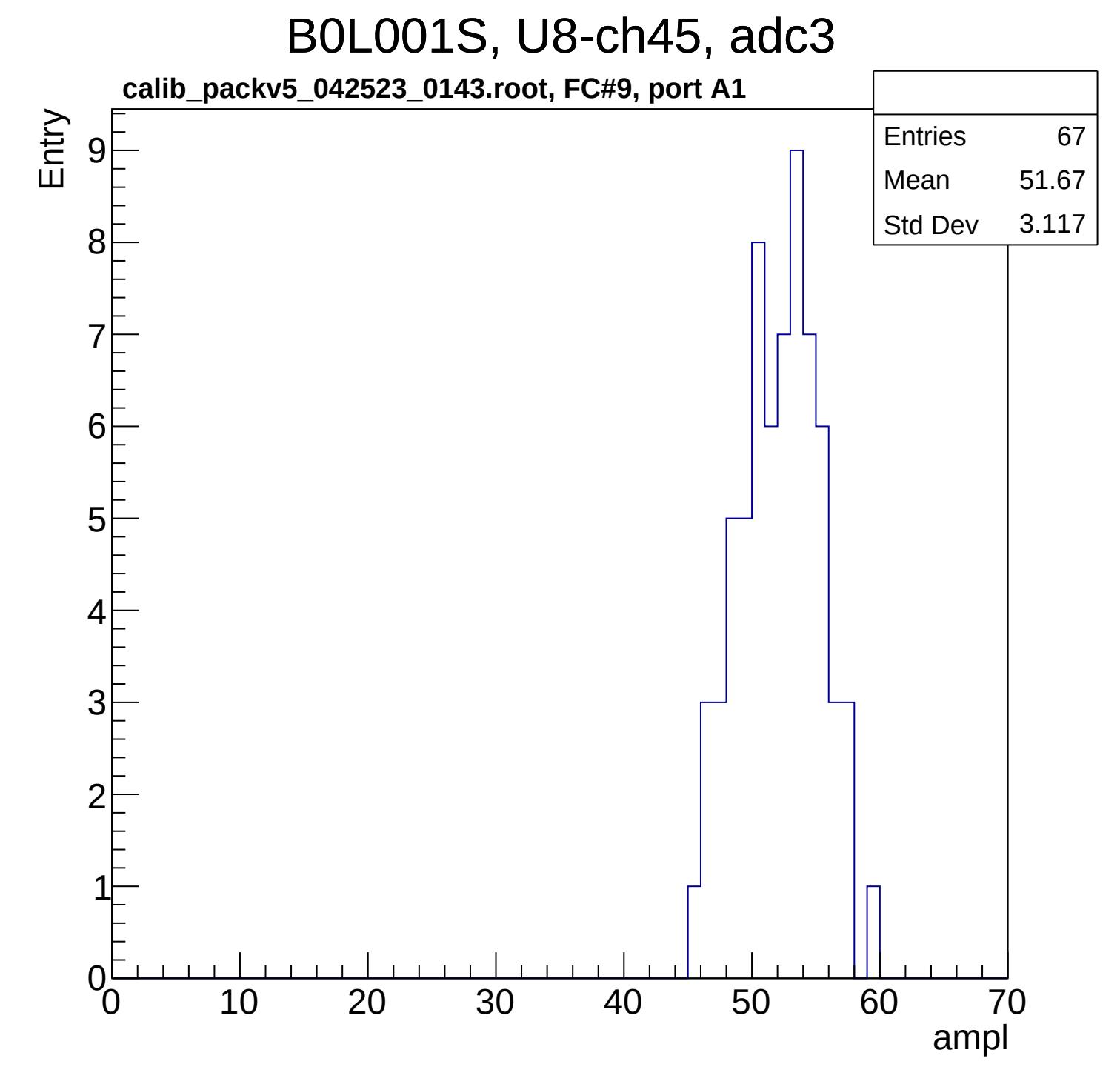

B0L001S, U8-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 31.39
Std Dev 3.32
Gaus mean : 31.9161
Gaus Width: 3.6983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.99
Std Dev 3.574
Gaus mean : 38.5715
Gaus Width: 4.5164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.9
Std Dev 3.801
Gaus mean : 45.2485
Gaus Width: 4.3402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.67
Std Dev 3.117
Entry
ampl
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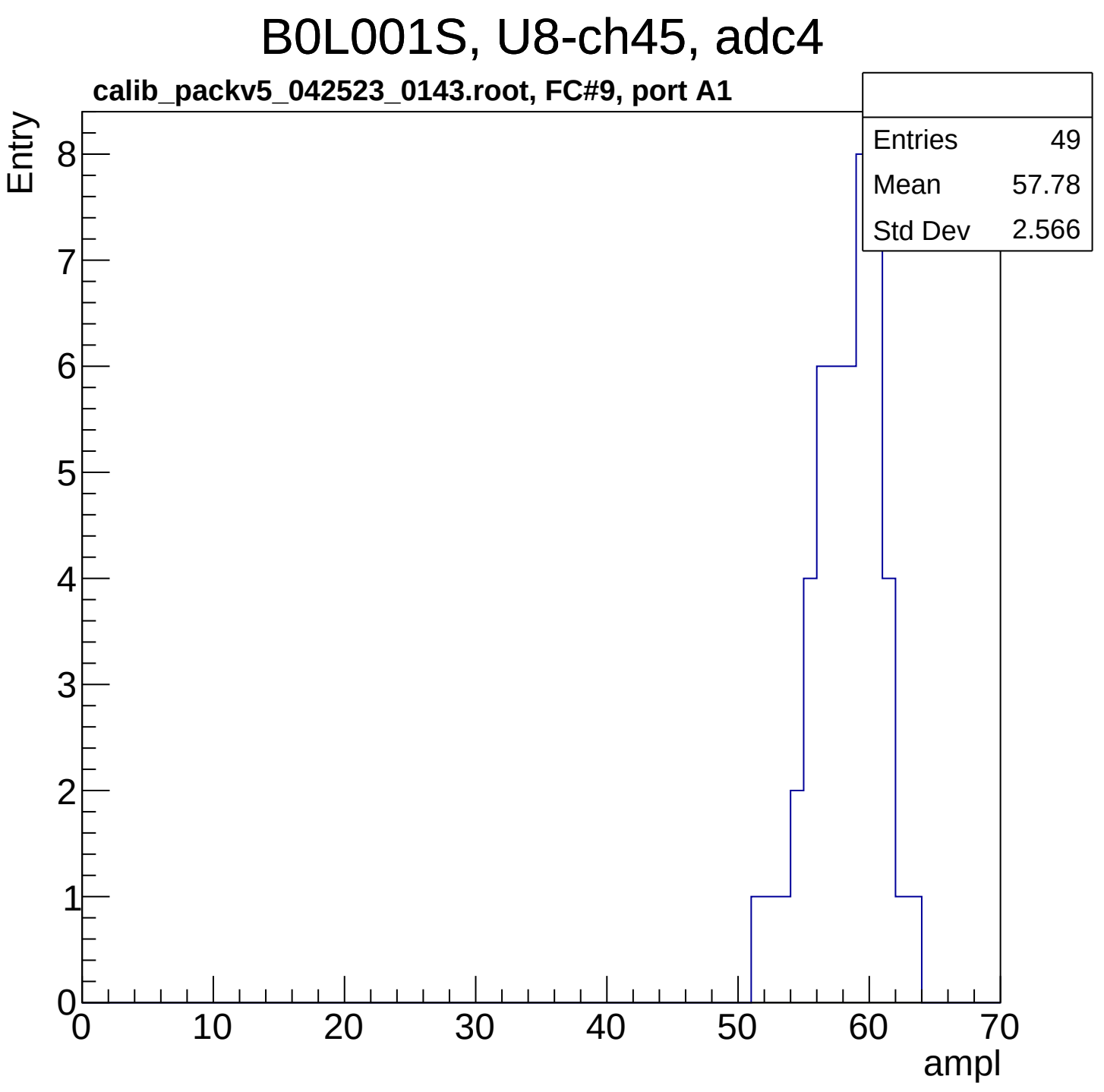
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.78
Std Dev 2.566
Entry
0
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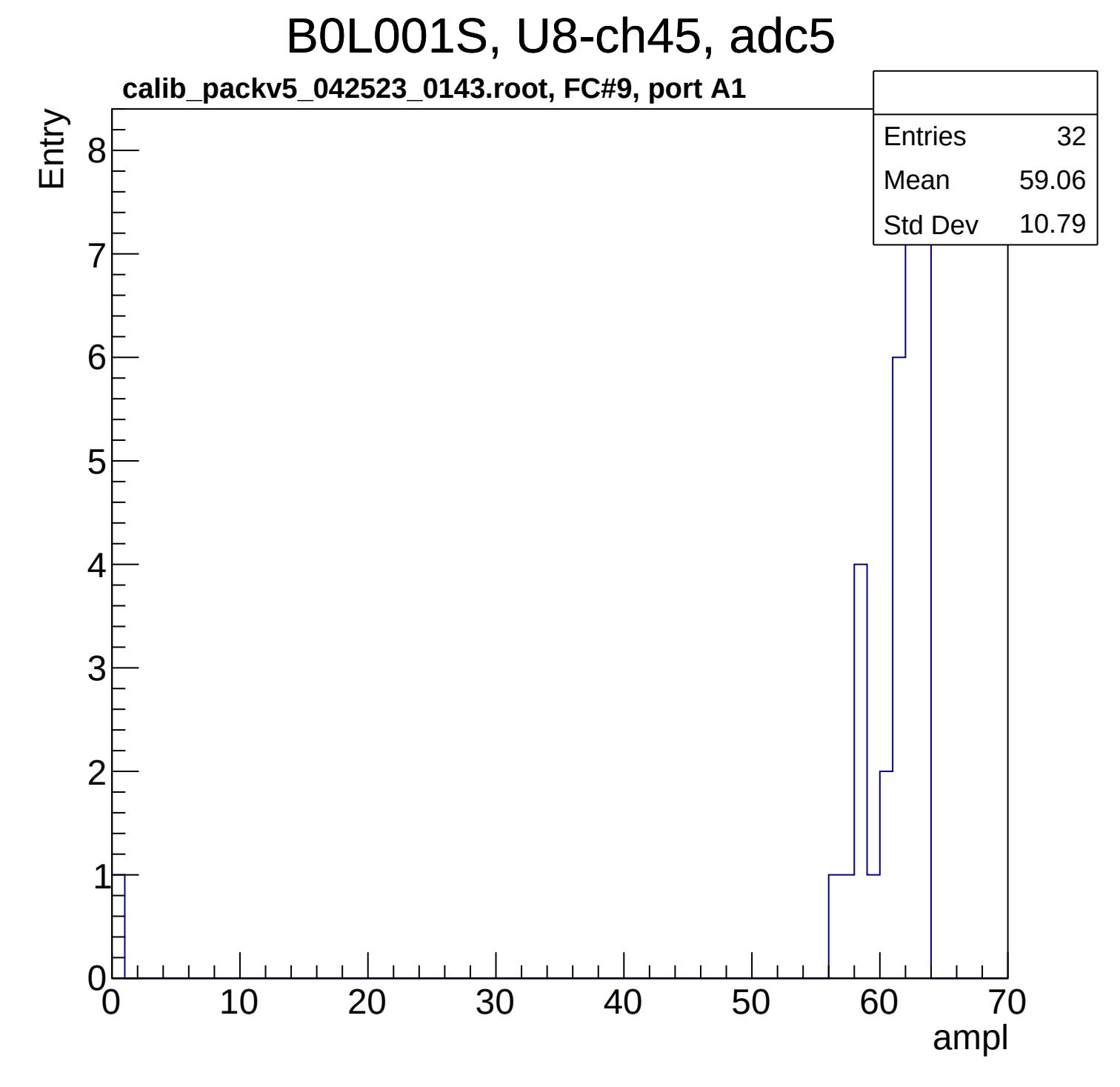
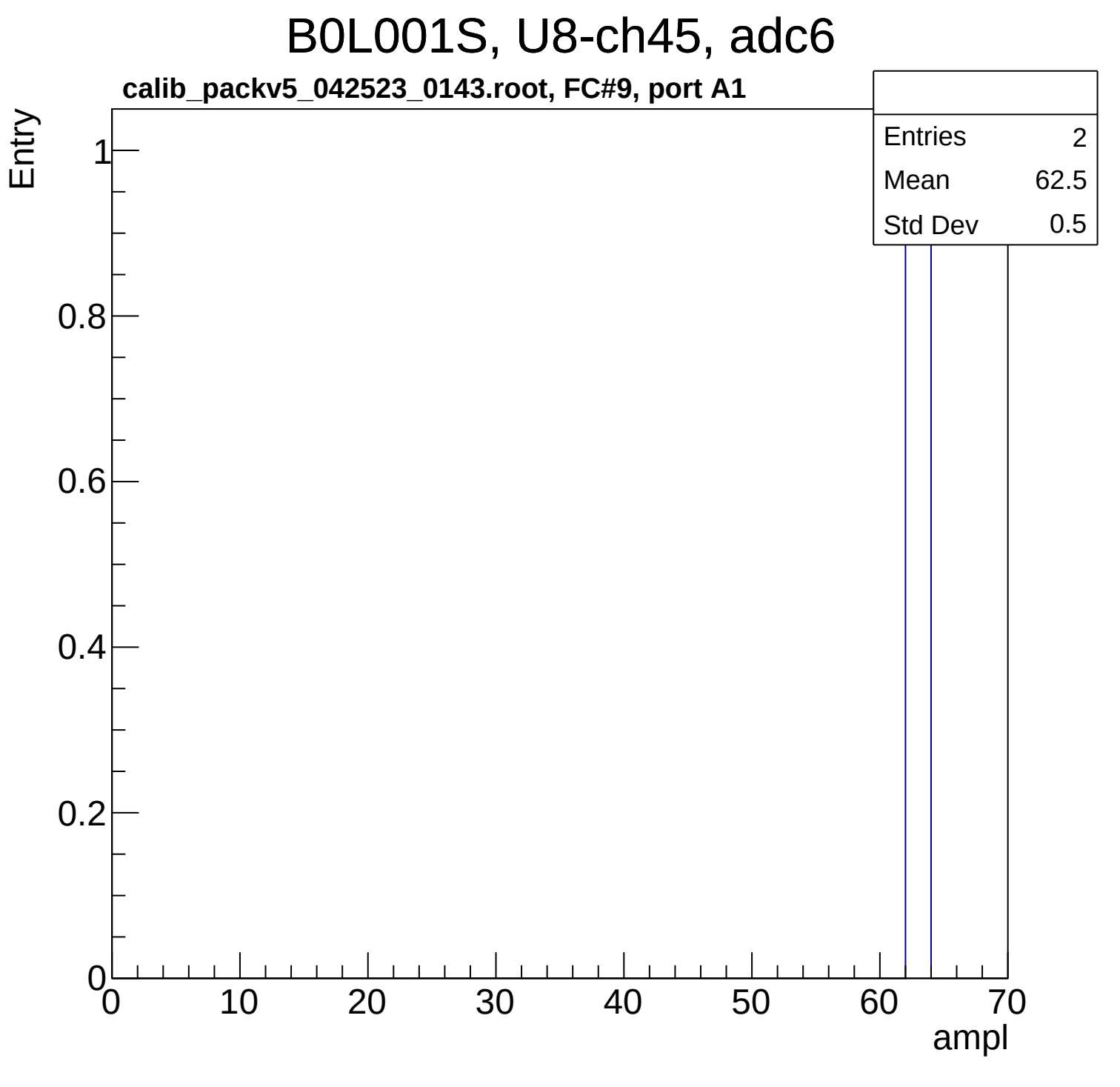
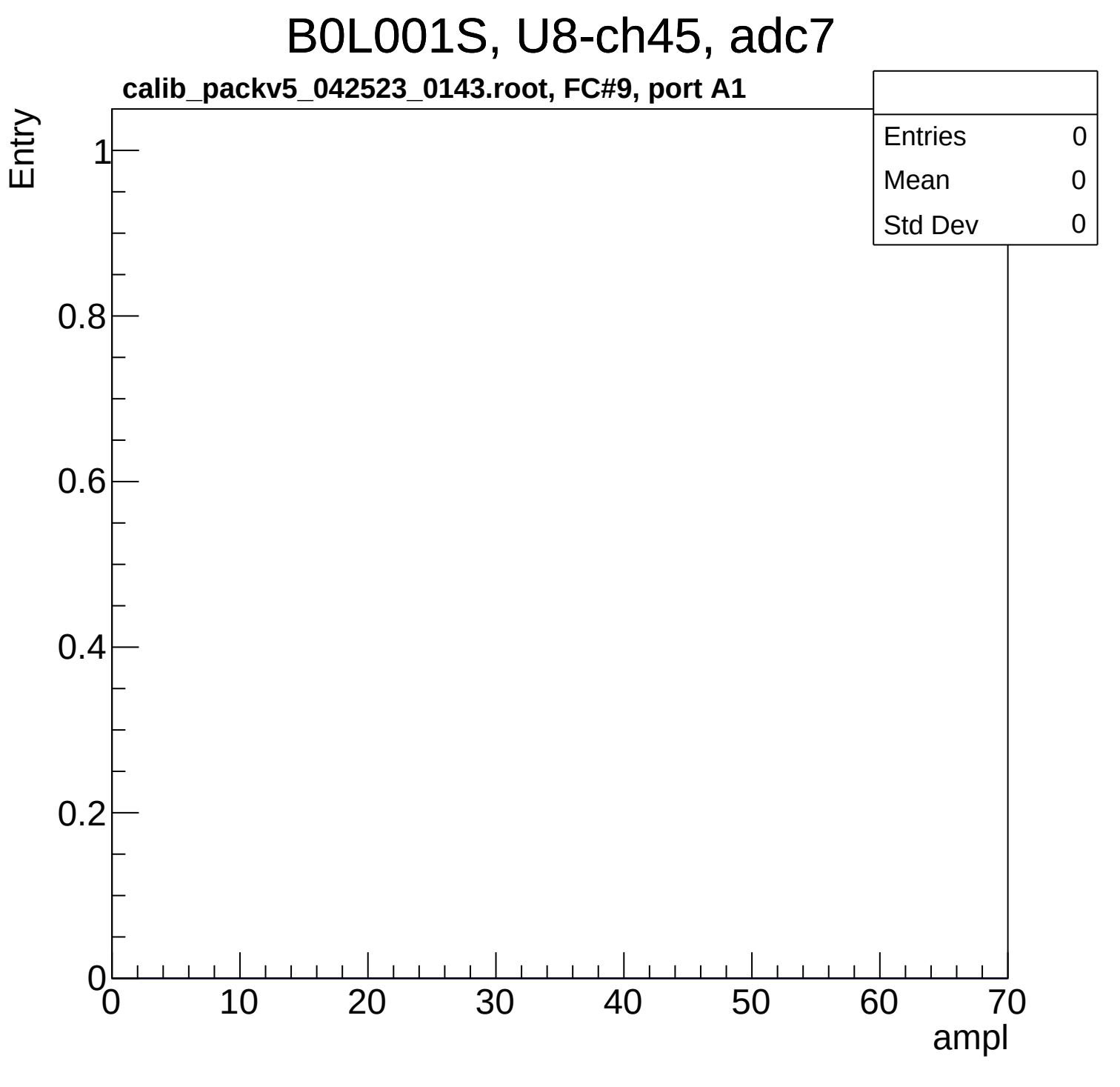
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.06
Std Dev 10.79
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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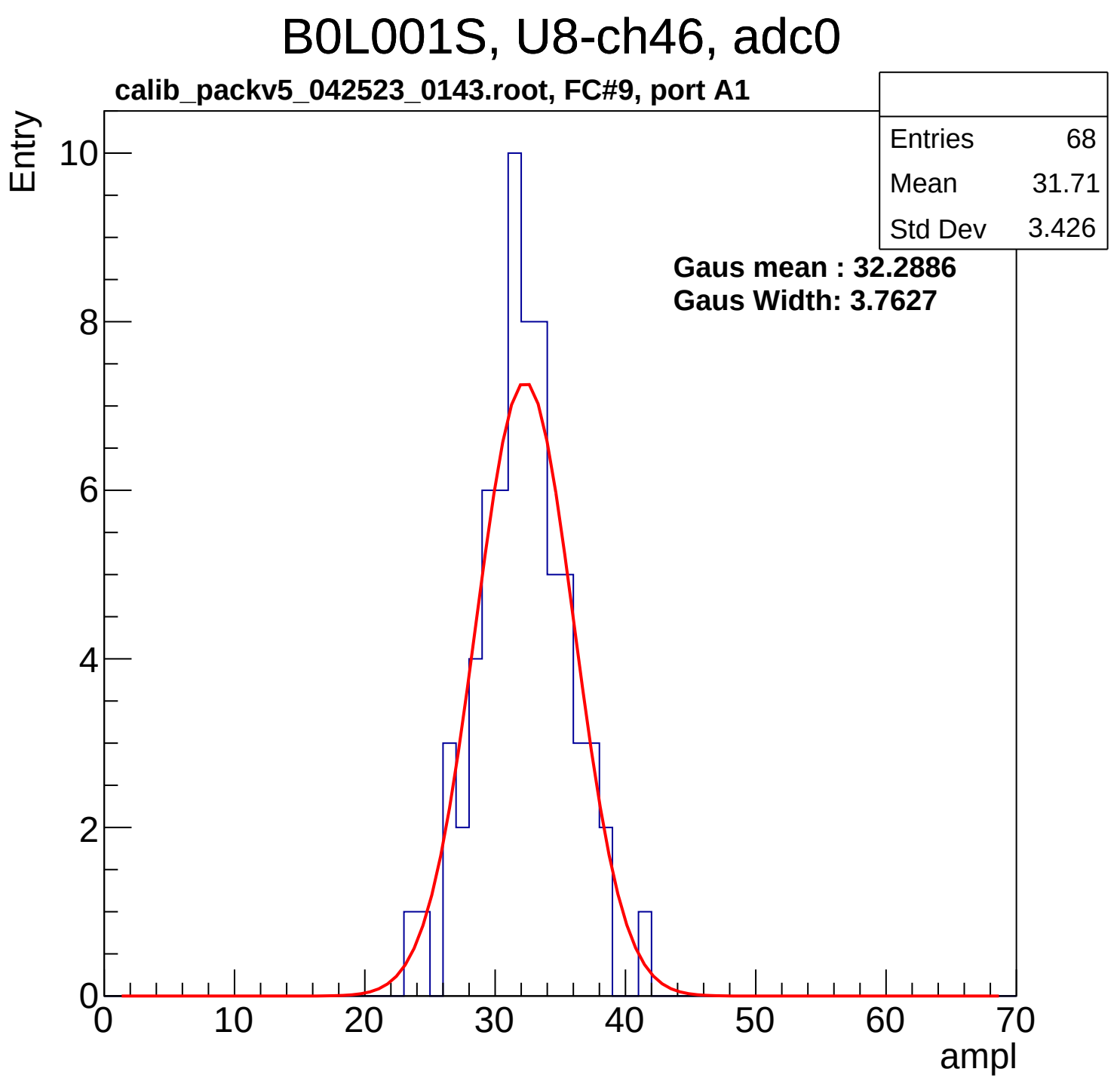
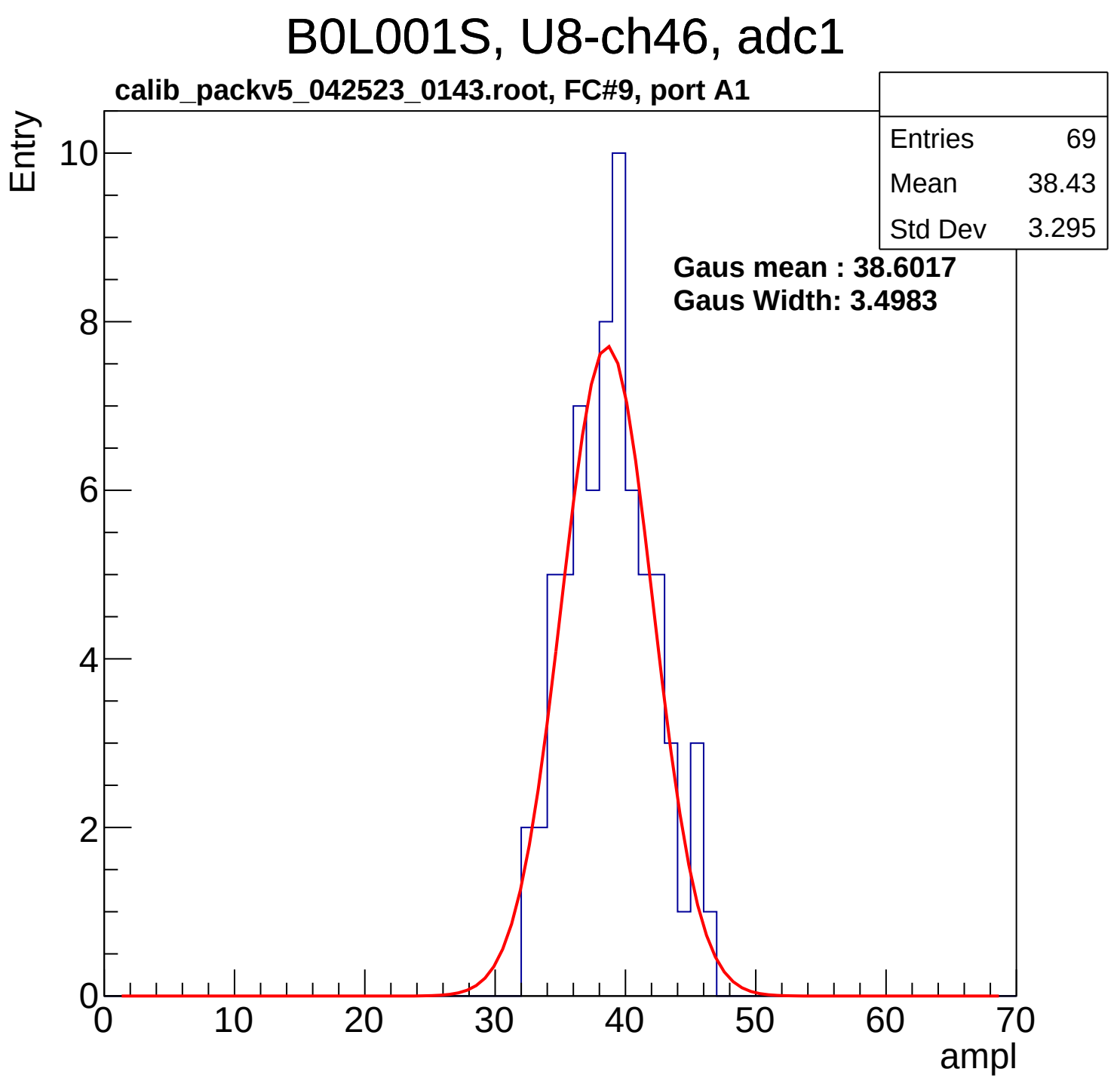
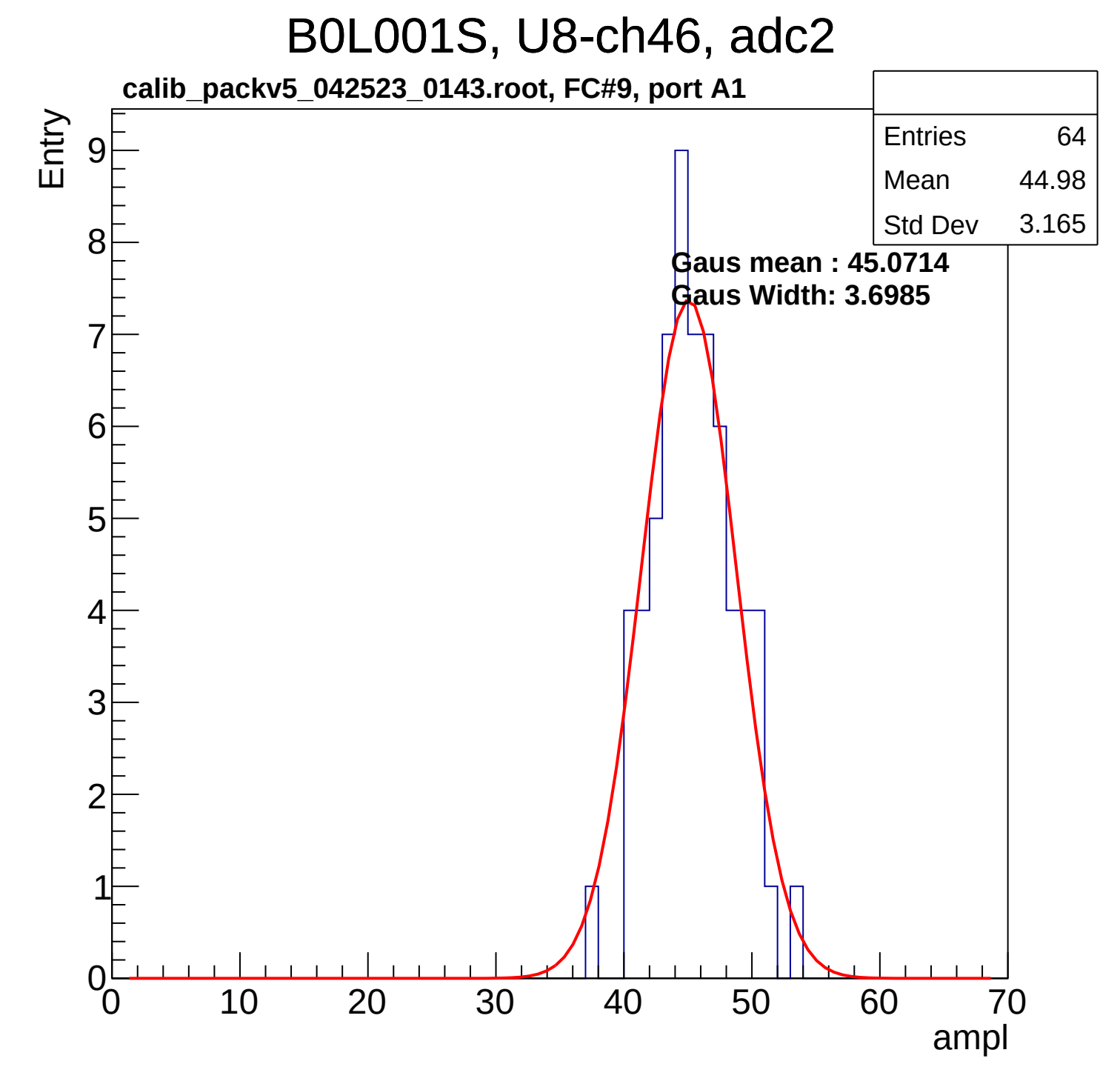
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.71
Std Dev 3.426
Gaus mean : 32.2886
Gaus Width: 3.7627
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.43
Std Dev 3.295
Gaus mean : 38.6017
Gaus Width: 3.4983
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 44.98
Std Dev 3.165
Gaus mean : 45.0714
Gaus Width: 3.6985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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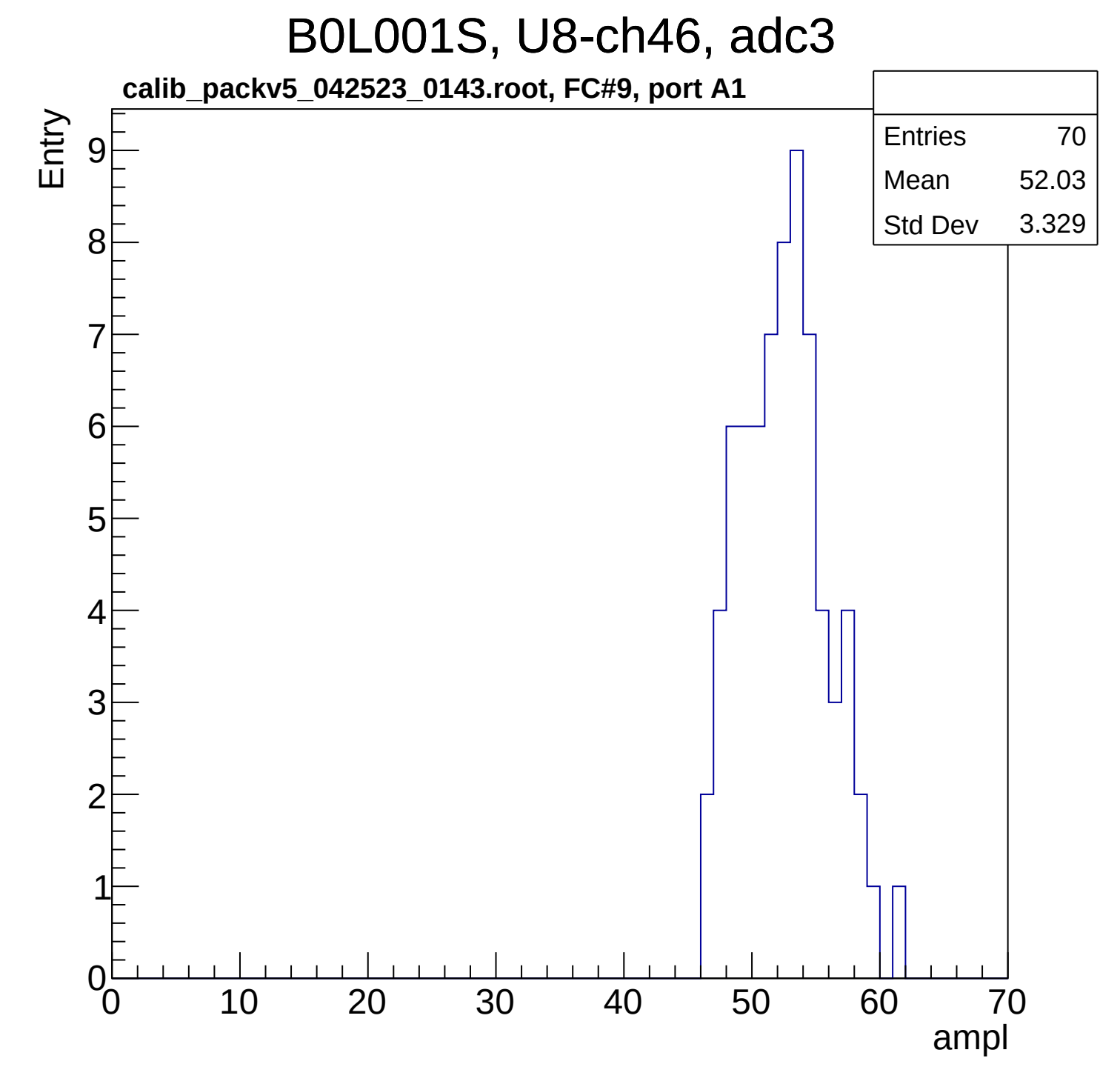
B0L001S, U8-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 52.03
Std Dev 3.329
Entry
ampl
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1
2
3
4
5
6
7
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0
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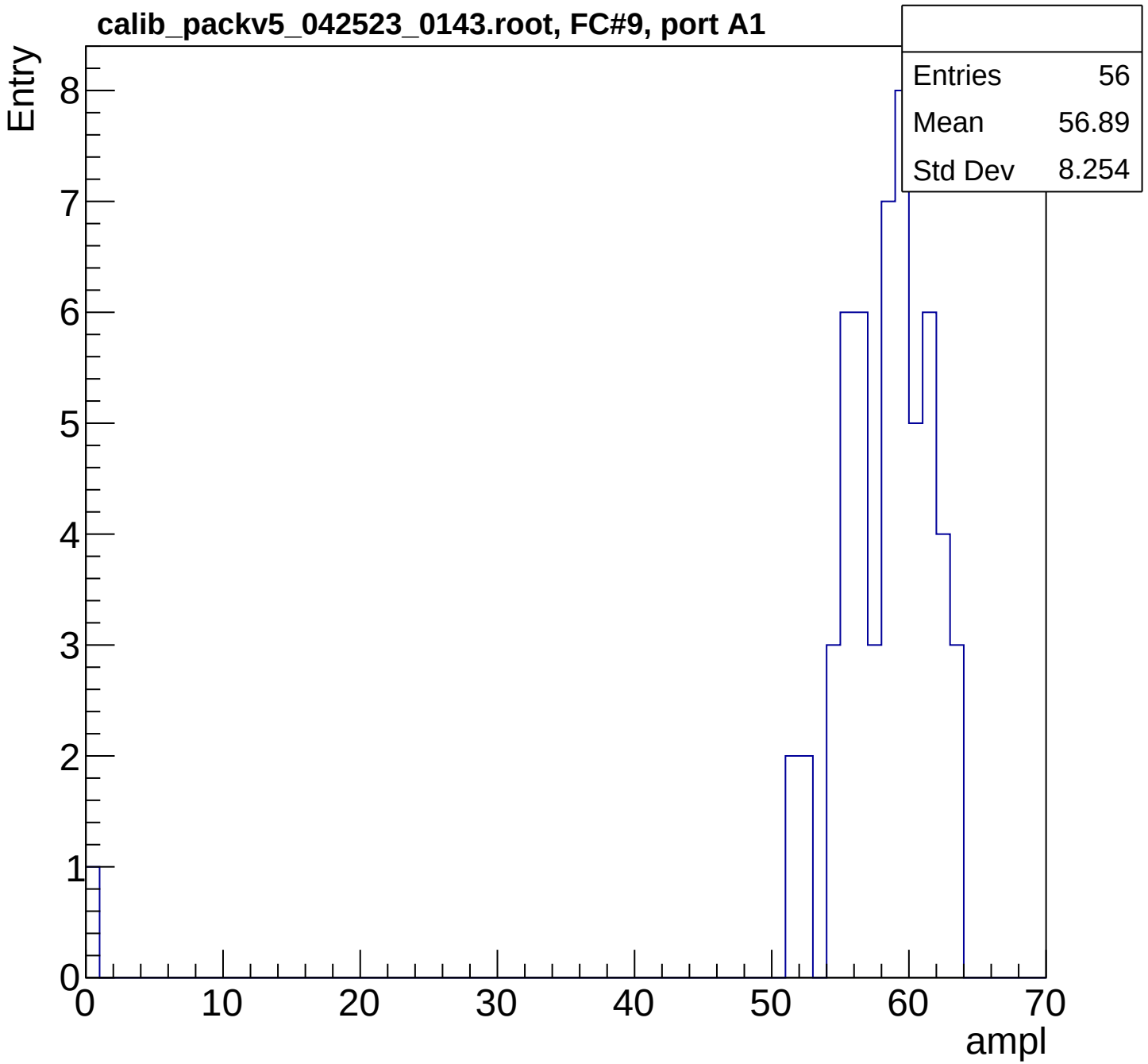
B0L001S, U8-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.89
Std Dev 8.254
Entry
0
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2
3
4
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6
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8
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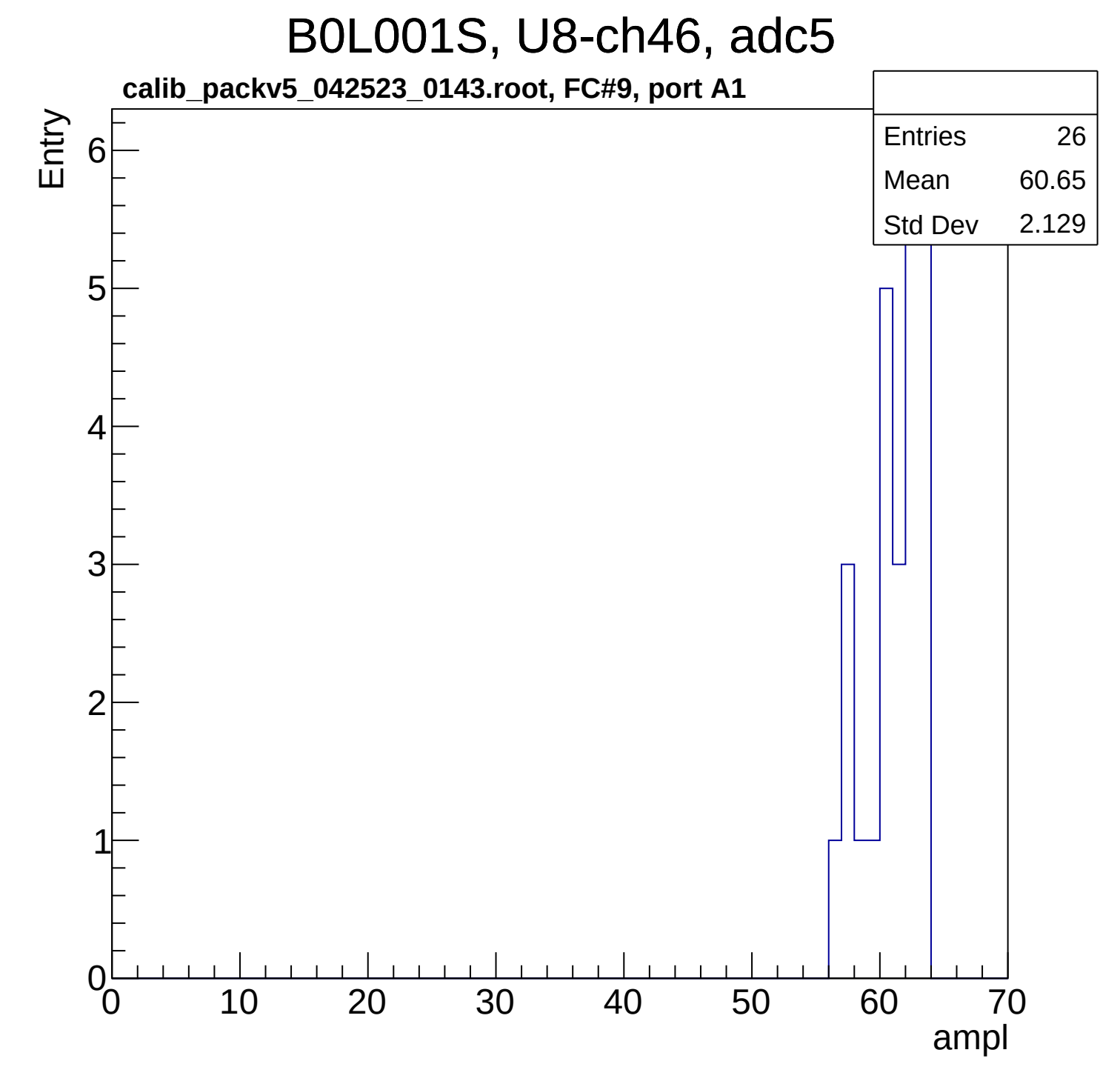
50
60
70
ampl


B0L001S, U8-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.65
Std Dev 2.129
Entry
0
1
2
3
4
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10
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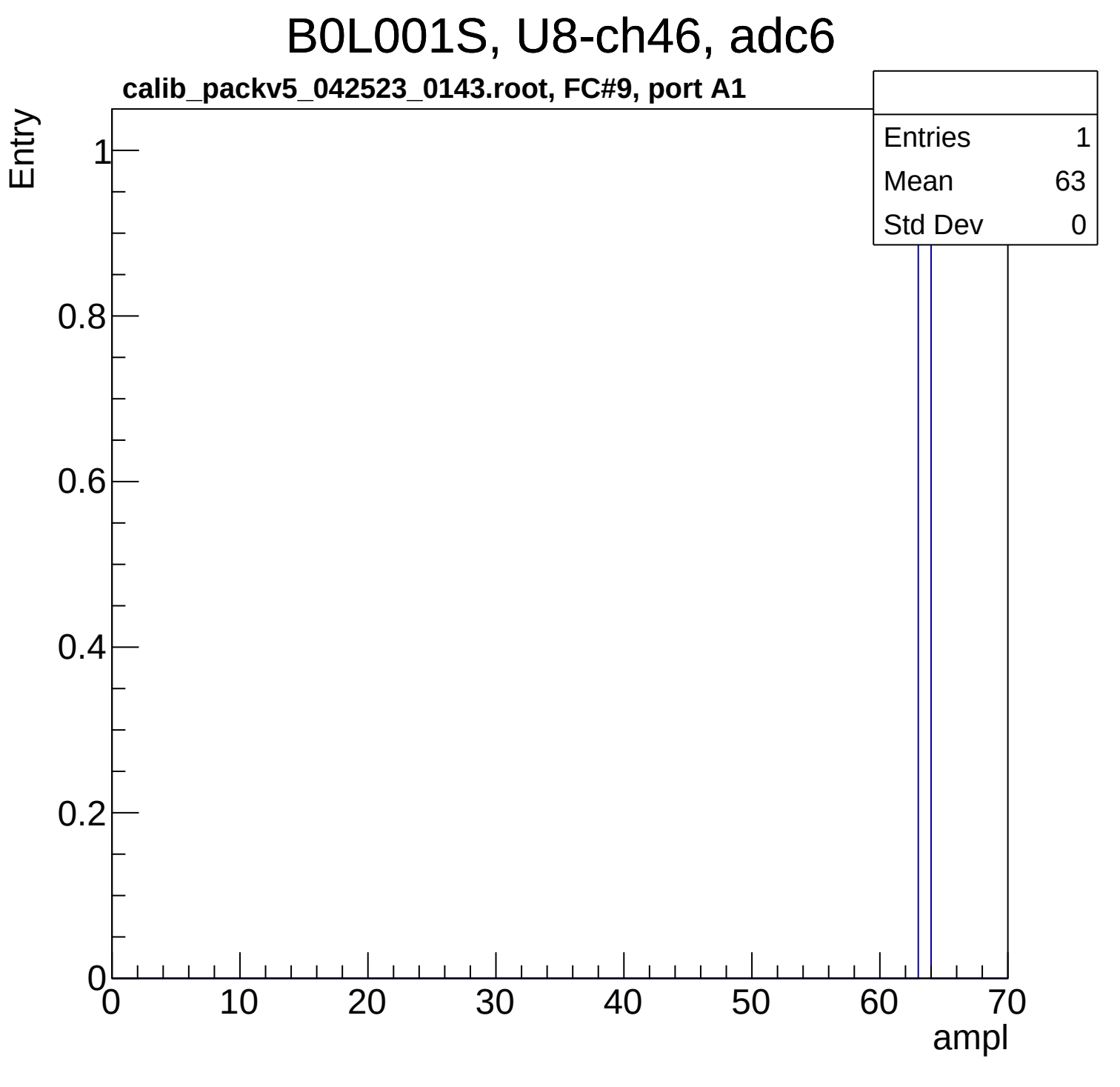
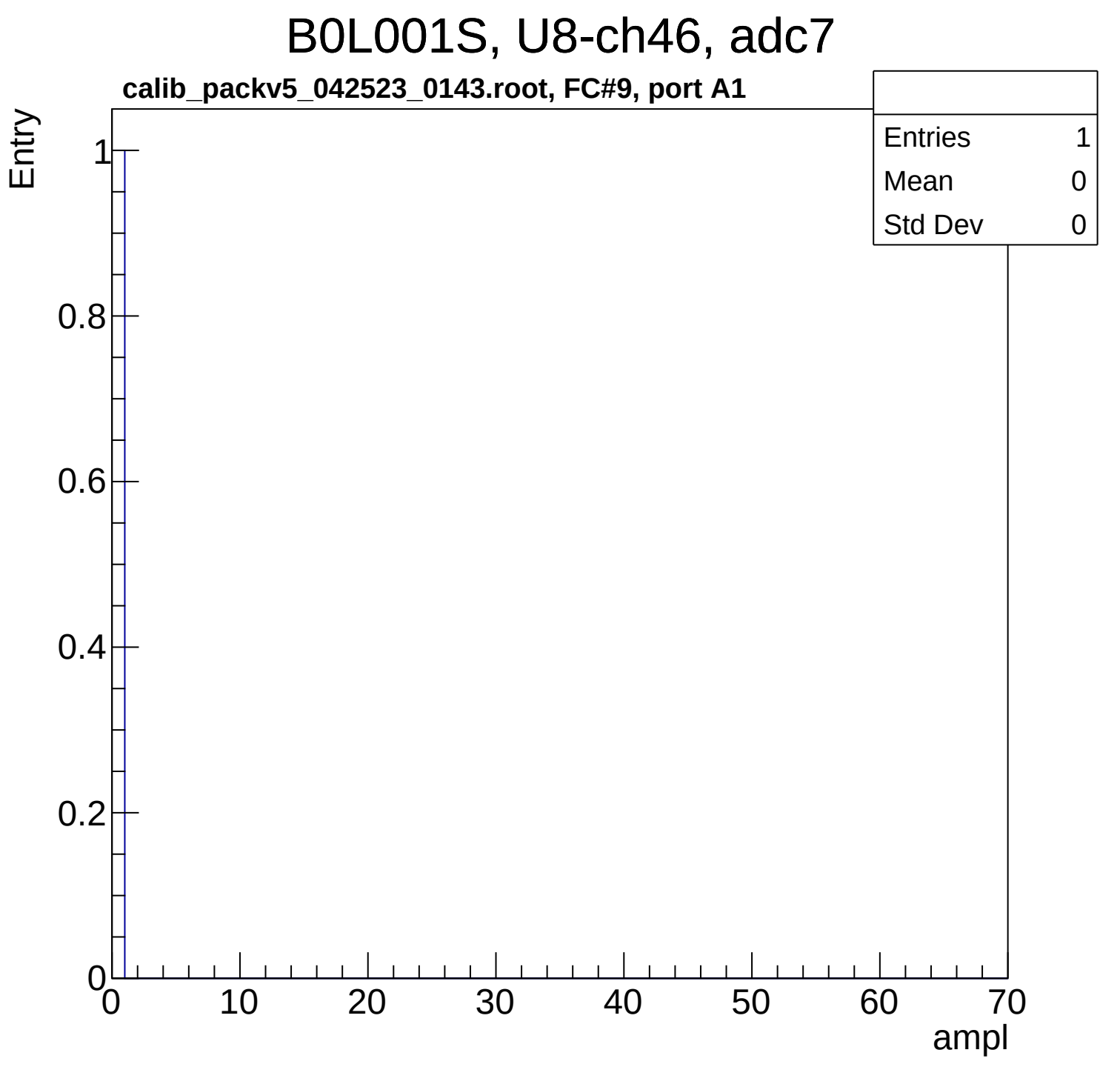
50
60
70
ampl


B0L001S, U8-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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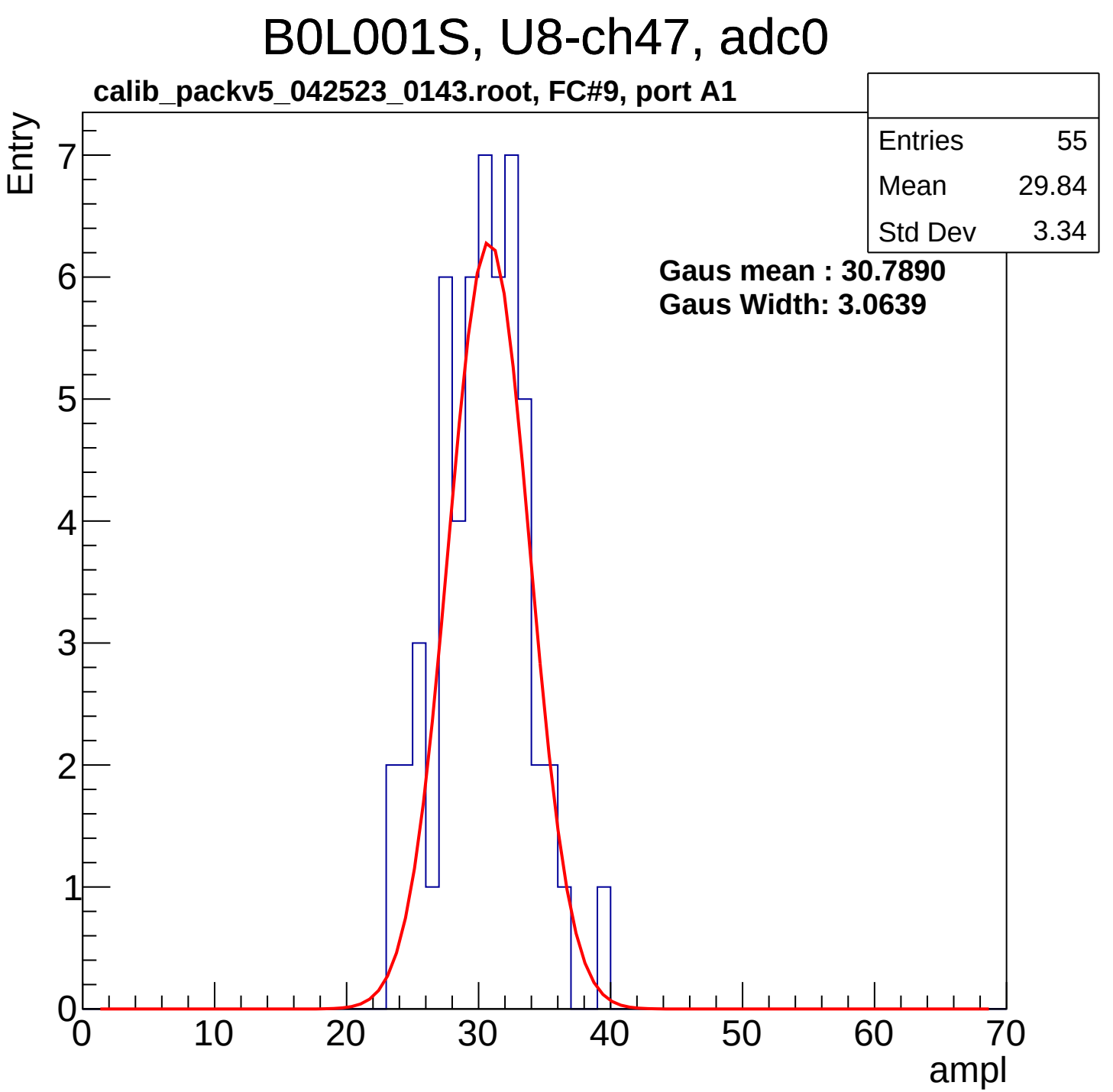
40
50
60
70
ampl


B0L001S, U8-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 29.84
Std Dev 3.34
Gaus mean : 30.7890
Gaus Width: 3.0639
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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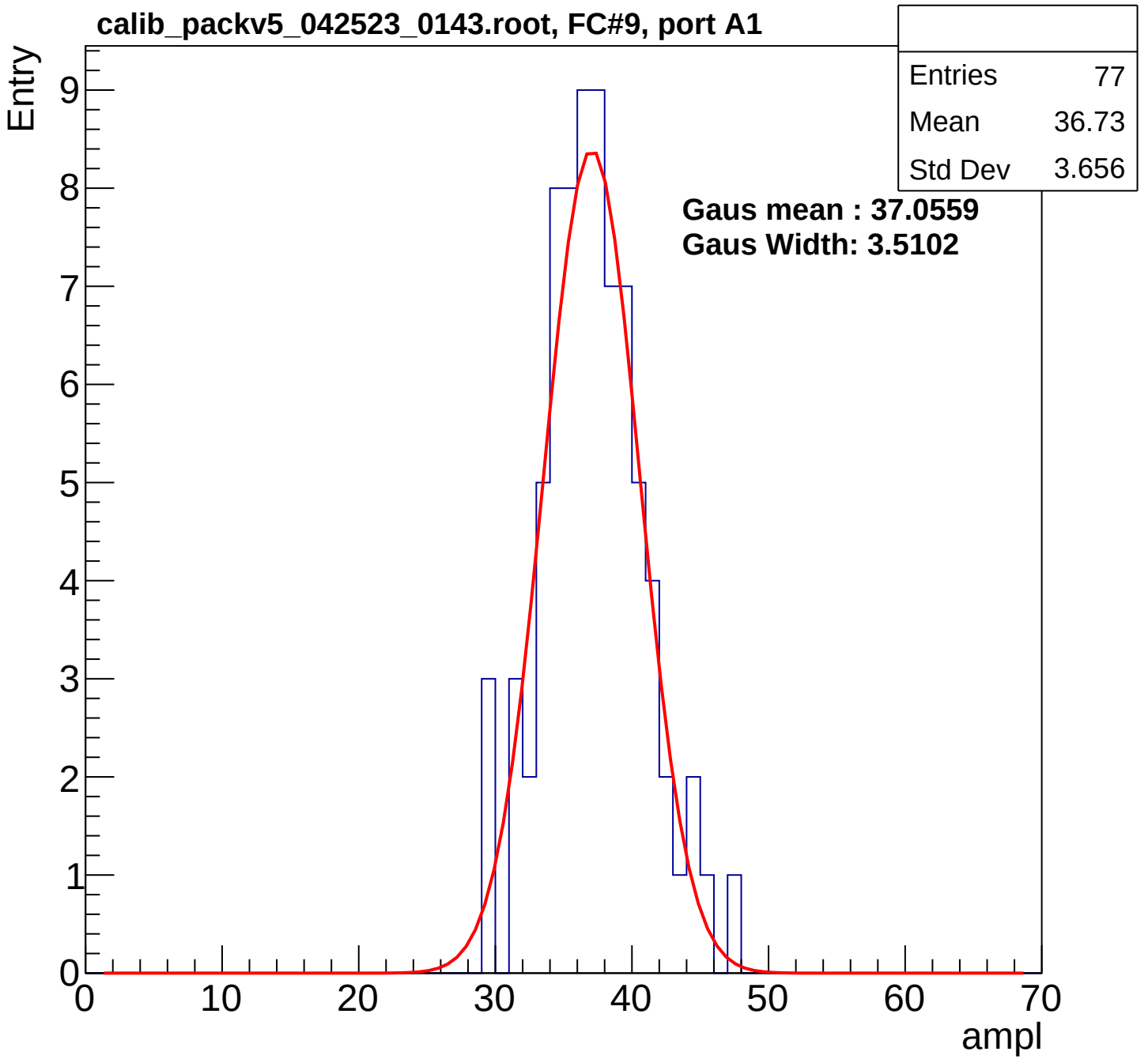
B0L001S, U8-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.73
Std Dev 3.656
Gaus mean : 37.0559
Gaus Width: 3.5102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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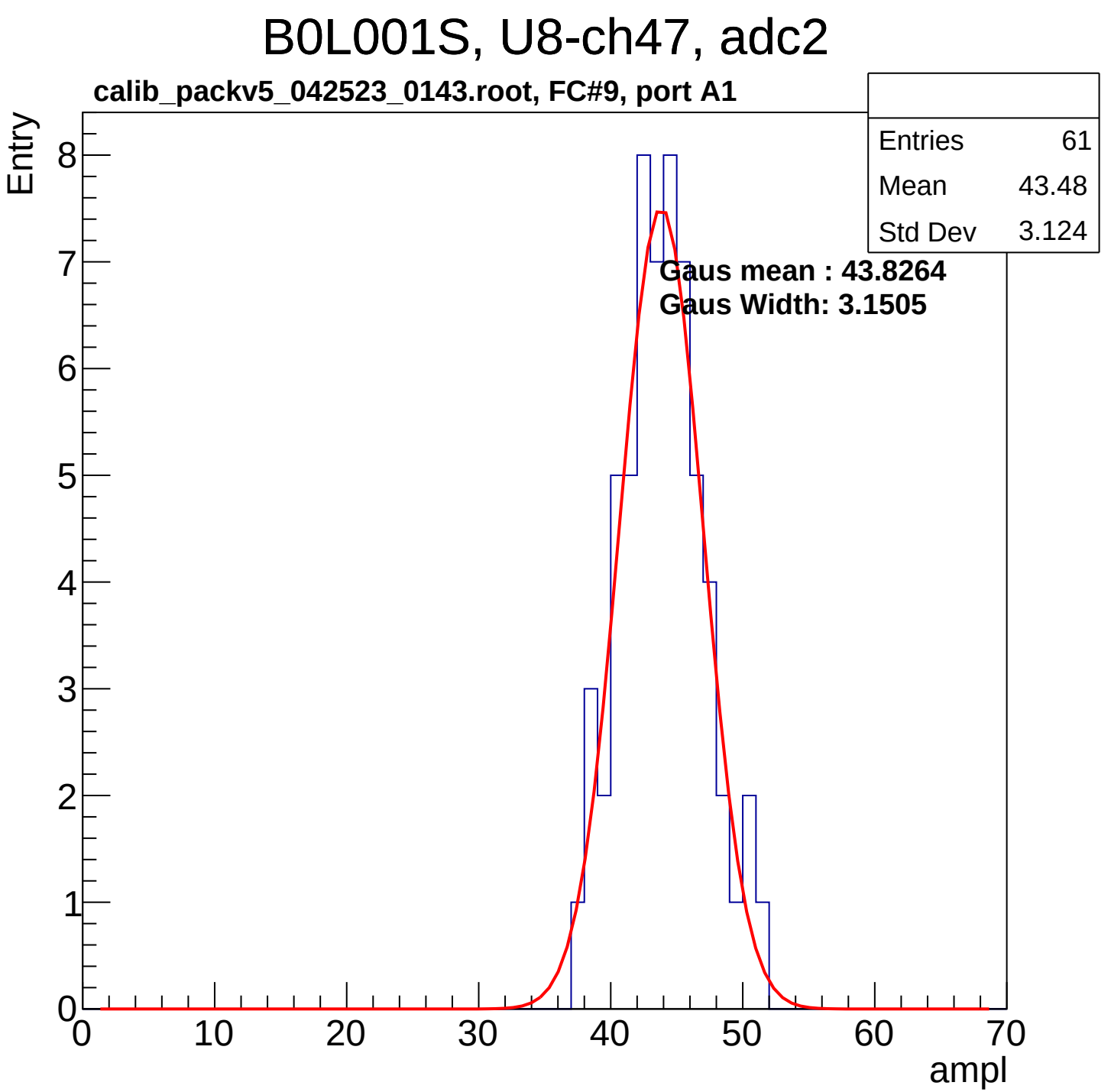
B0L001S, U8-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.48
Std Dev 3.124
Gaus mean : 43.8264
Gaus Width: 3.1505
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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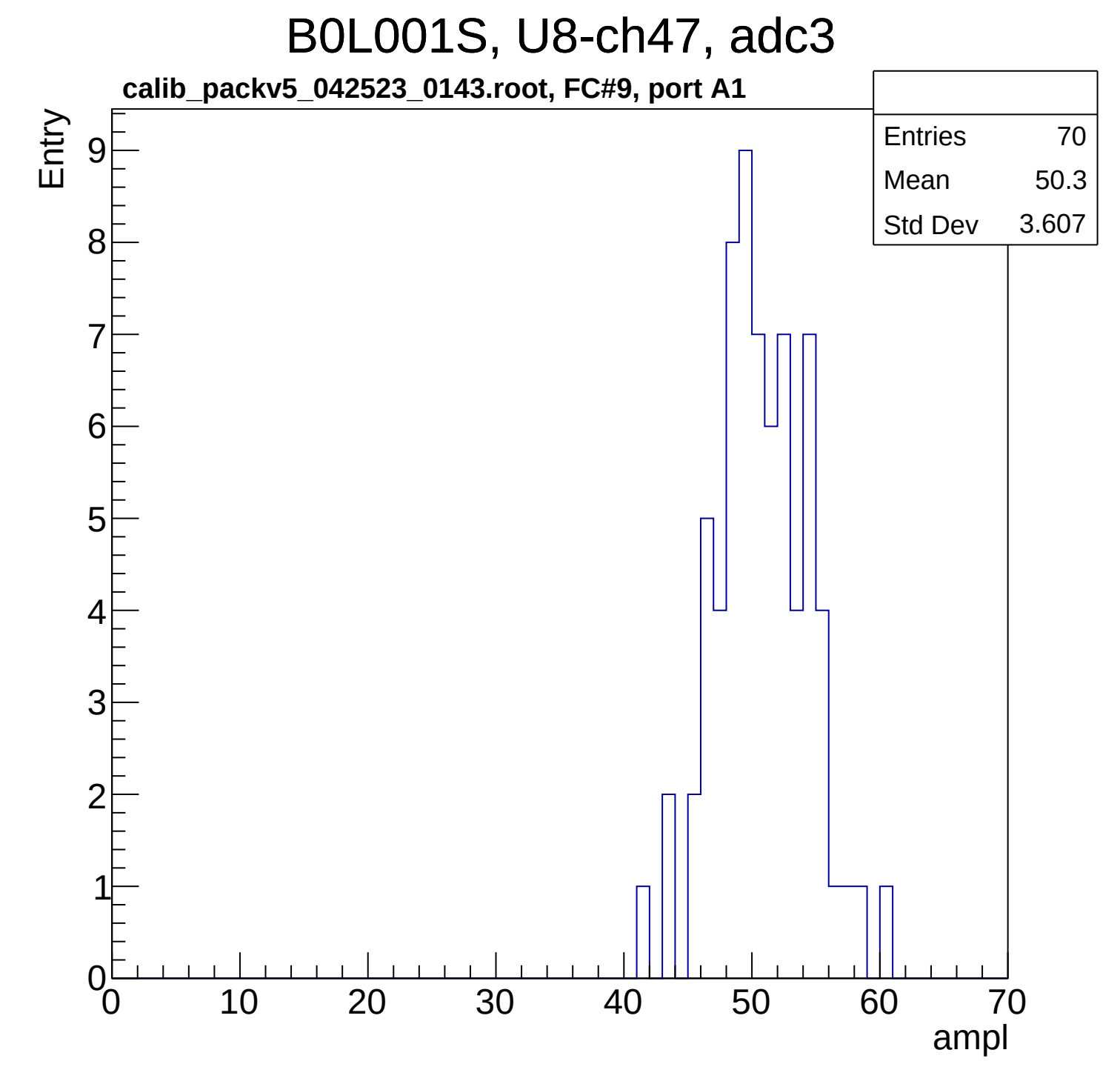
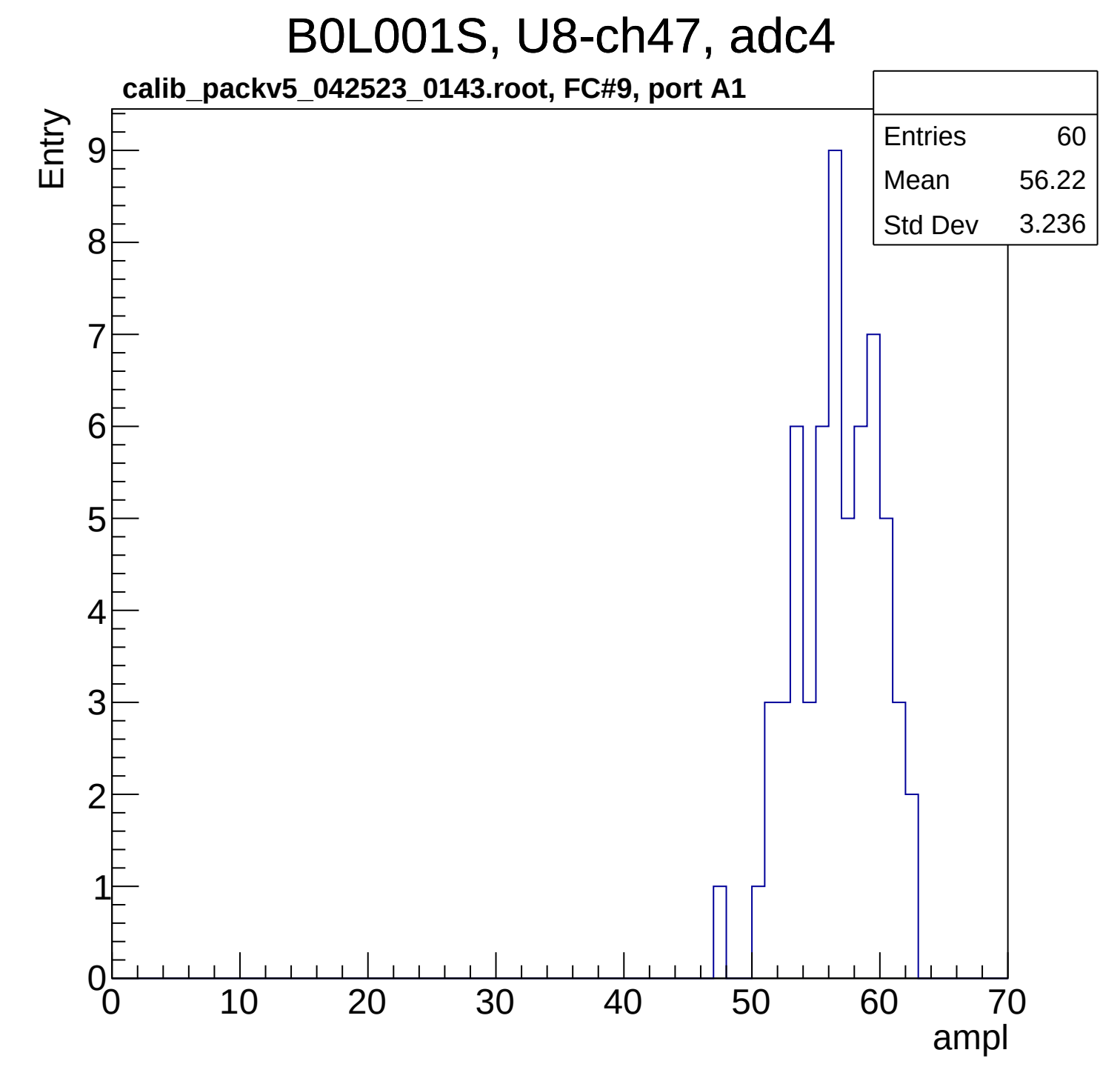
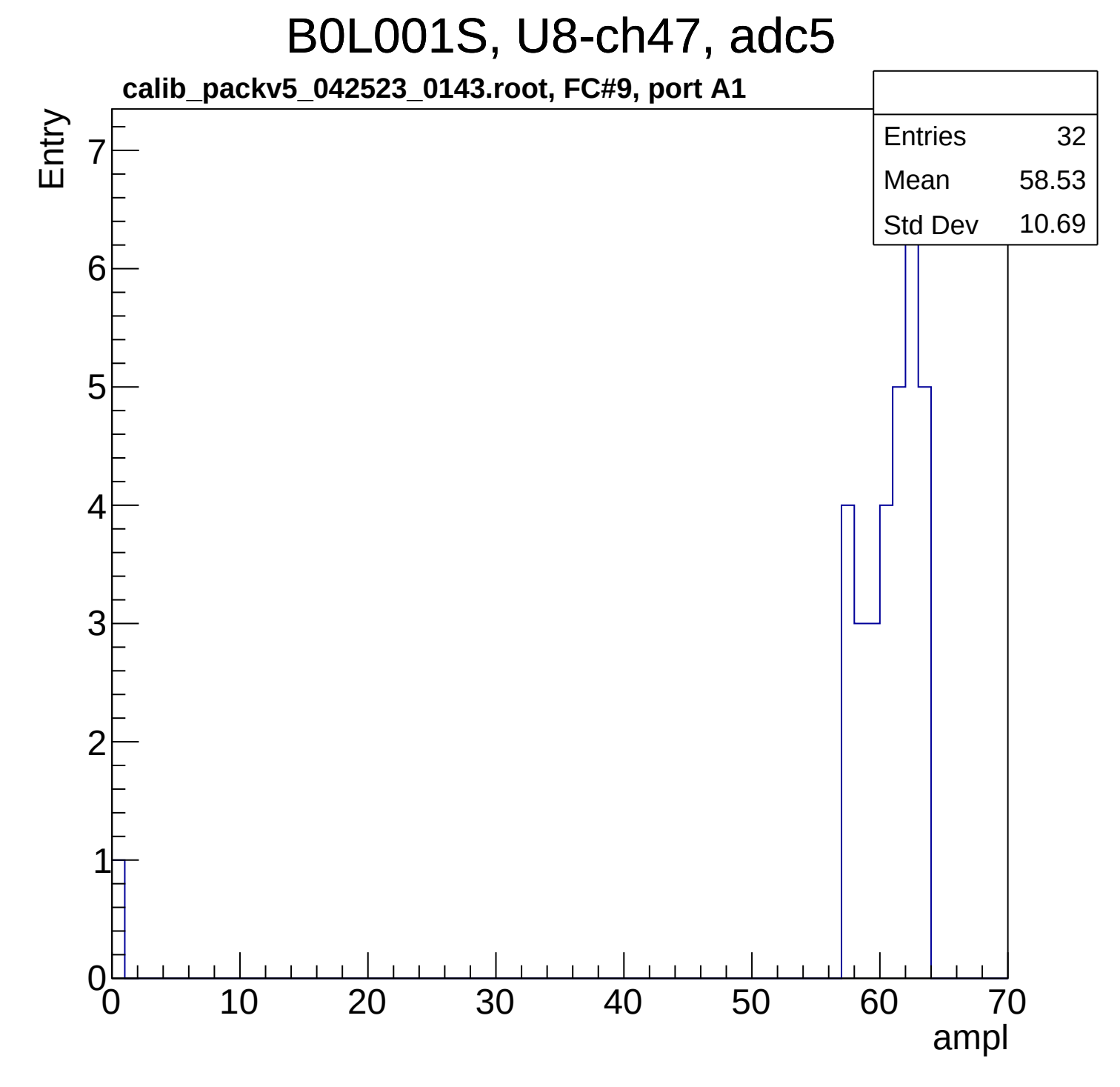

B0L001S, U8-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.3
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.22
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.53
Std Dev 10.69
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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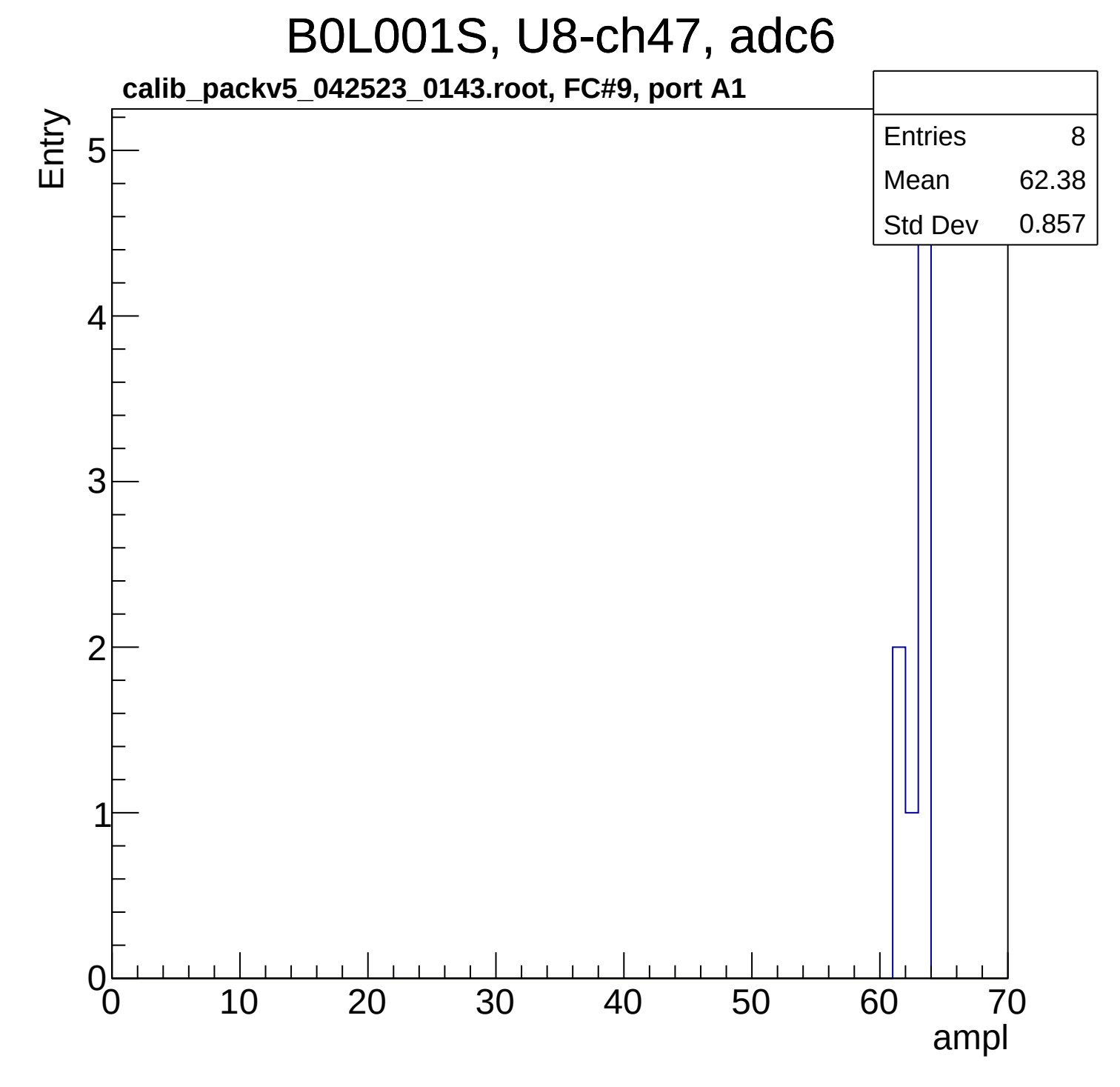
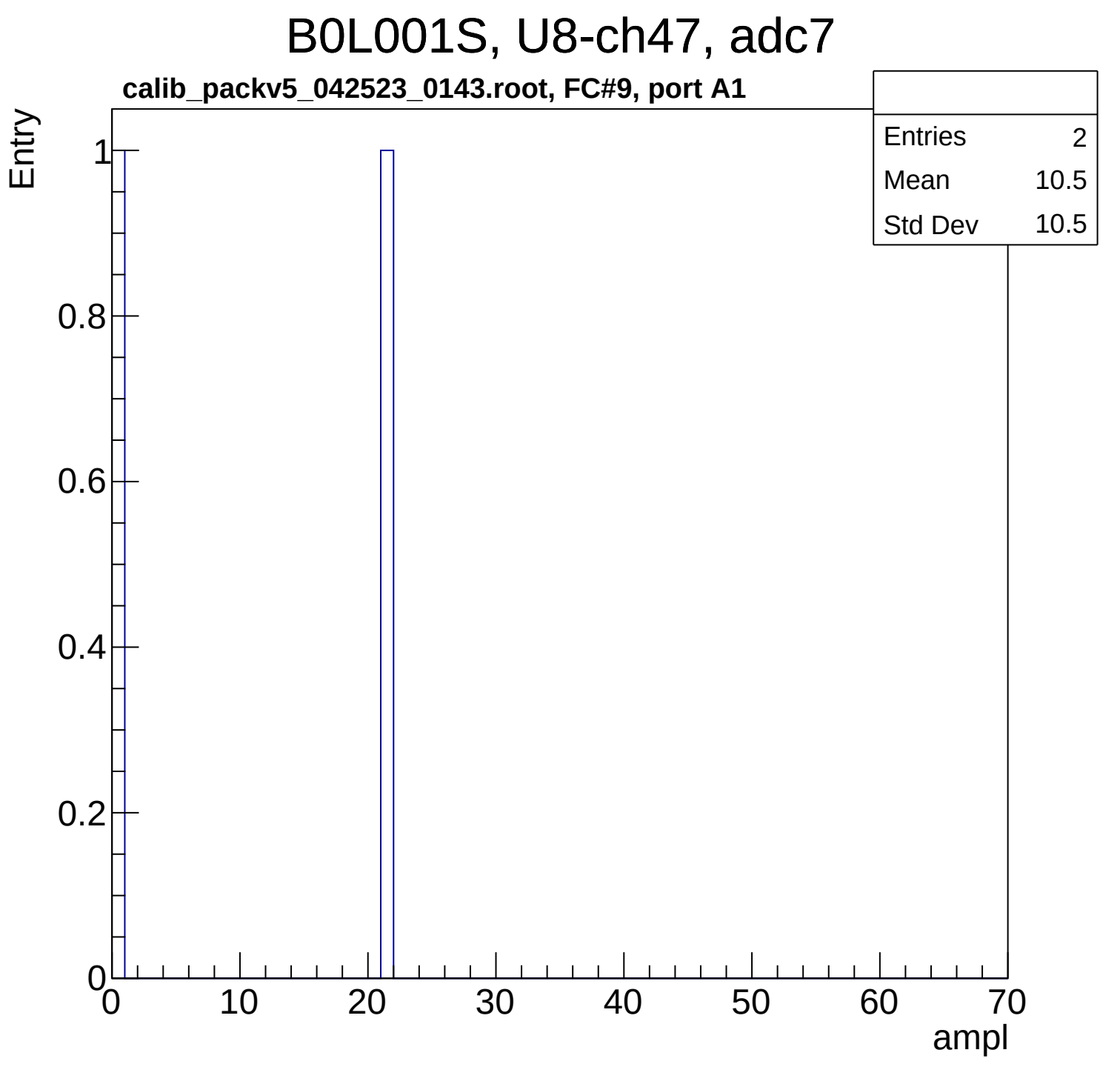
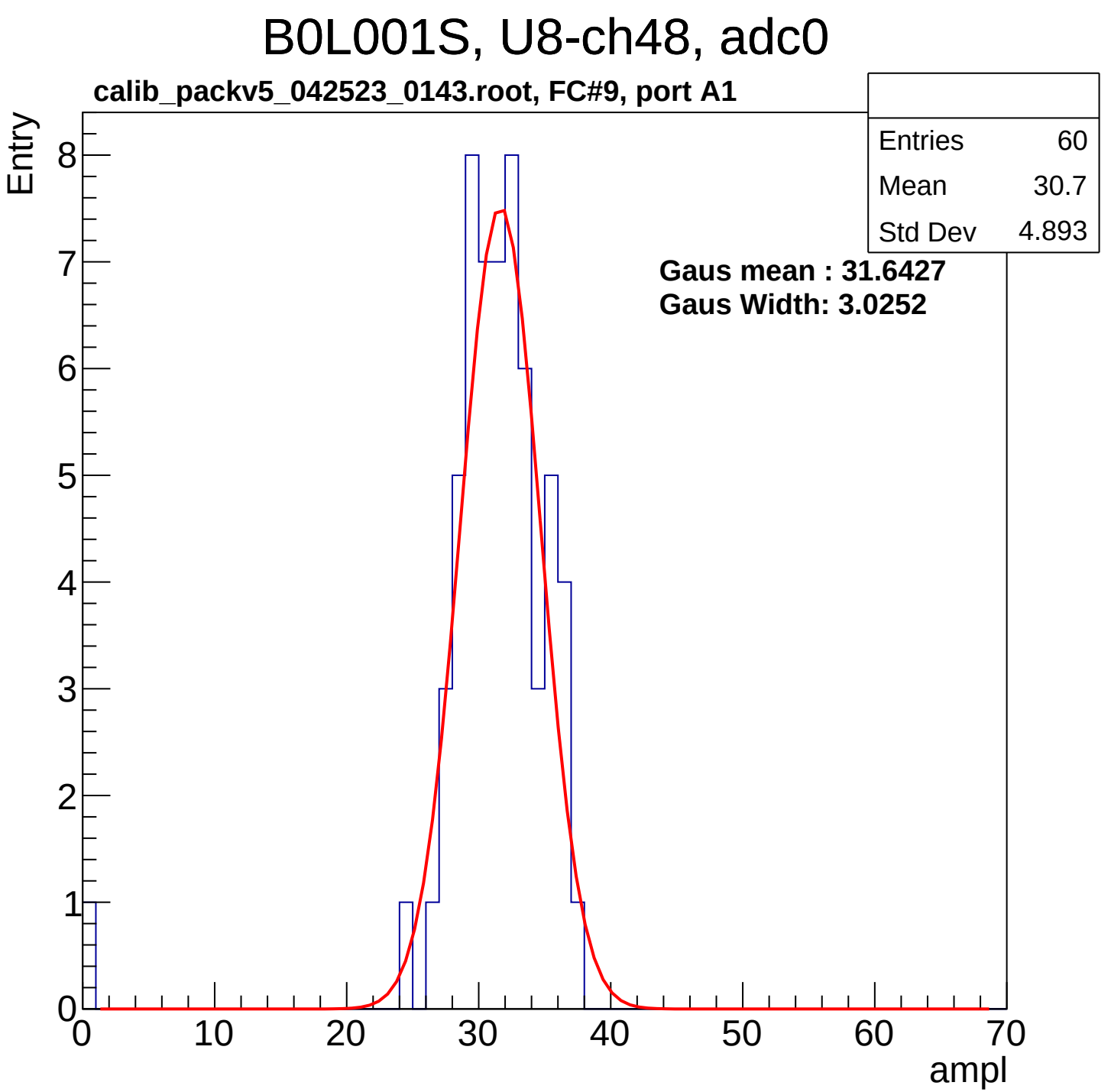
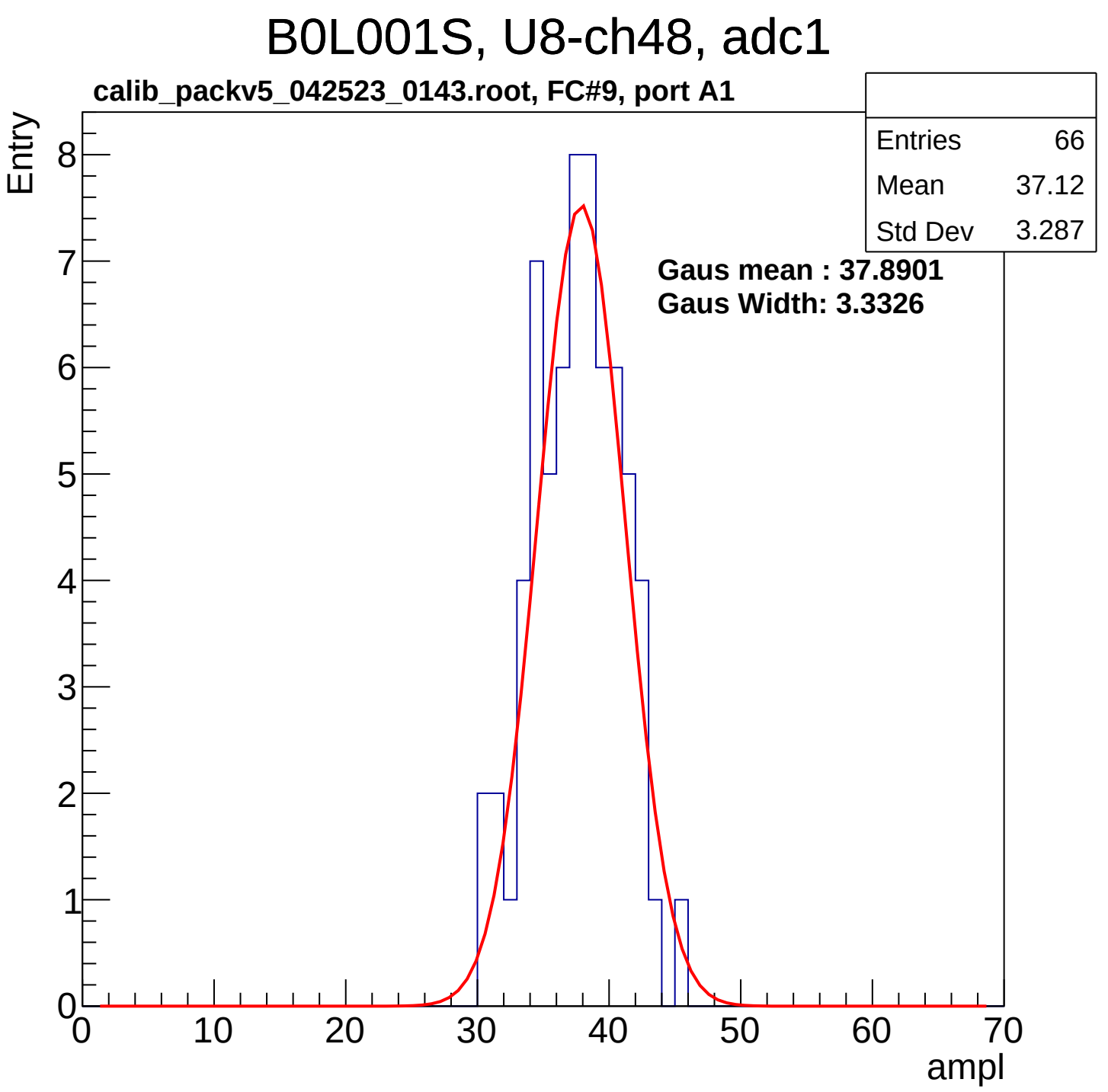

B0L001S, U8-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.38
Std Dev 0.857
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.7
Std Dev 4.893
Gaus mean : 31.6427
Gaus Width: 3.0252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.12
Std Dev 3.287
Gaus mean : 37.8901
Gaus Width: 3.3326
Entry
ampl
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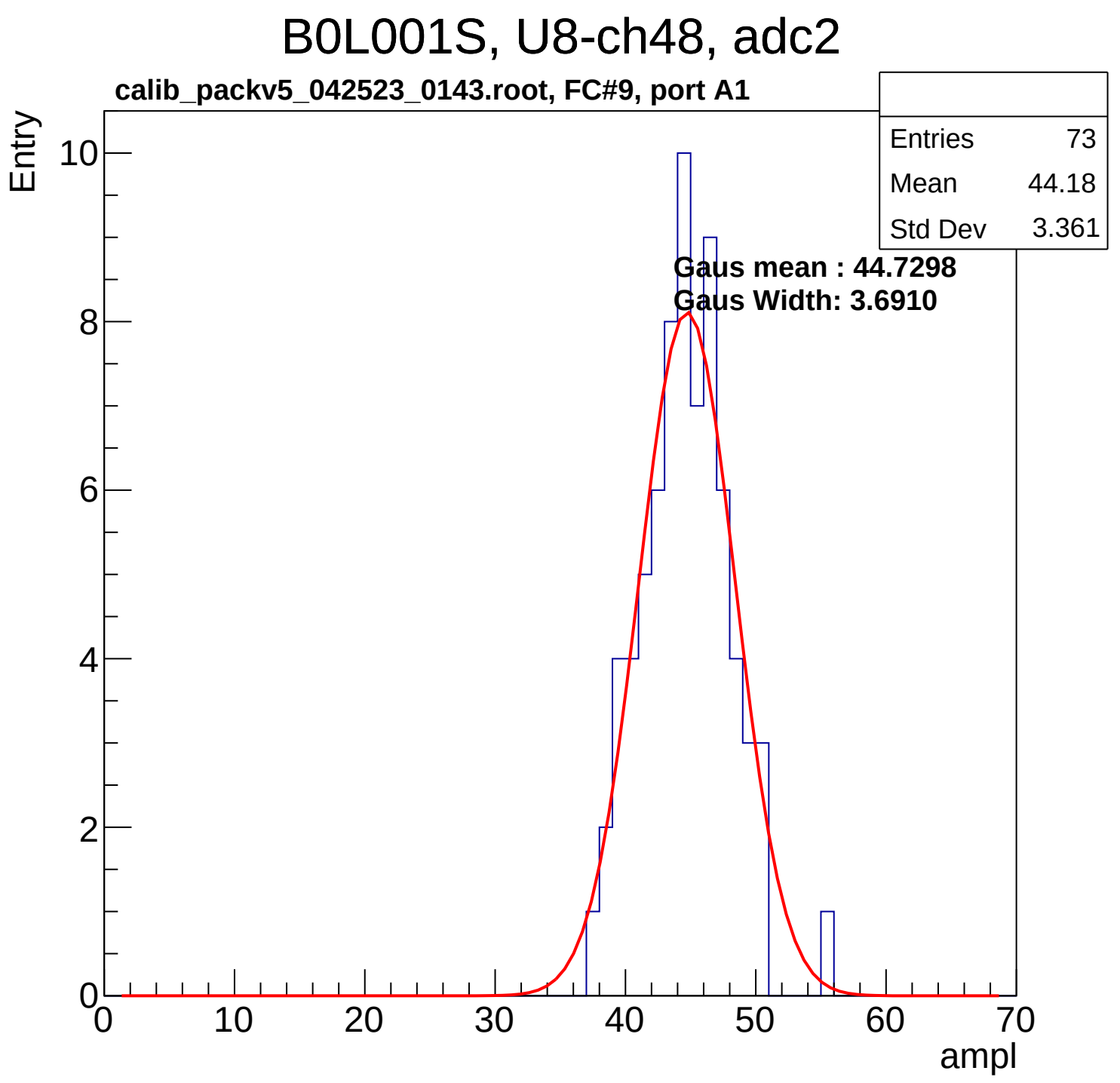
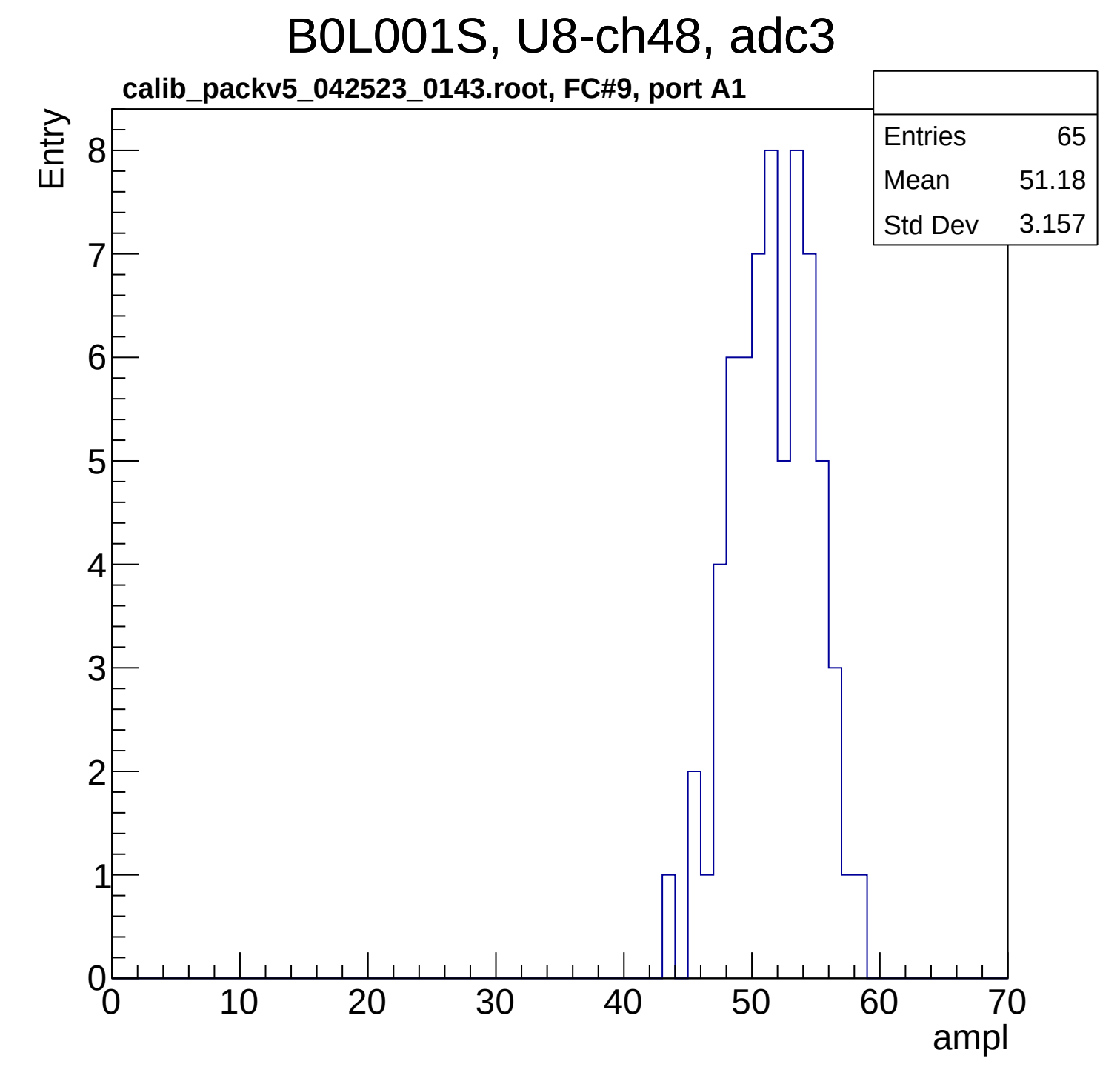
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.18
Std Dev 3.361
Gaus mean : 44.7298
Gaus Width: 3.6910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.18
Std Dev 3.157
Entry
ampl
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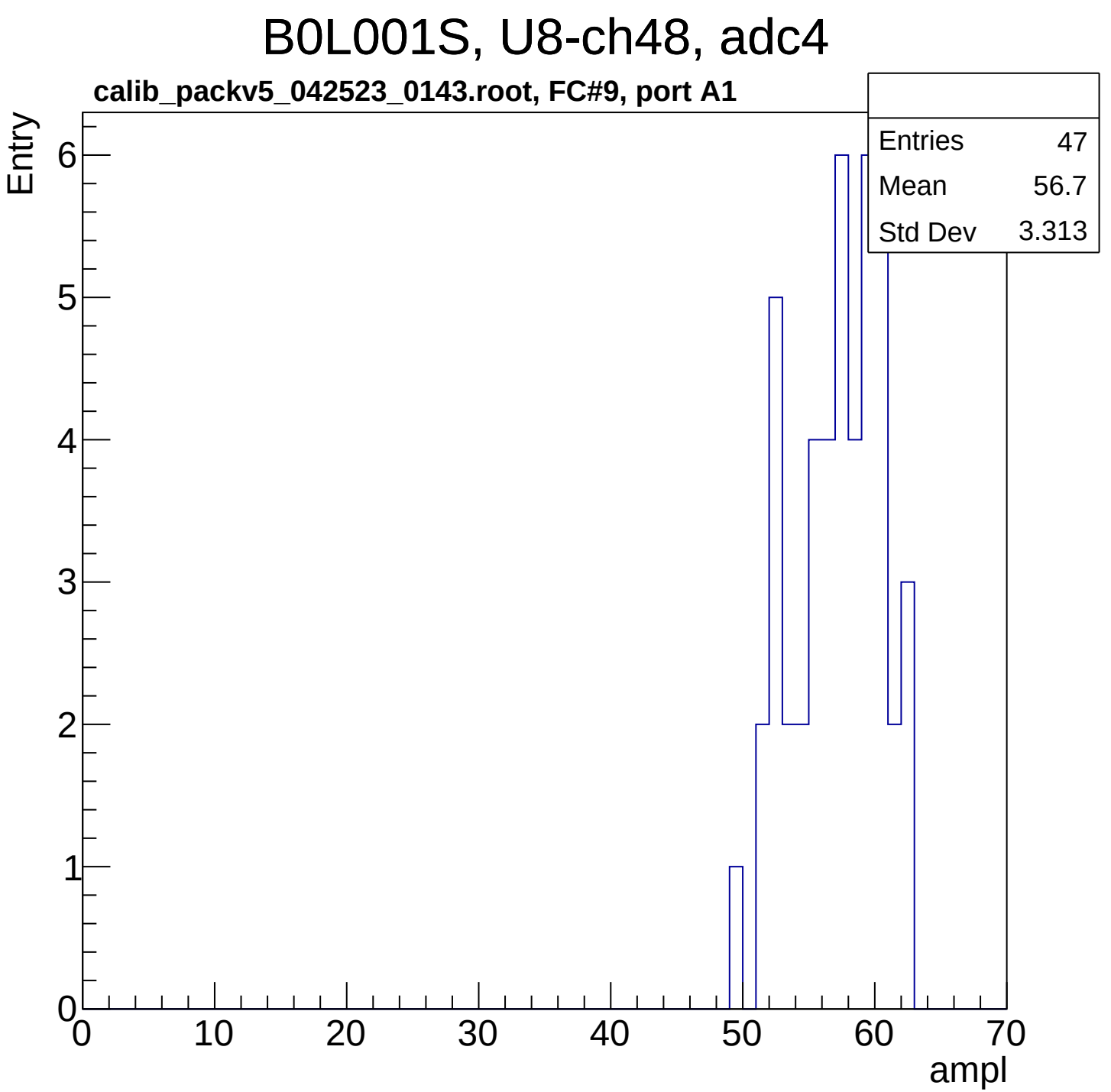
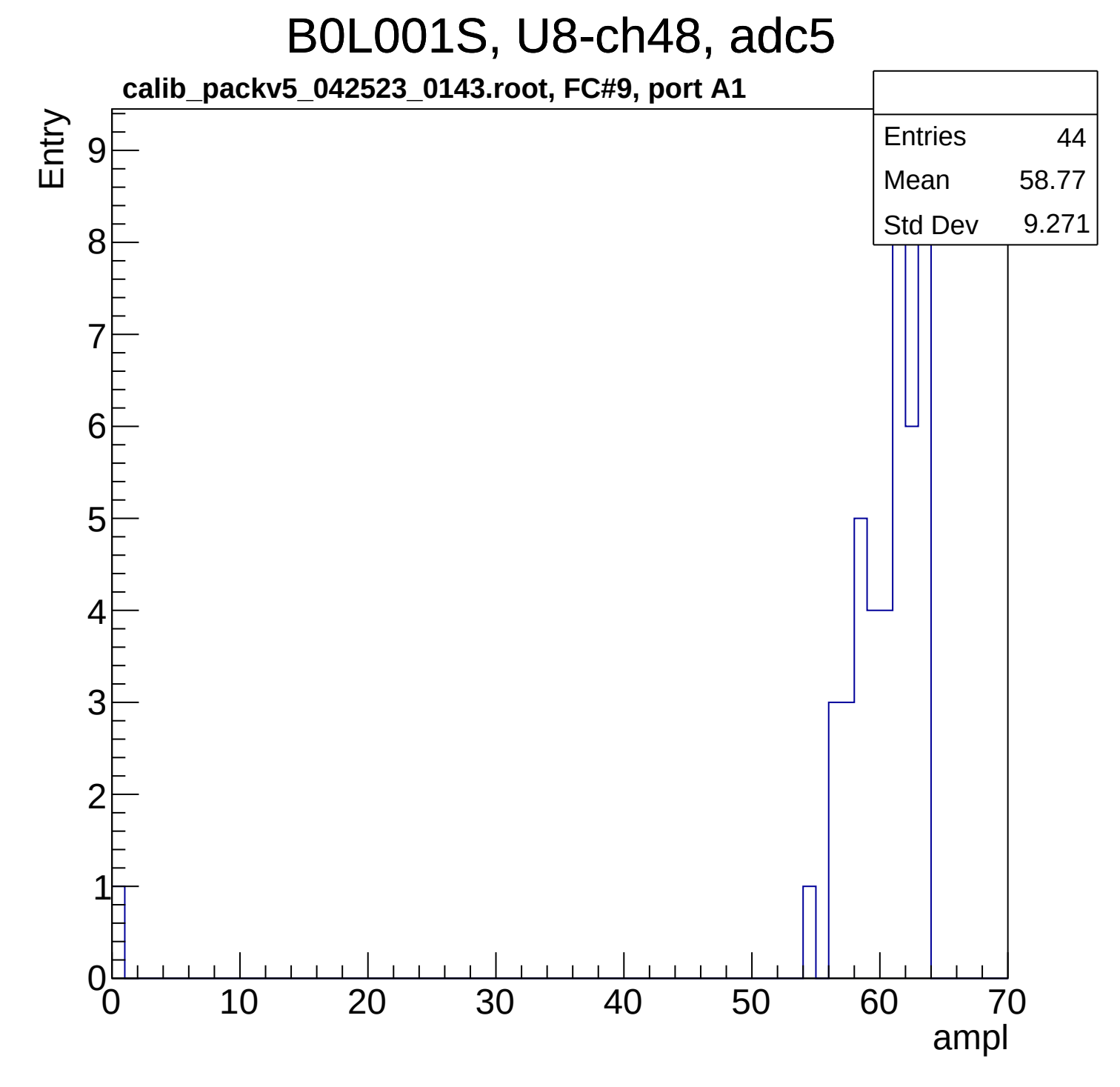
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.7
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U8-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.77
Std Dev 9.271
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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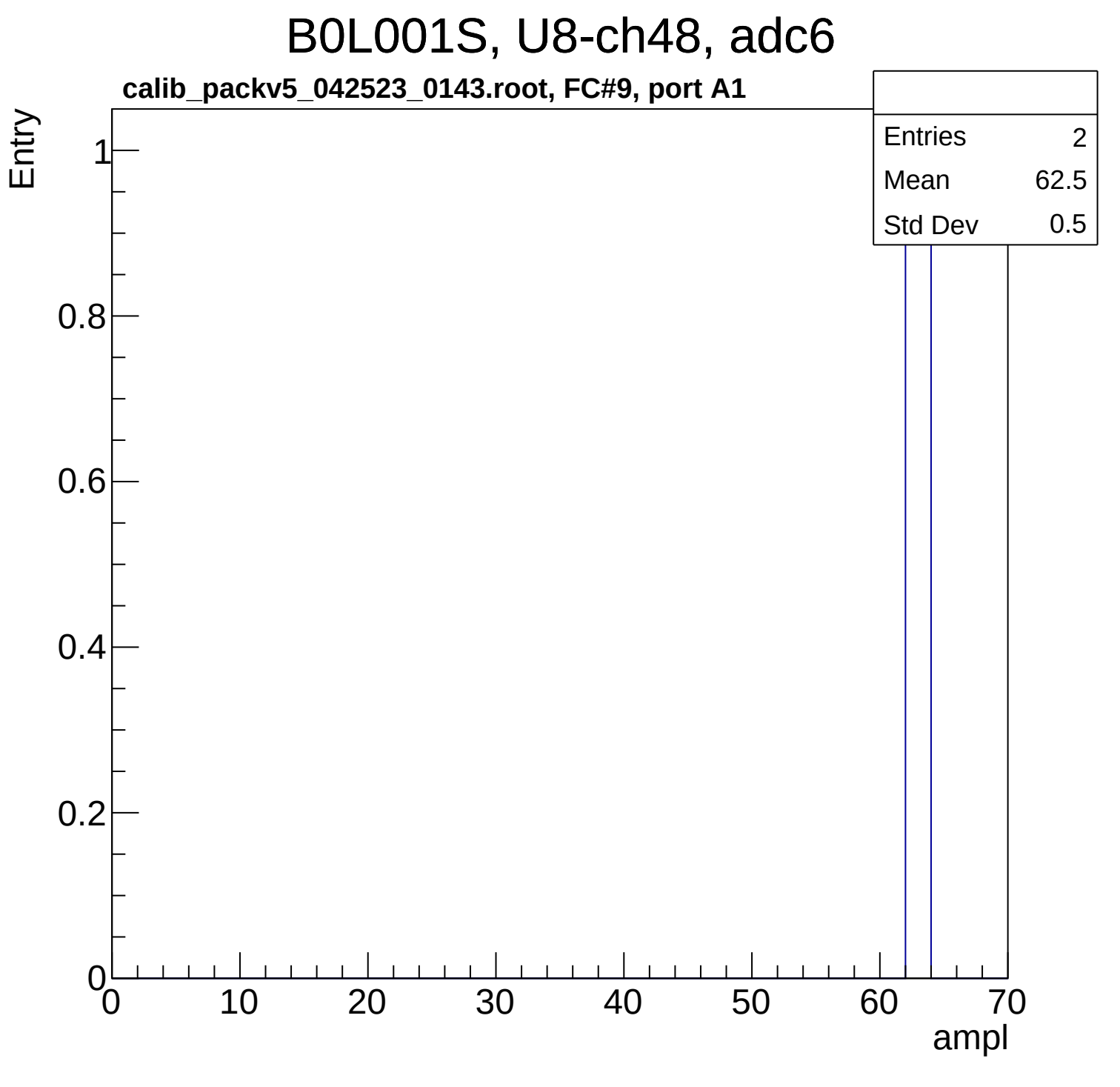
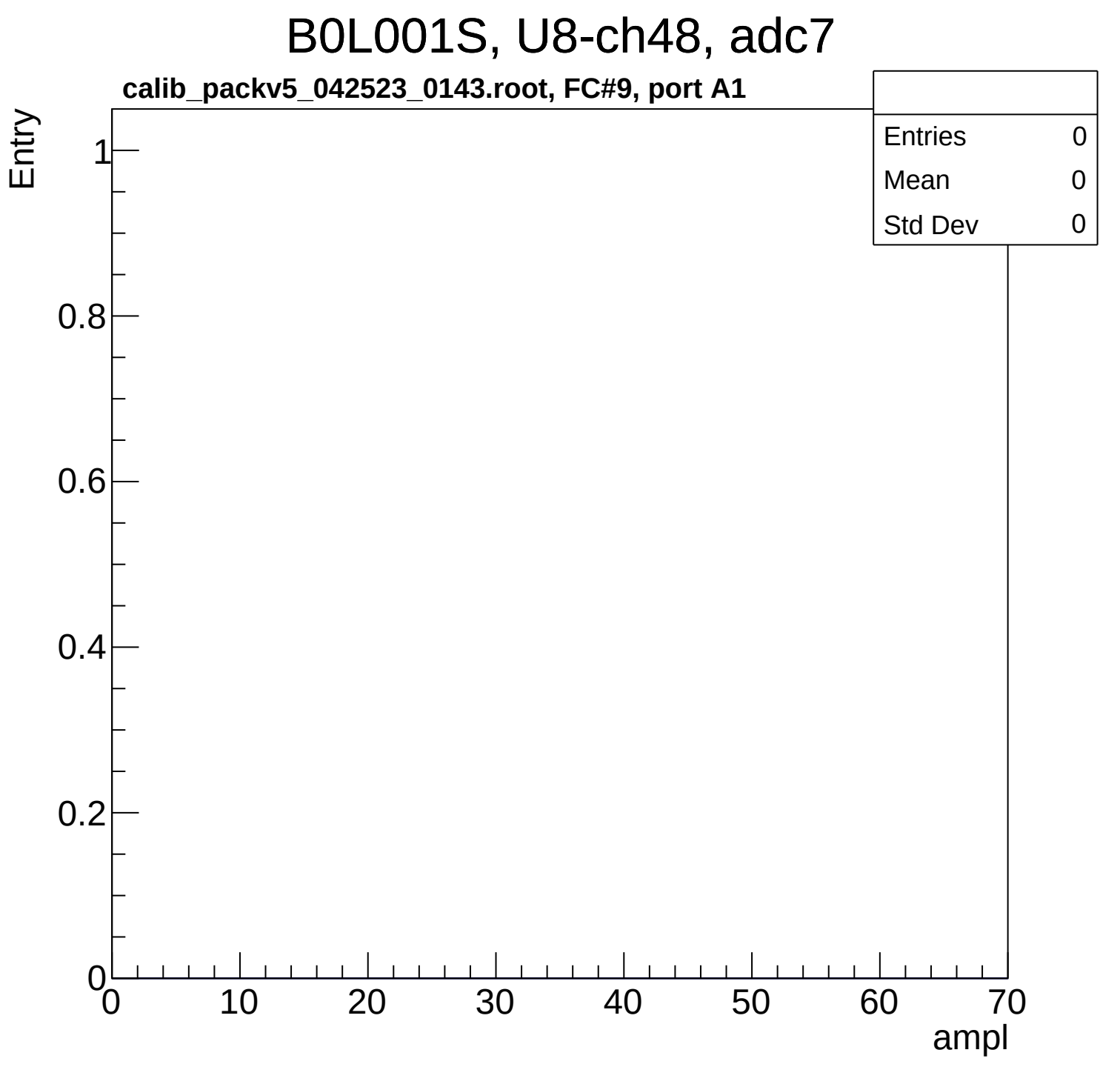

B0L001S, U8-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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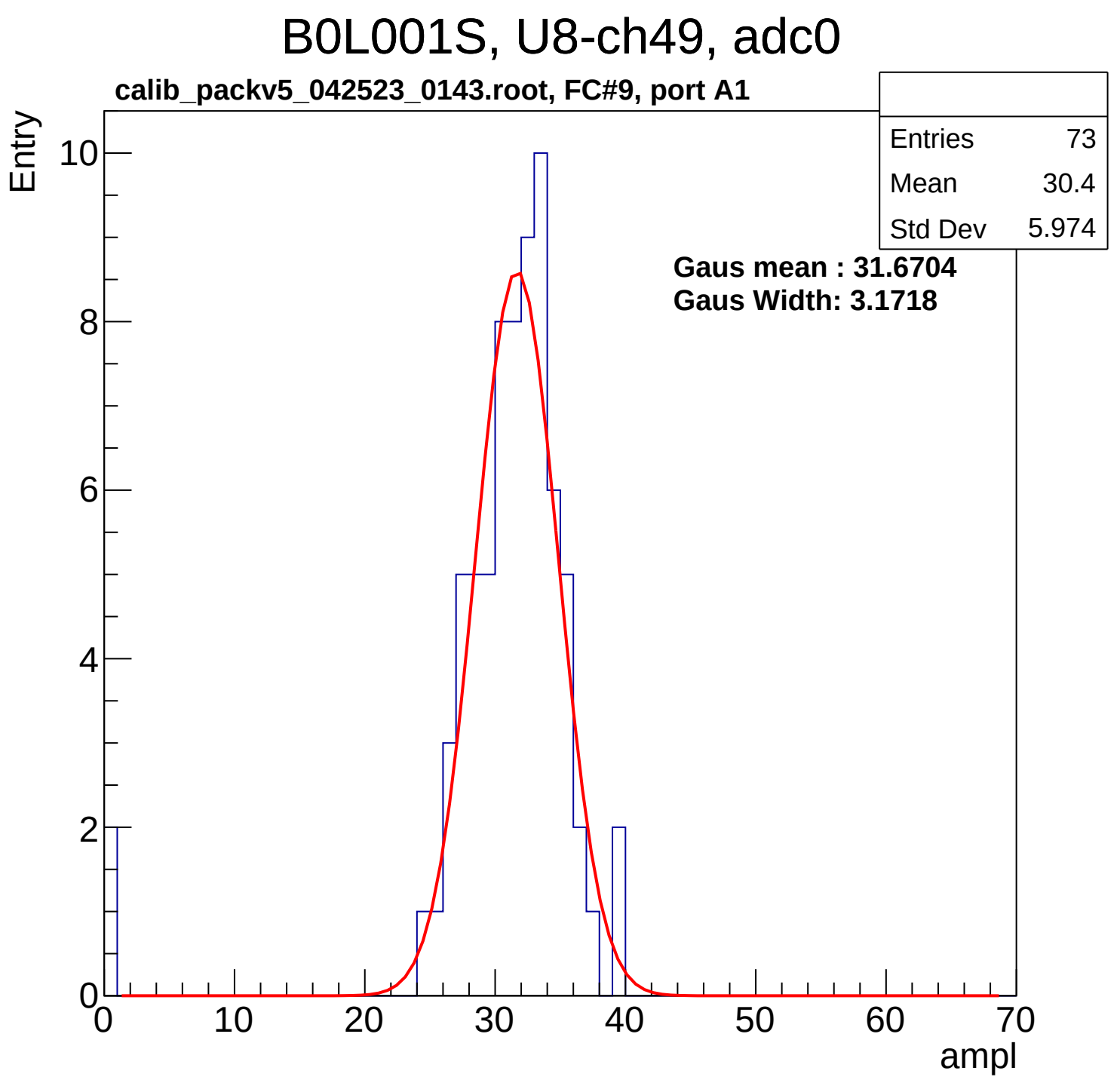
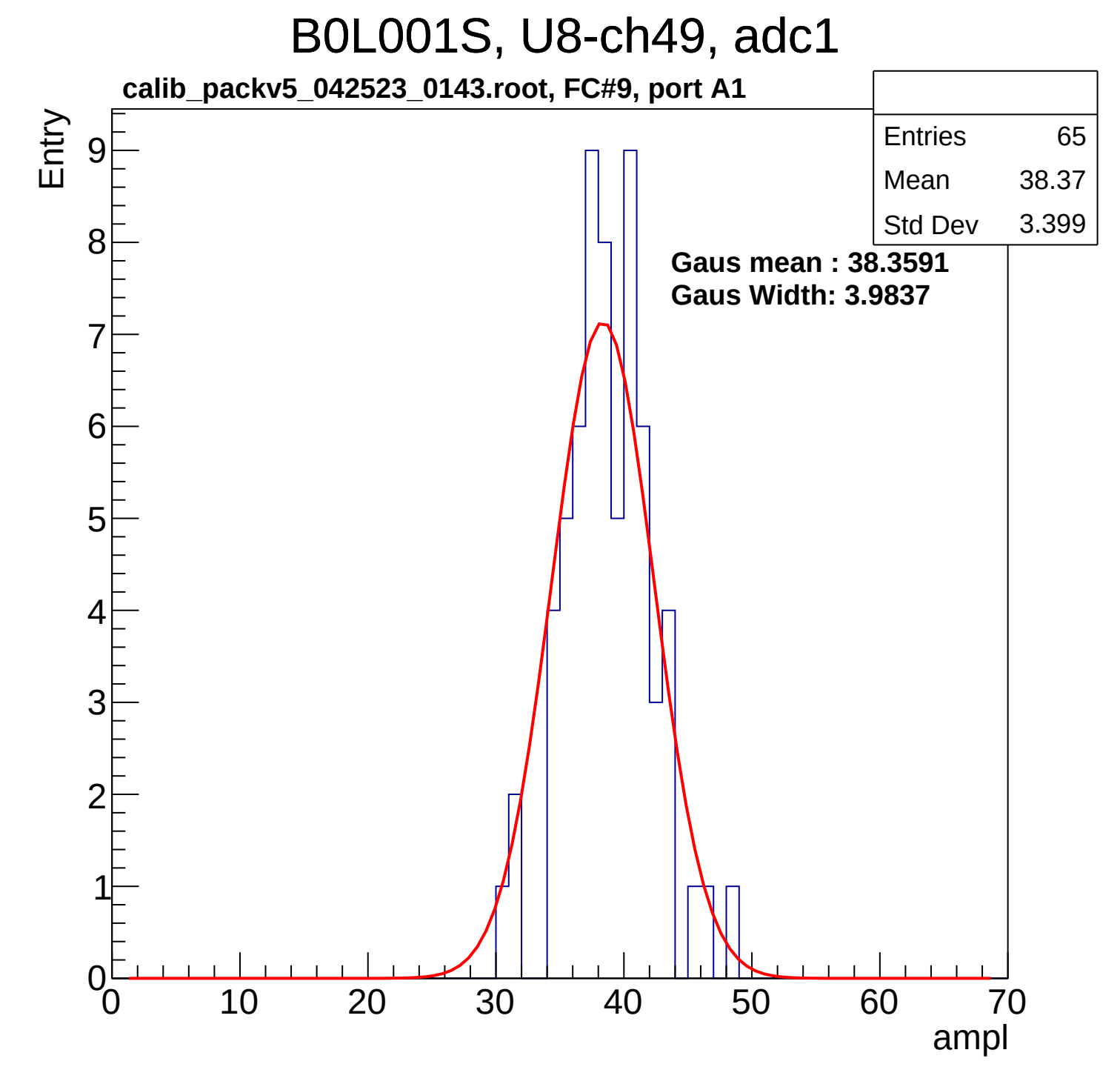
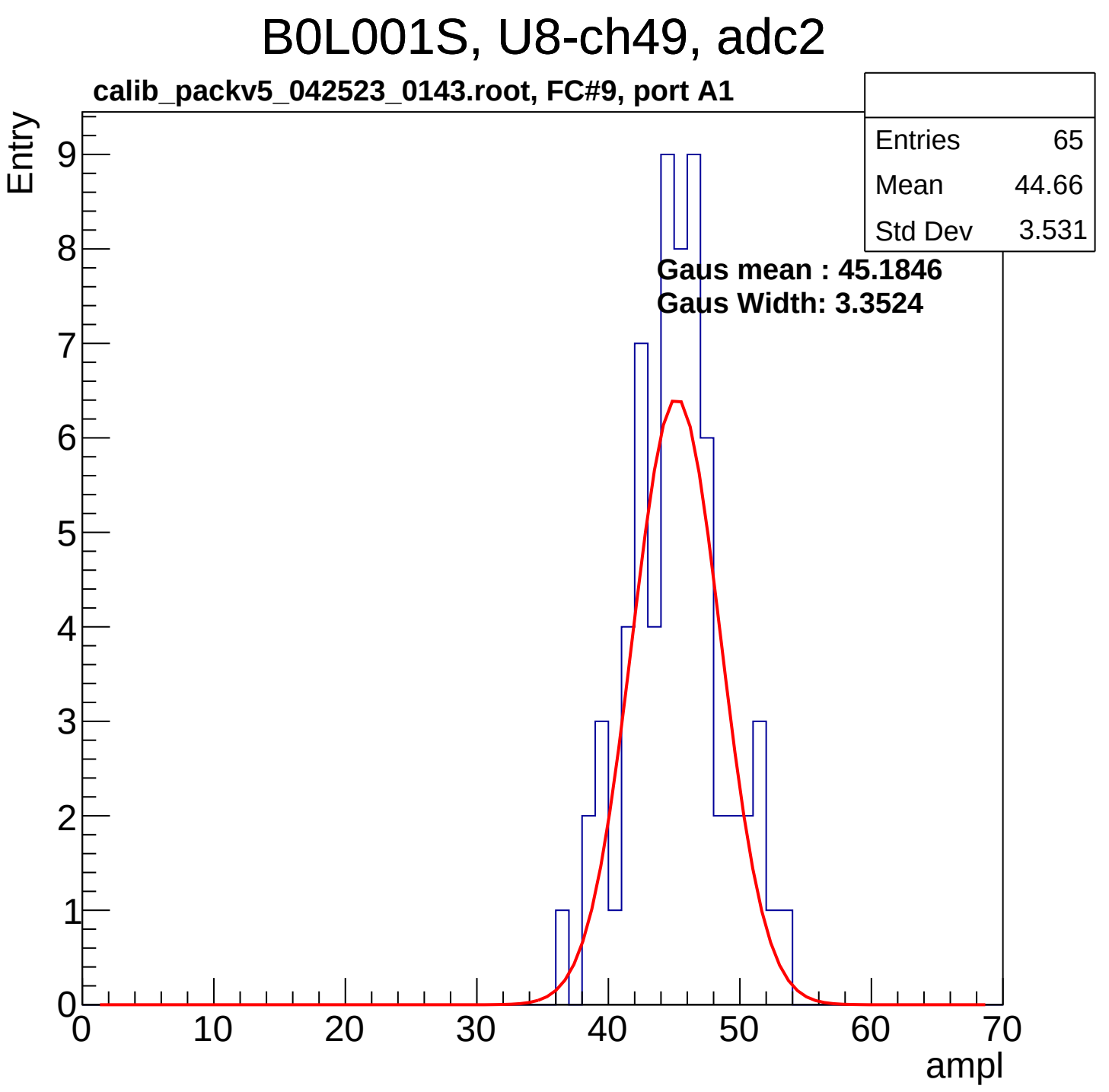
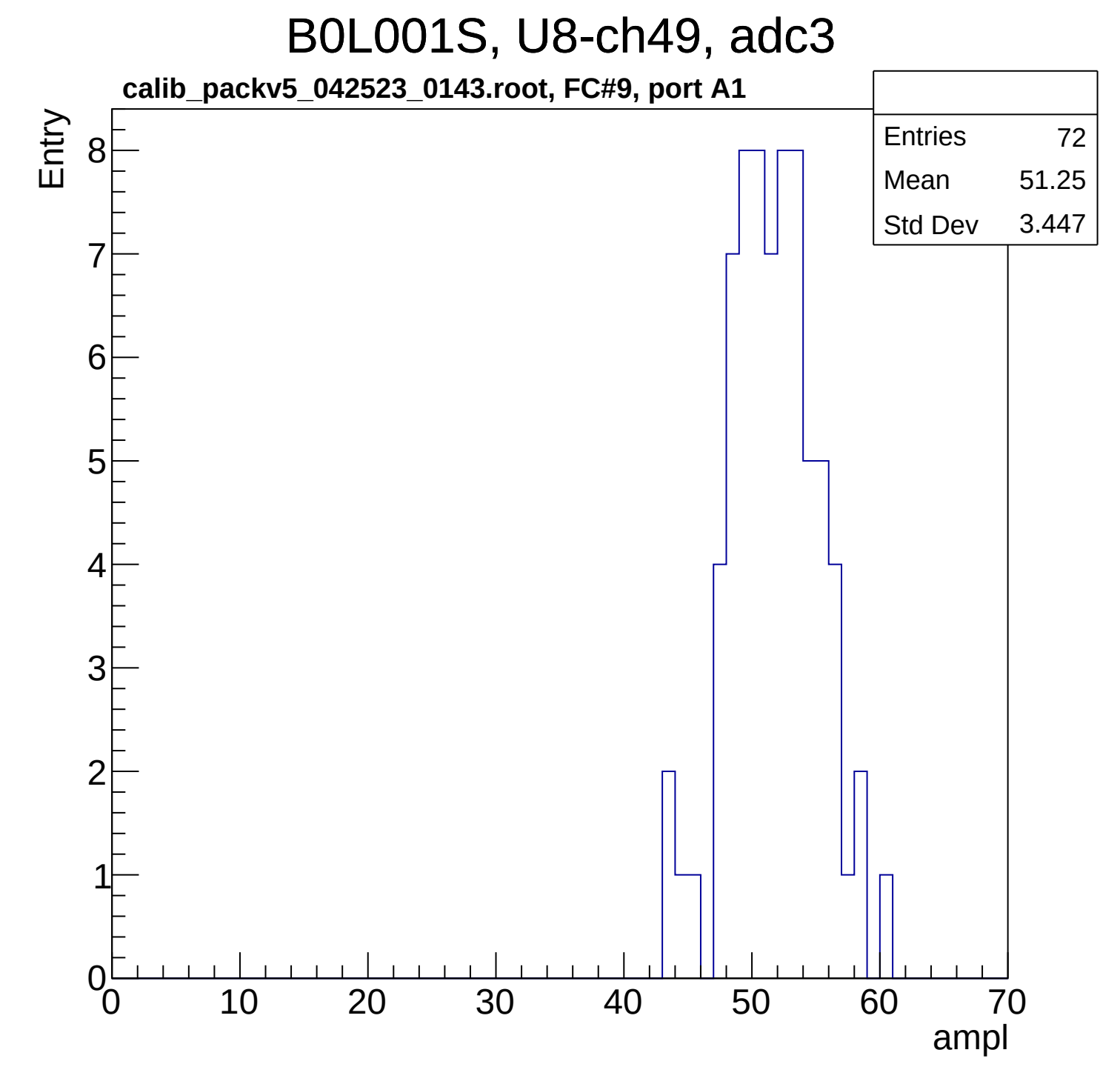
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.4
Std Dev 5.974
Gaus mean : 31.6704
Gaus Width: 3.1718
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.37
Std Dev 3.399
Gaus mean : 38.3591
Gaus Width: 3.9837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.66
Std Dev 3.531
Gaus mean : 45.1846
Gaus Width: 3.3524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.25
Std Dev 3.447
Entry
ampl
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3
4
5
6
7
8
0
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60
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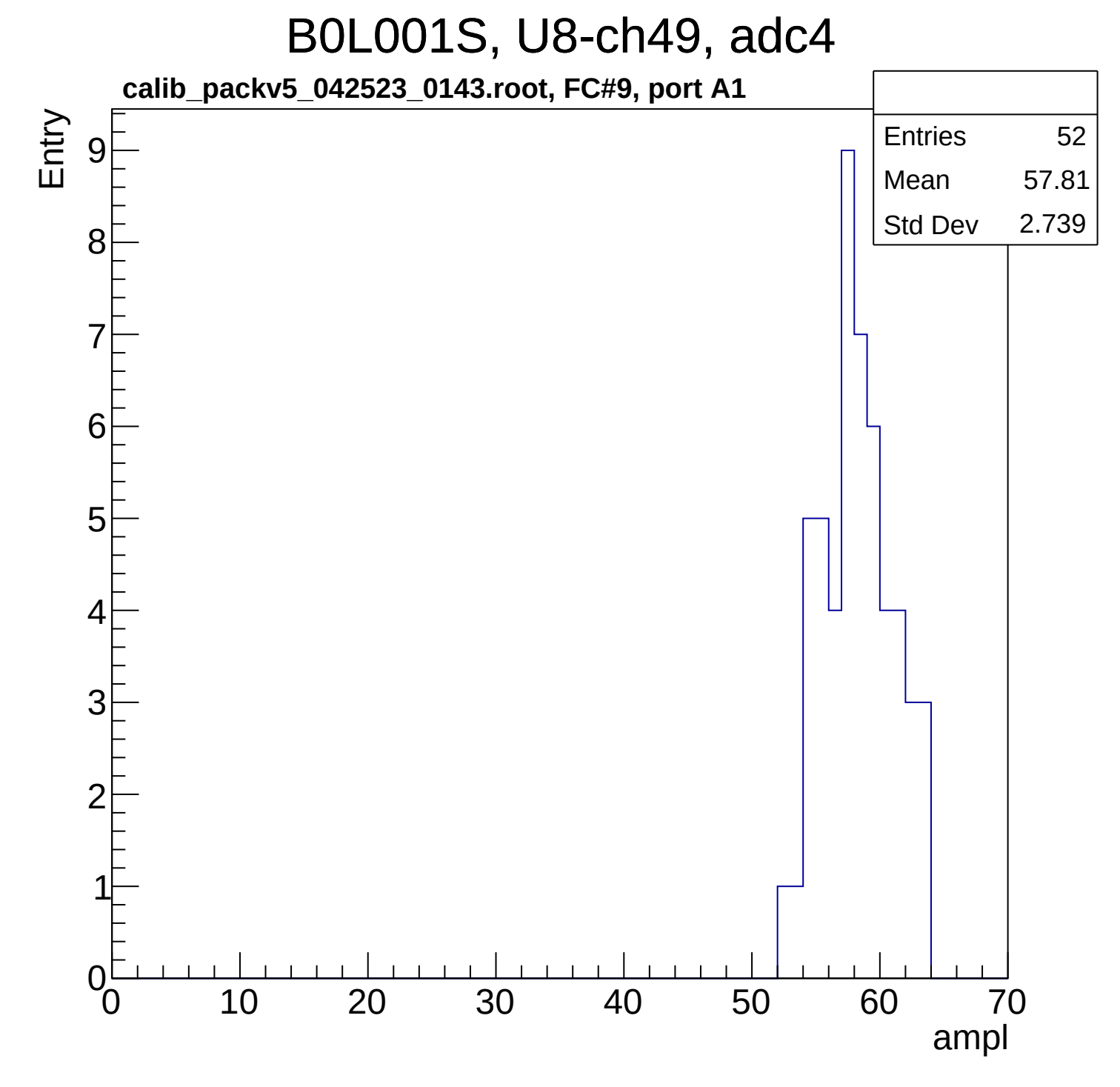
B0L001S, U8-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 57.81
Std Dev 2.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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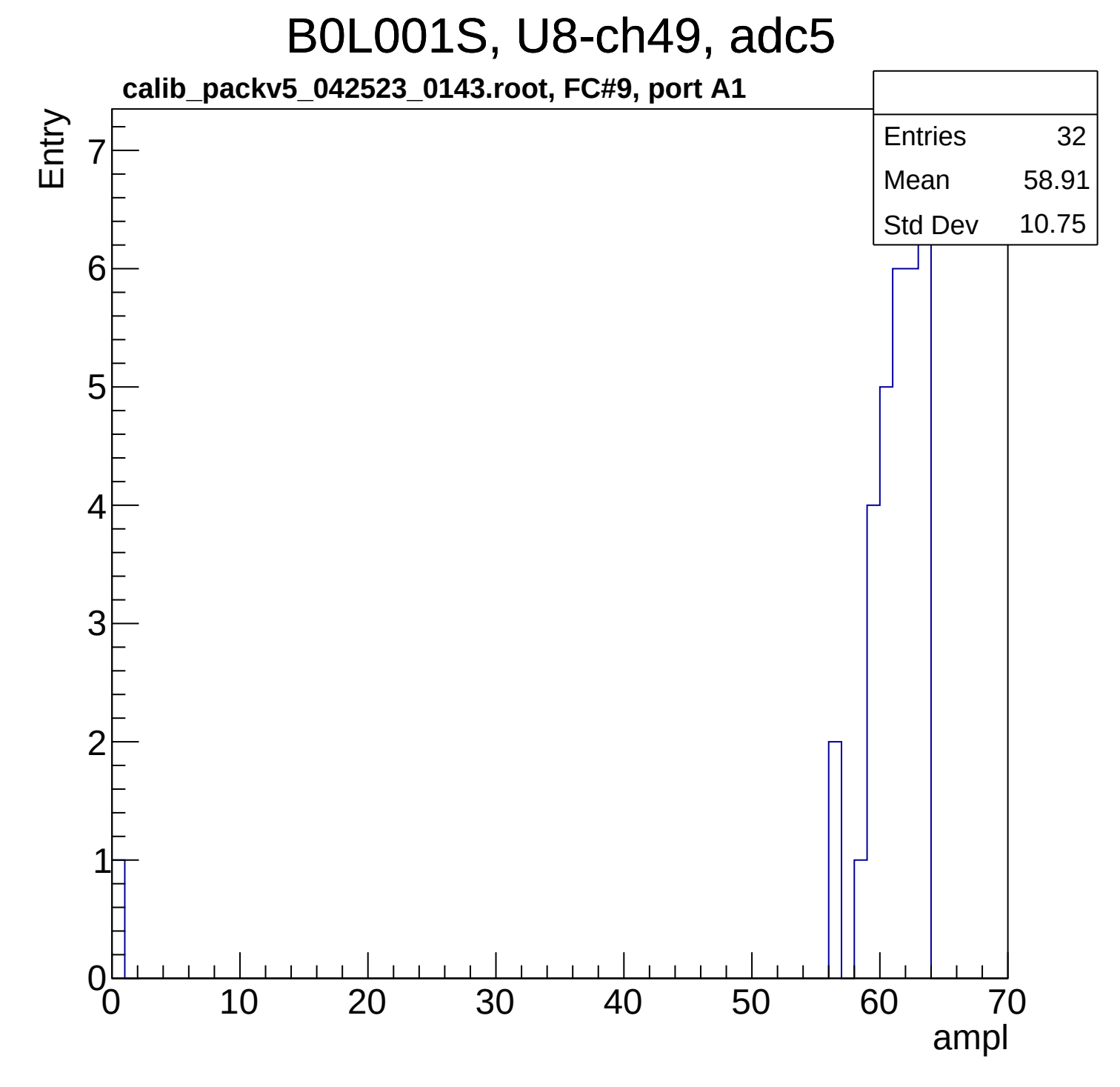
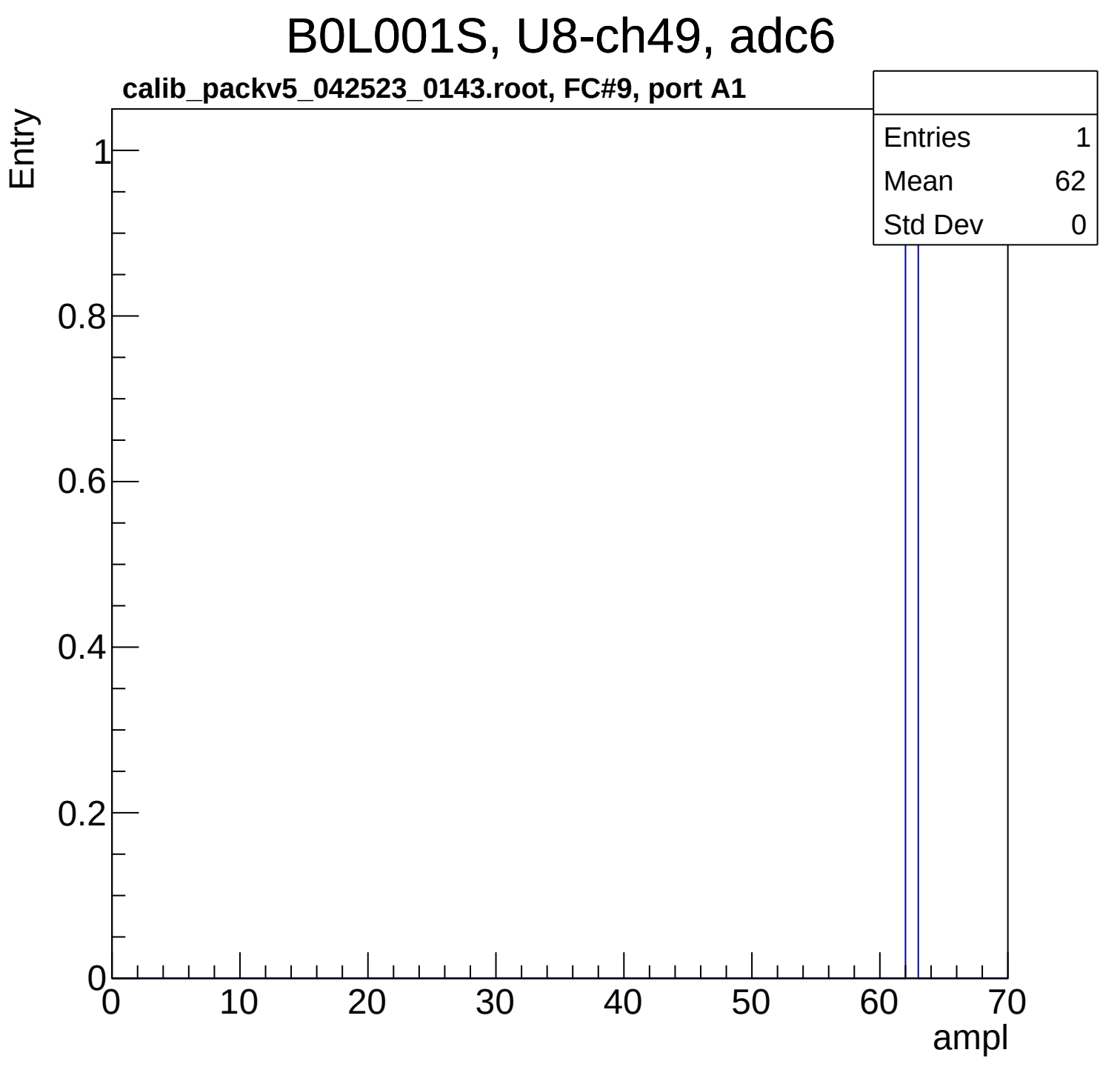

B0L001S, U8-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.91
Std Dev 10.75
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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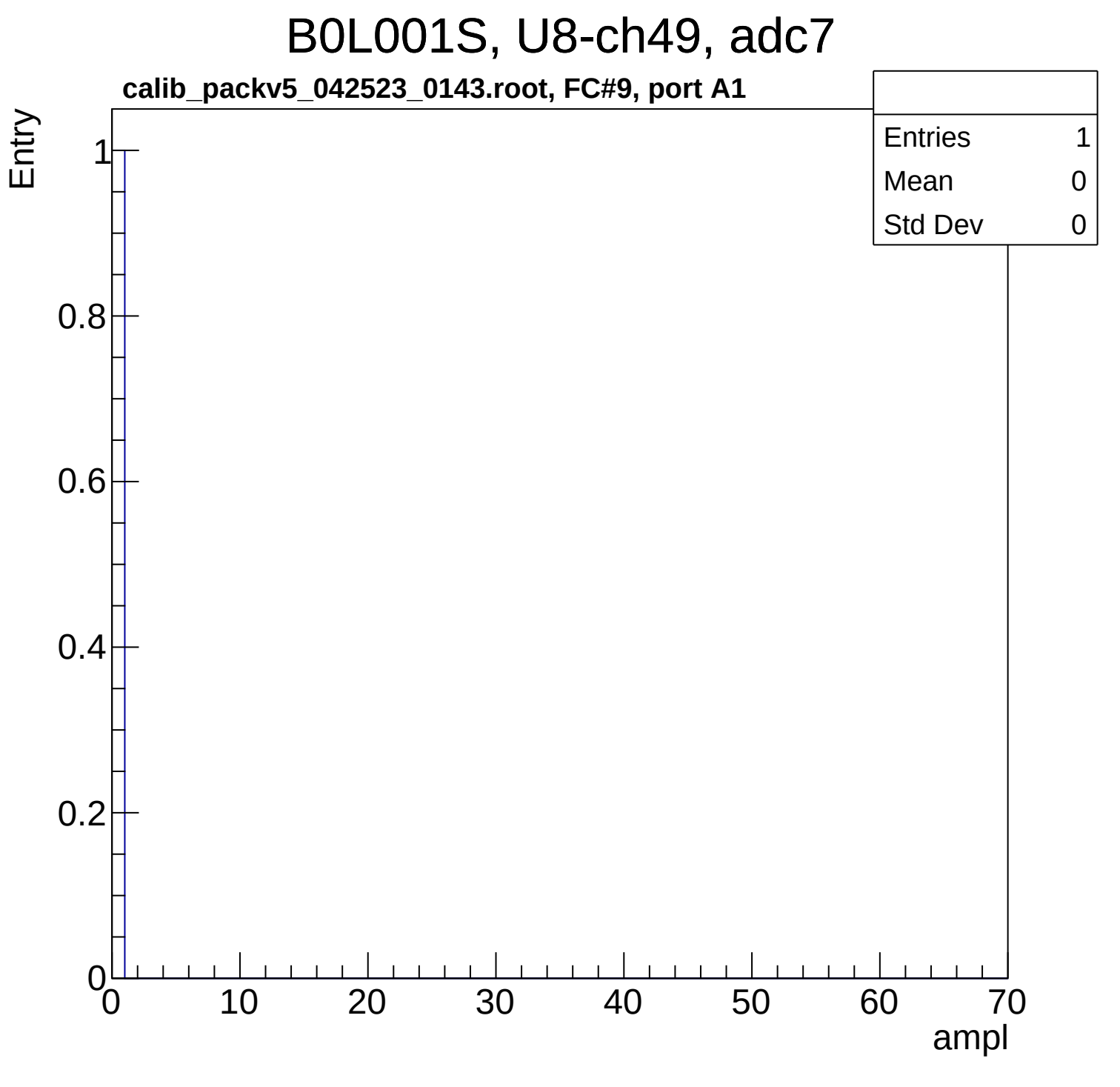
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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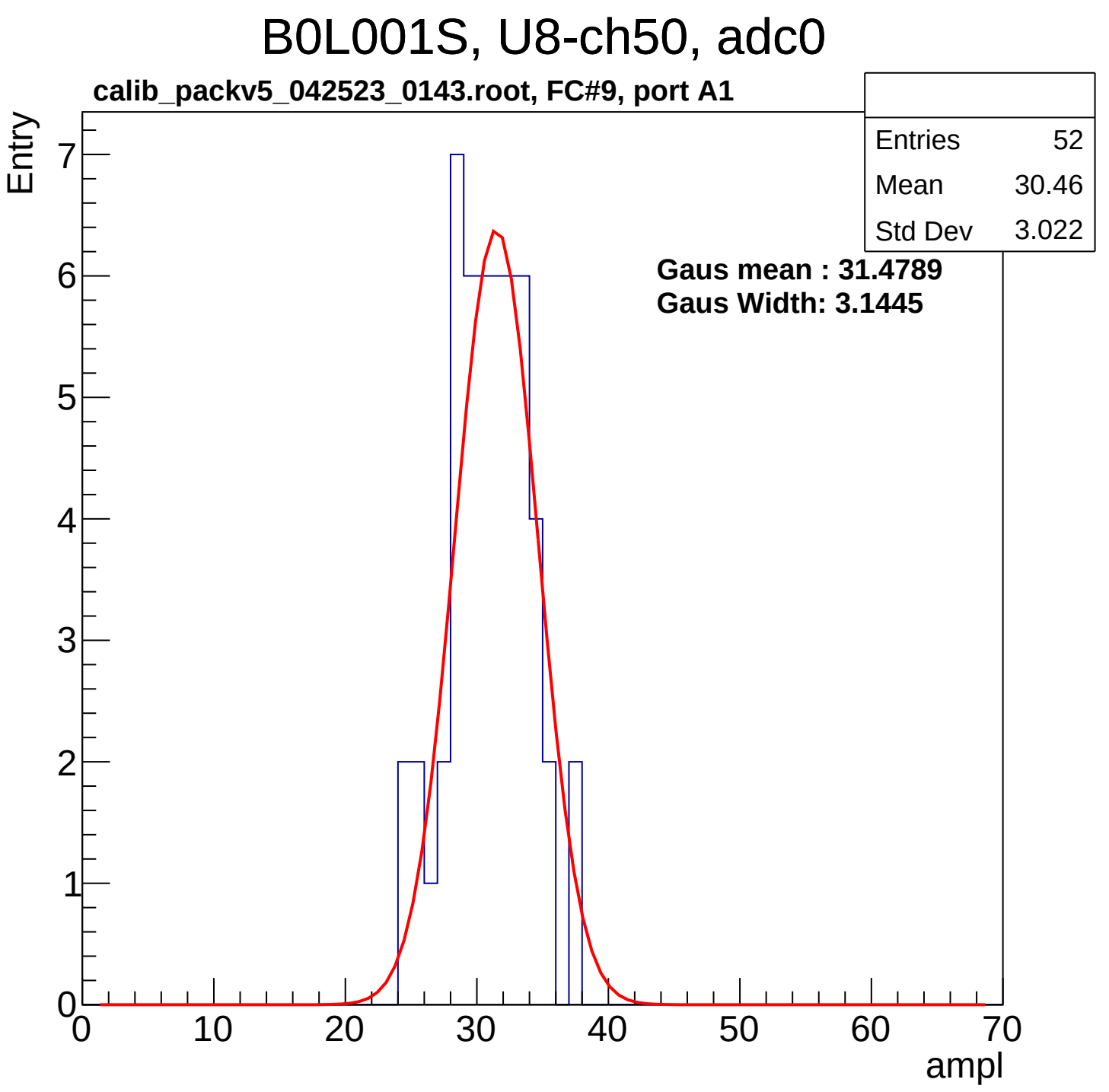
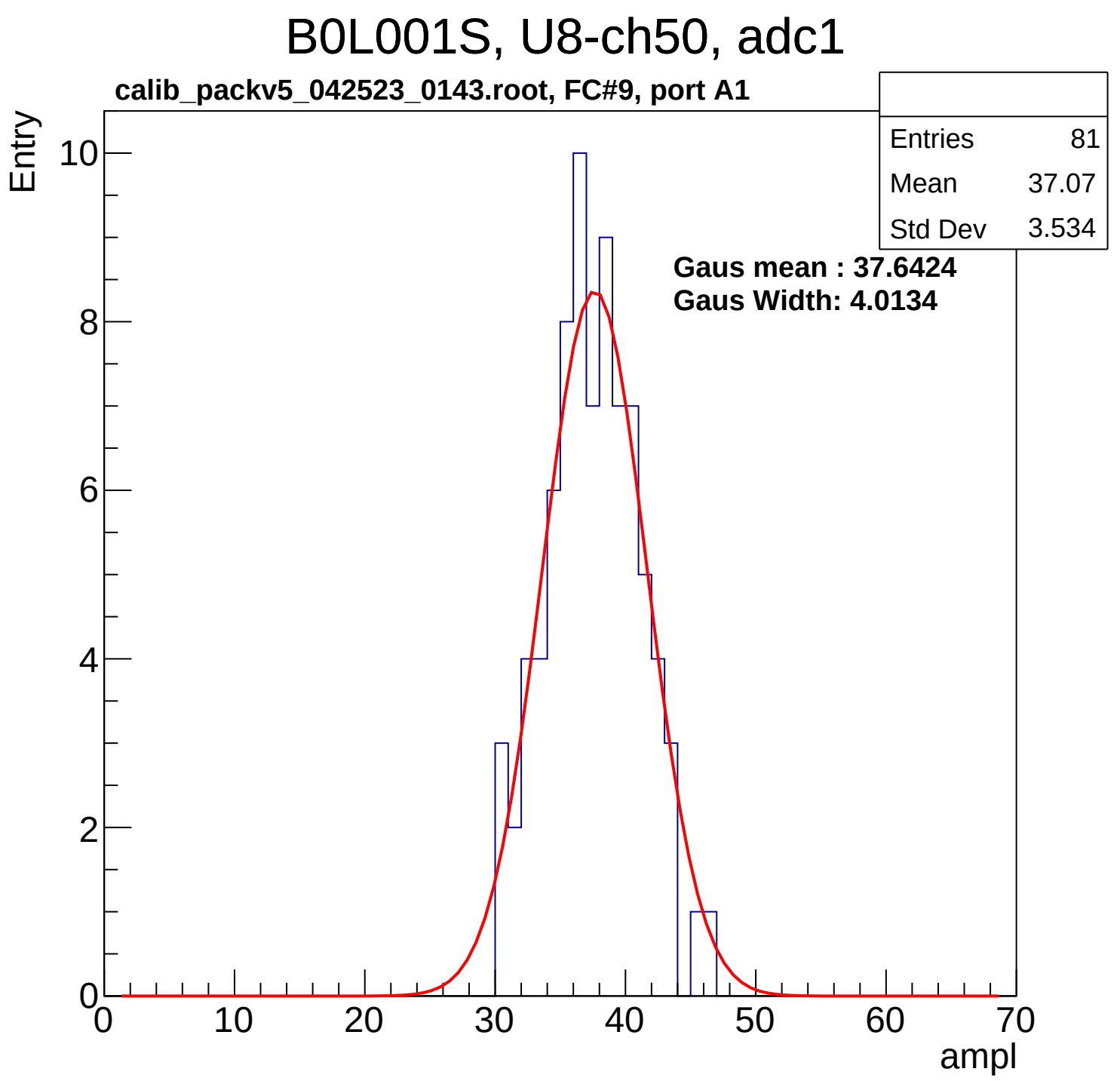
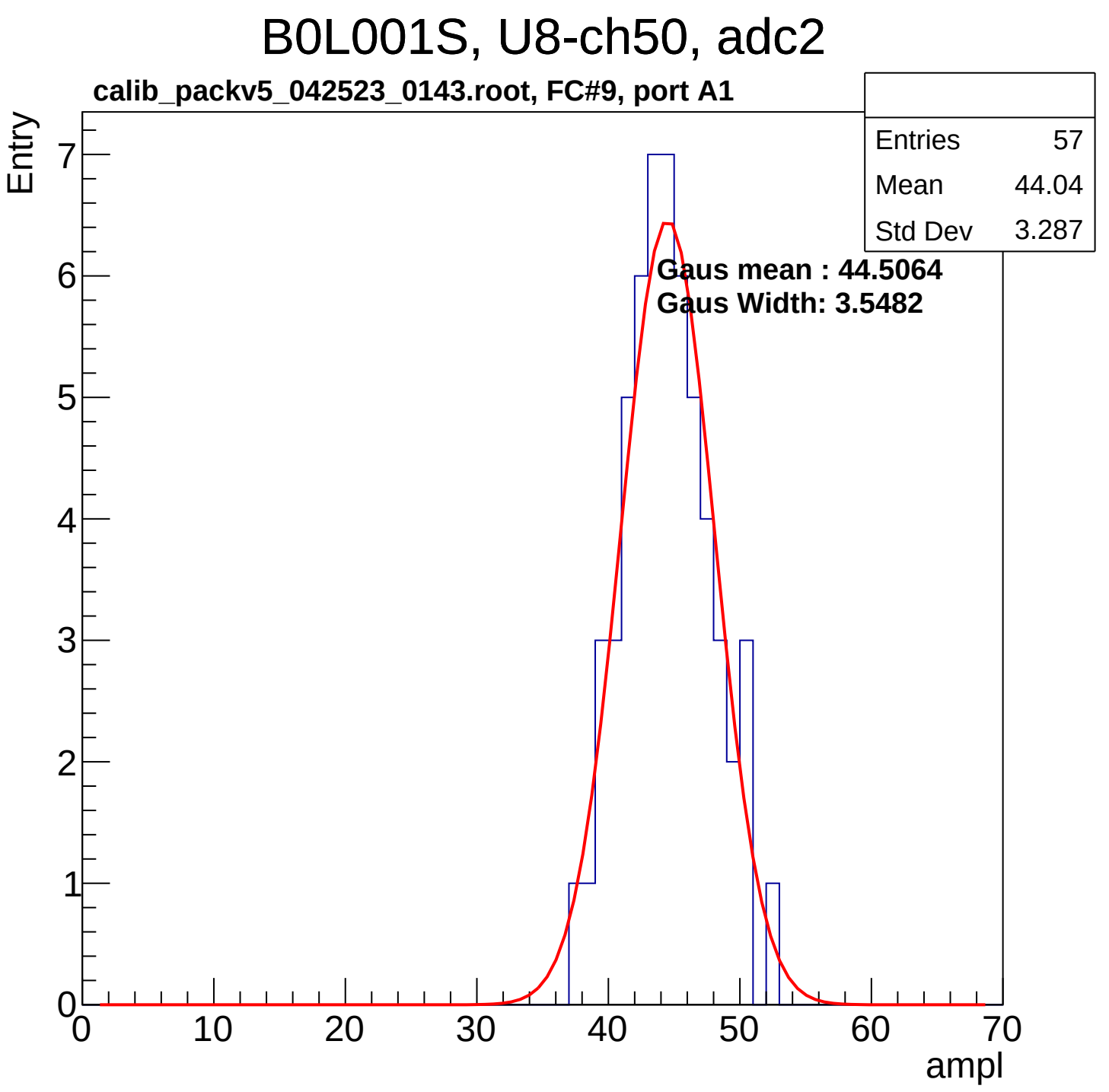
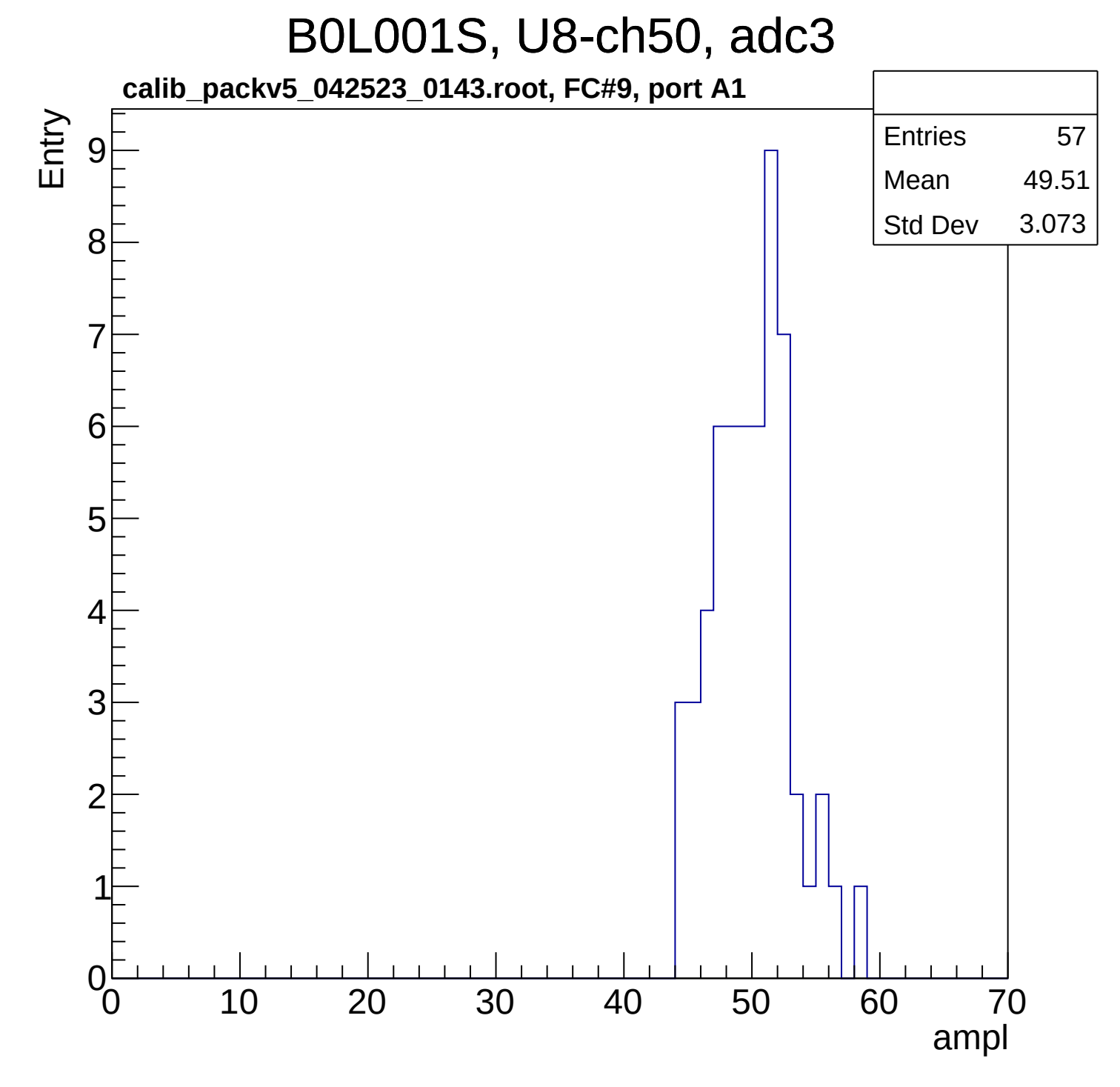
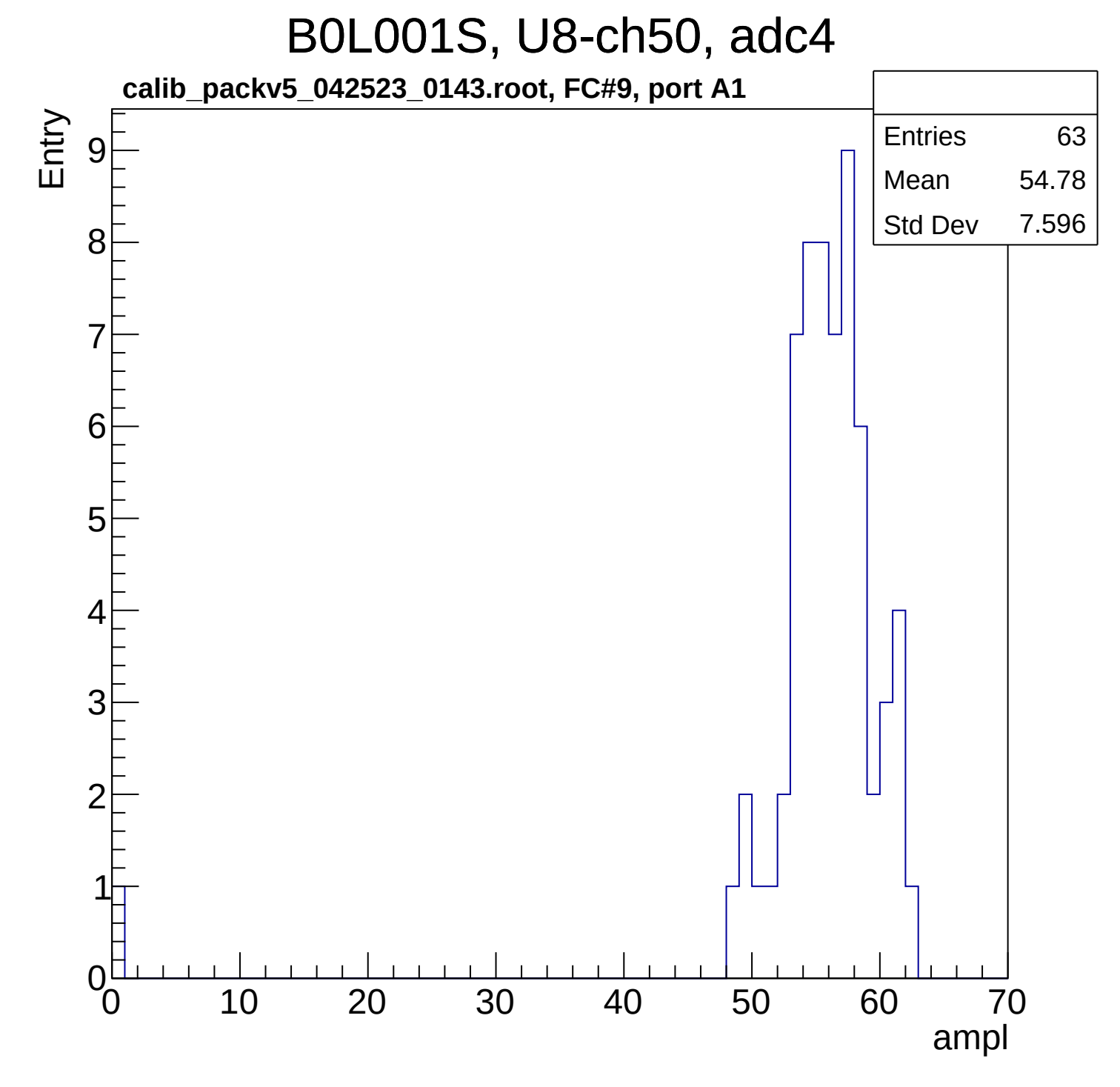
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

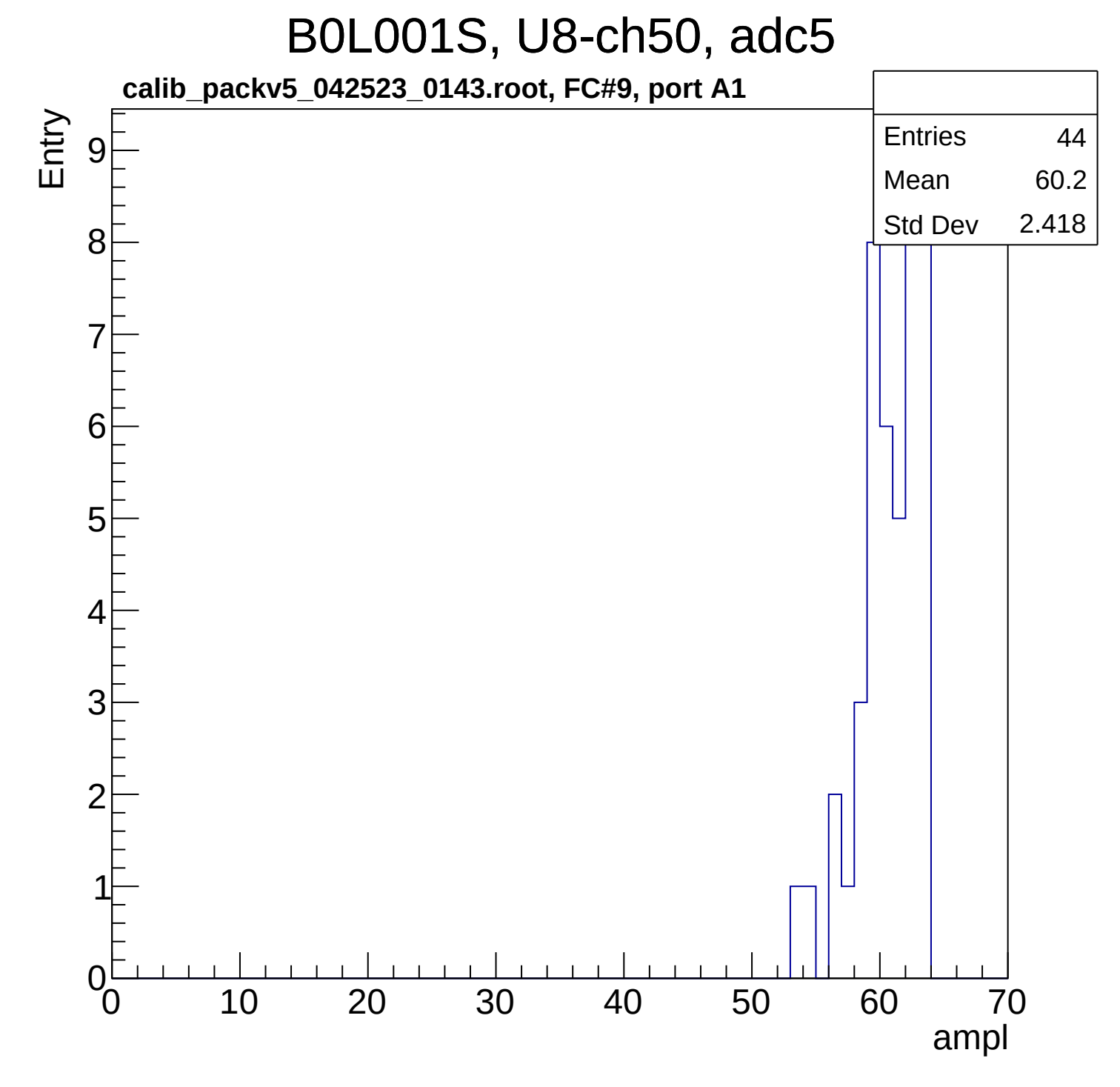

B0L001S, U8-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 30.46
Std Dev 3.022
Gaus mean : 31.4789
Gaus Width: 3.1445
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

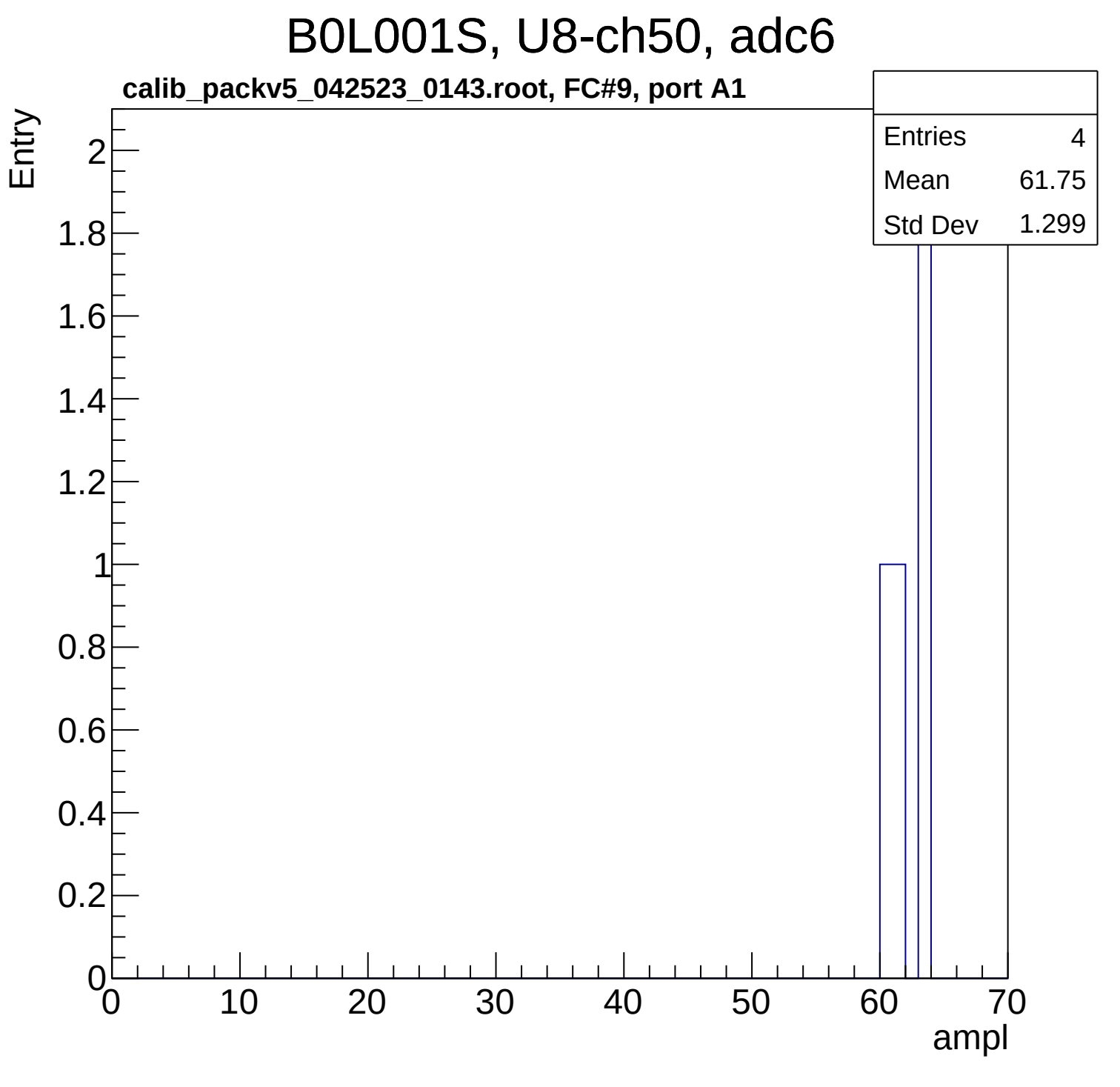

B0L001S, U8-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 37.07
Std Dev 3.534
Gaus mean : 37.6424
Gaus Width: 4.0134
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

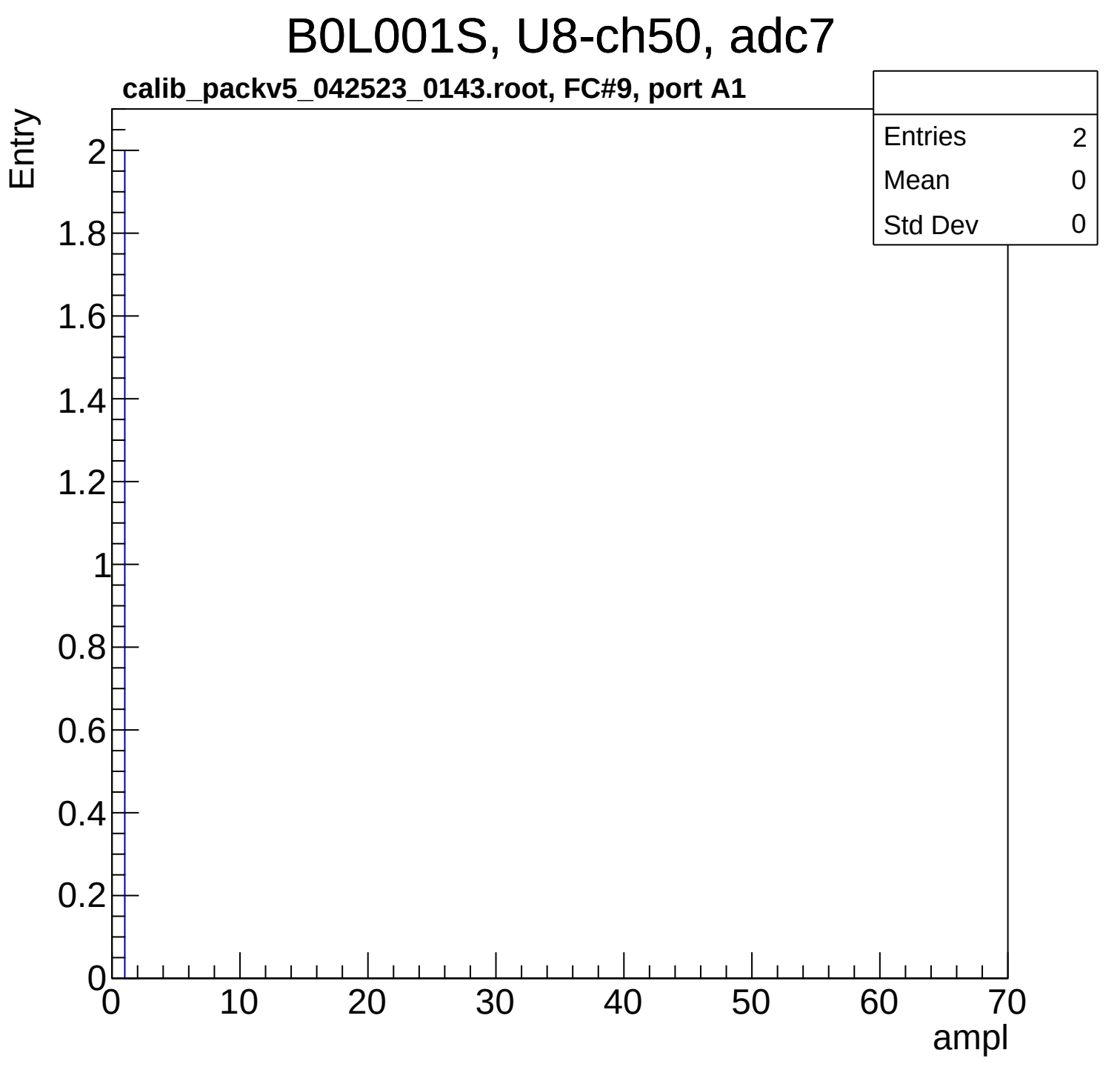

B0L001S, U8-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.04
Std Dev 3.287
Gaus mean : 44.5064
Gaus Width: 3.5482
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

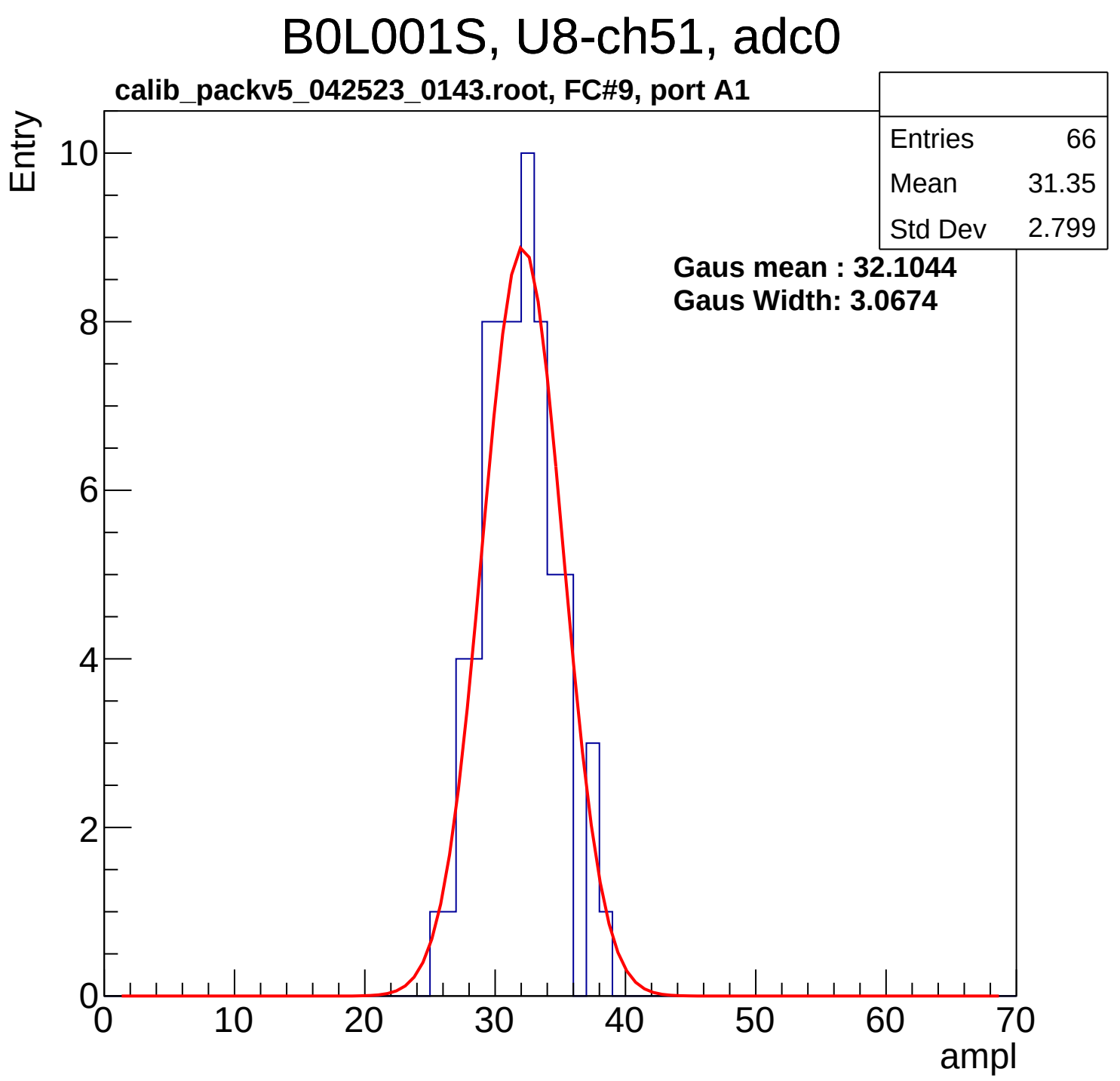

B0L001S, U8-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.51
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

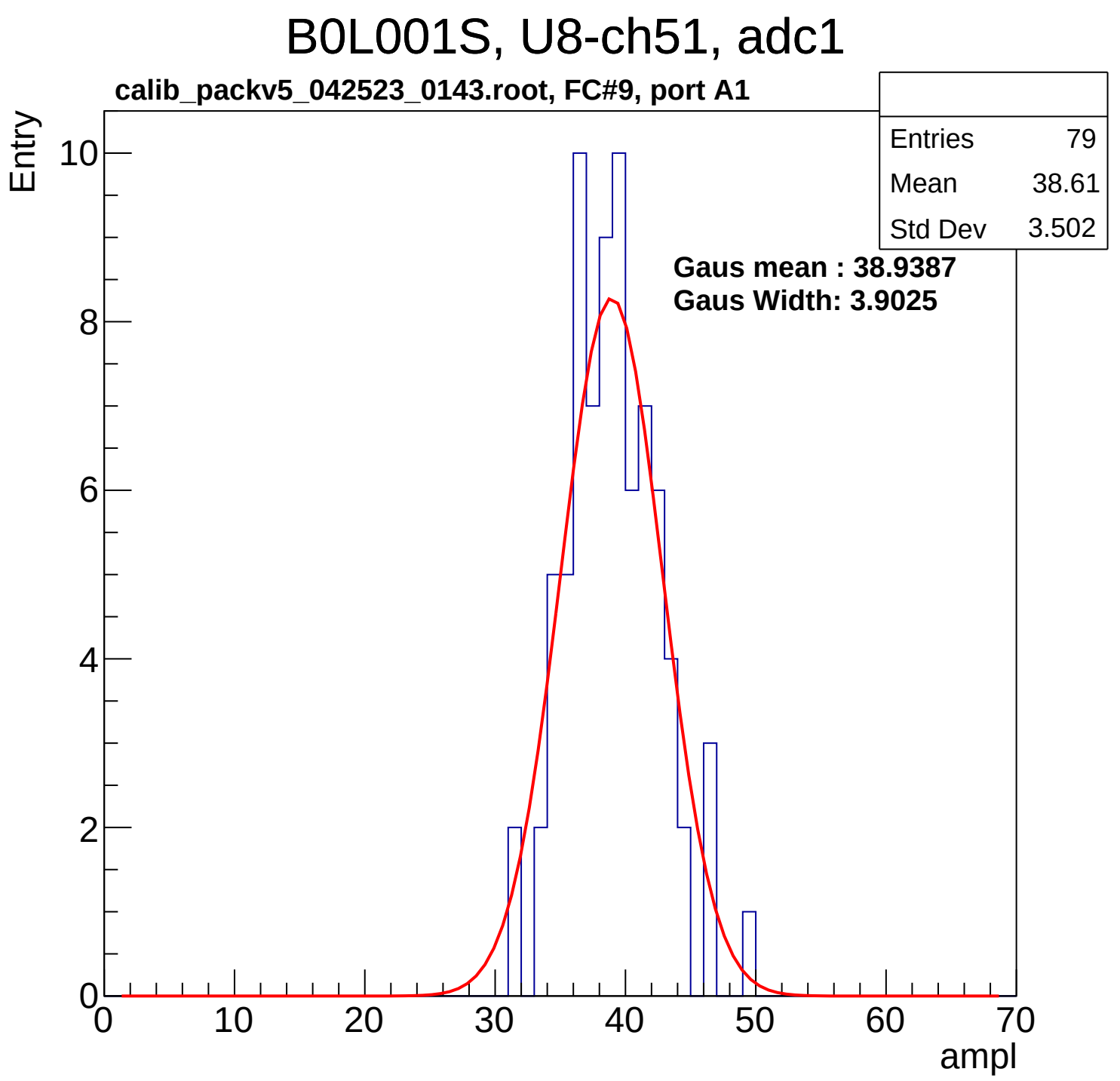

B0L001S, U8-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 54.78
Std Dev 7.596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

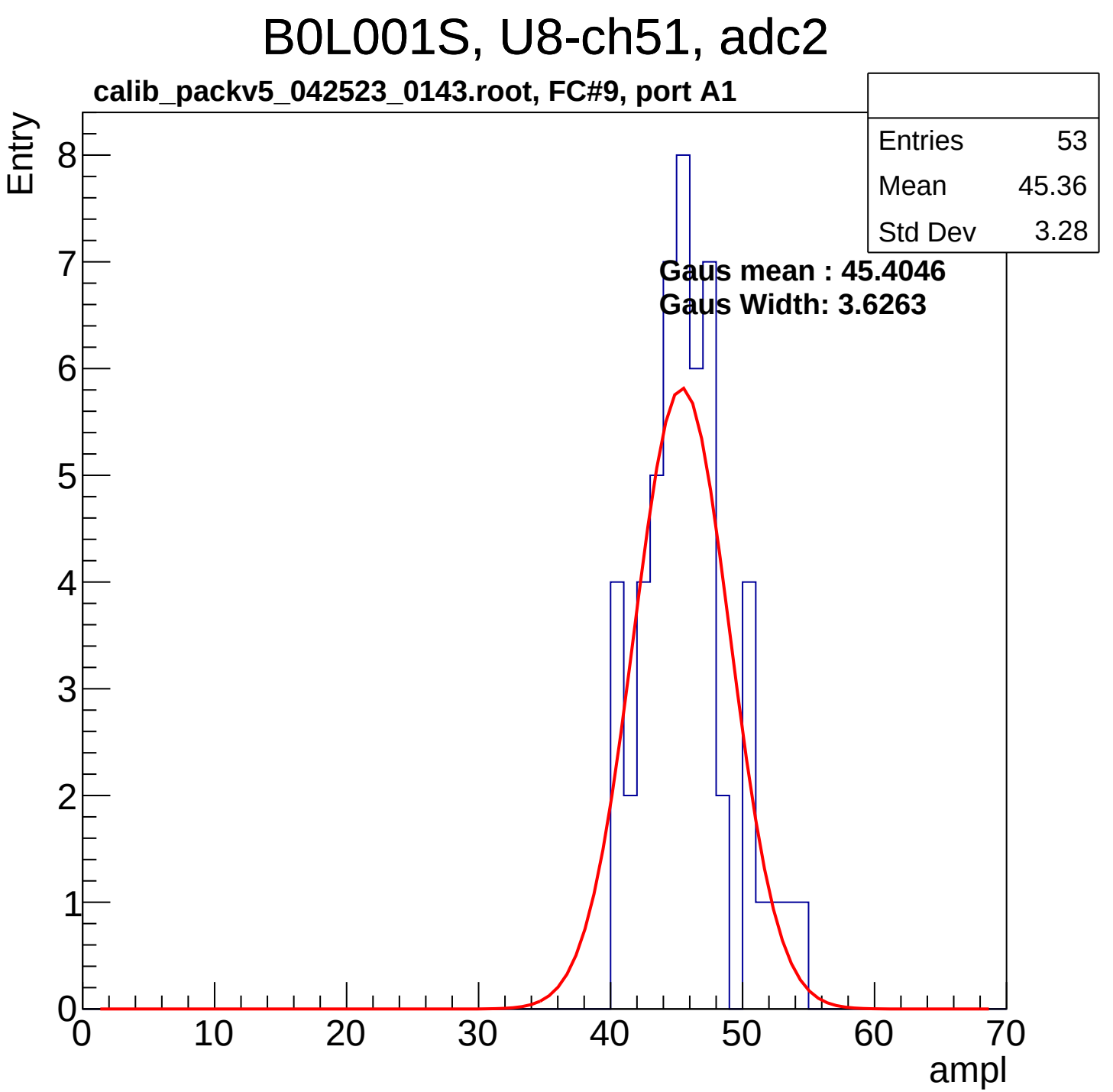

B0L001S, U8-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 60.2
Std Dev 2.418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

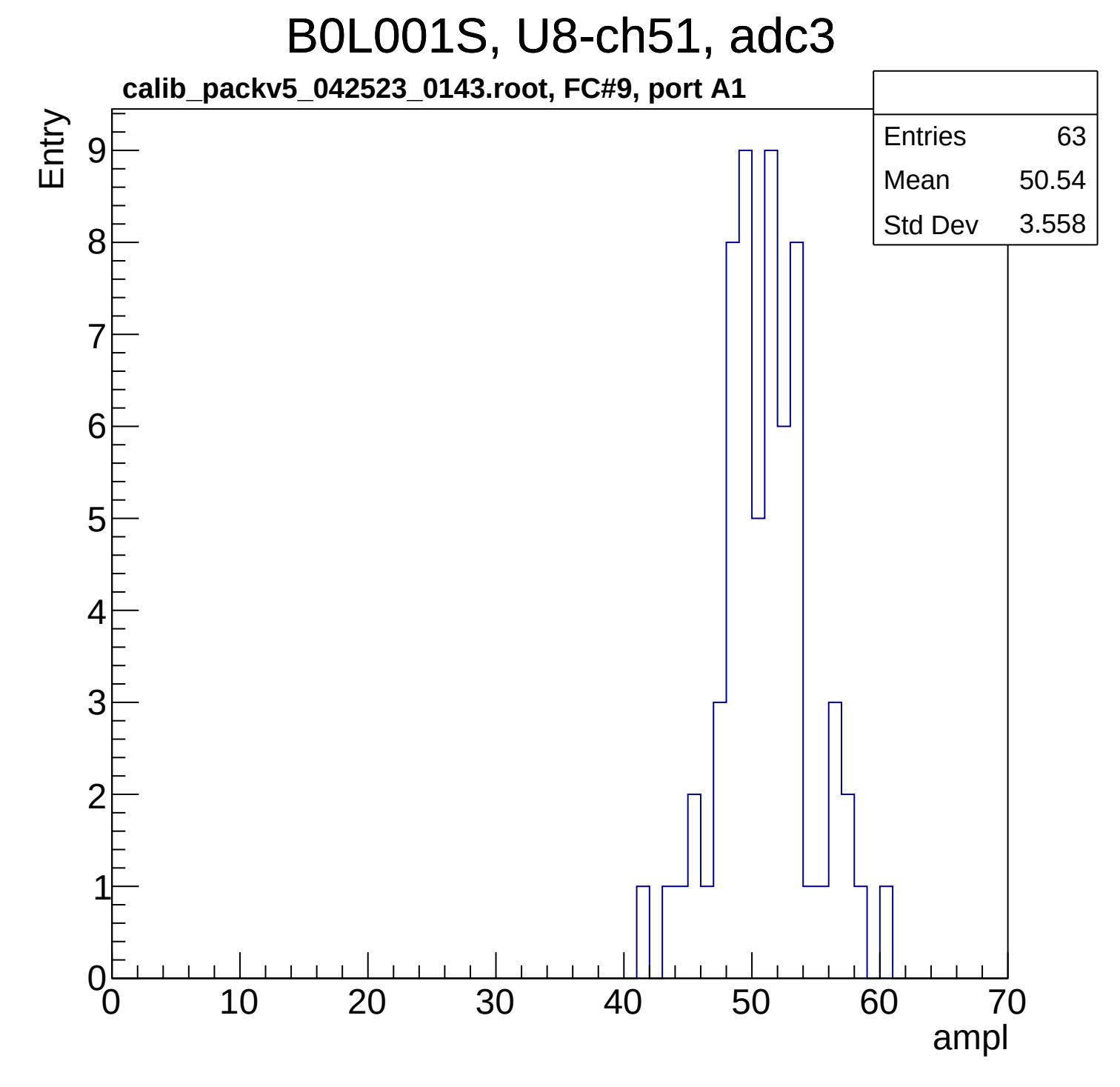

B0L001S, U8-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

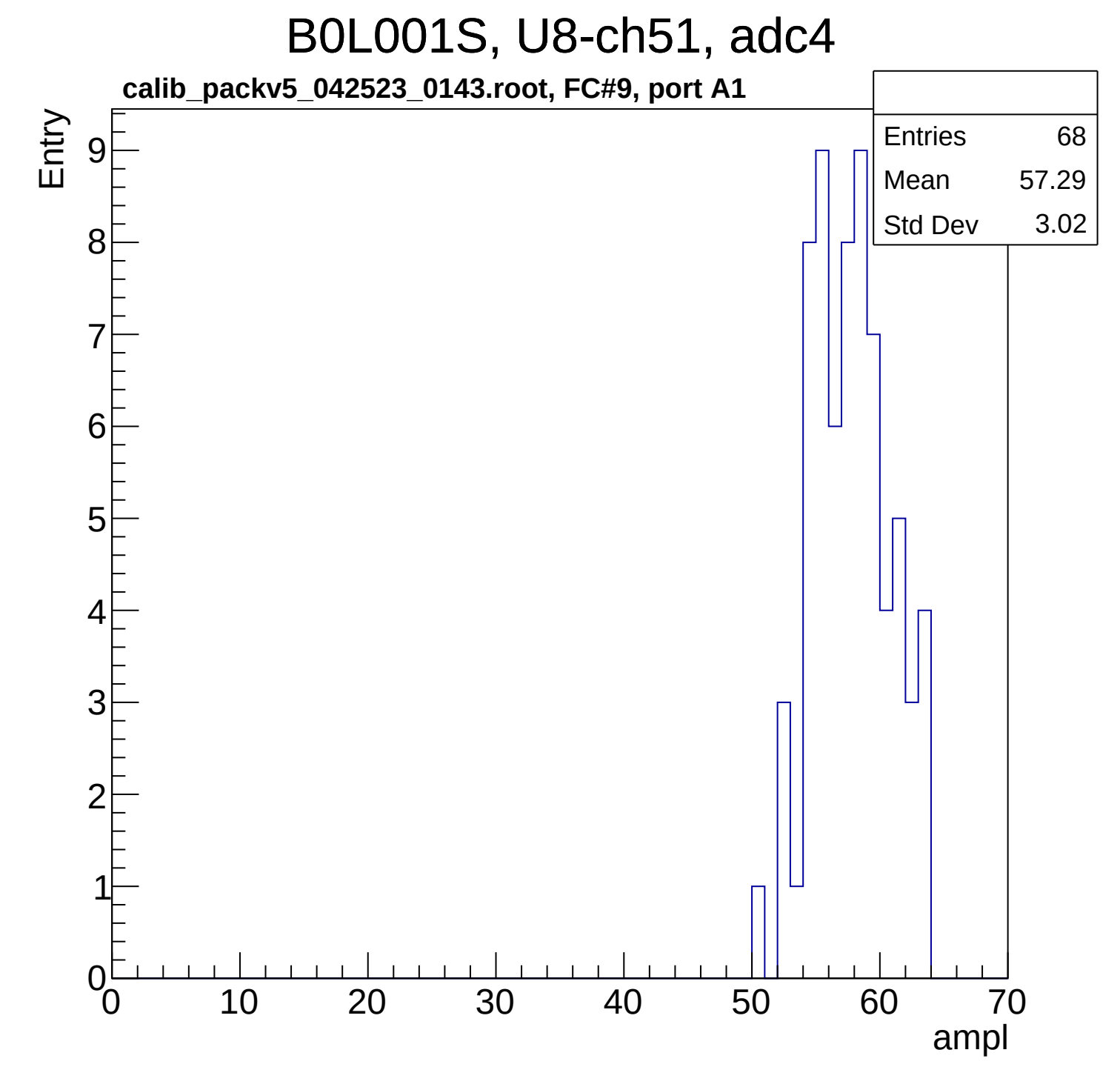

B0L001S, U8-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

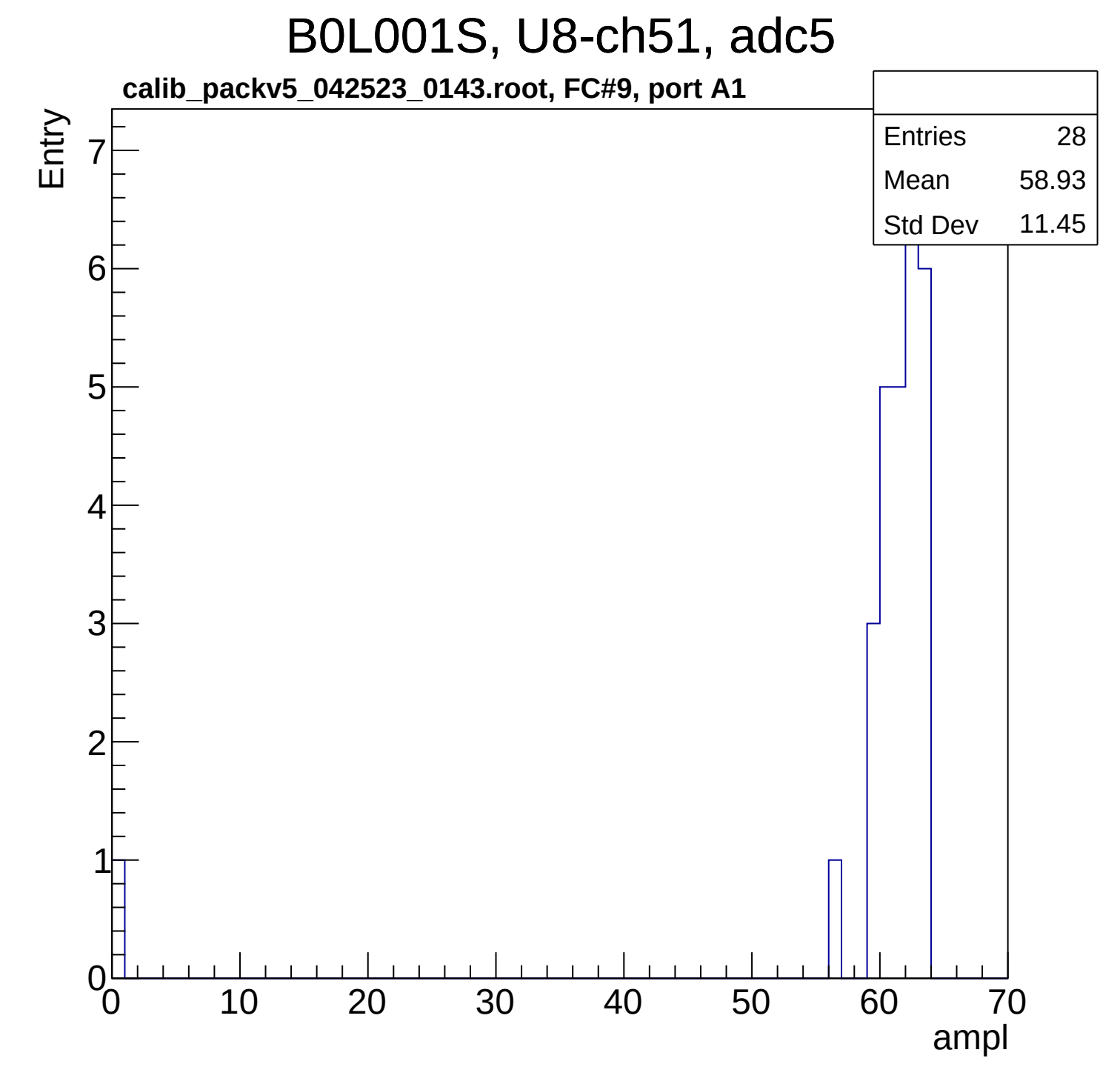

B0L001S, U8-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.35
Std Dev 2.799
Gaus mean : 32.1044
Gaus Width: 3.0674
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

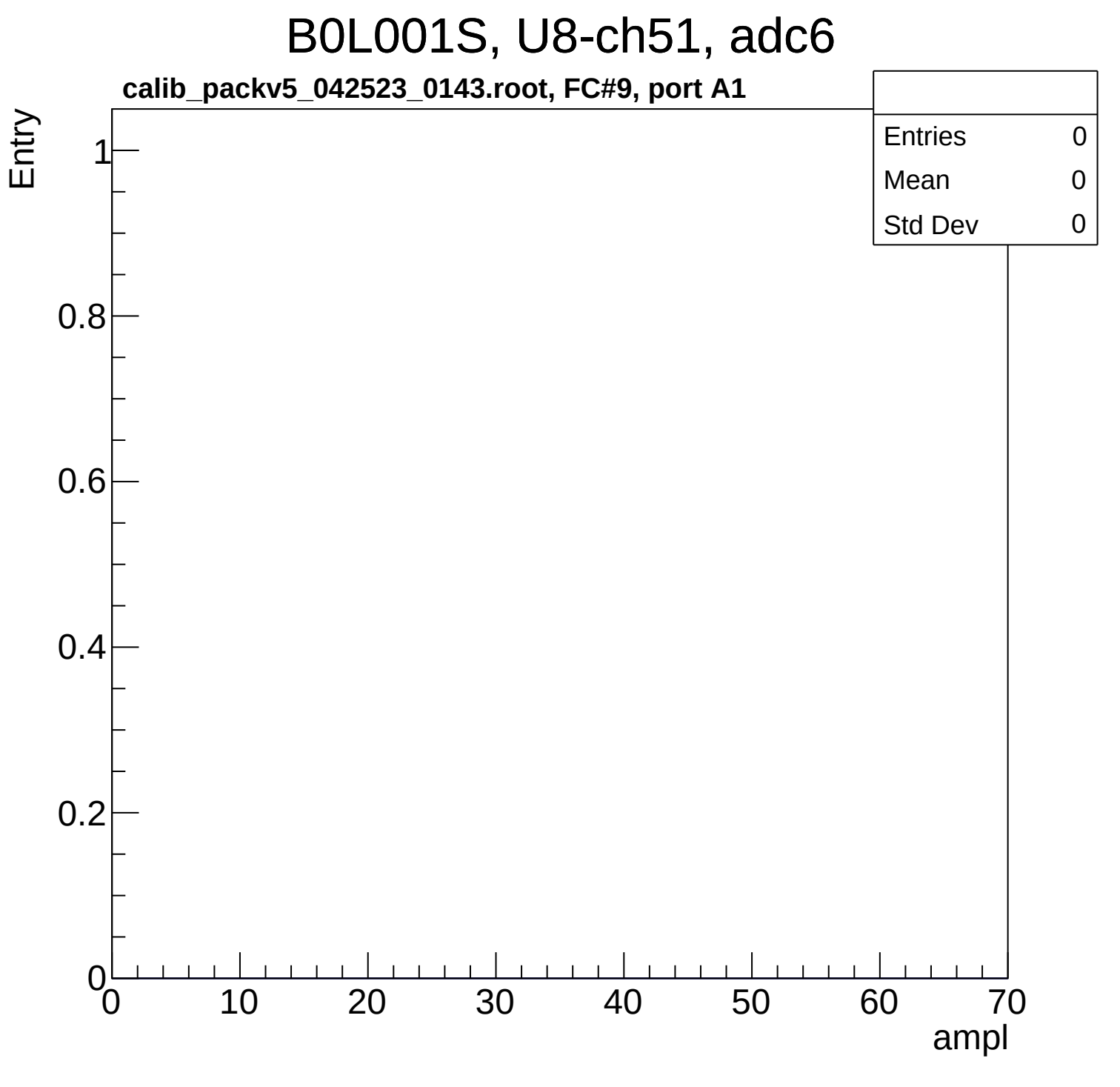

B0L001S, U8-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 38.61
Std Dev 3.502
Gaus mean : 38.9387
Gaus Width: 3.9025
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

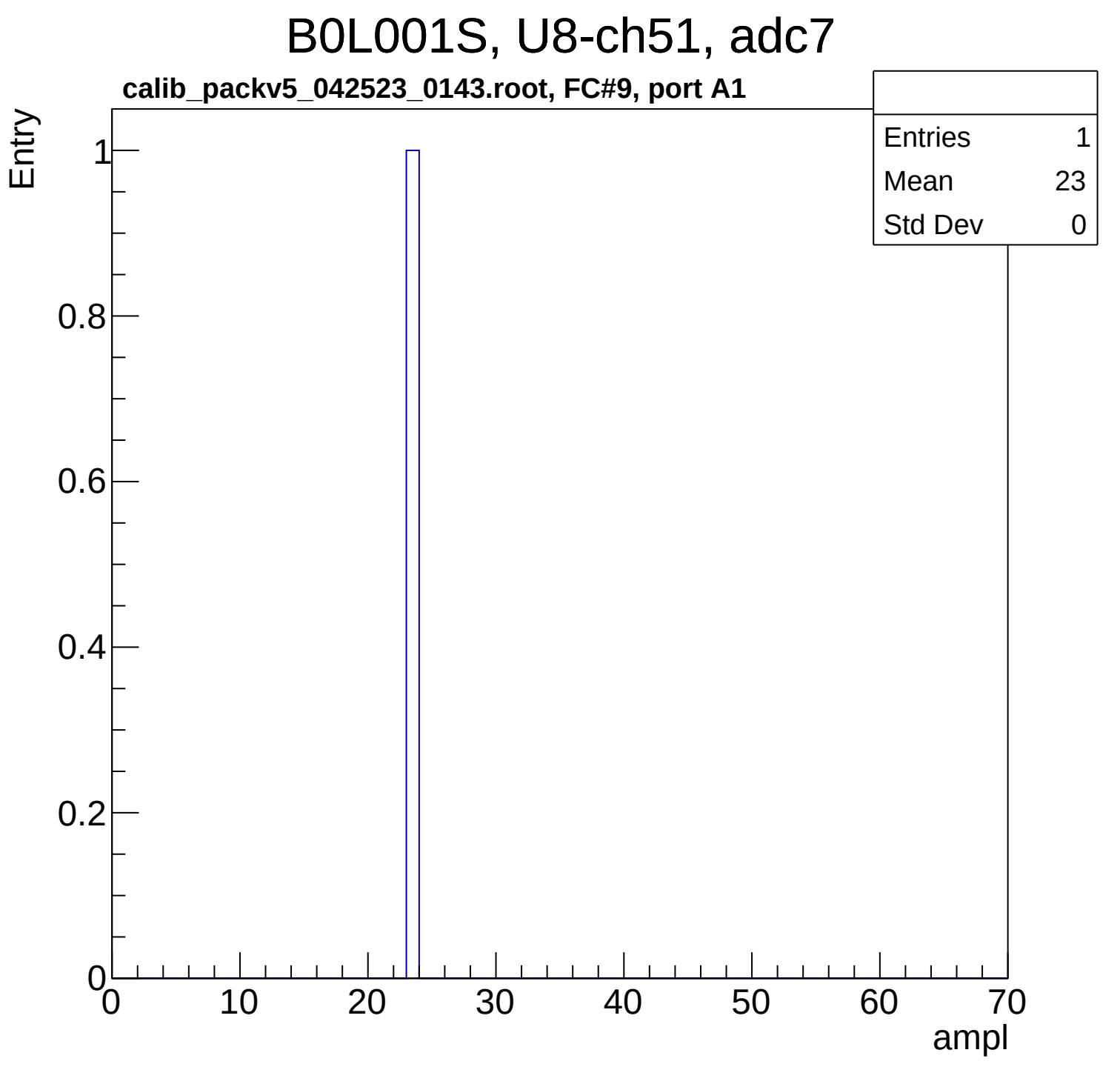

B0L001S, U8-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 45.36
Std Dev 3.28
Gaus mean : 45.4046
Gaus Width: 3.6263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

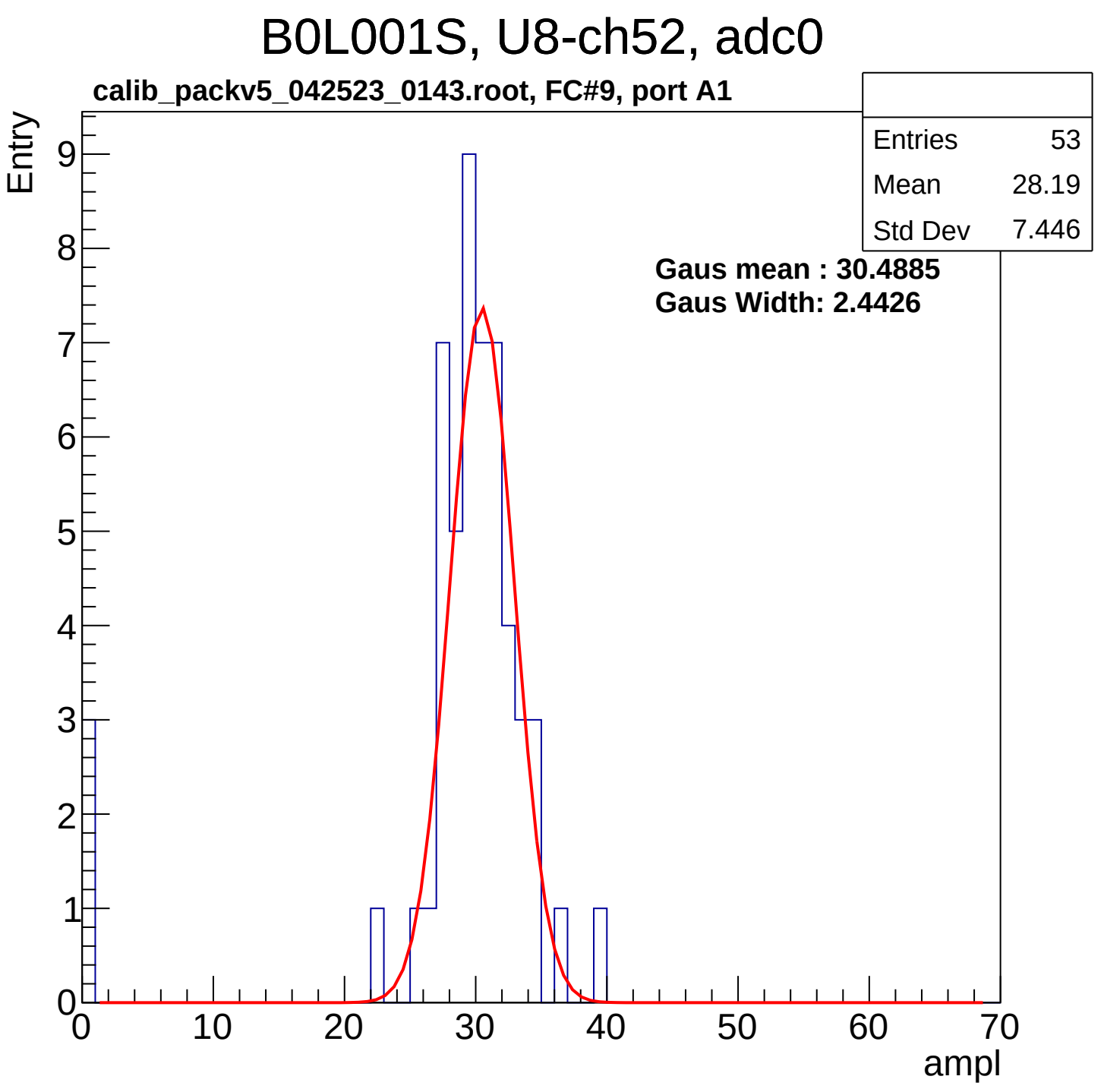

B0L001S, U8-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.54
Std Dev 3.558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

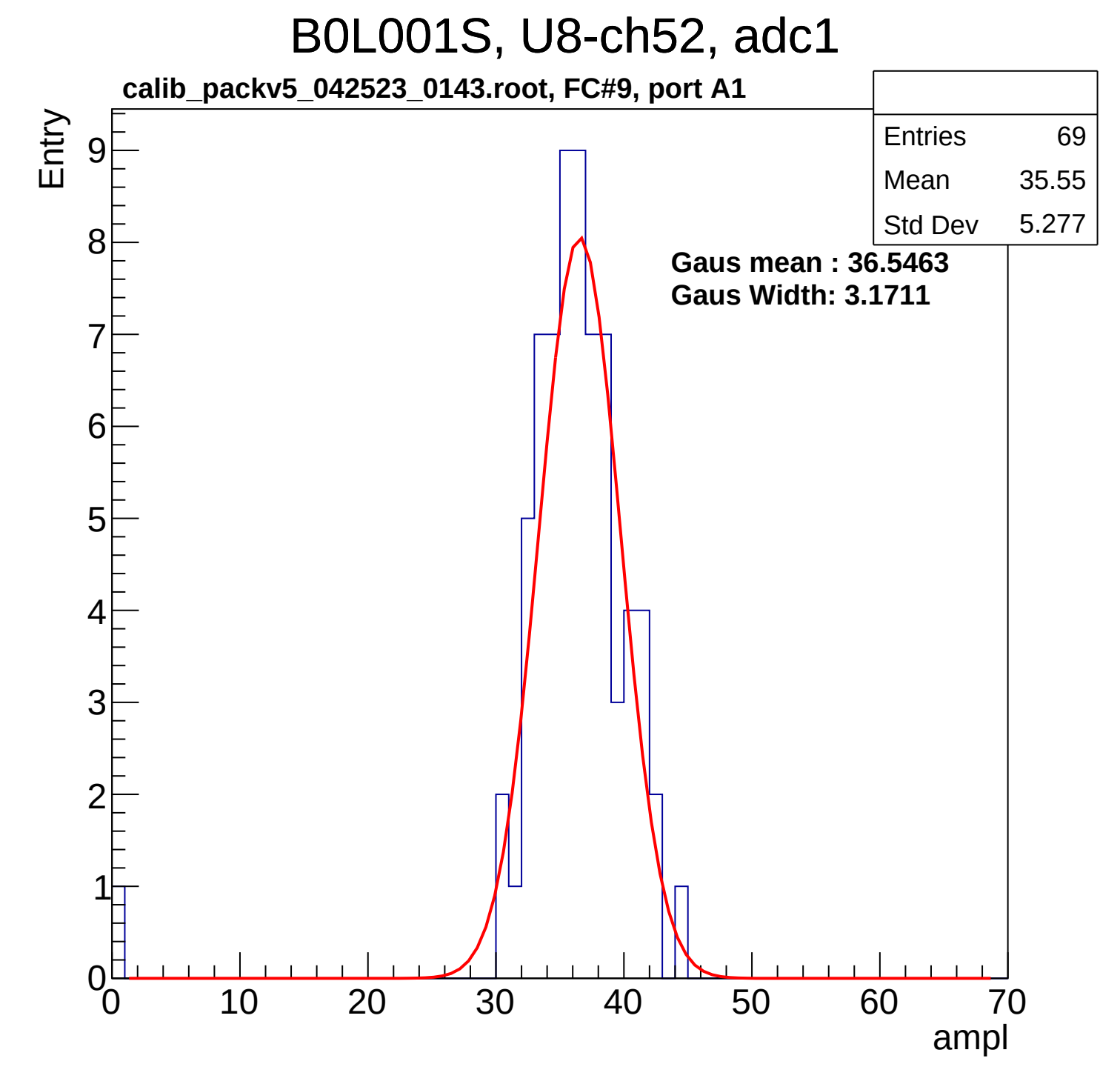

B0L001S, U8-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.29
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

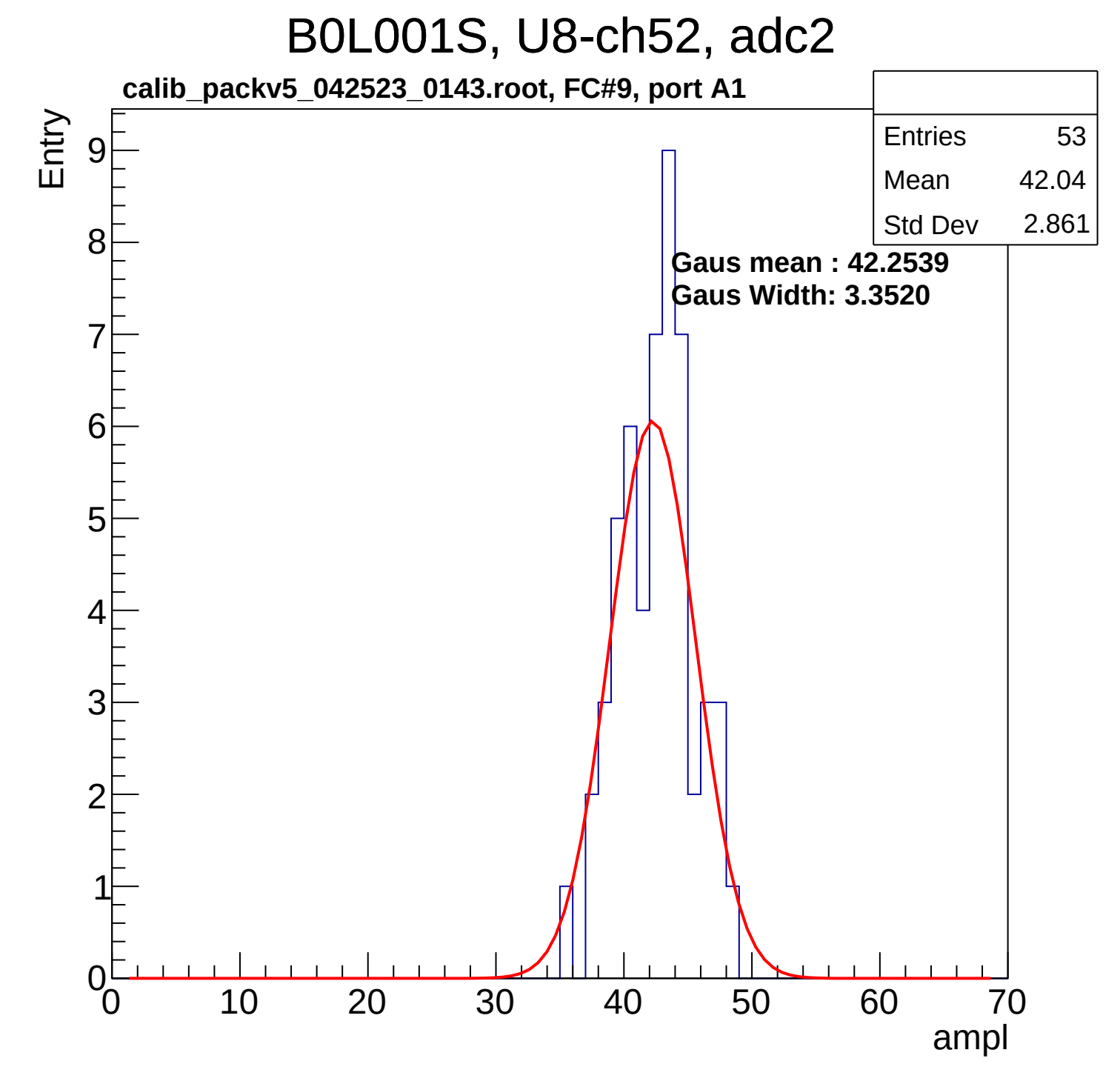

B0L001S, U8-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.93
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

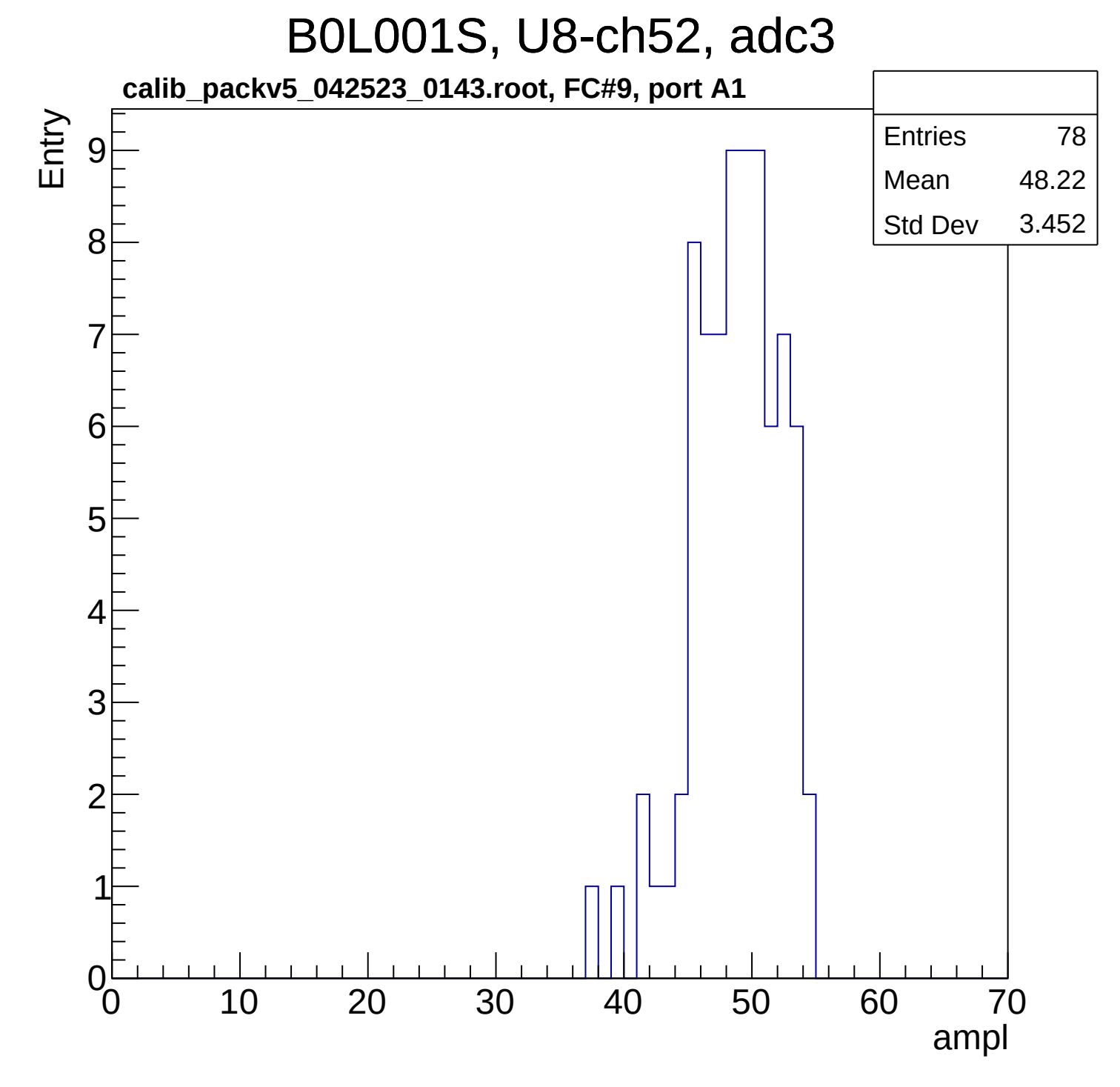

B0L001S, U8-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 28.19
Std Dev 7.446
Gaus mean : 30.4885
Gaus Width: 2.4426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 35.55
Std Dev 5.277
Gaus mean : 36.5463
Gaus Width: 3.1711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 42.04
Std Dev 2.861
Gaus mean : 42.2539
Gaus Width: 3.3520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 48.22
Std Dev 3.452
Entry
ampl
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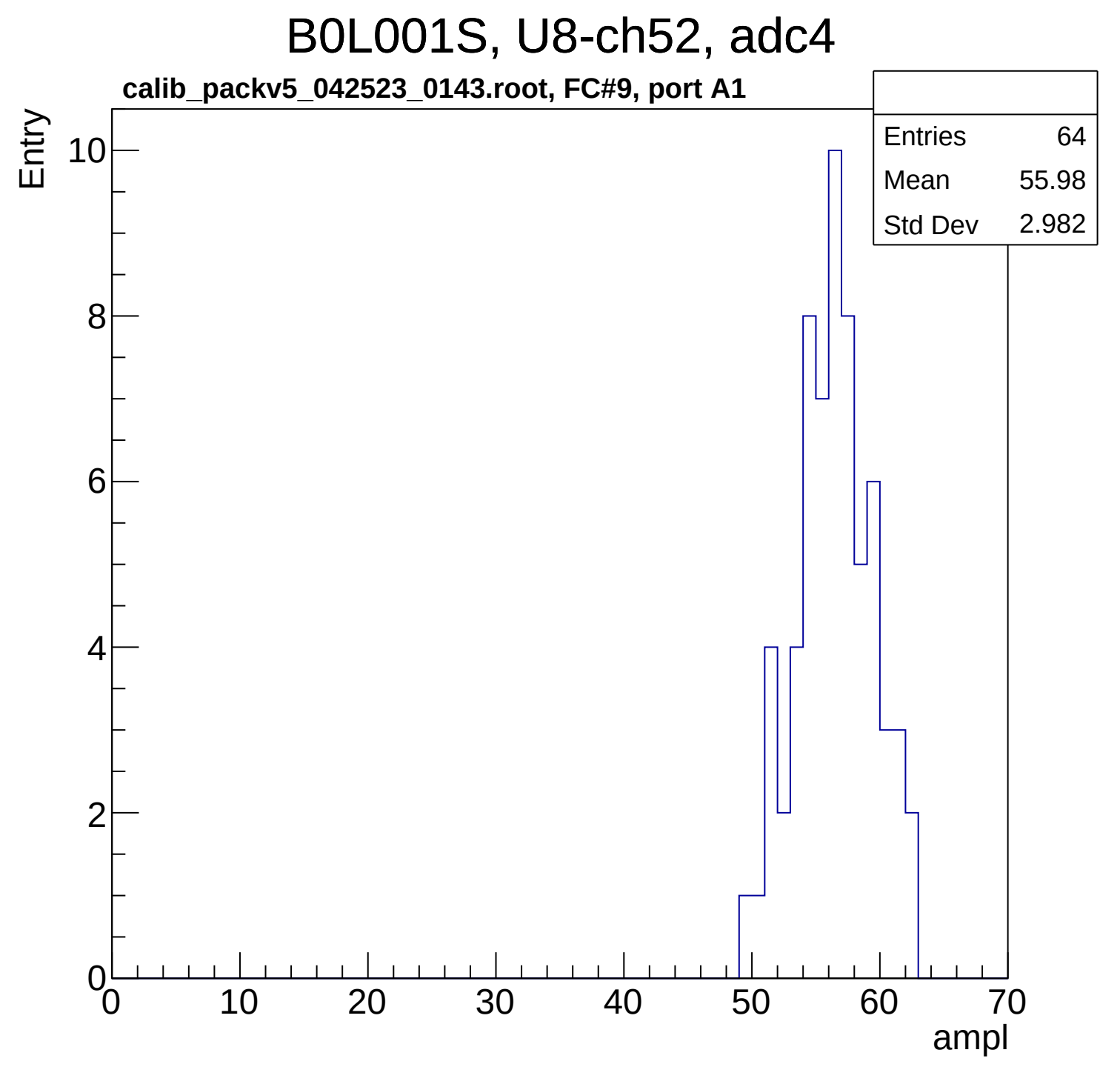
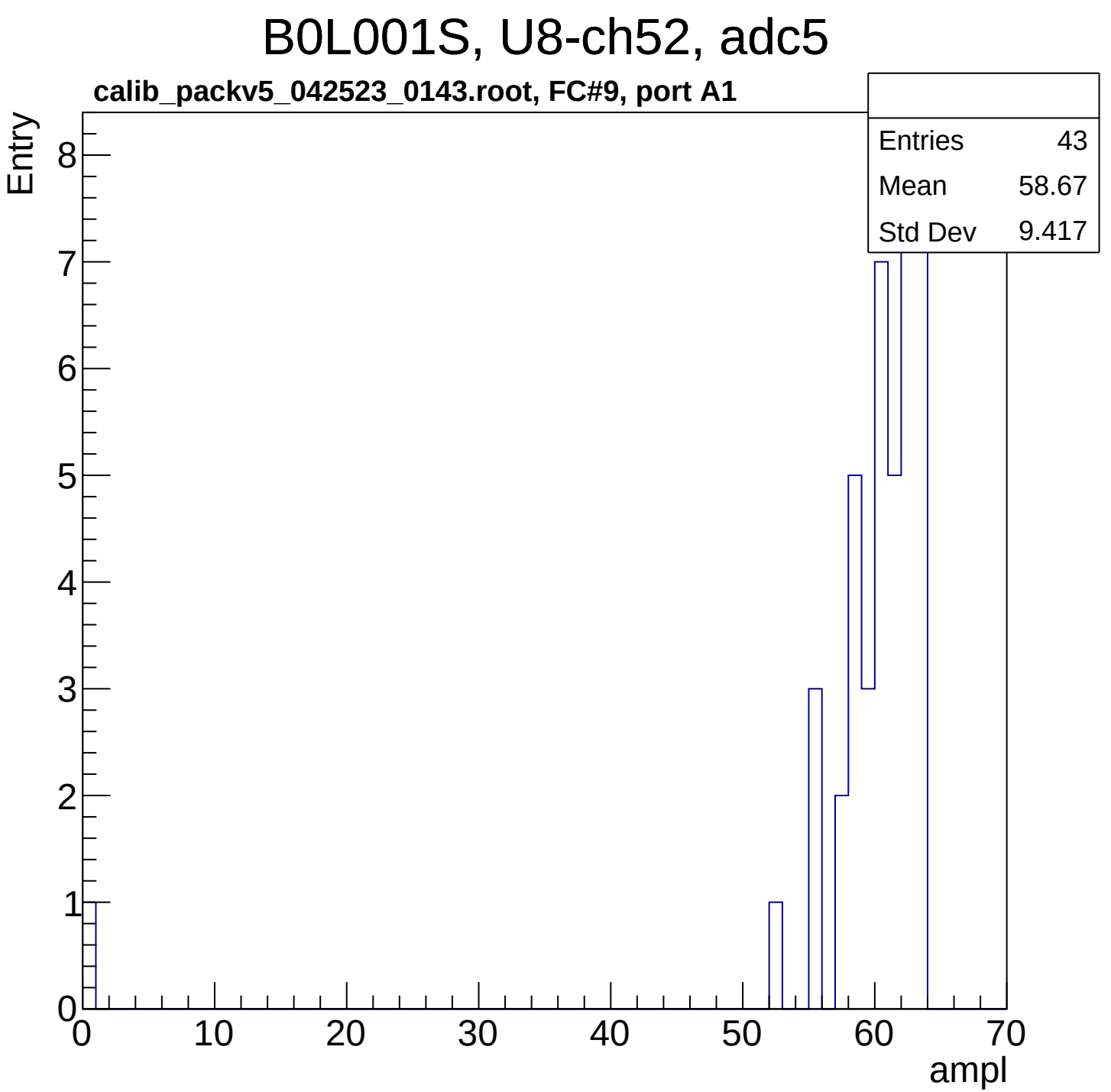
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.98
Std Dev 2.982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.67
Std Dev 9.417
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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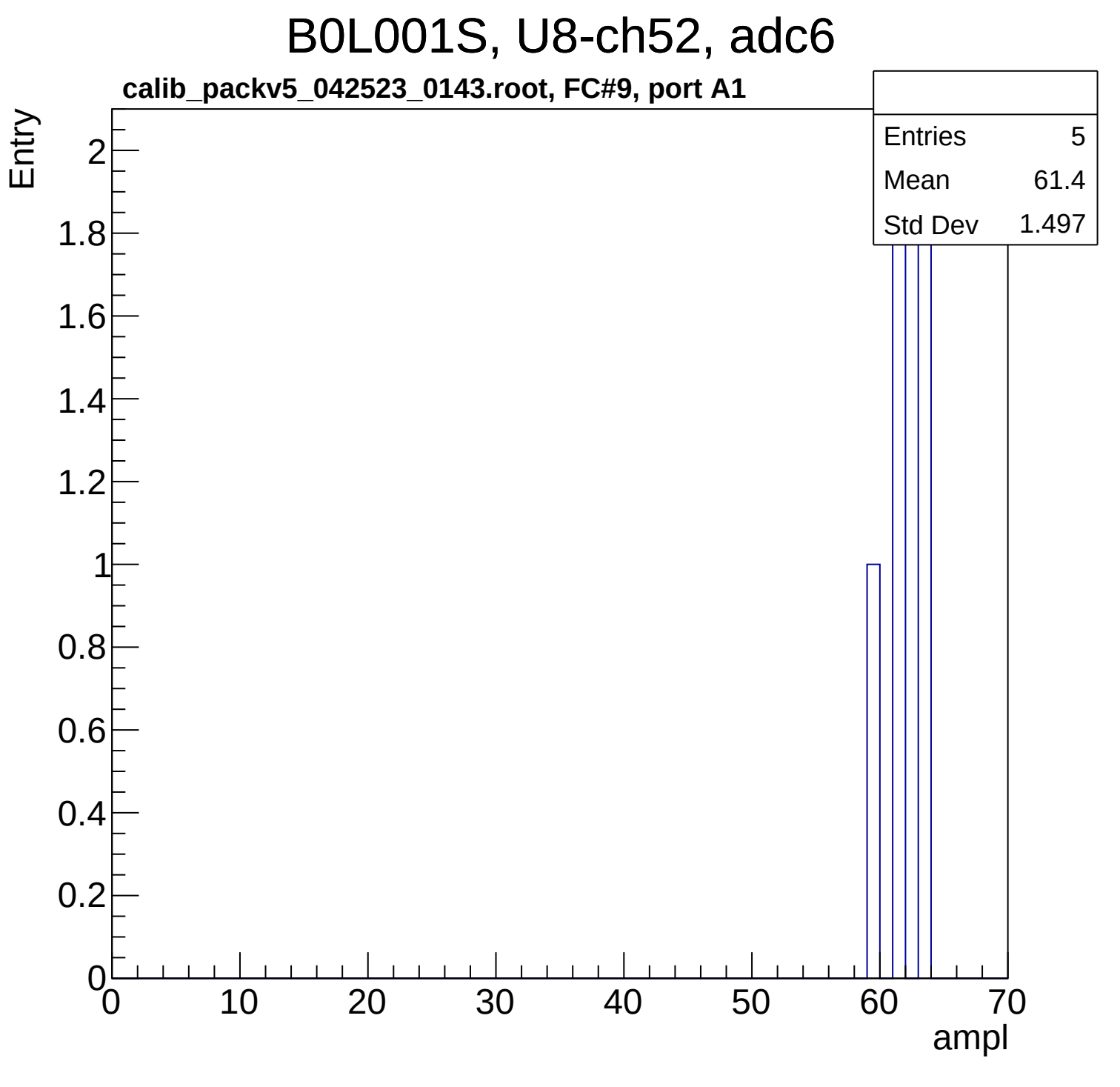
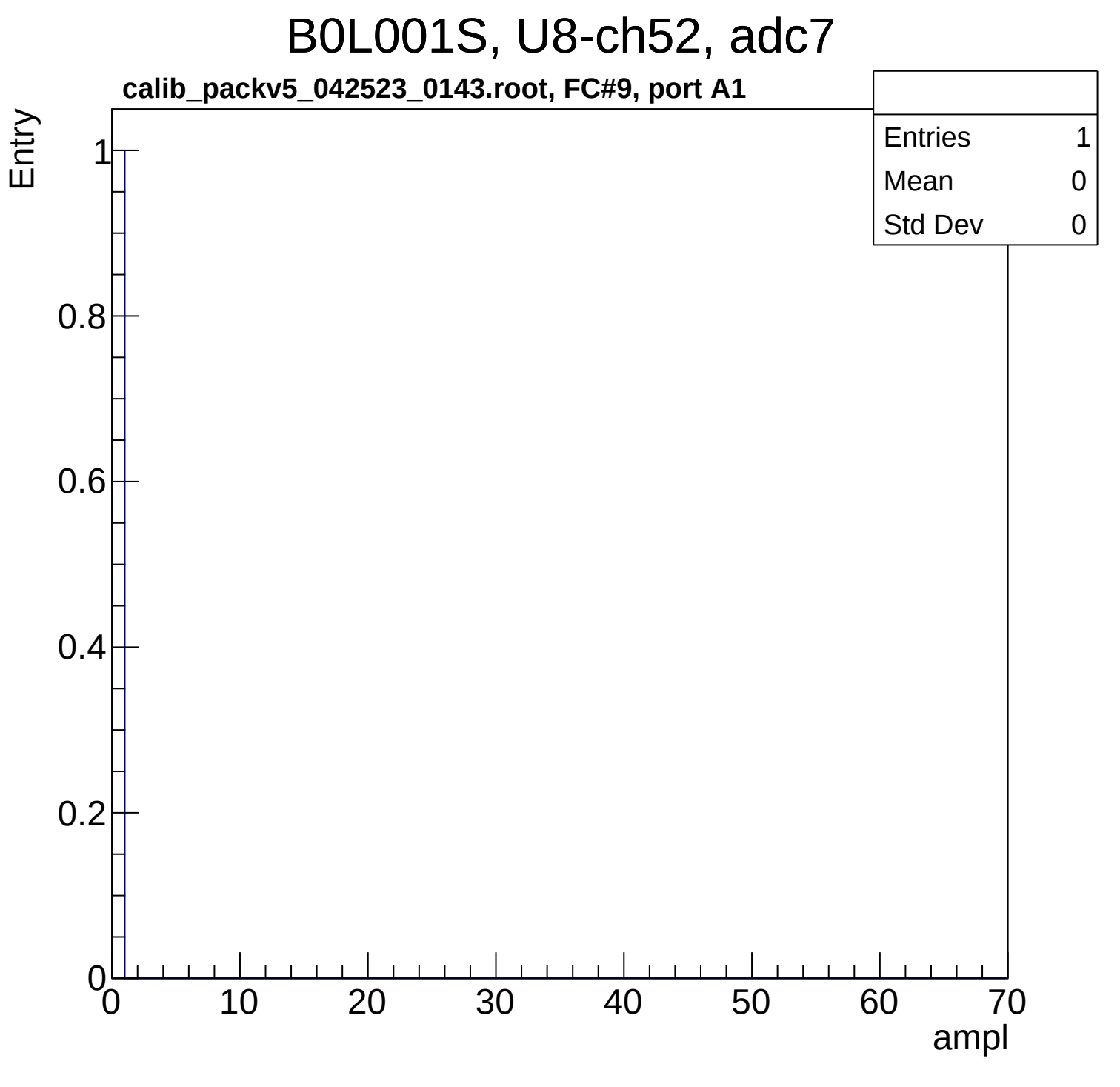
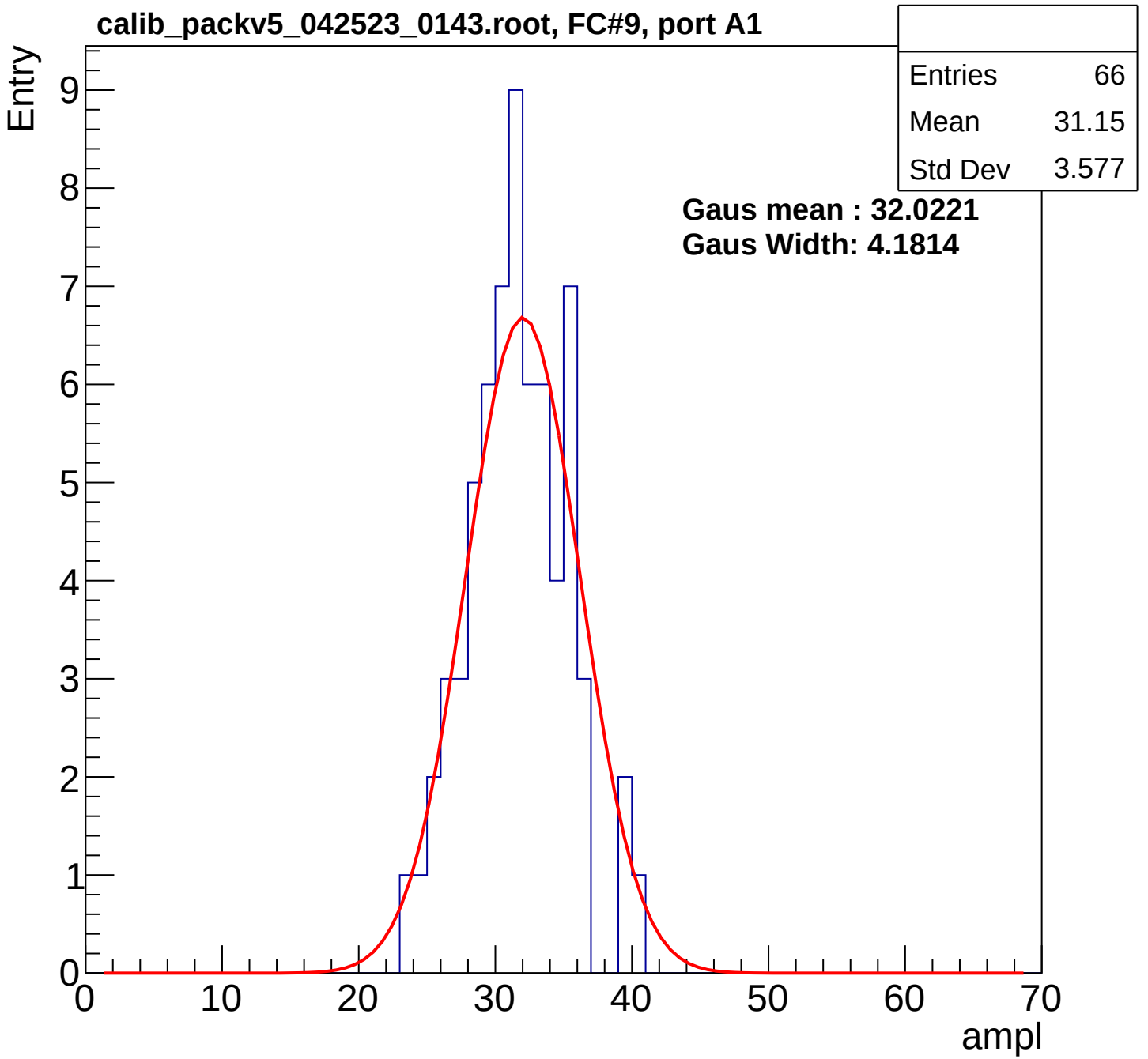
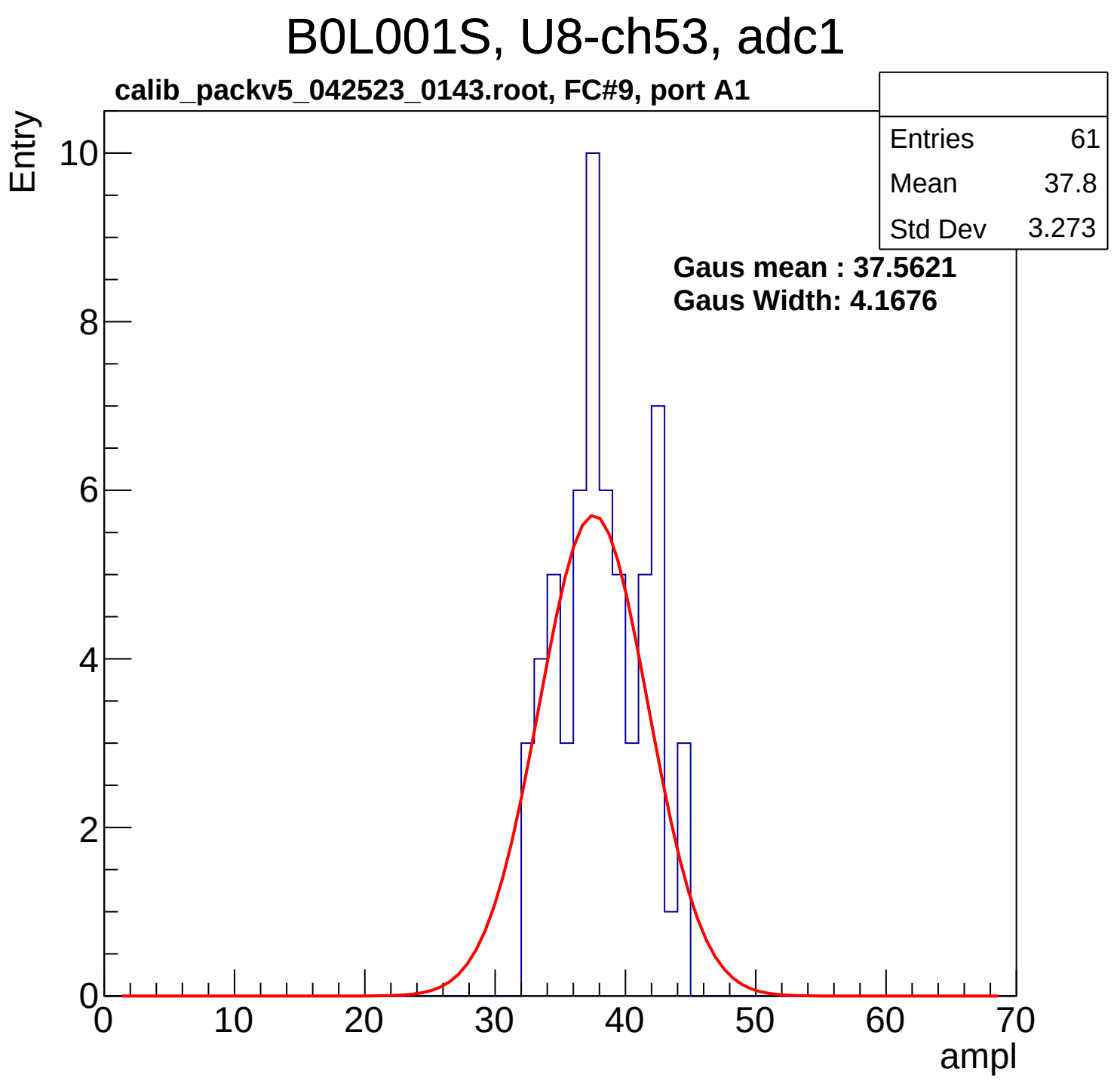
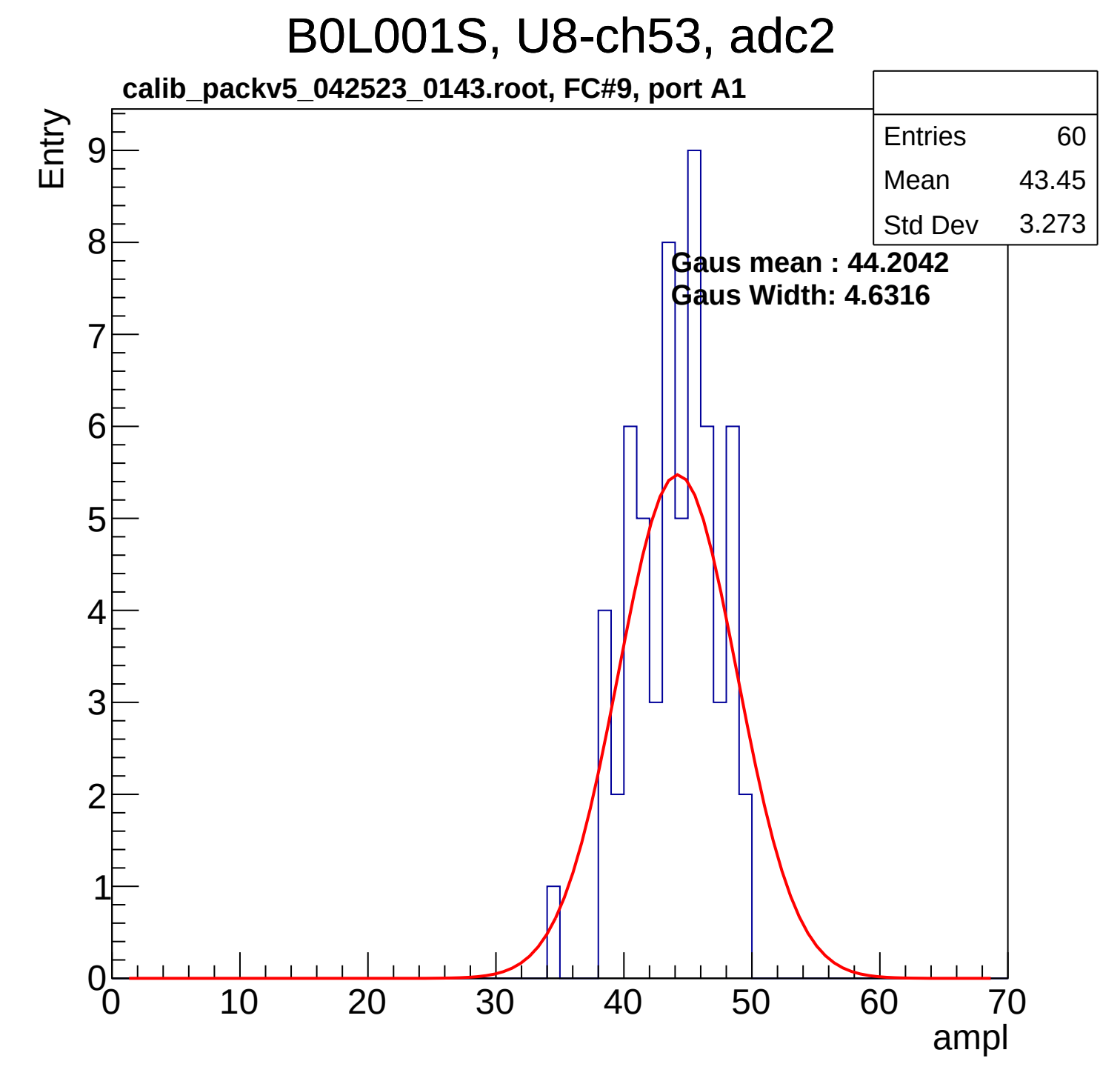
60
70

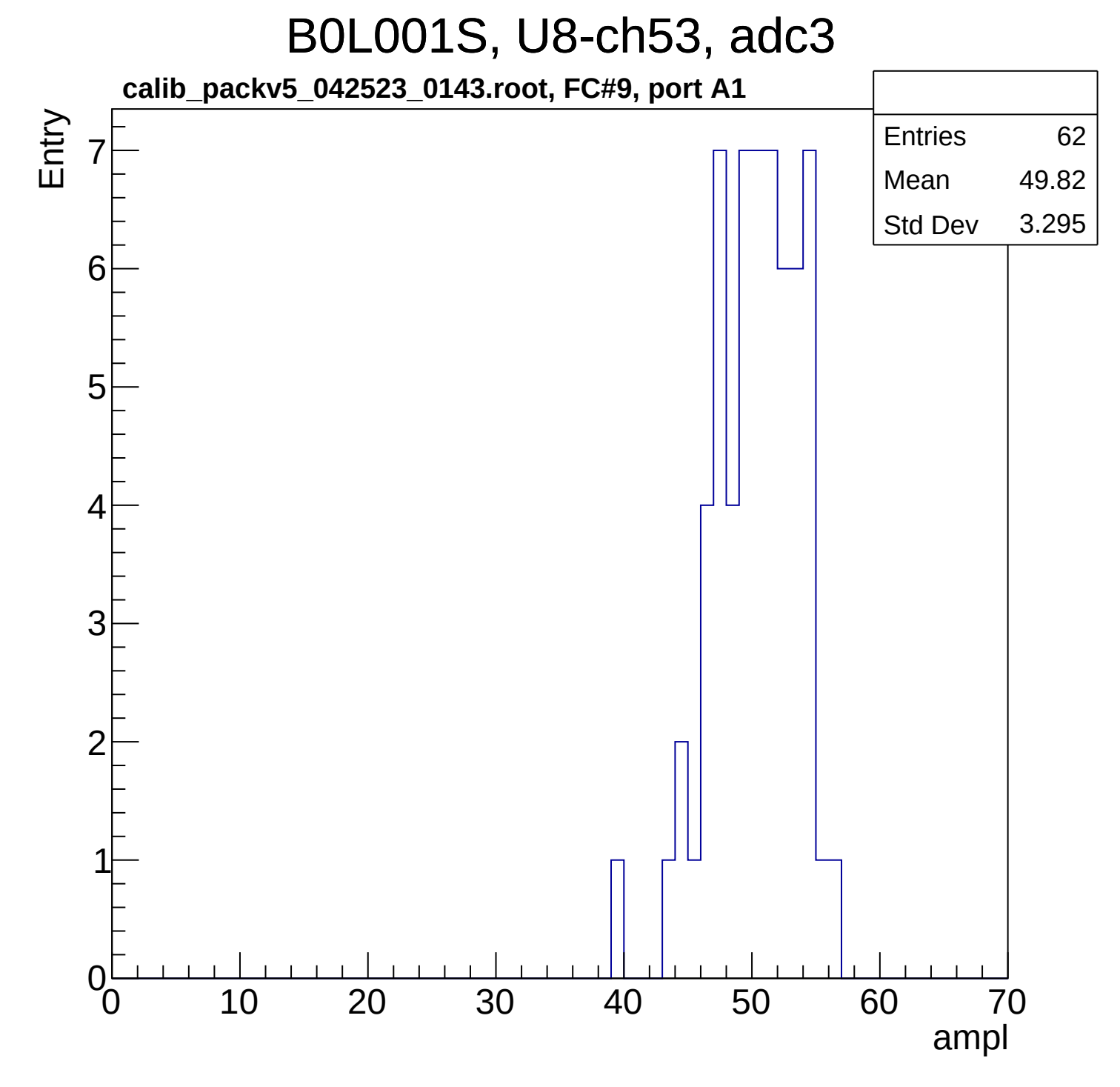

B0L001S, U8-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

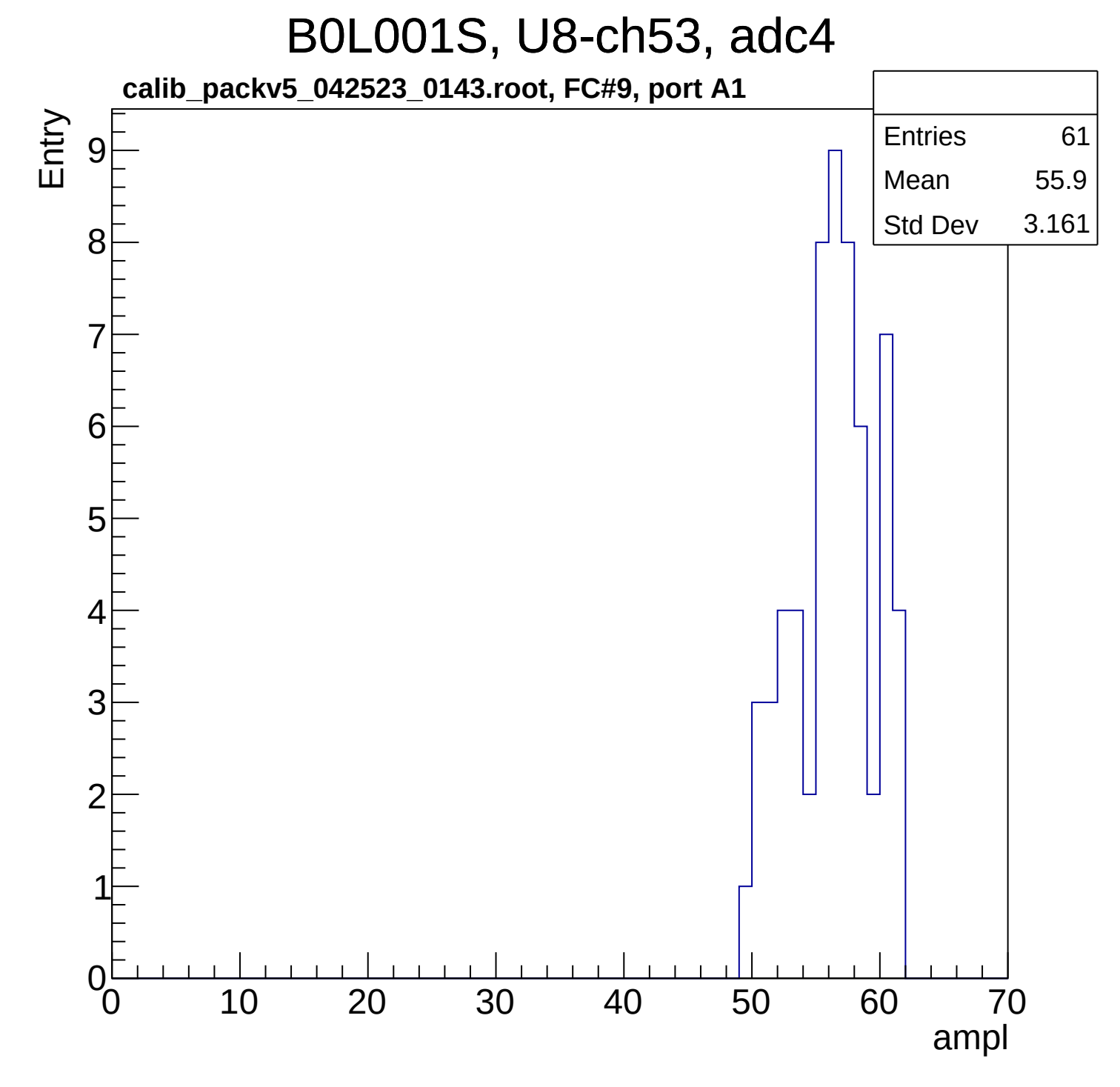

B0L001S, U8-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

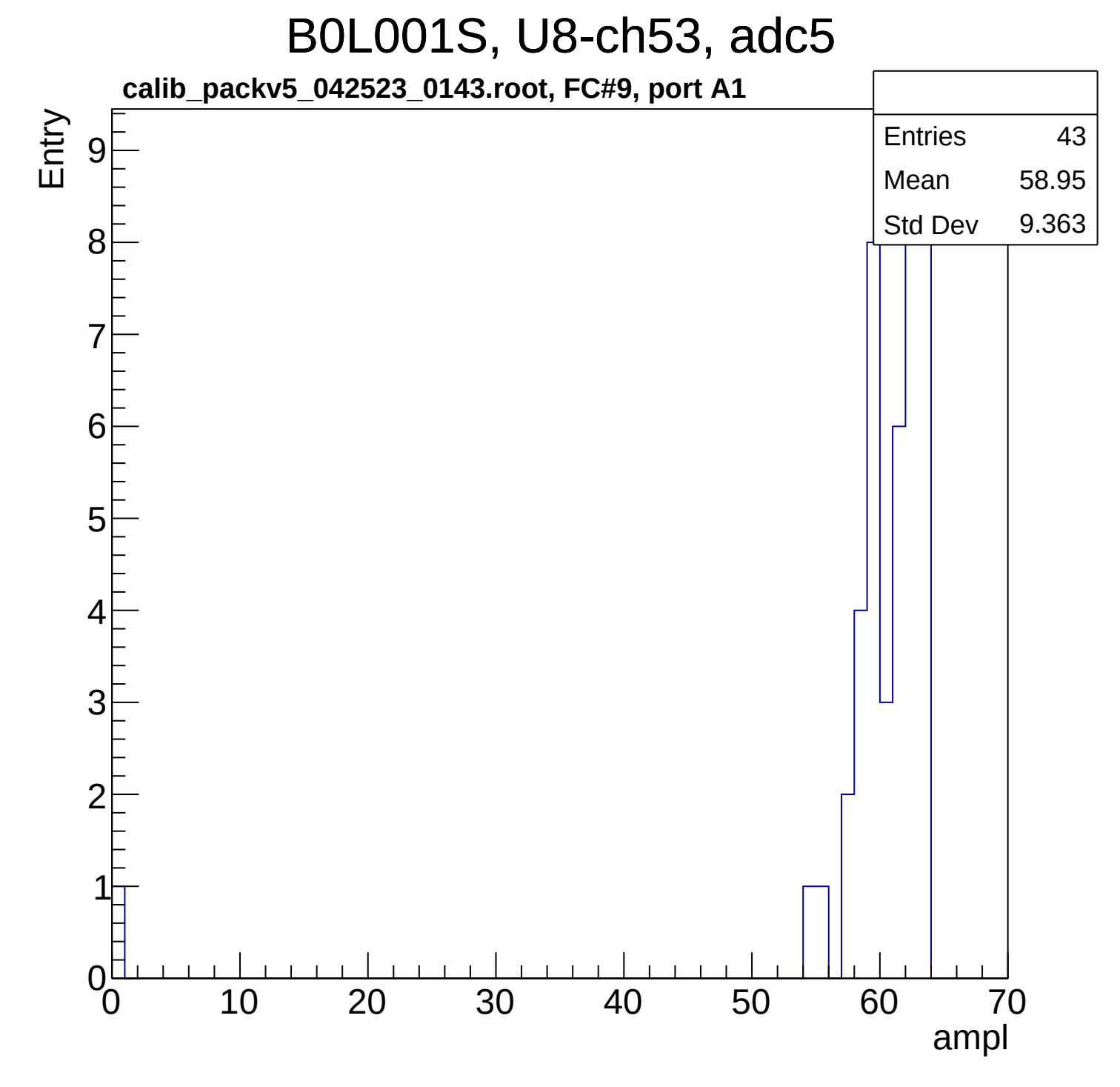

B0L001S, U8-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.15
Std Dev 3.577
Gaus mean : 32.0221
Gaus Width: 4.1814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.8
Std Dev 3.273
Gaus mean : 37.5621
Gaus Width: 4.1676
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.45
Std Dev 3.273
Gaus mean : 44.2042
Gaus Width: 4.6316
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 49.82
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 55.9
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.95
Std Dev 9.363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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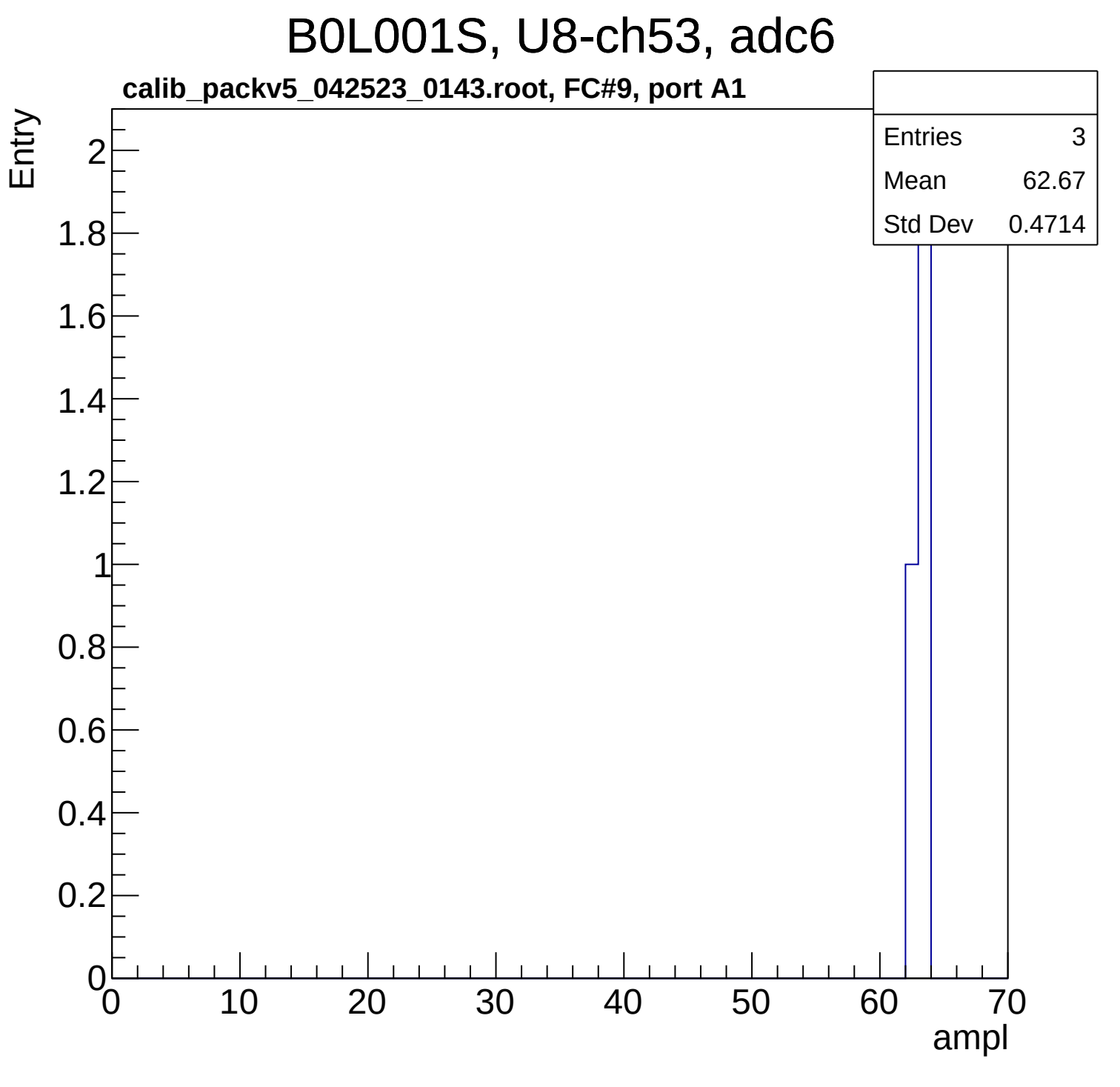
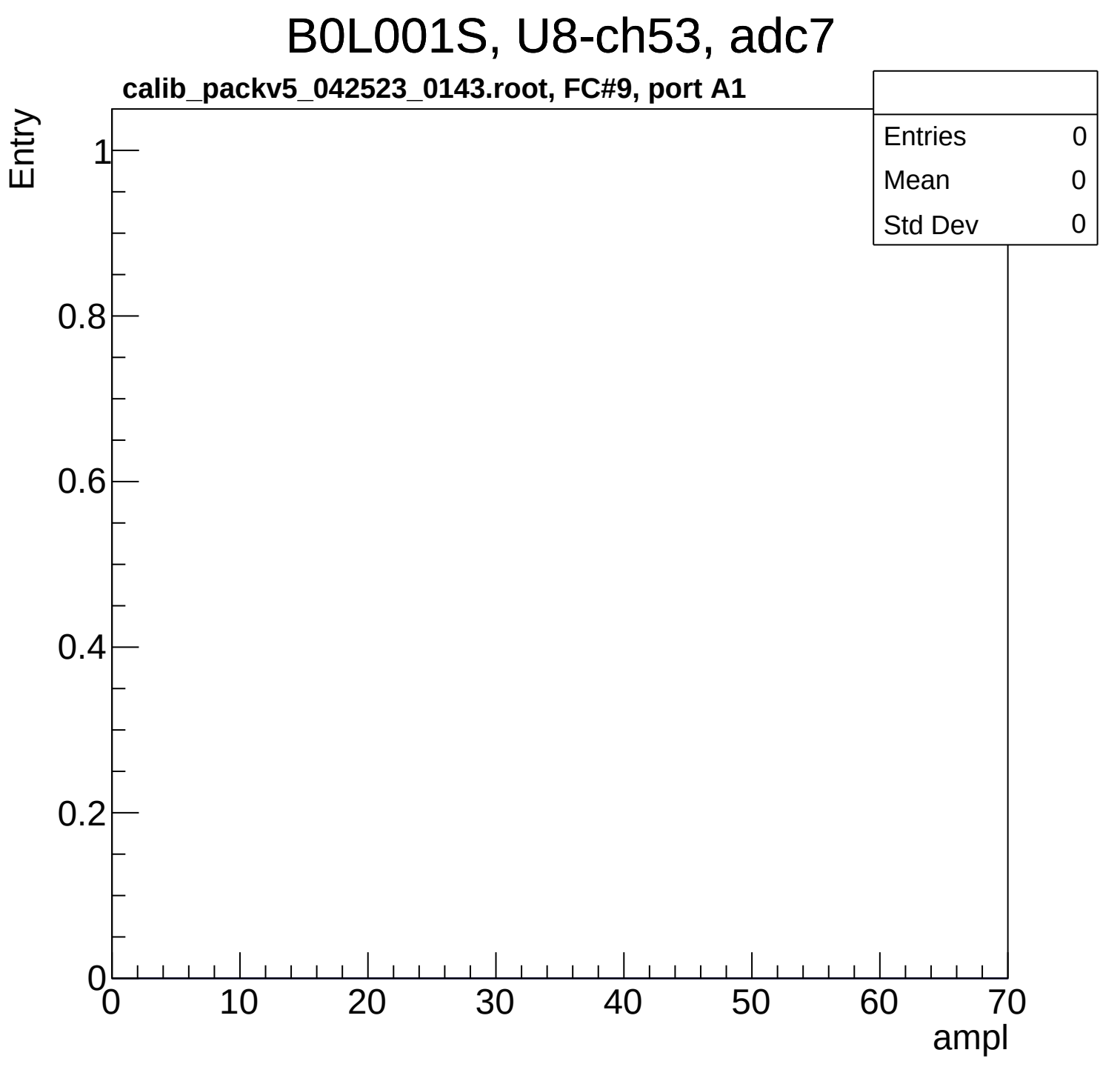
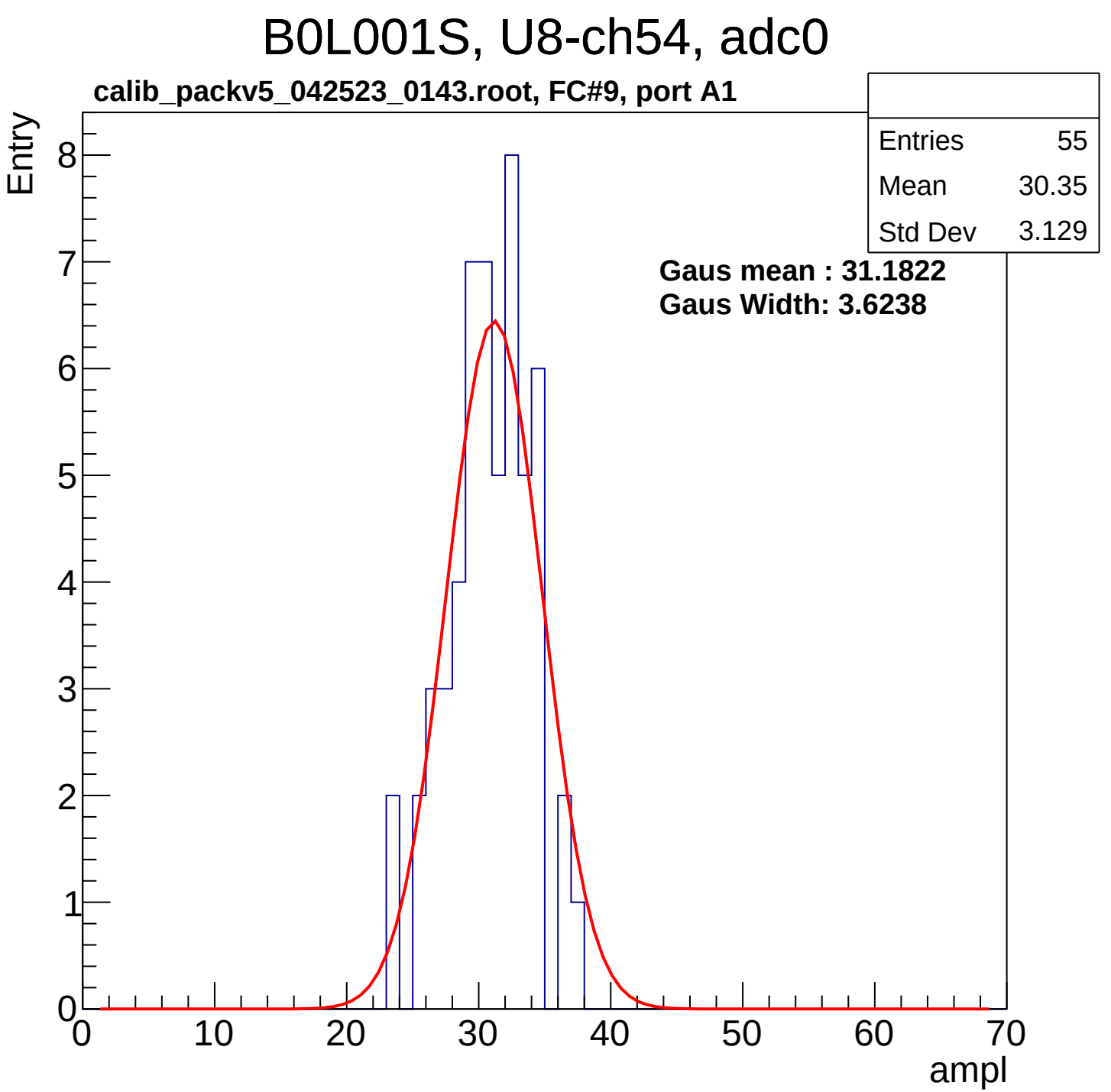
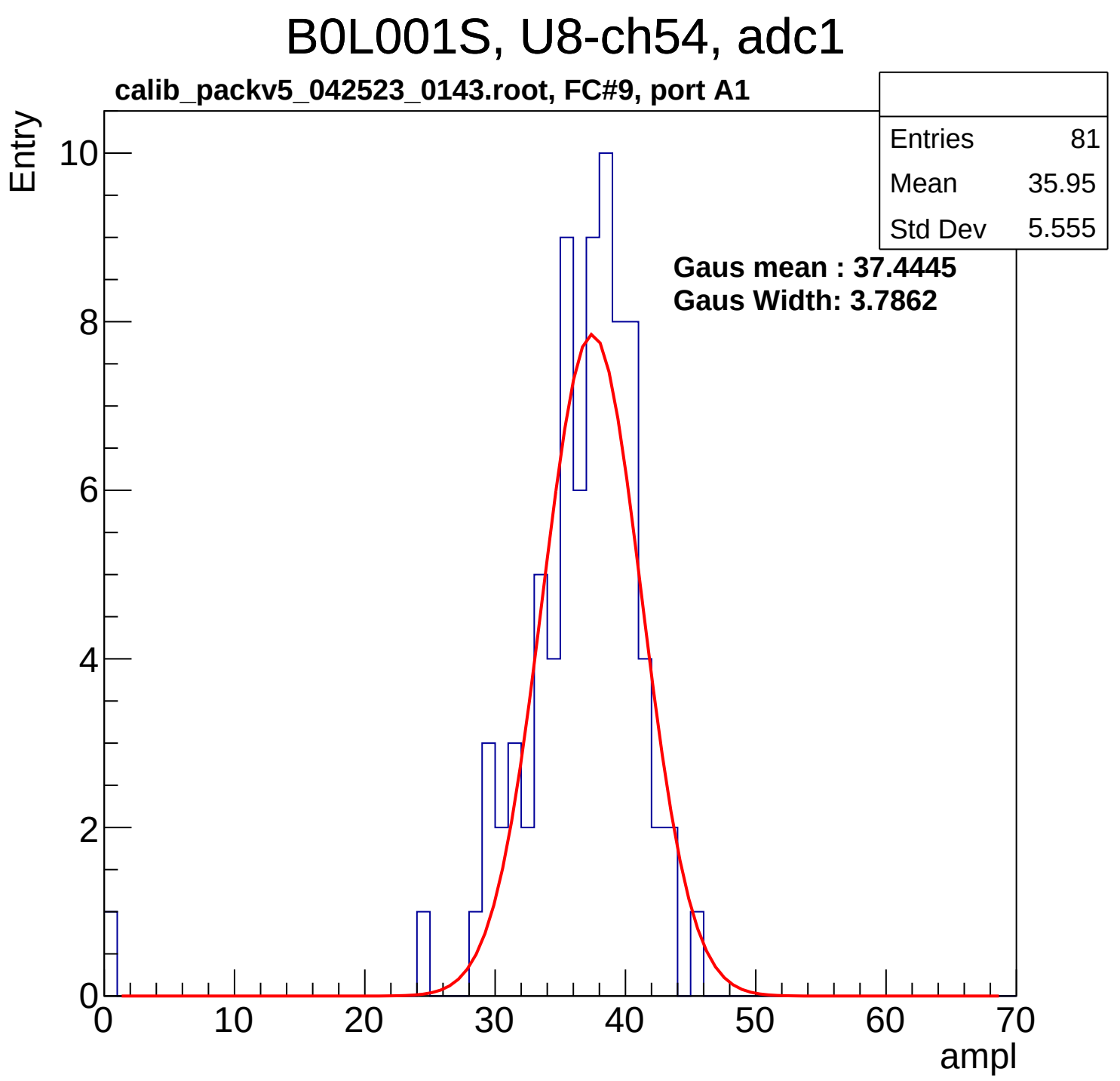

B0L001S, U8-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.35
Std Dev 3.129
Gaus mean : 31.1822
Gaus Width: 3.6238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 35.95
Std Dev 5.555
Gaus mean : 37.4445
Gaus Width: 3.7862
Entry
ampl
0
2
4
6
8
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0
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40
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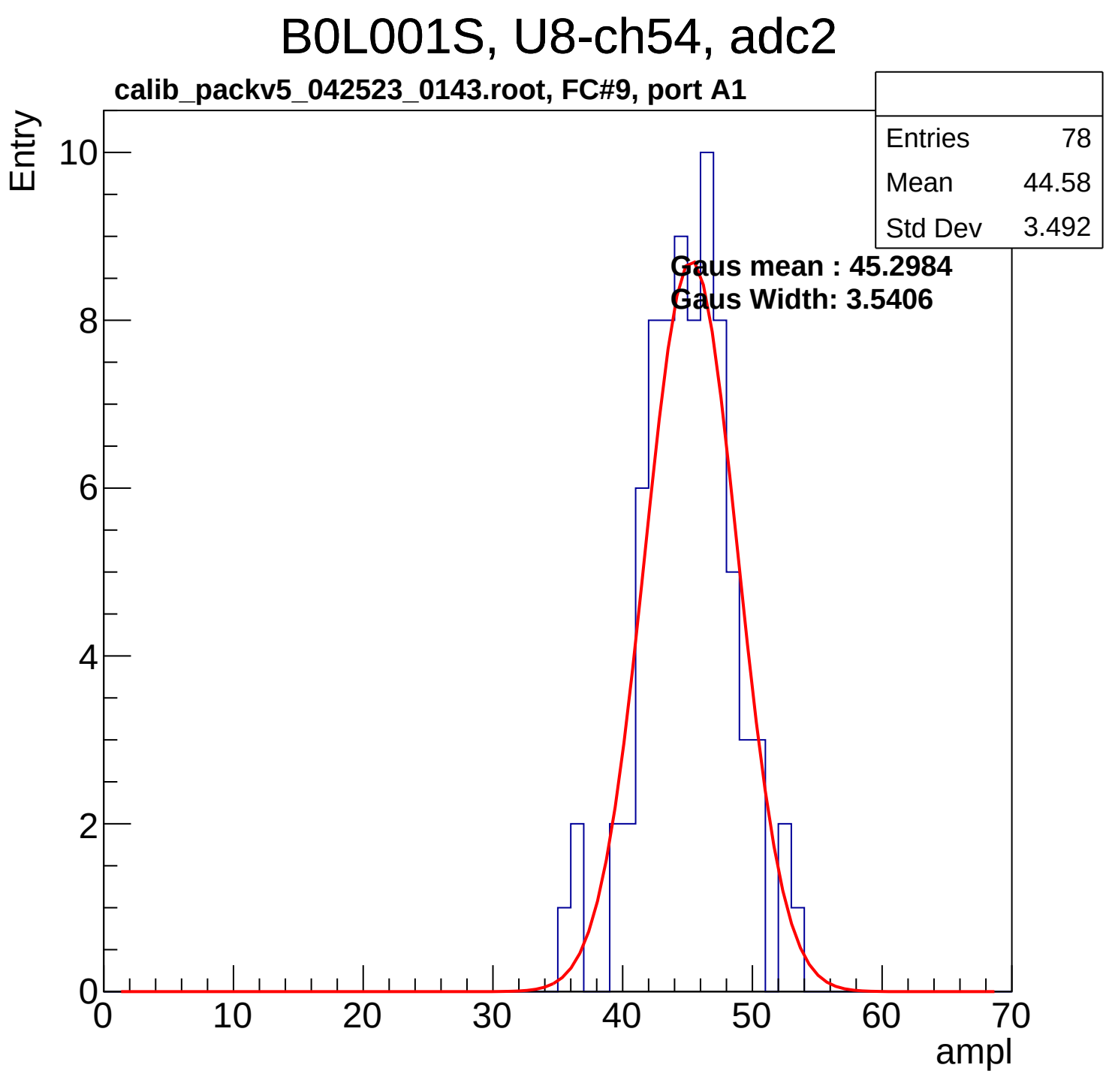
B0L001S, U8-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.58
Std Dev 3.492
Gaus mean : 45.2984
Gaus Width: 3.5406
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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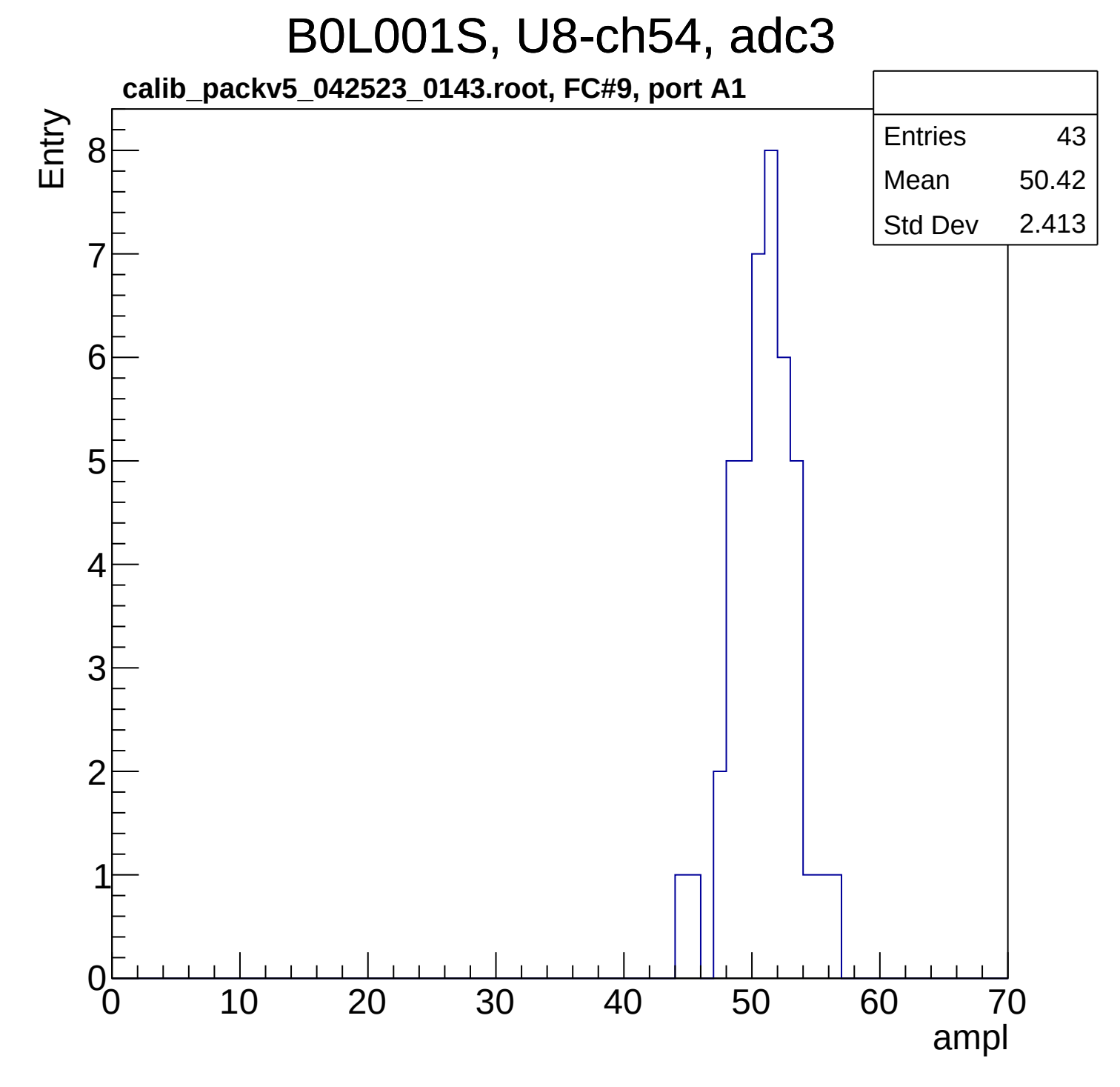
B0L001S, U8-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 50.42
Std Dev 2.413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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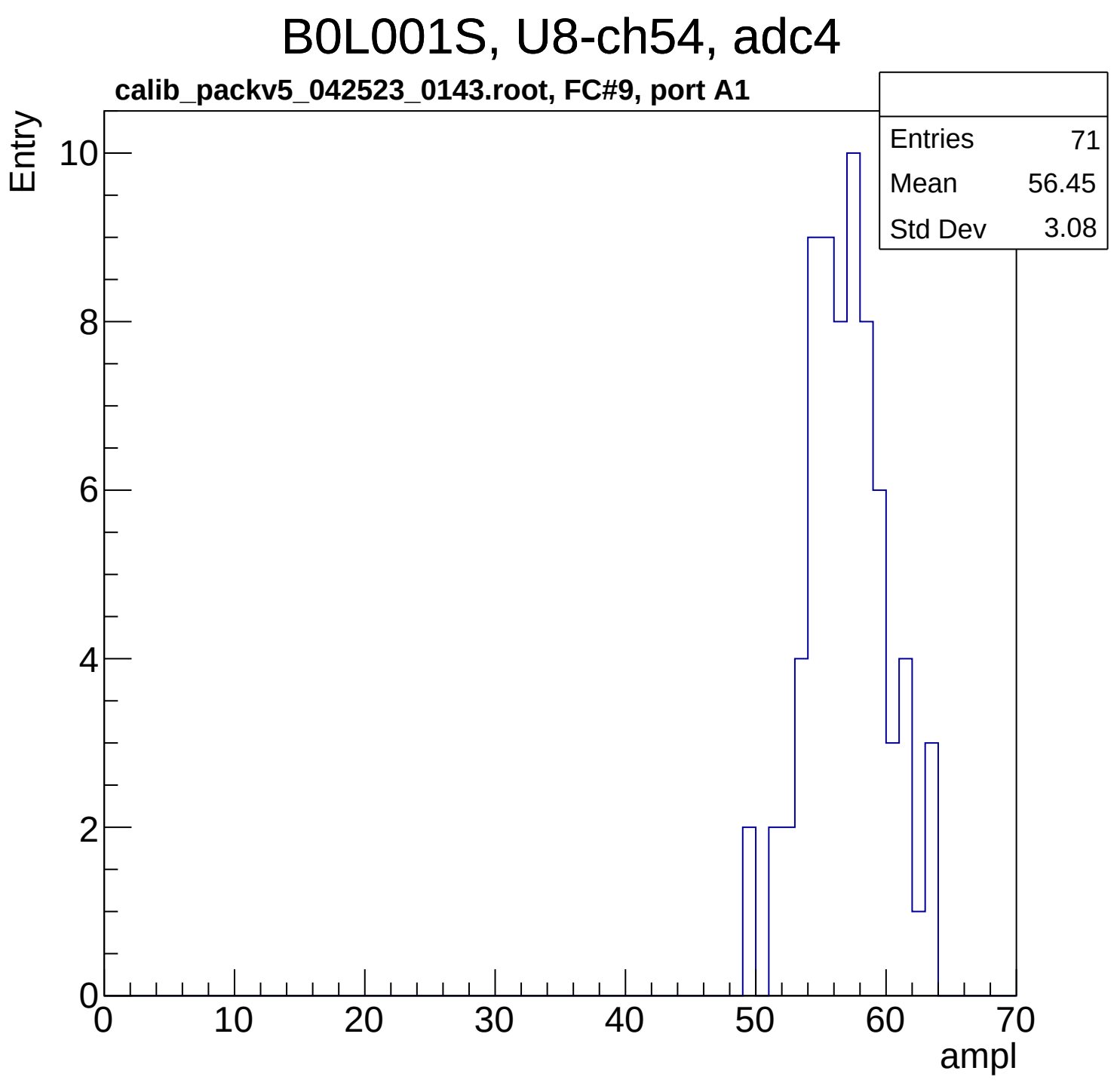
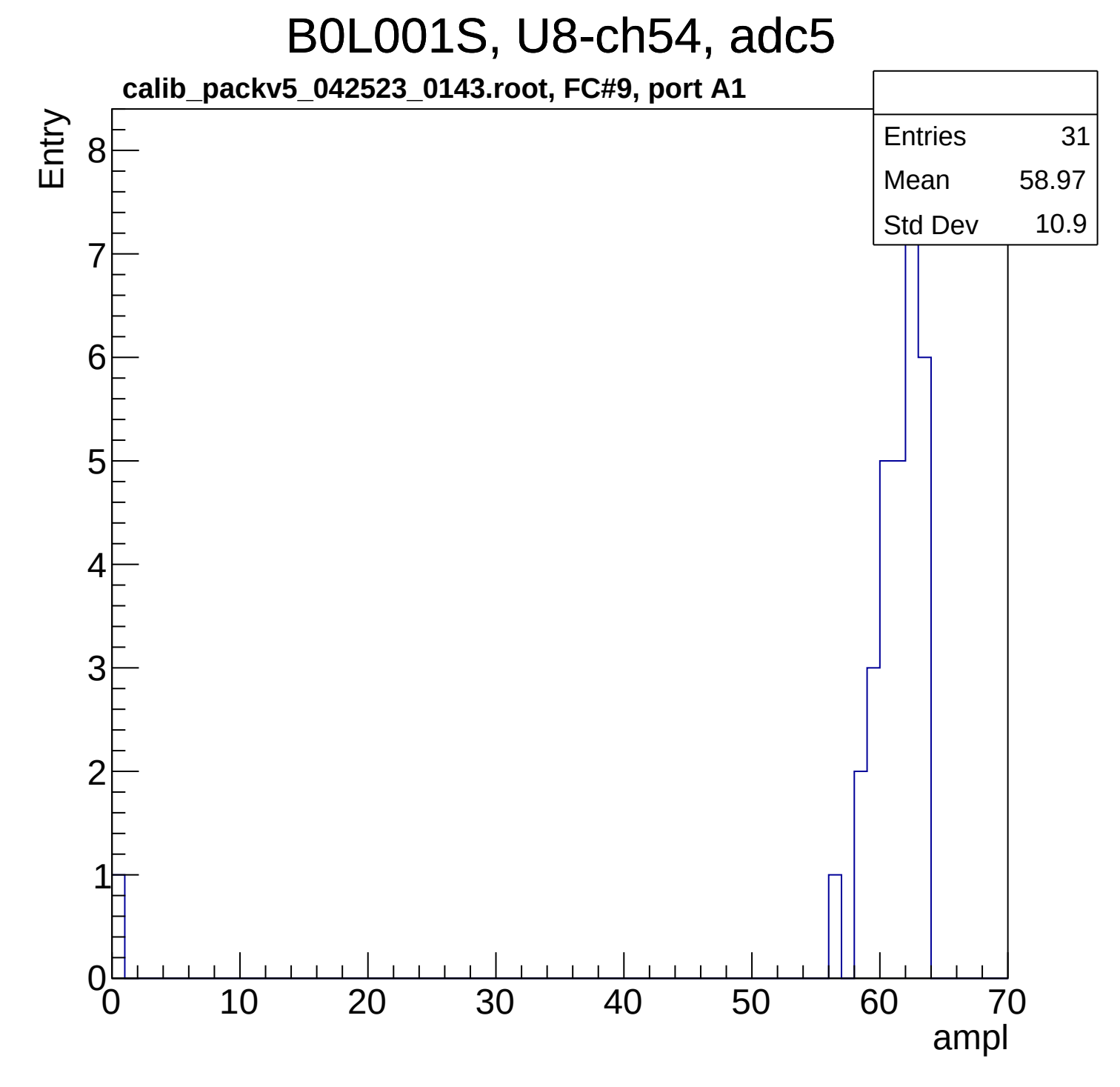

B0L001S, U8-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 56.45
Std Dev 3.08
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.97
Std Dev 10.9
Entry
0
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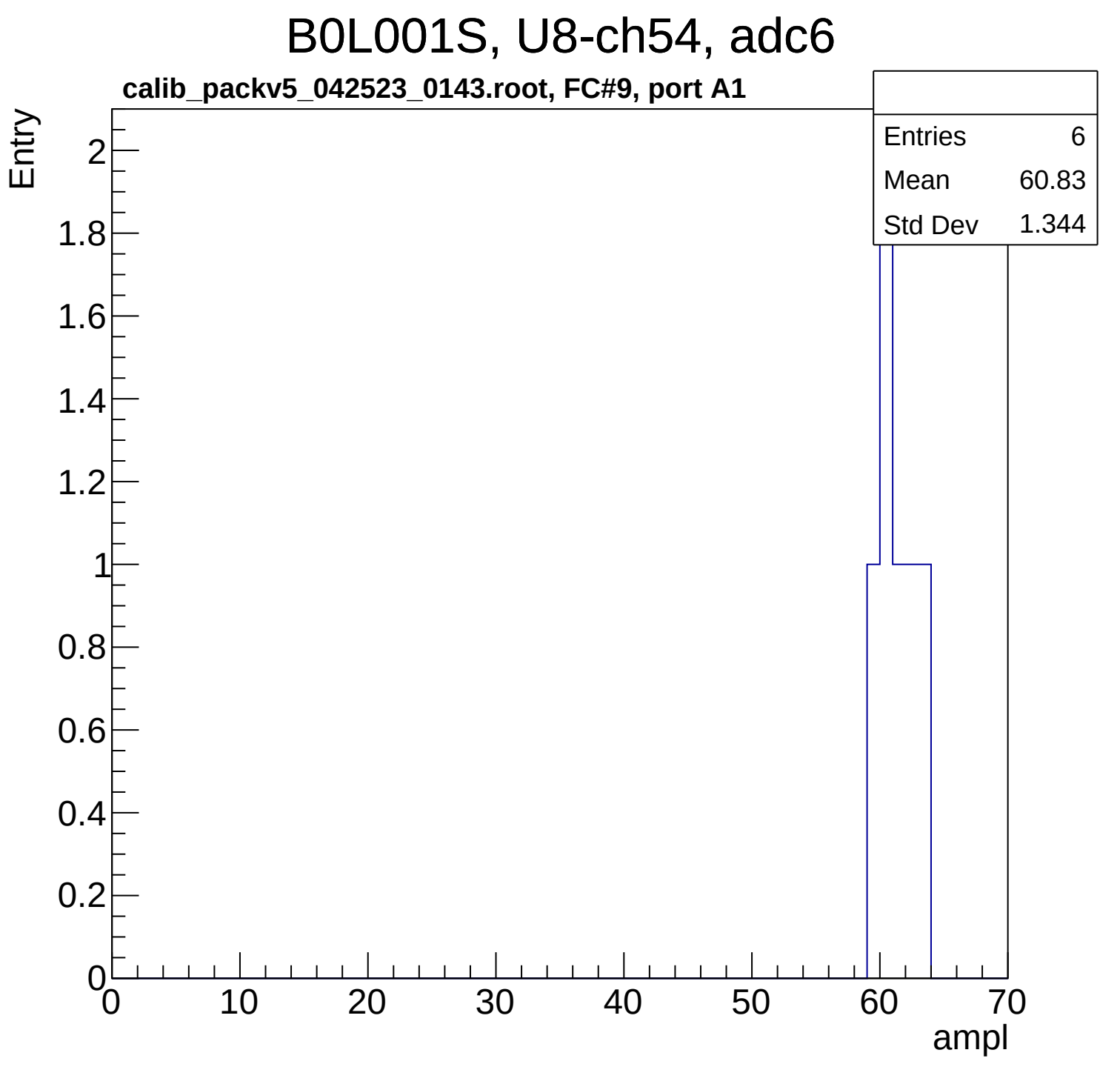
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 60.83
Std Dev 1.344
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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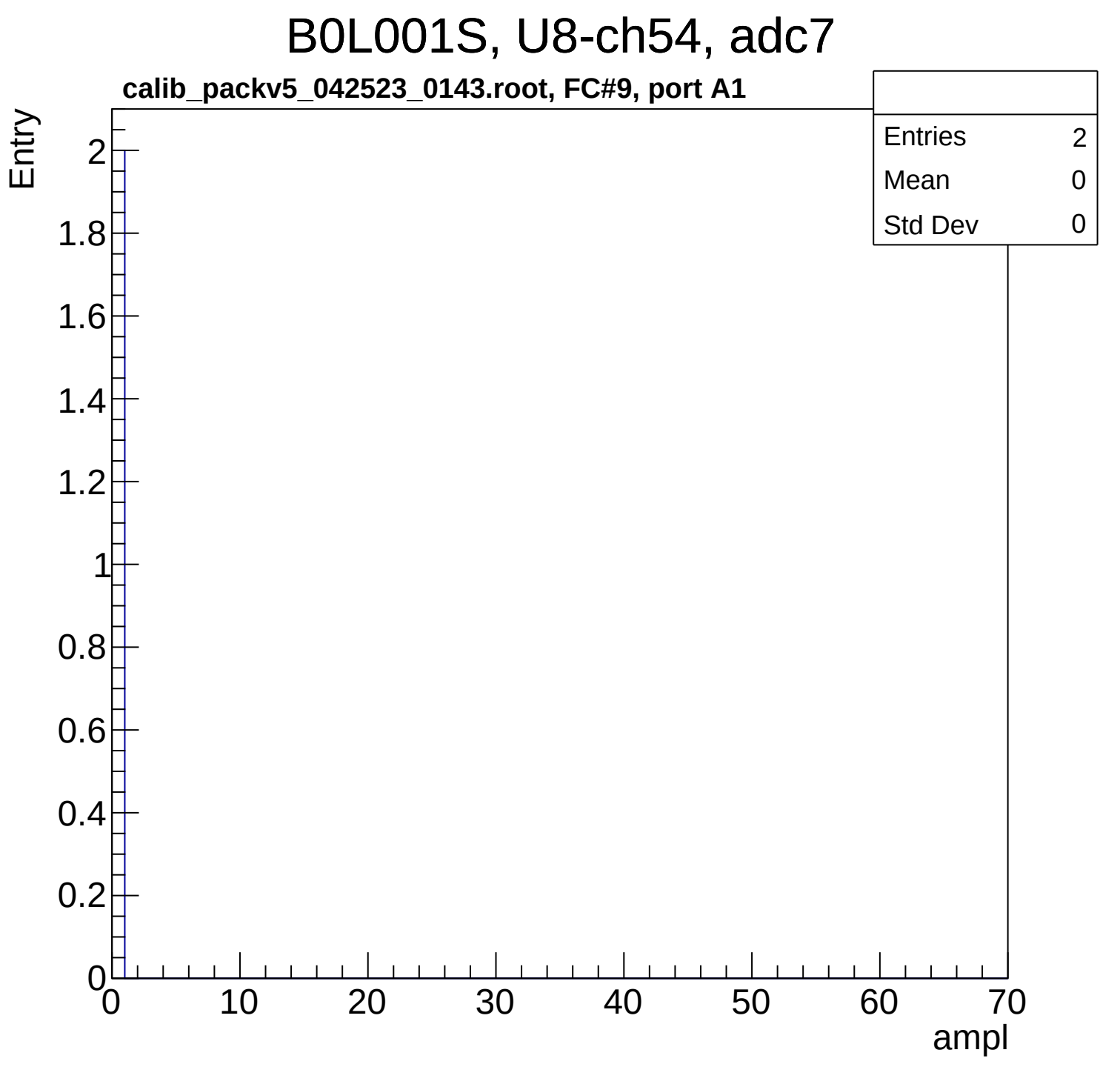
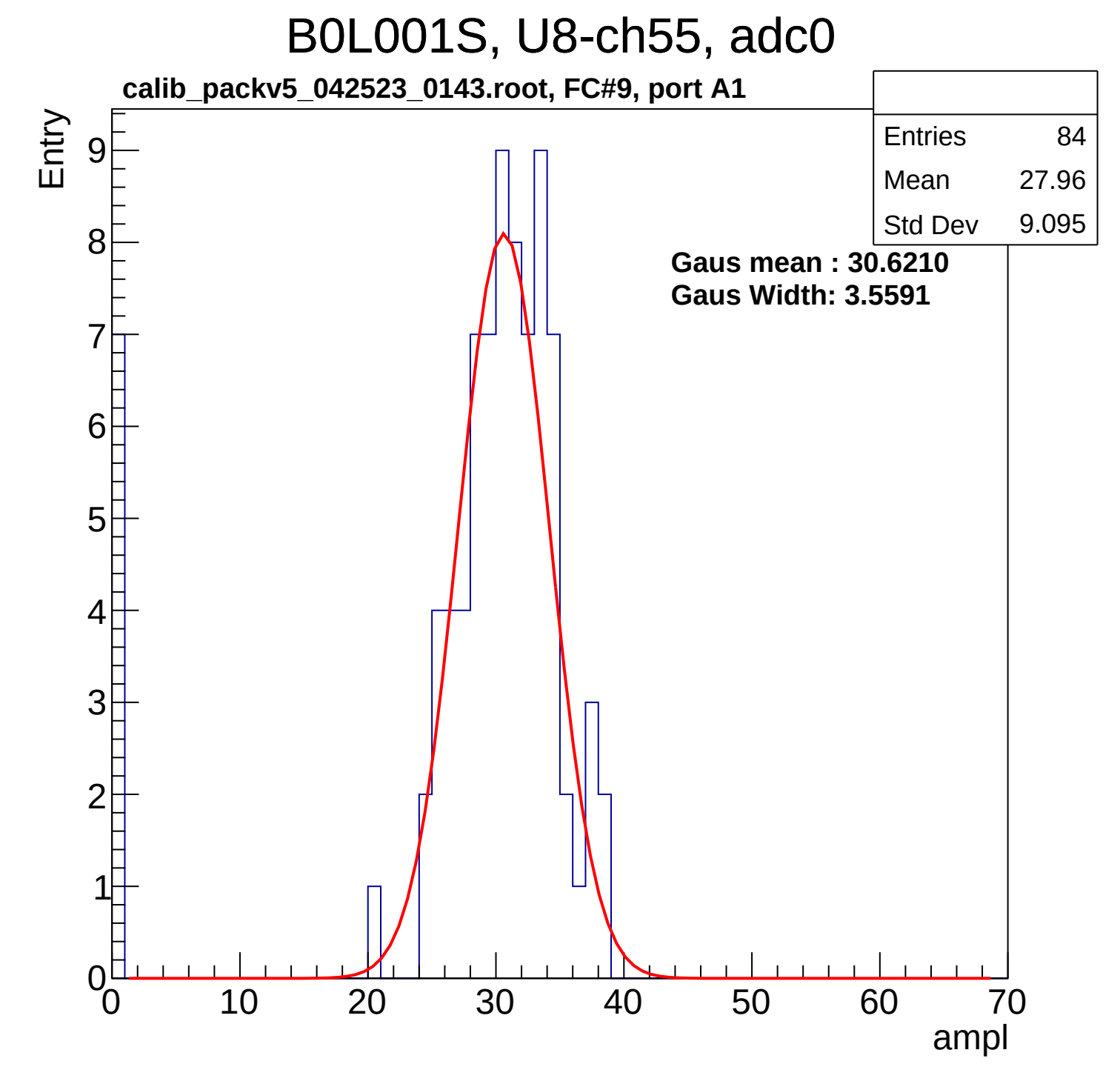

B0L001S, U8-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 27.96
Std Dev 9.095
Gaus mean : 30.6210
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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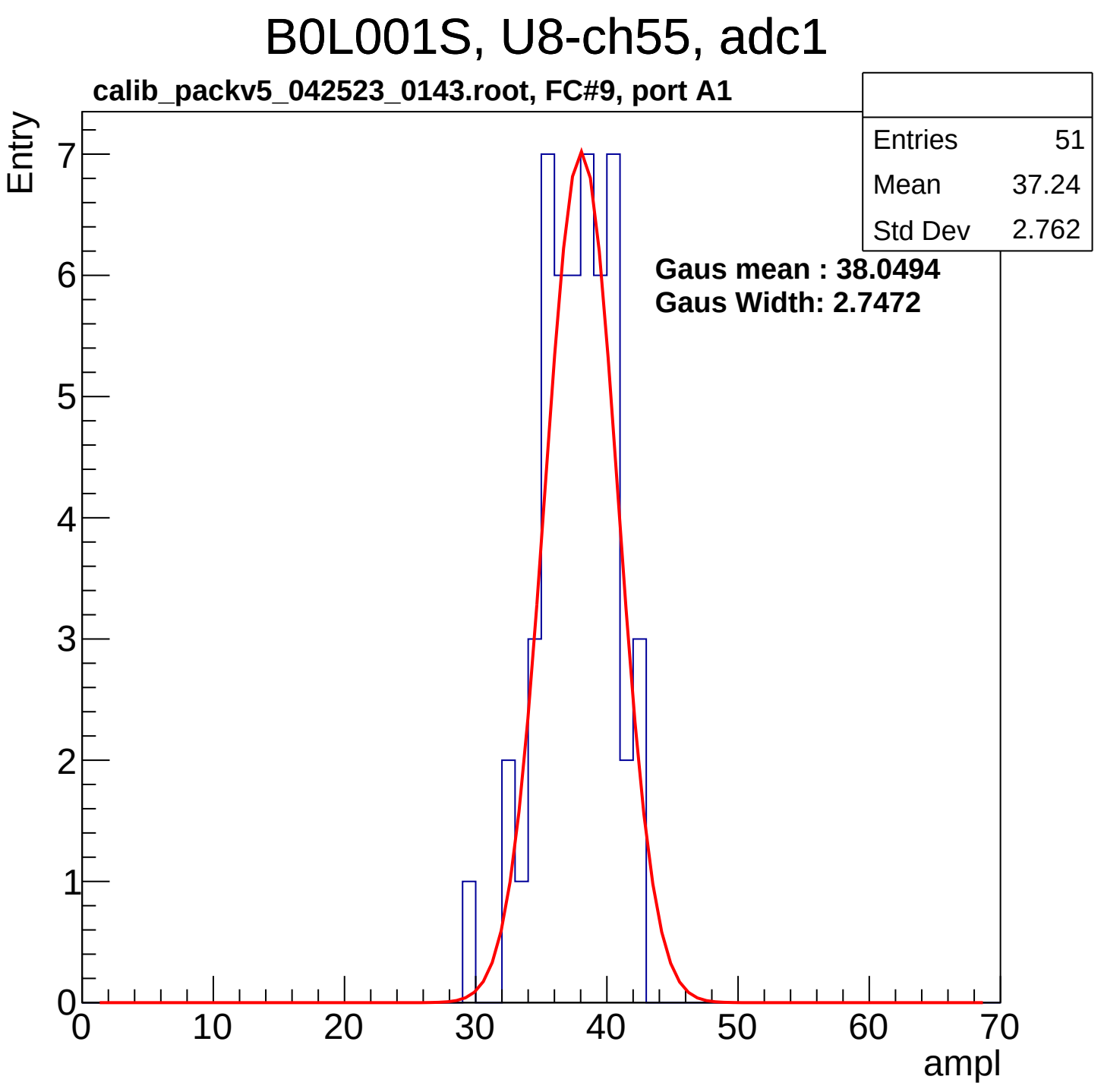
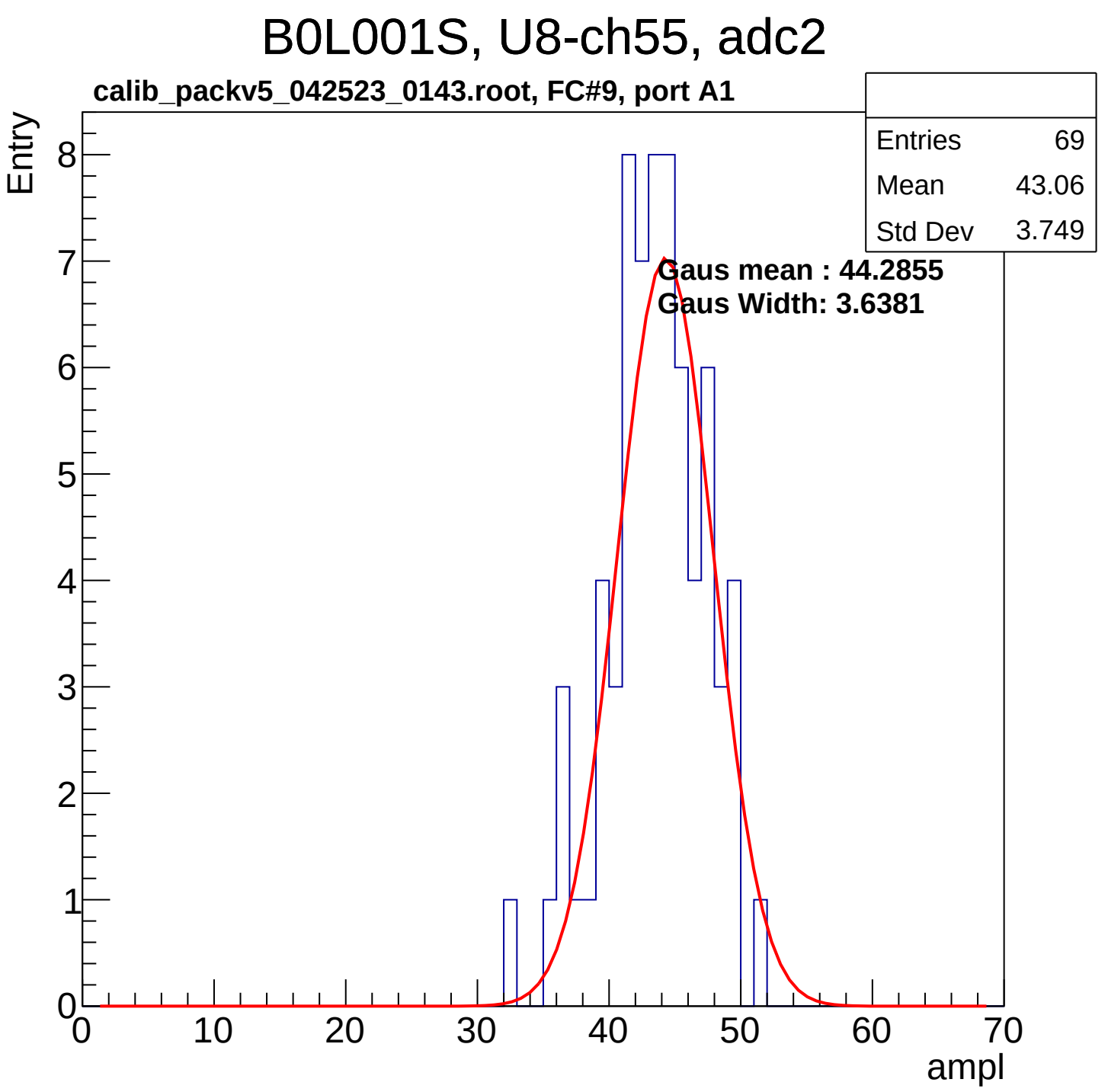
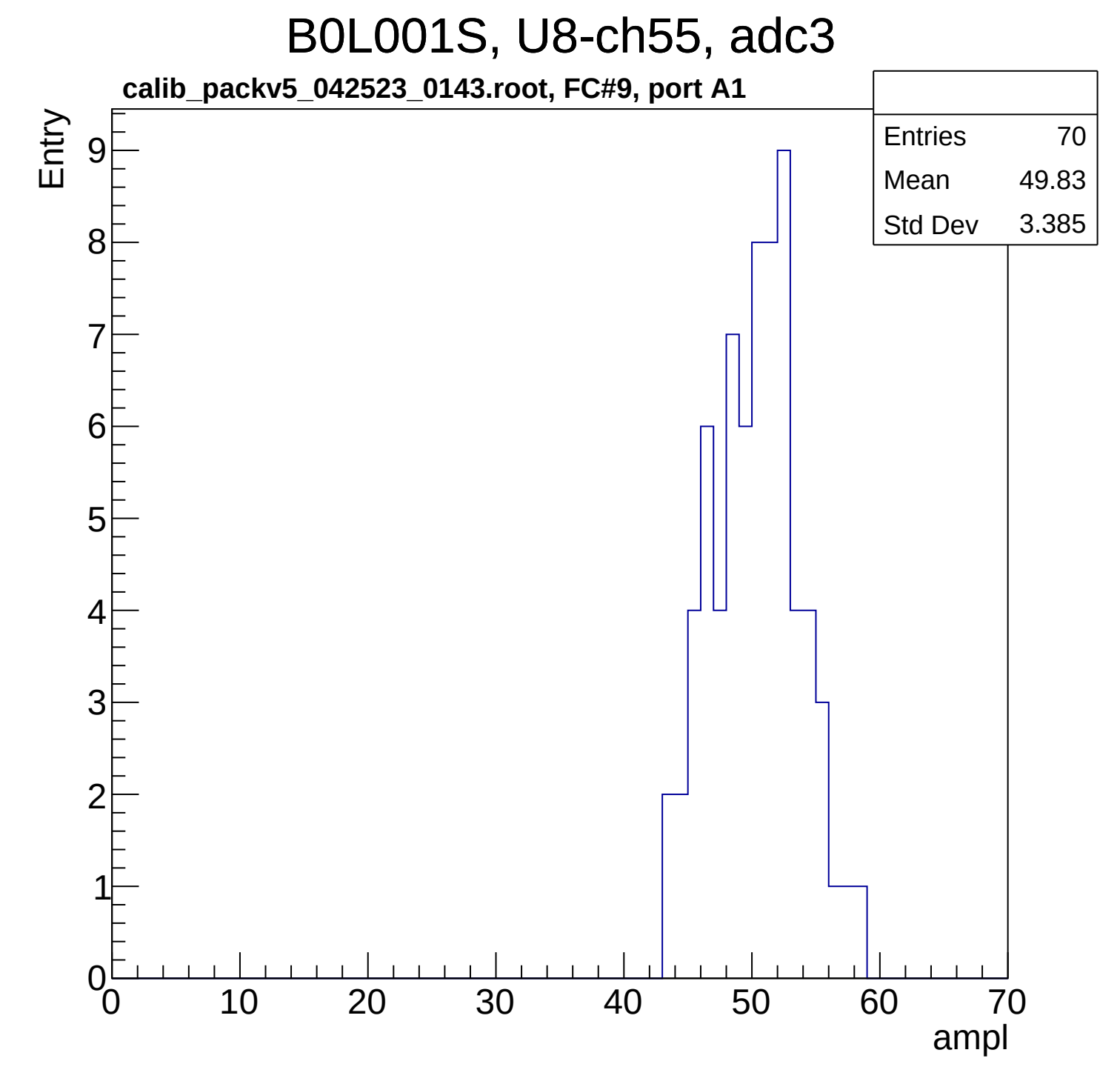

B0L001S, U8-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 37.24
Std Dev 2.762
Gaus mean : 38.0494
Gaus Width: 2.7472
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.06
Std Dev 3.749
Gaus mean : 44.2855
Gaus Width: 3.6381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.83
Std Dev 3.385
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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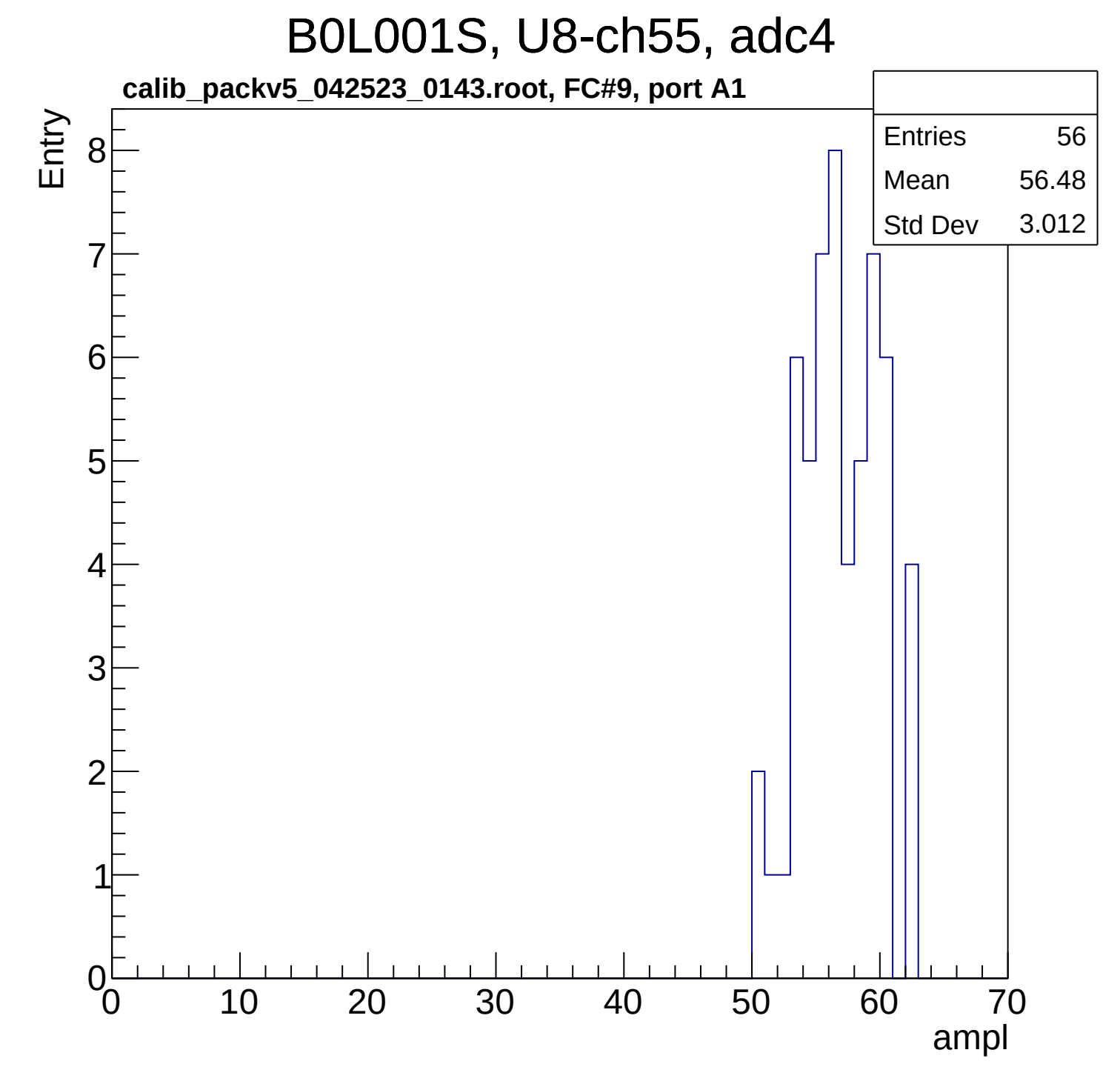
B0L001S, U8-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.48
Std Dev 3.012
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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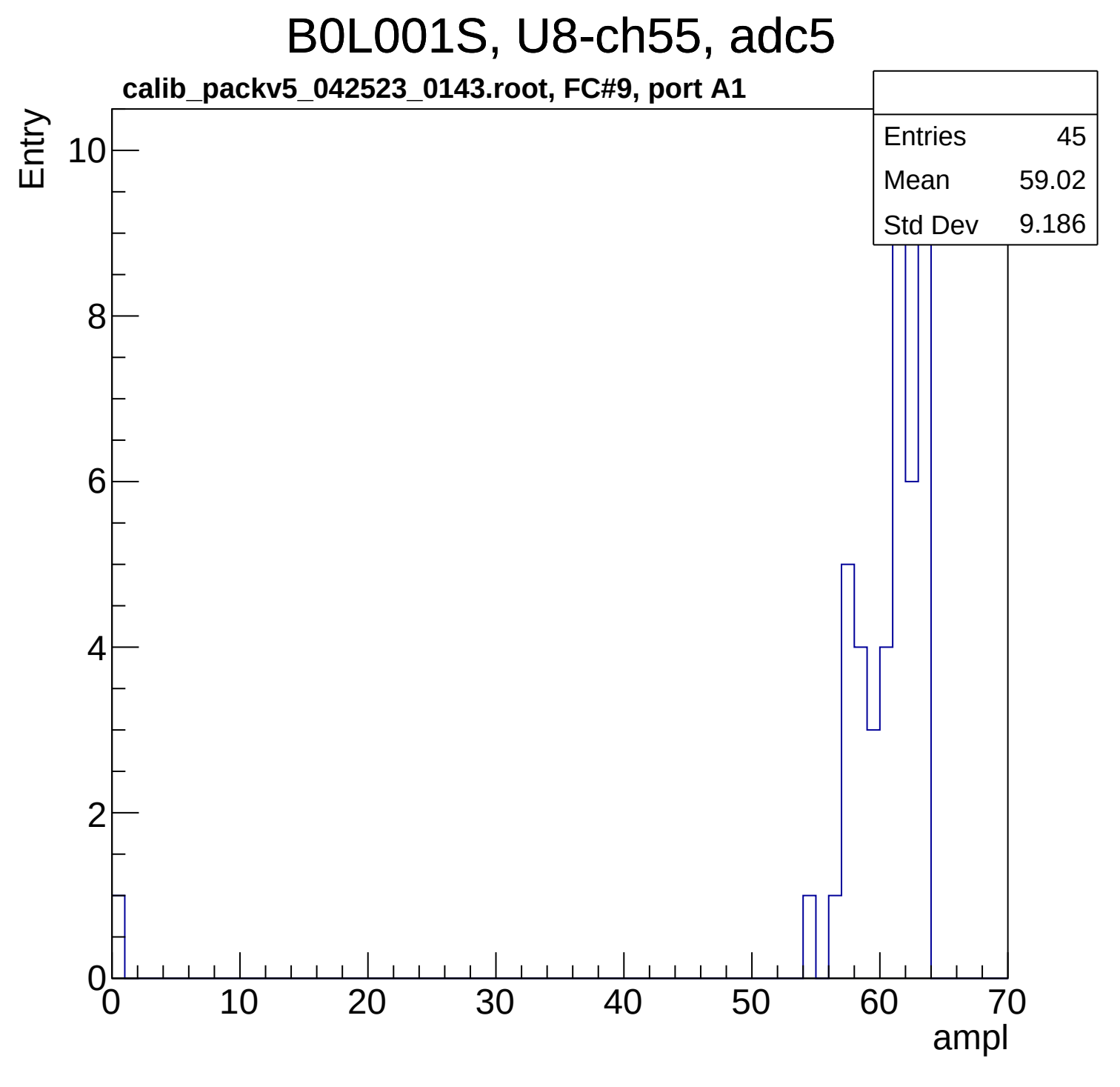
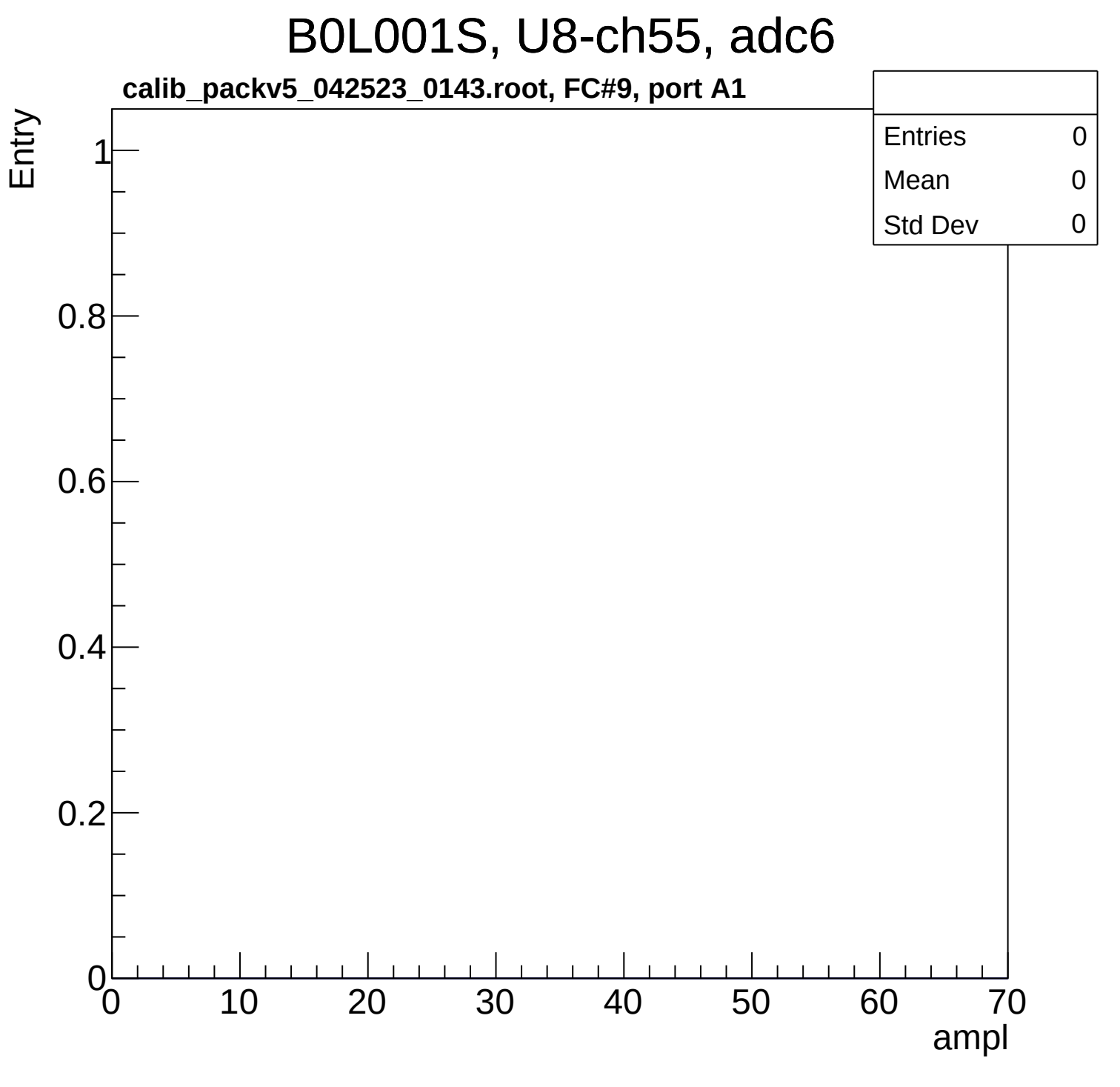
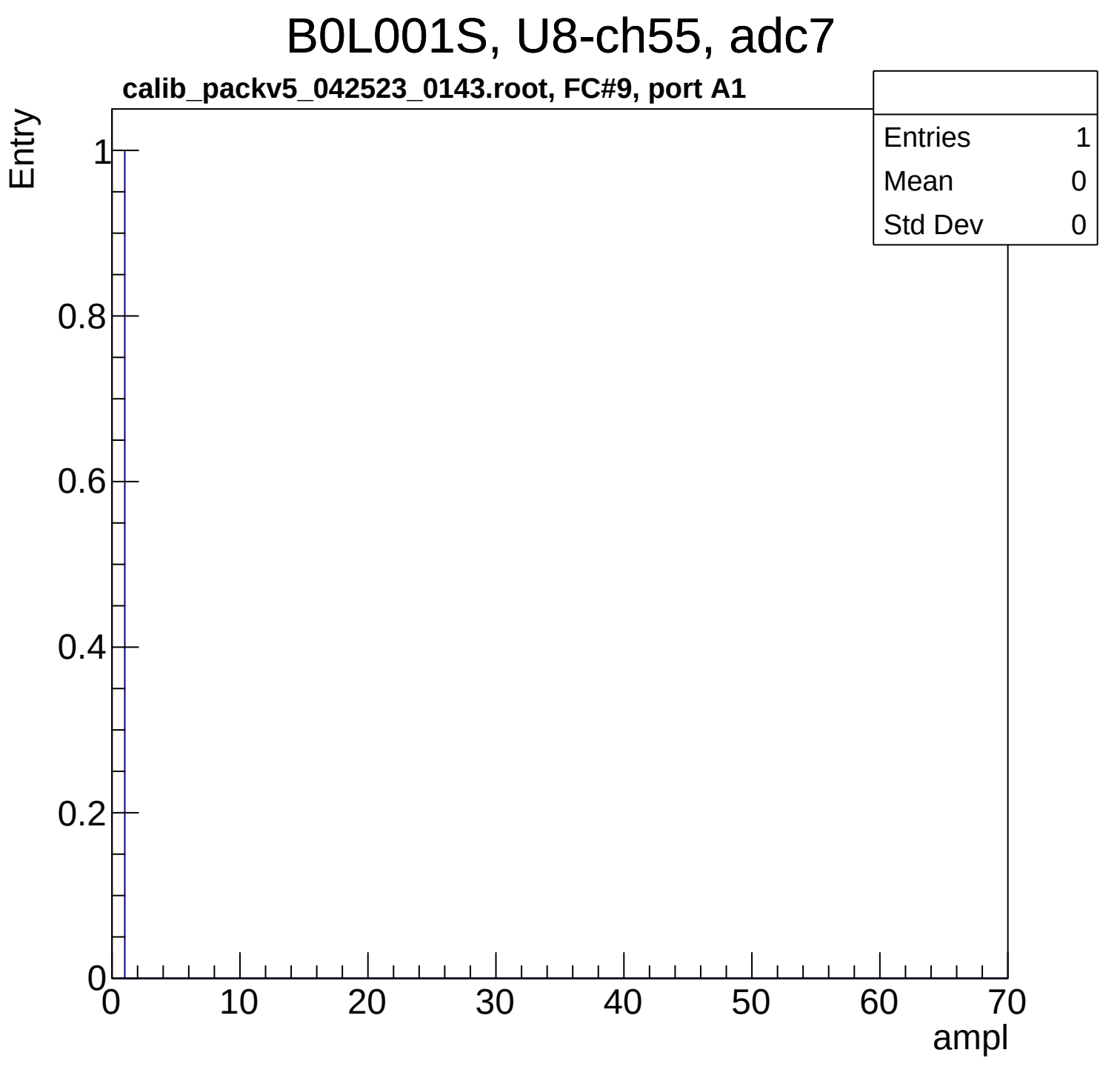
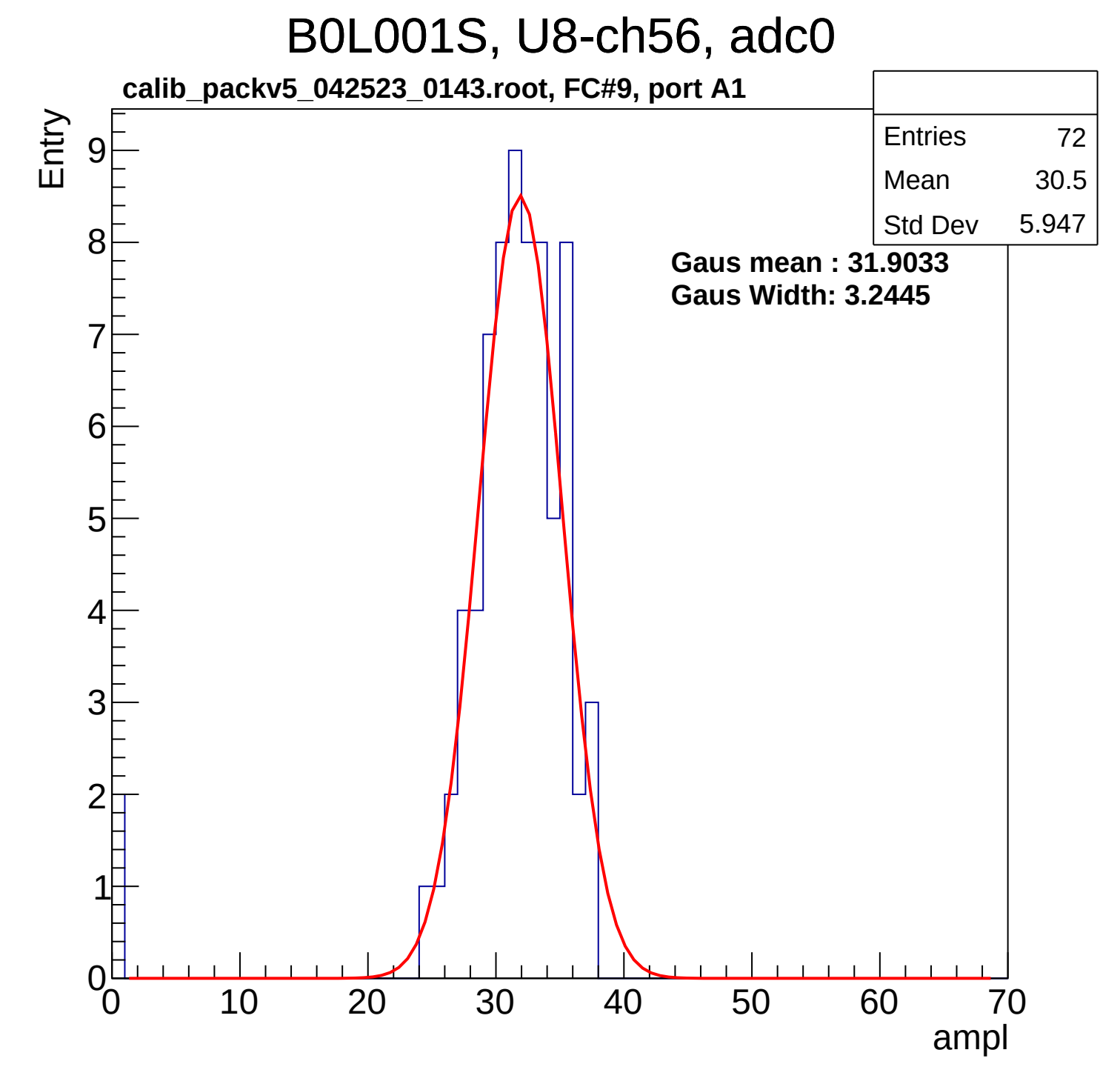
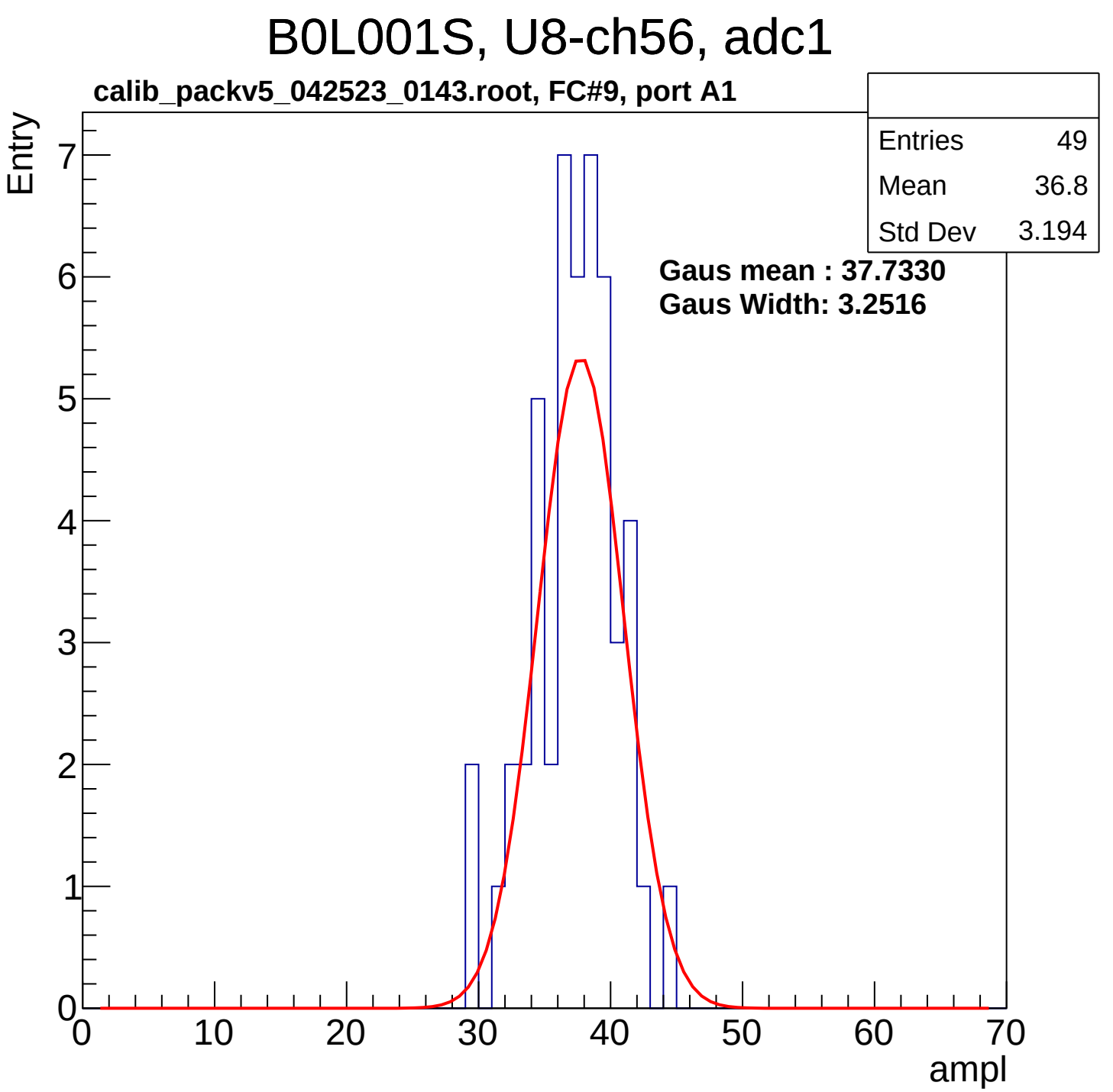

B0L001S, U8-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.02
Std Dev 9.186
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.5
Std Dev 5.947
Gaus mean : 31.9033
Gaus Width: 3.2445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 36.8
Std Dev 3.194
Gaus mean : 37.7330
Gaus Width: 3.2516
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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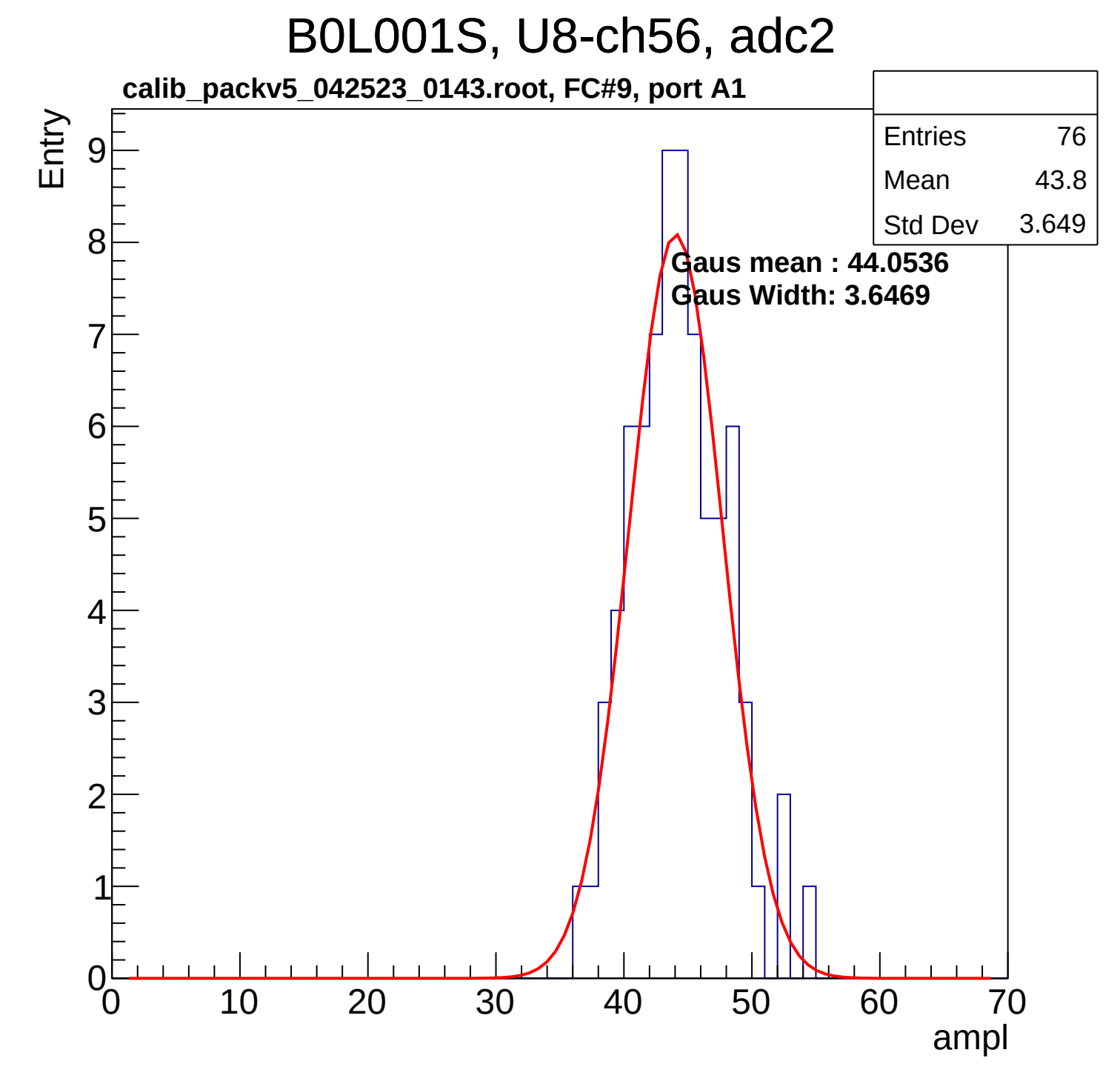
B0L001S, U8-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 43.8
Std Dev 3.649
Gaus mean : 44.0536
Gaus Width: 3.6469
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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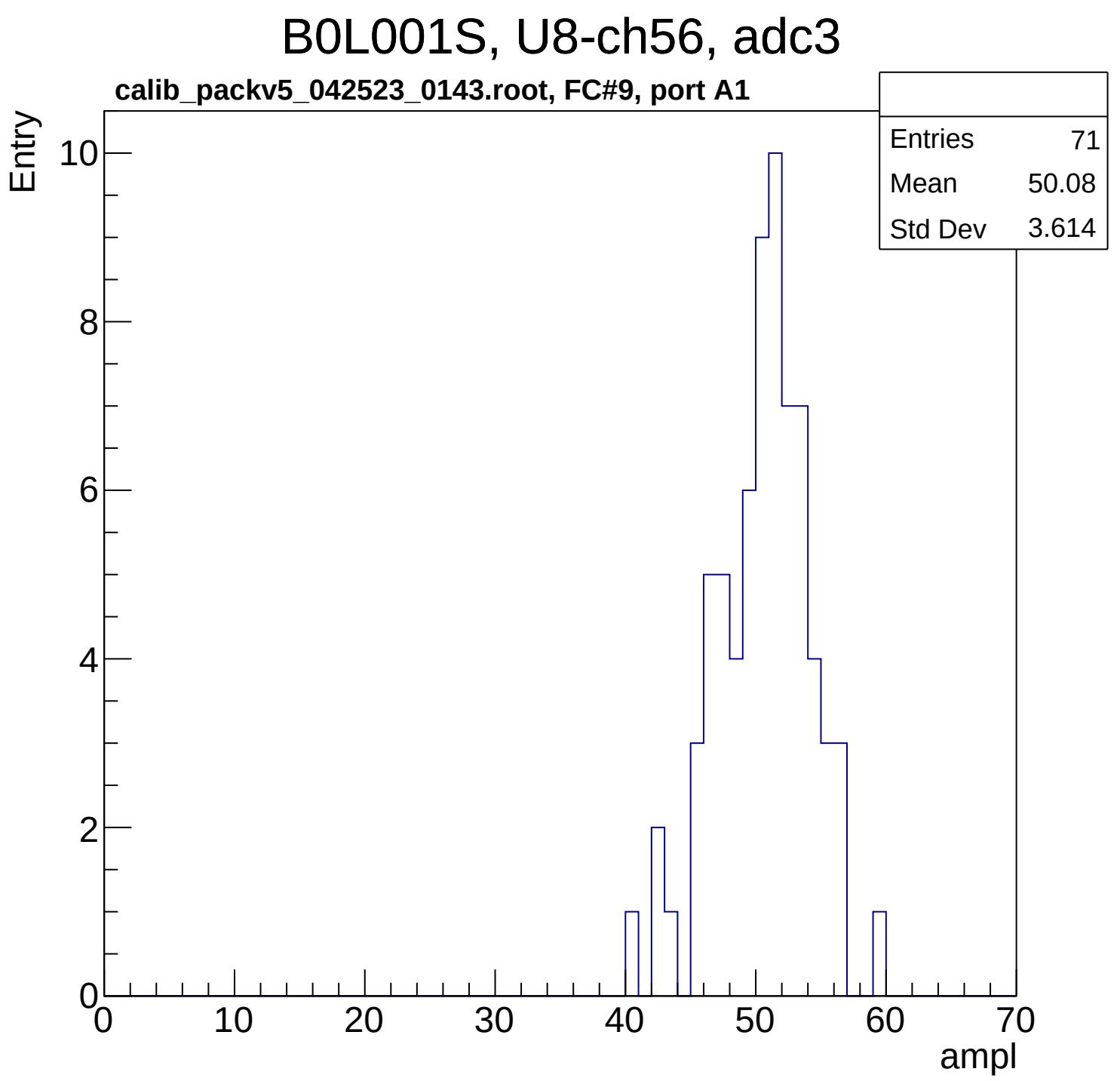
B0L001S, U8-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.08
Std Dev 3.614
Entry
ampl
0
2
4
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0
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60
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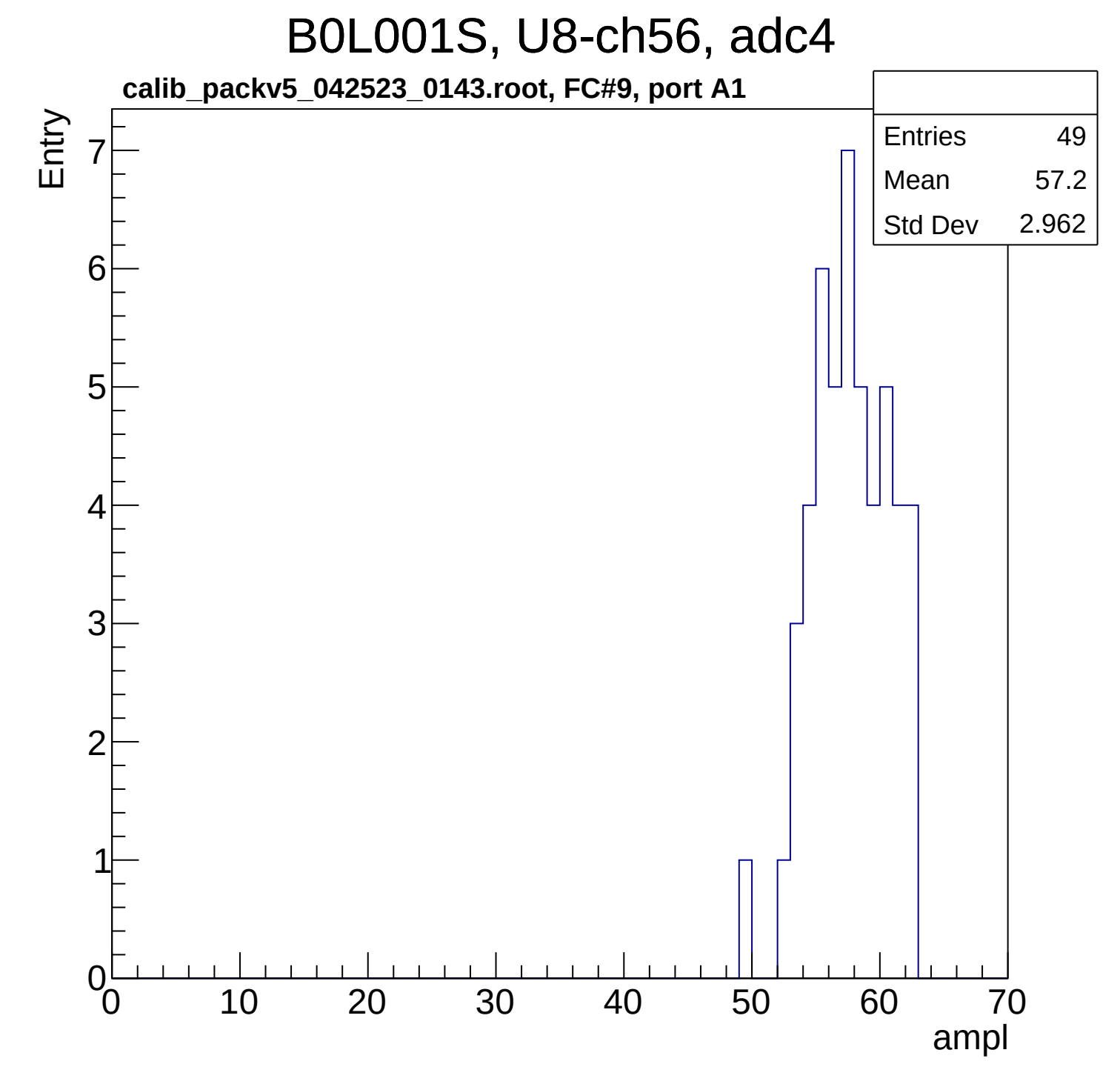
B0L001S, U8-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.2
Std Dev 2.962
Entry
ampl
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6
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0
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40
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60
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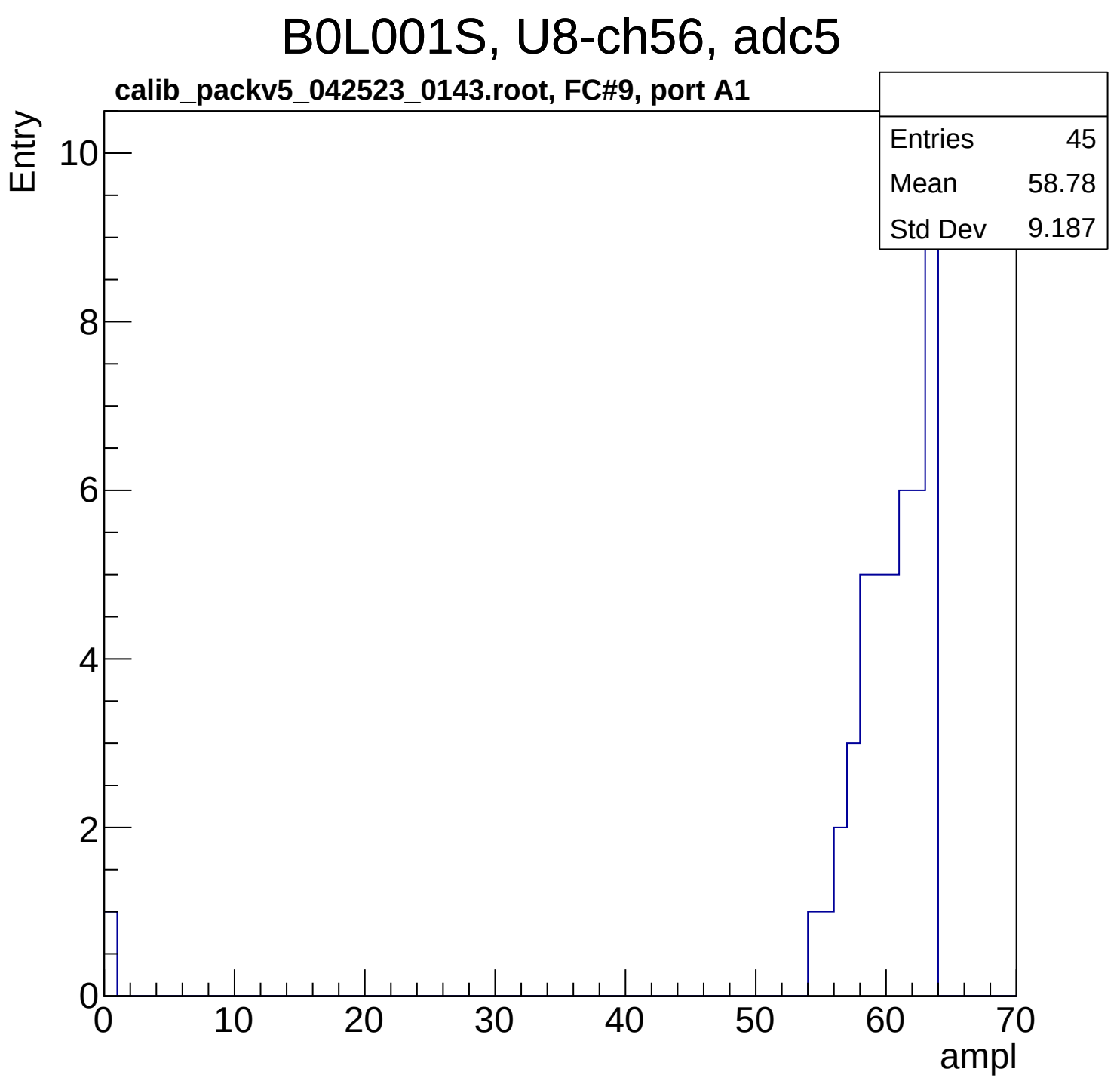
B0L001S, U8-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.78
Std Dev 9.187
Entry
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2
4
6
8
10
0
10
20
30
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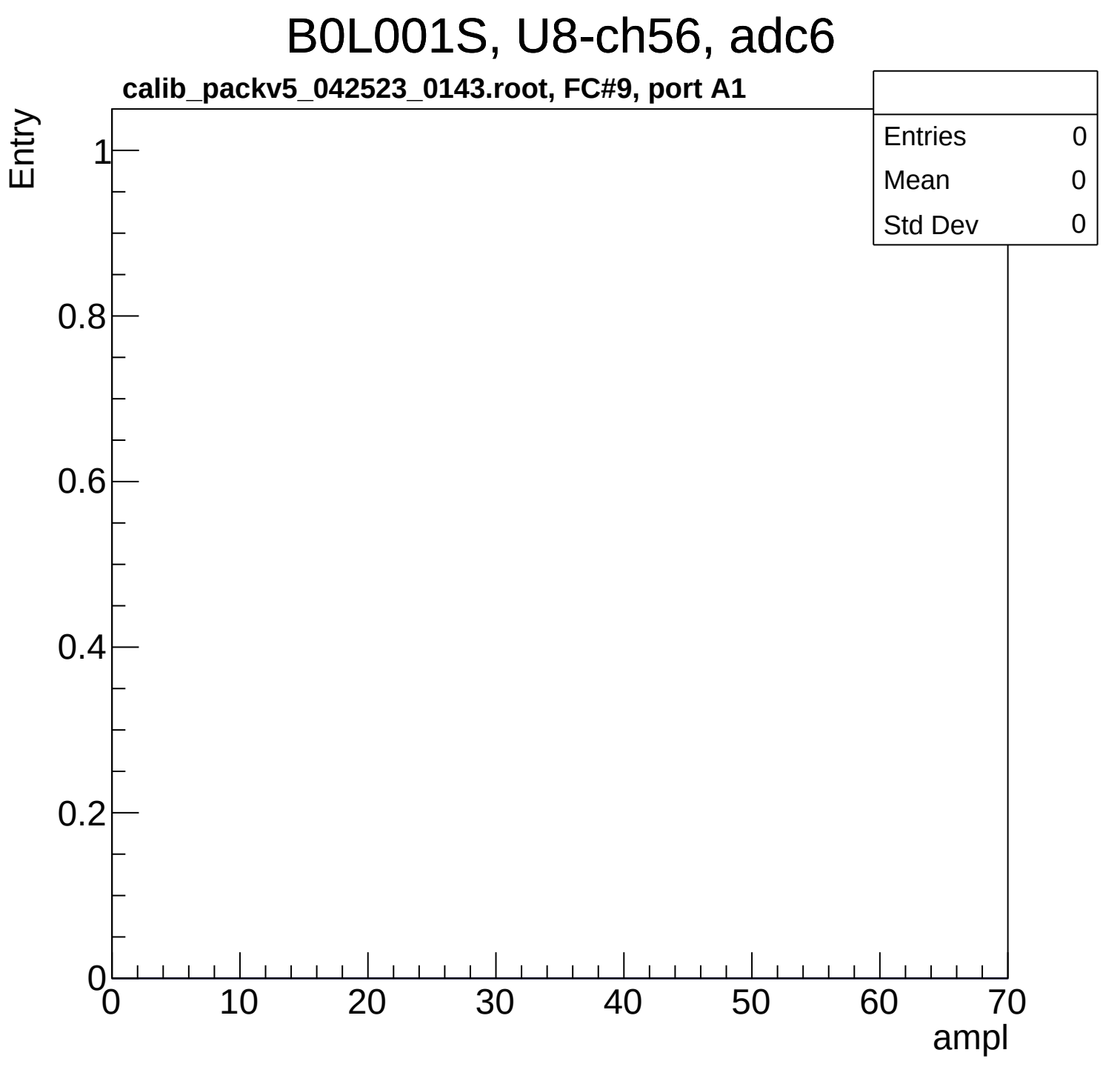
50
60
70
ampl


B0L001S, U8-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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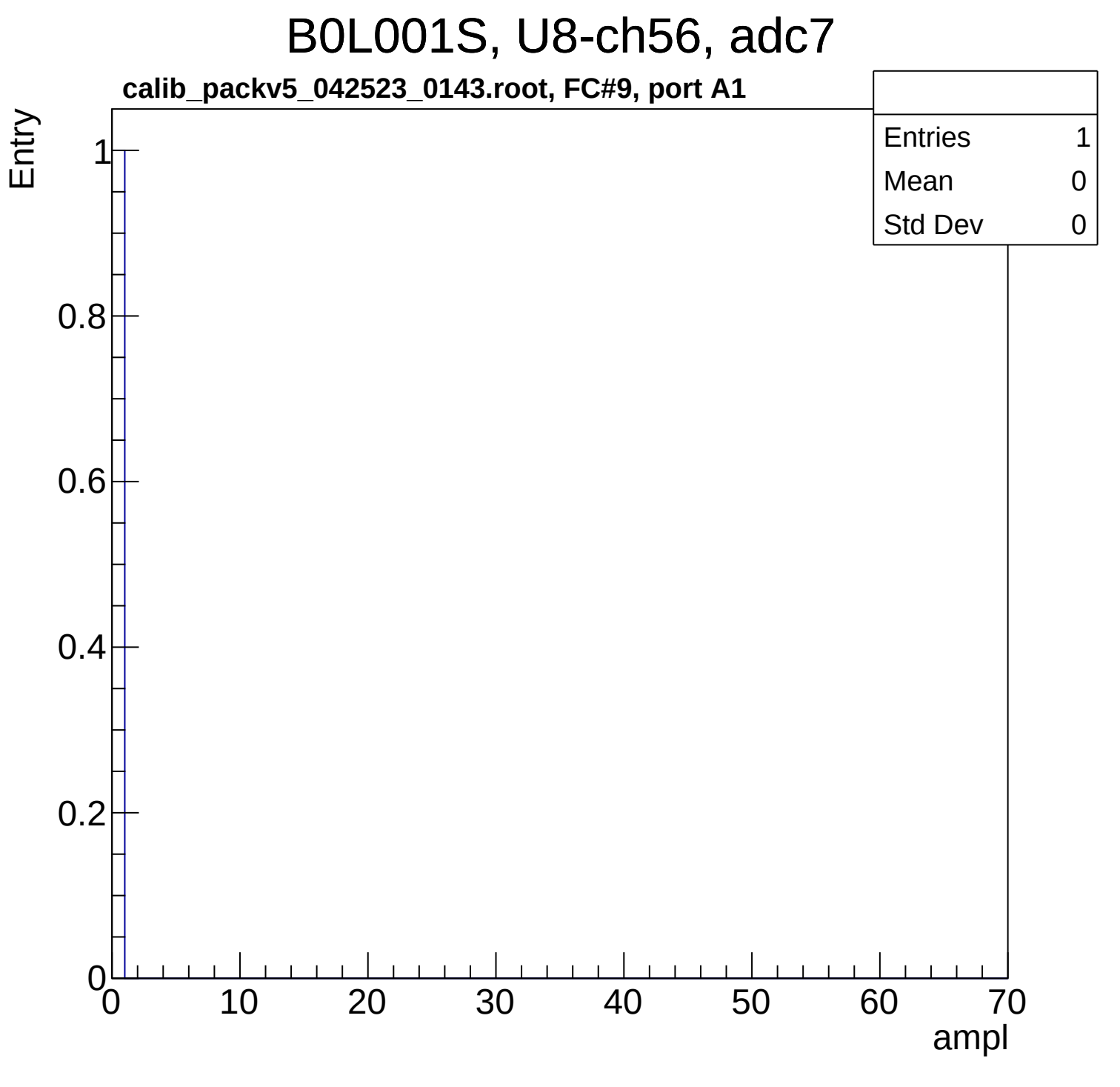
40
50
60
70
ampl


B0L001S, U8-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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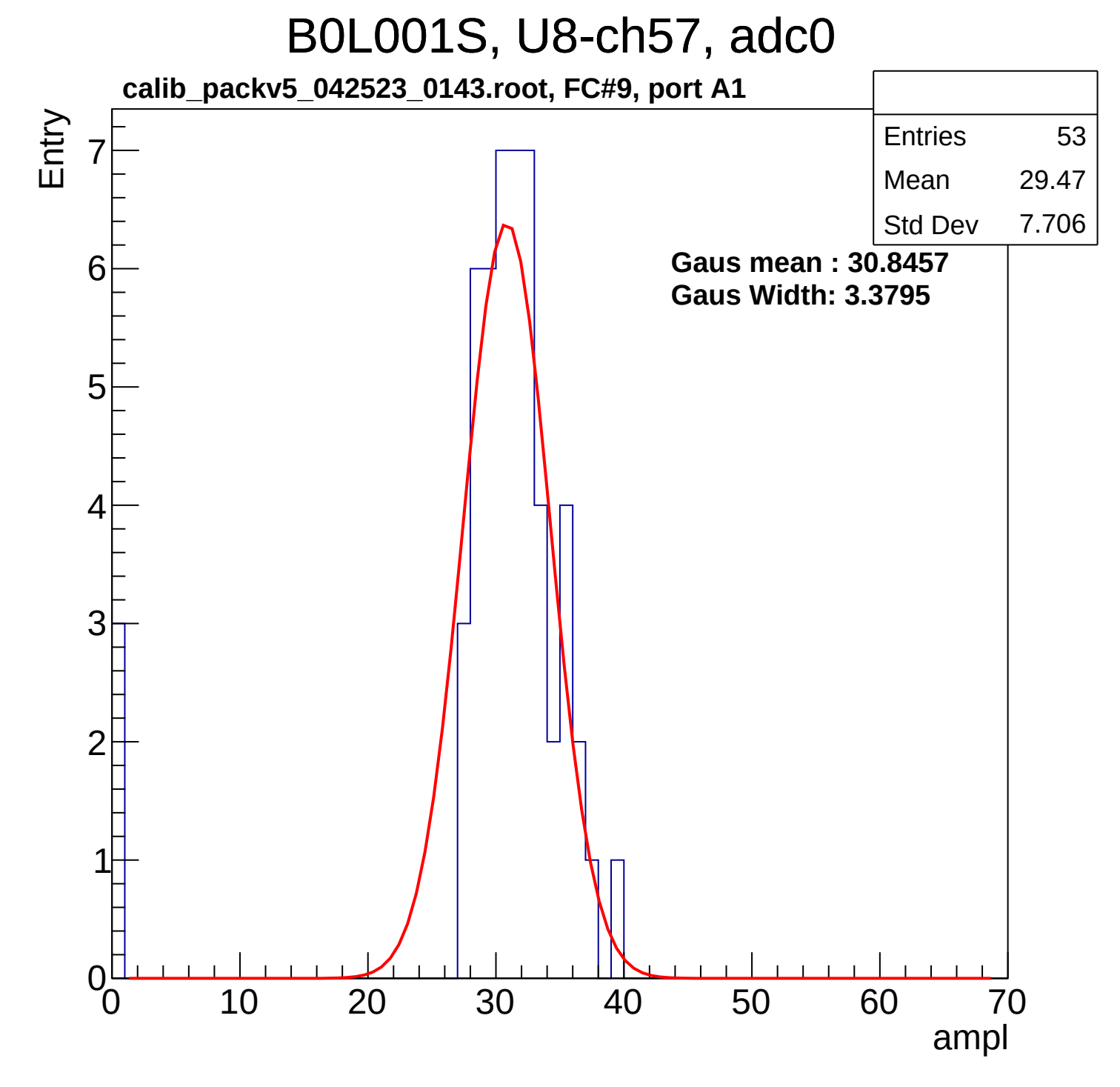
50
60
70
ampl


B0L001S, U8-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 29.47
Std Dev 7.706
Gaus mean : 30.8457
Gaus Width: 3.3795
Entry
ampl
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3
4
5
6
7
0
10
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40
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60
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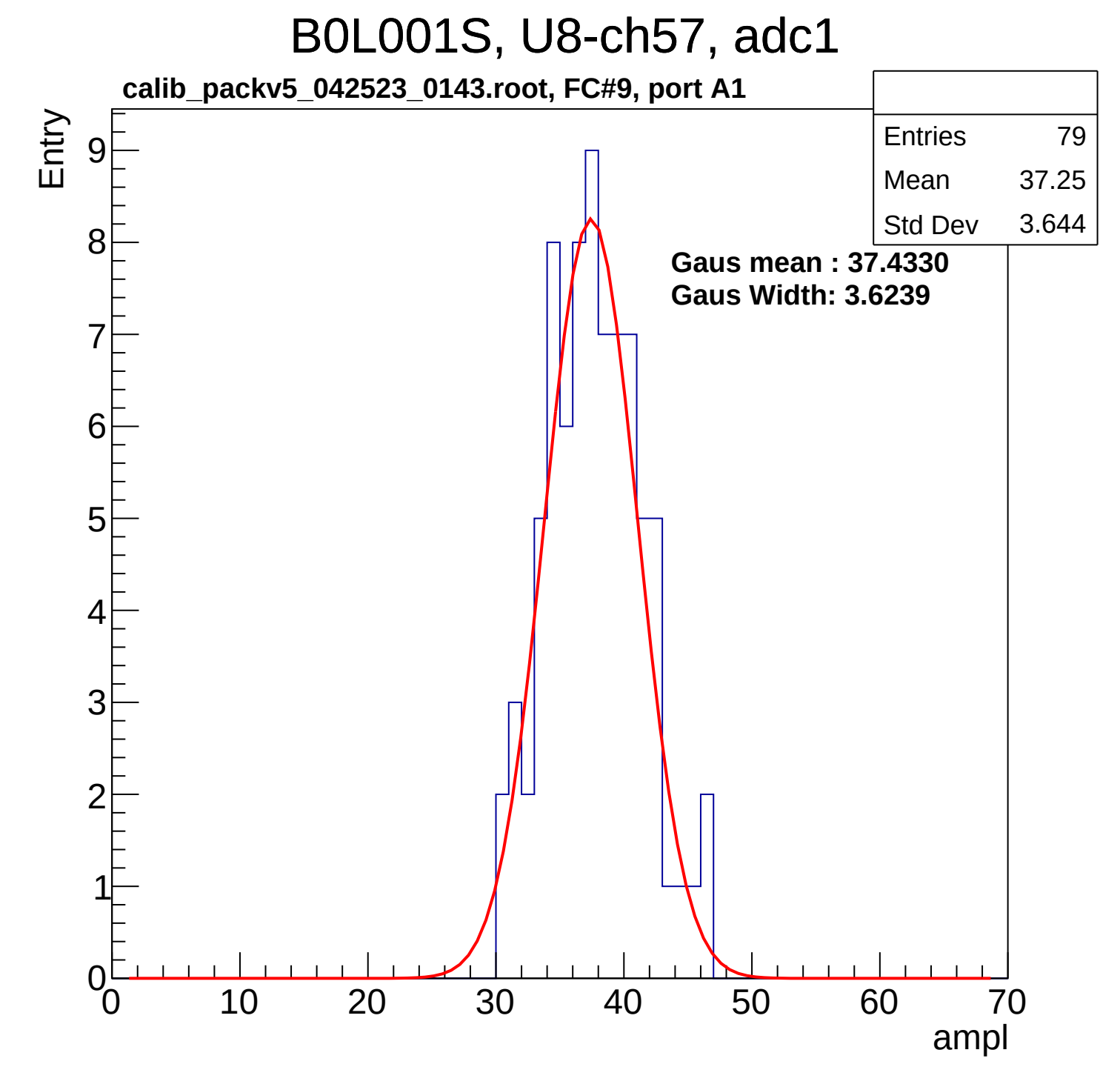
B0L001S, U8-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.25
Std Dev 3.644
Gaus mean : 37.4330
Gaus Width: 3.6239
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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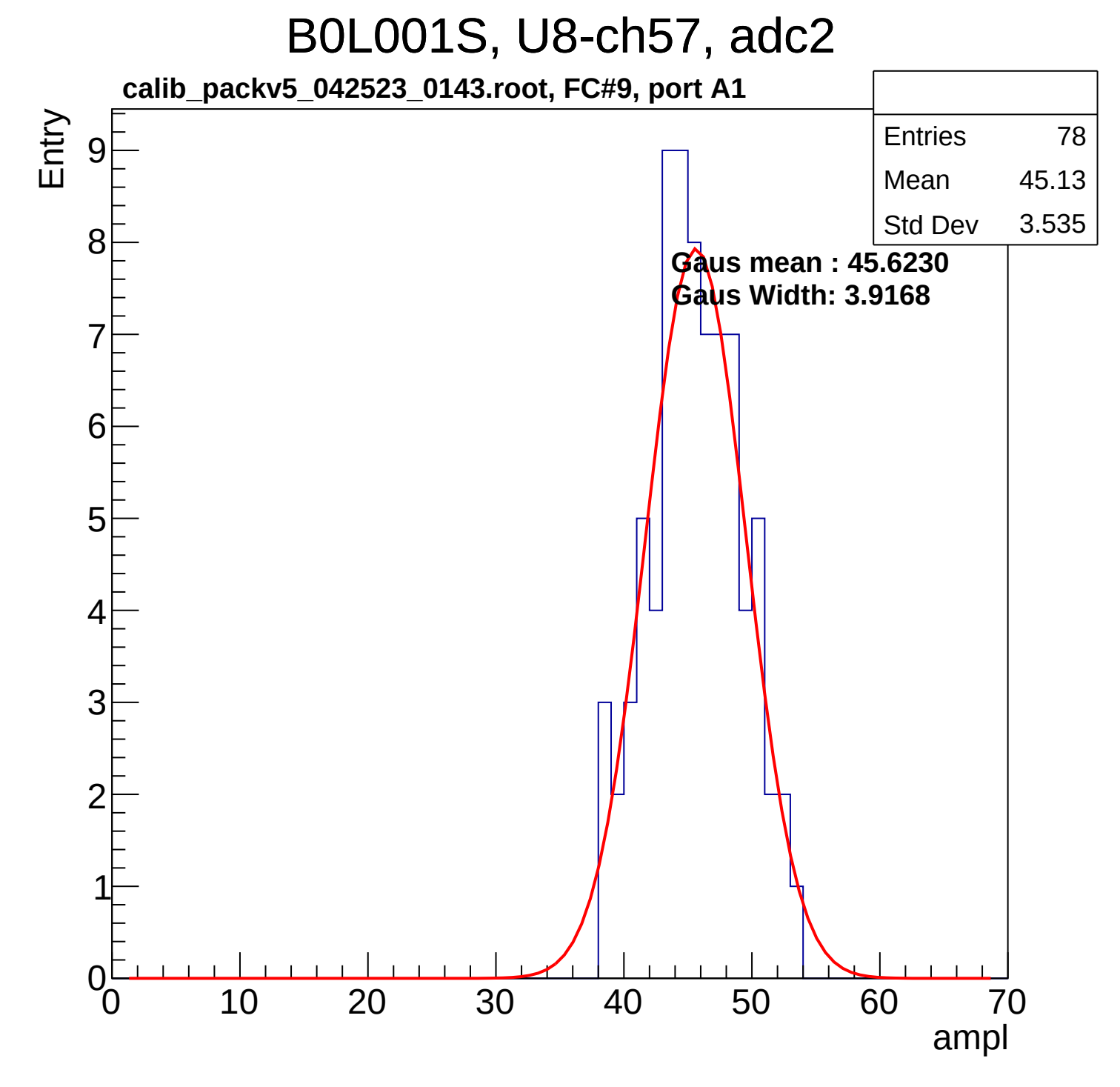
B0L001S, U8-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 45.13
Std Dev 3.535
Gaus mean : 45.6230
Gaus Width: 3.9168
Entry
ampl
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2
3
4
5
6
7
8
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0
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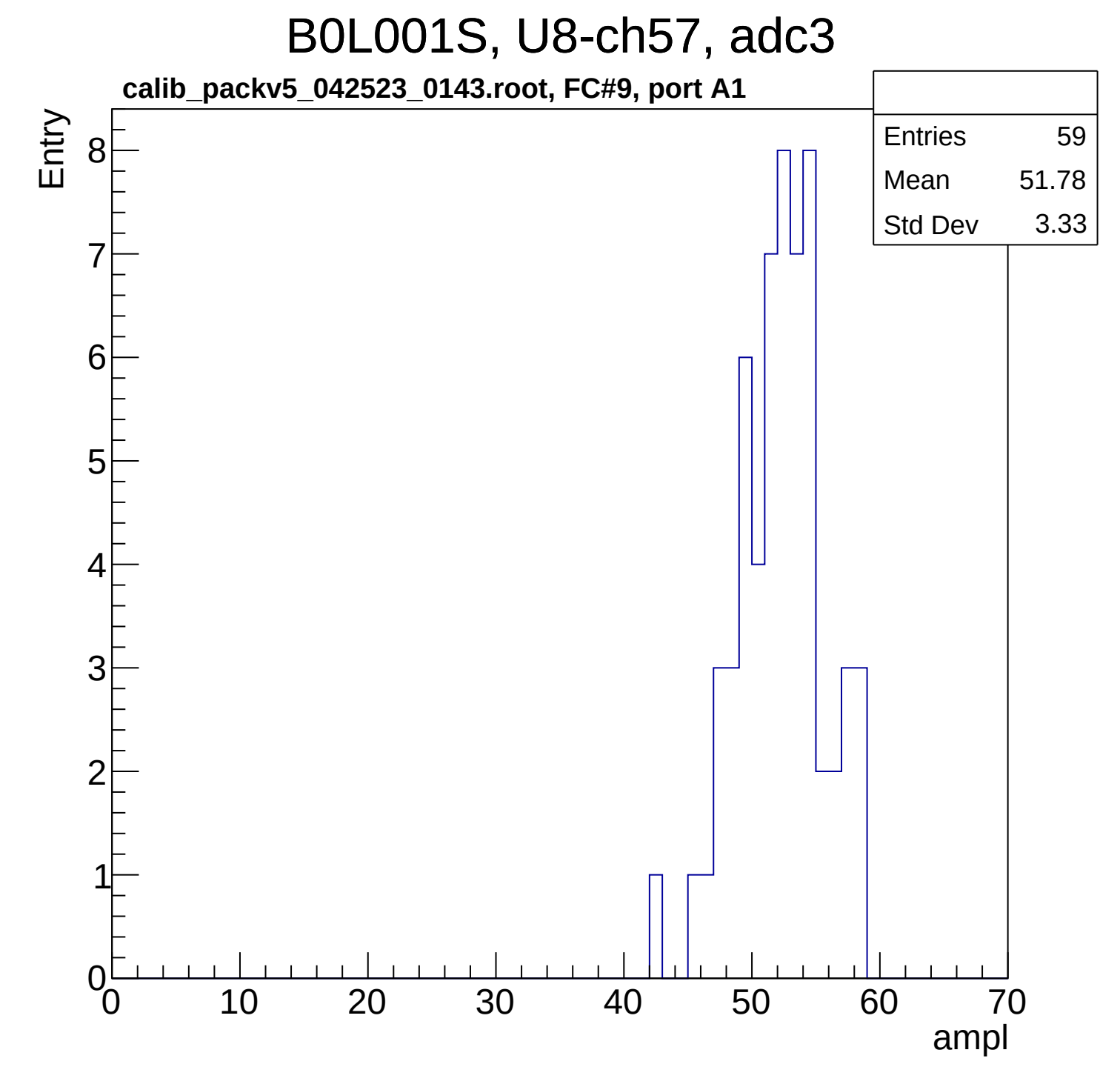
B0L001S, U8-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.78
Std Dev 3.33
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
50
60
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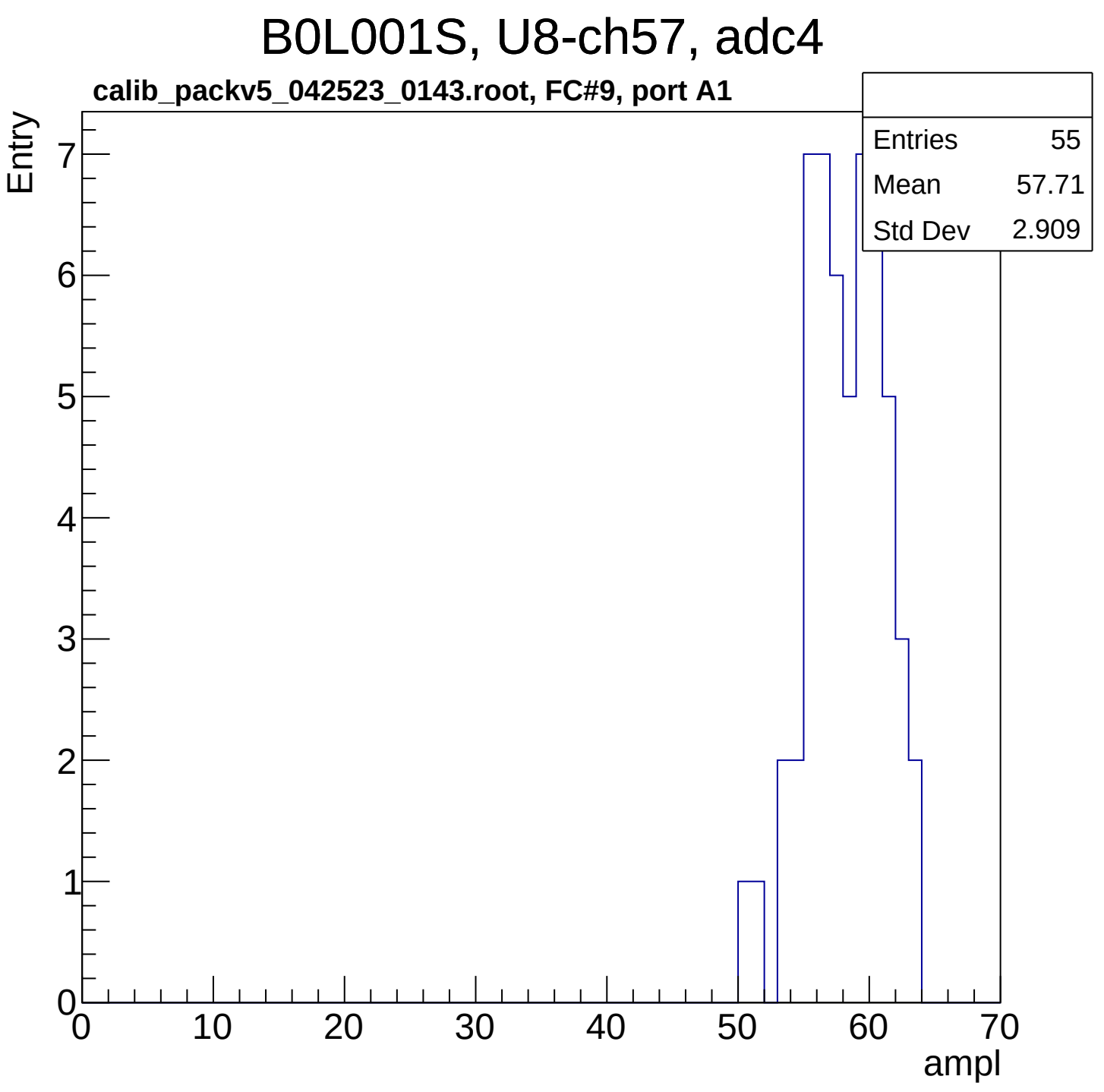
B0L001S, U8-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.71
Std Dev 2.909
Entry
ampl
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4
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60
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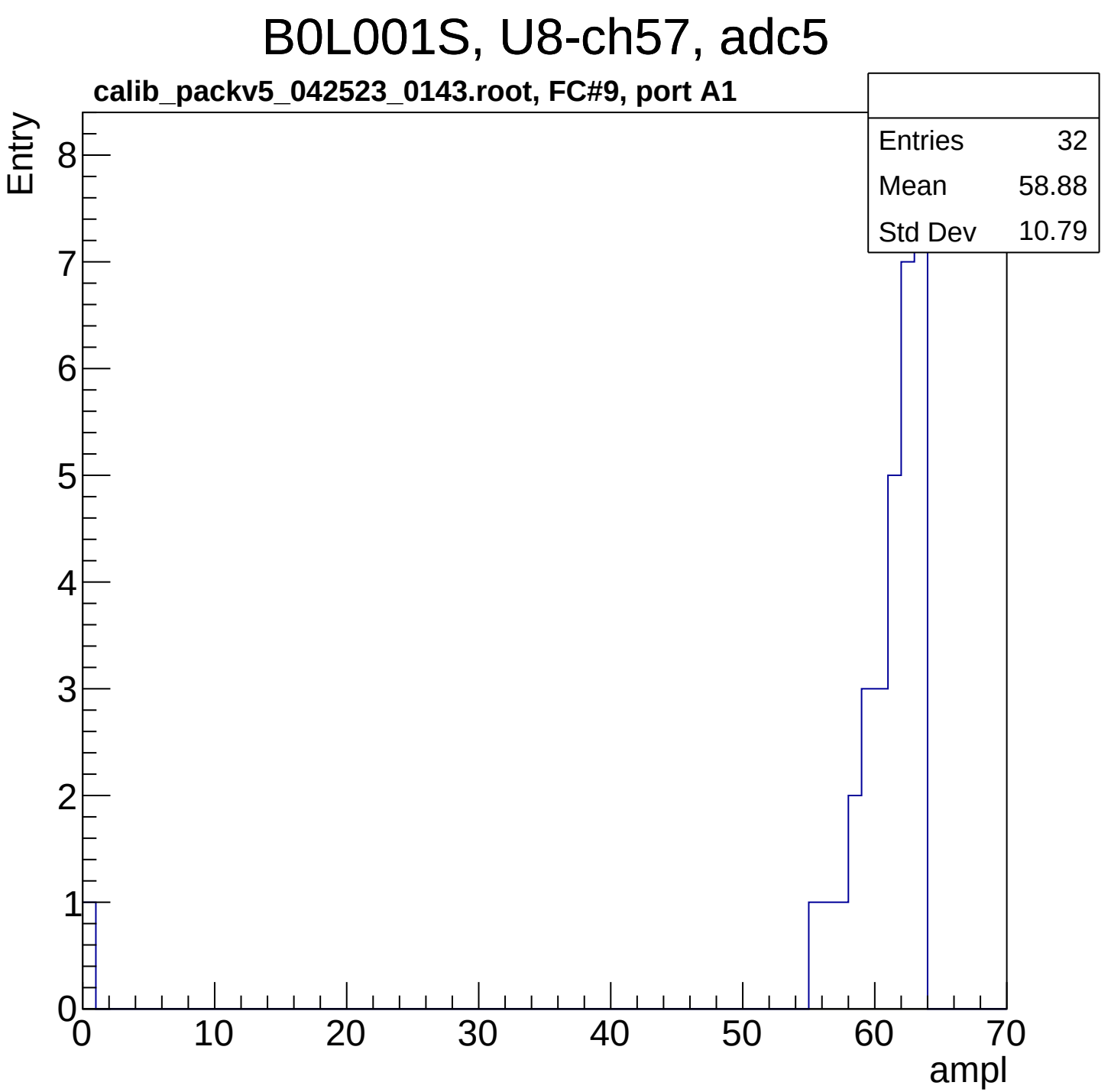
B0L001S, U8-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.88
Std Dev 10.79
Entry
ampl
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4
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60
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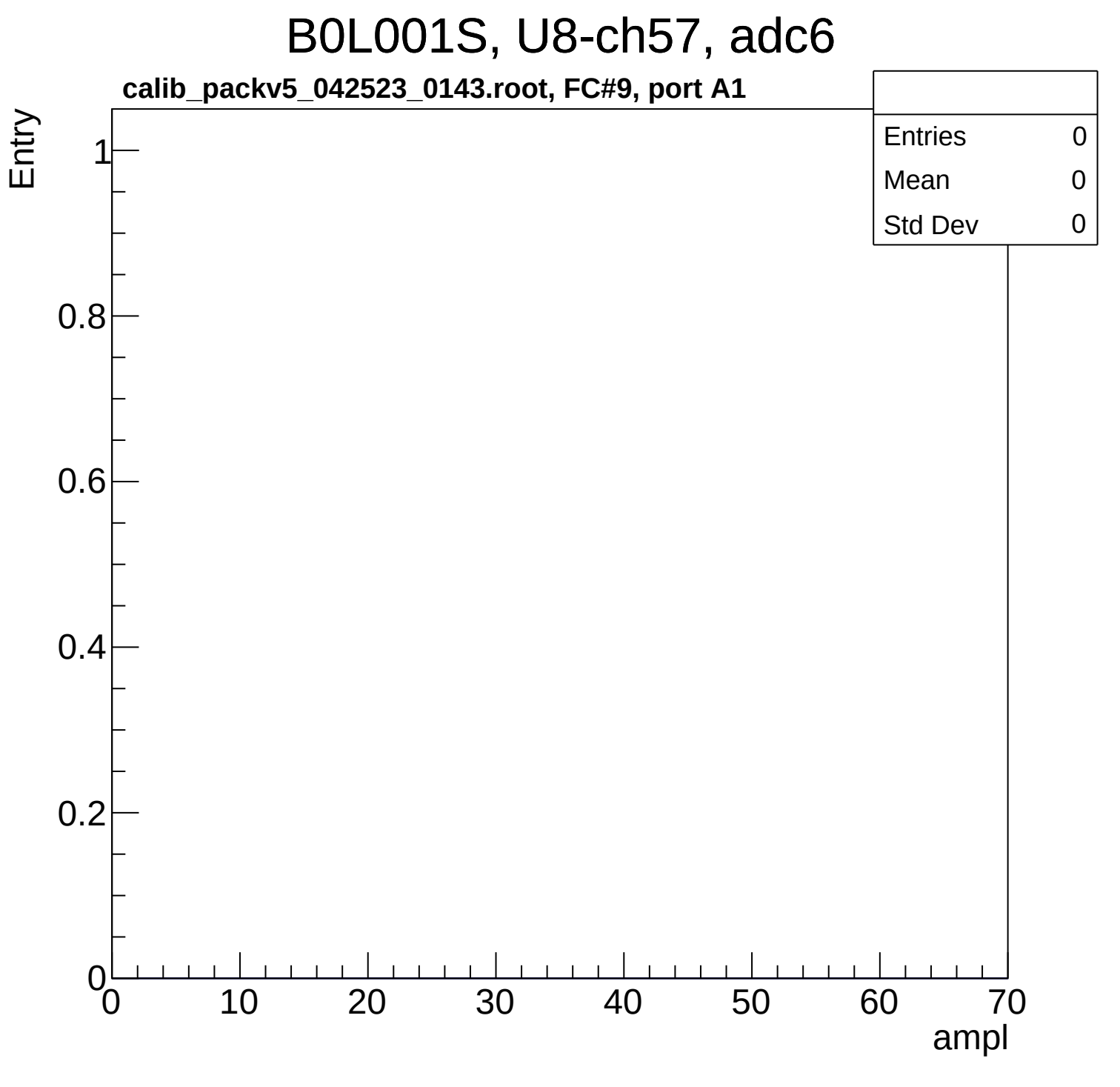
B0L001S, U8-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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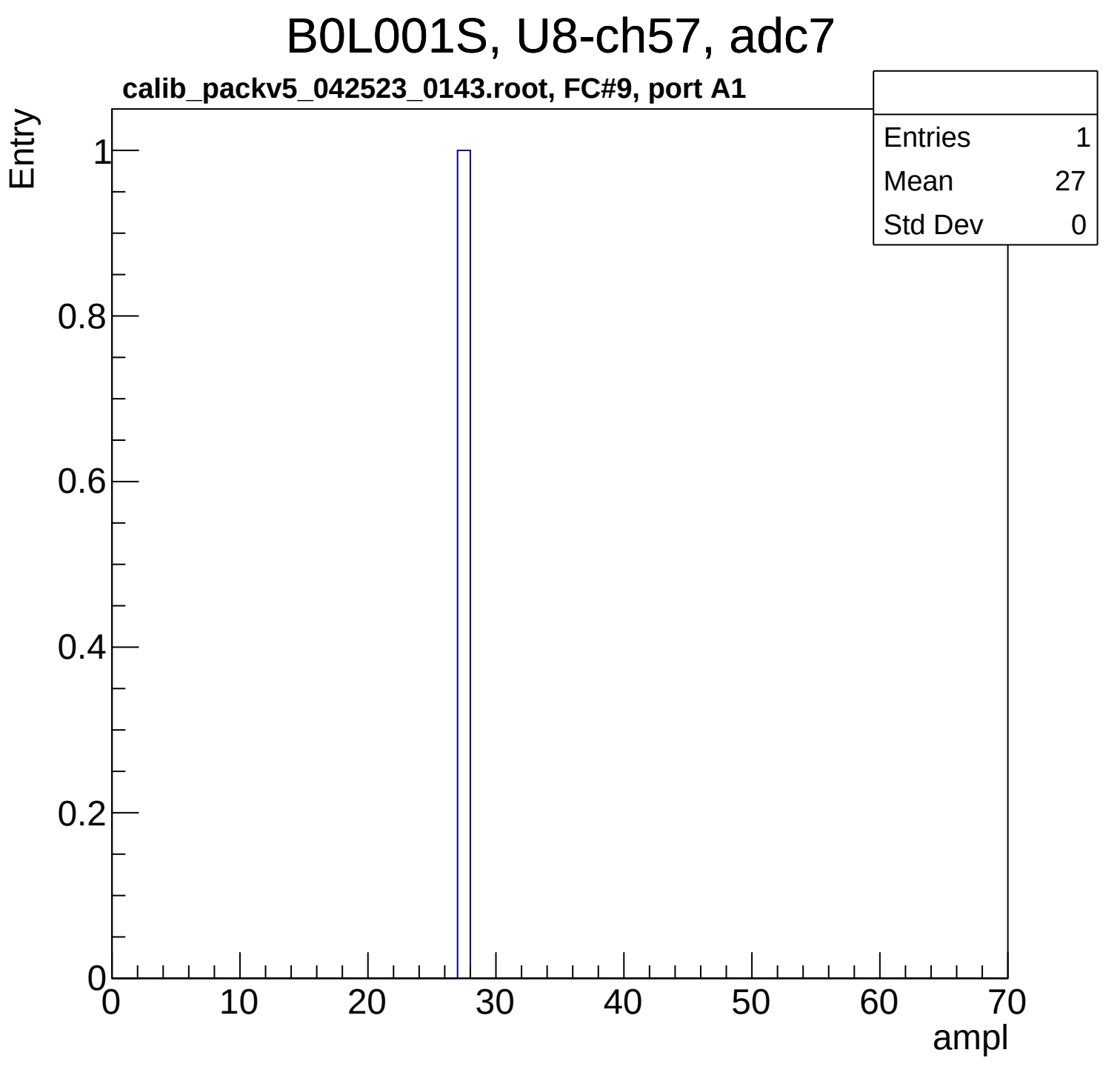
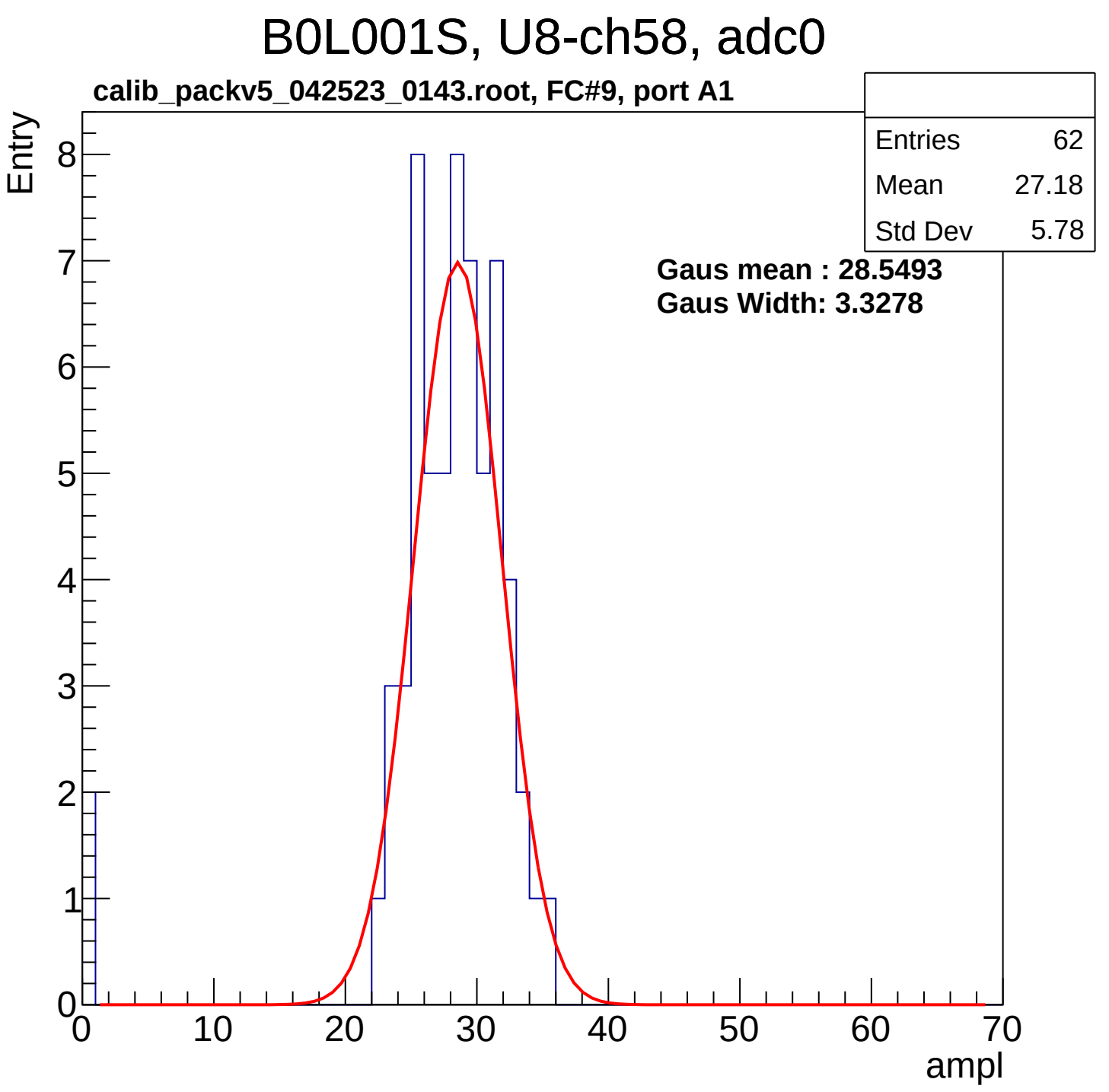
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 27
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 27.18
Std Dev 5.78
Gaus mean : 28.5493
Gaus Width: 3.3278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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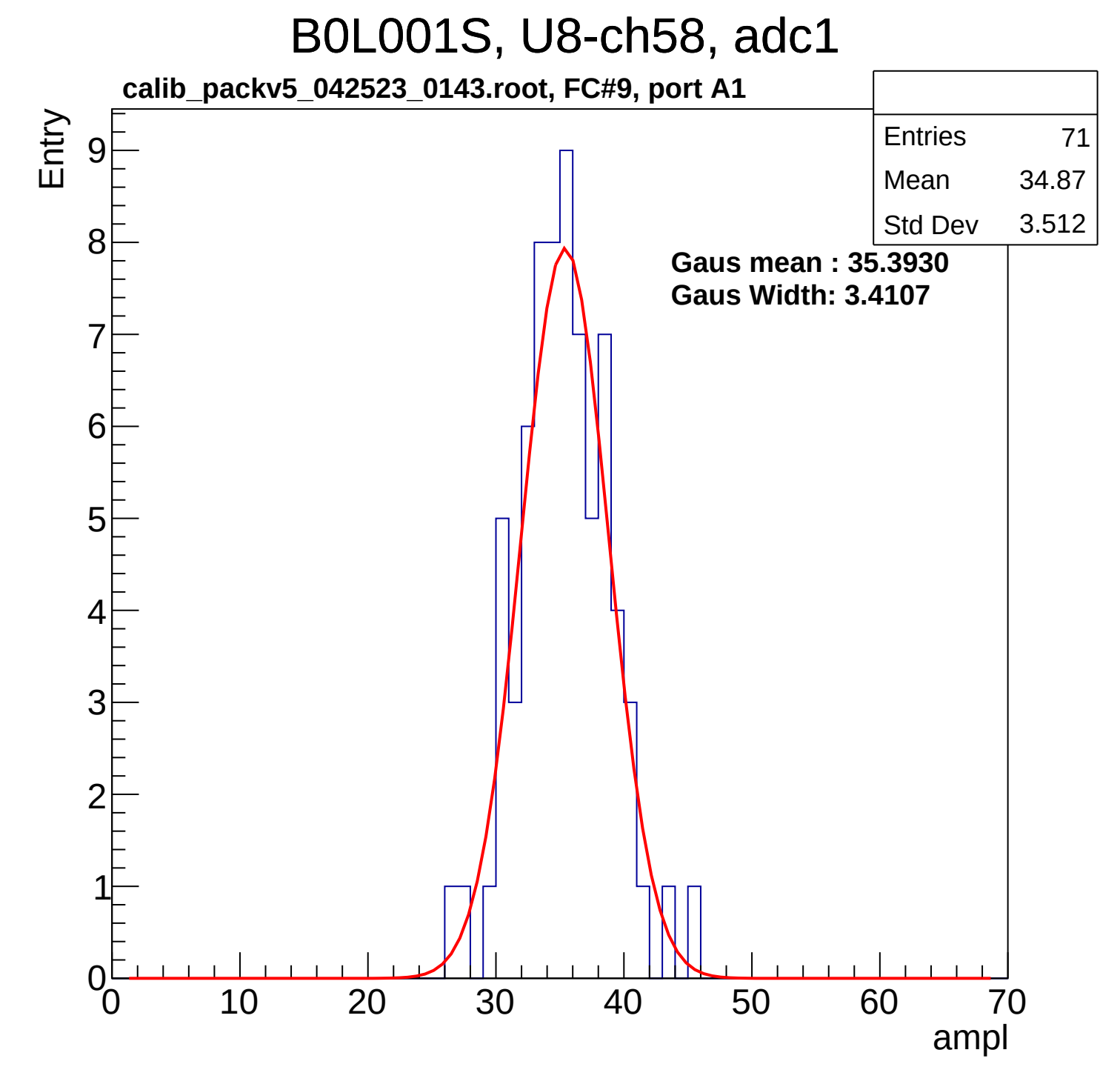
B0L001S, U8-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 34.87
Std Dev 3.512
Gaus mean : 35.3930
Gaus Width: 3.4107
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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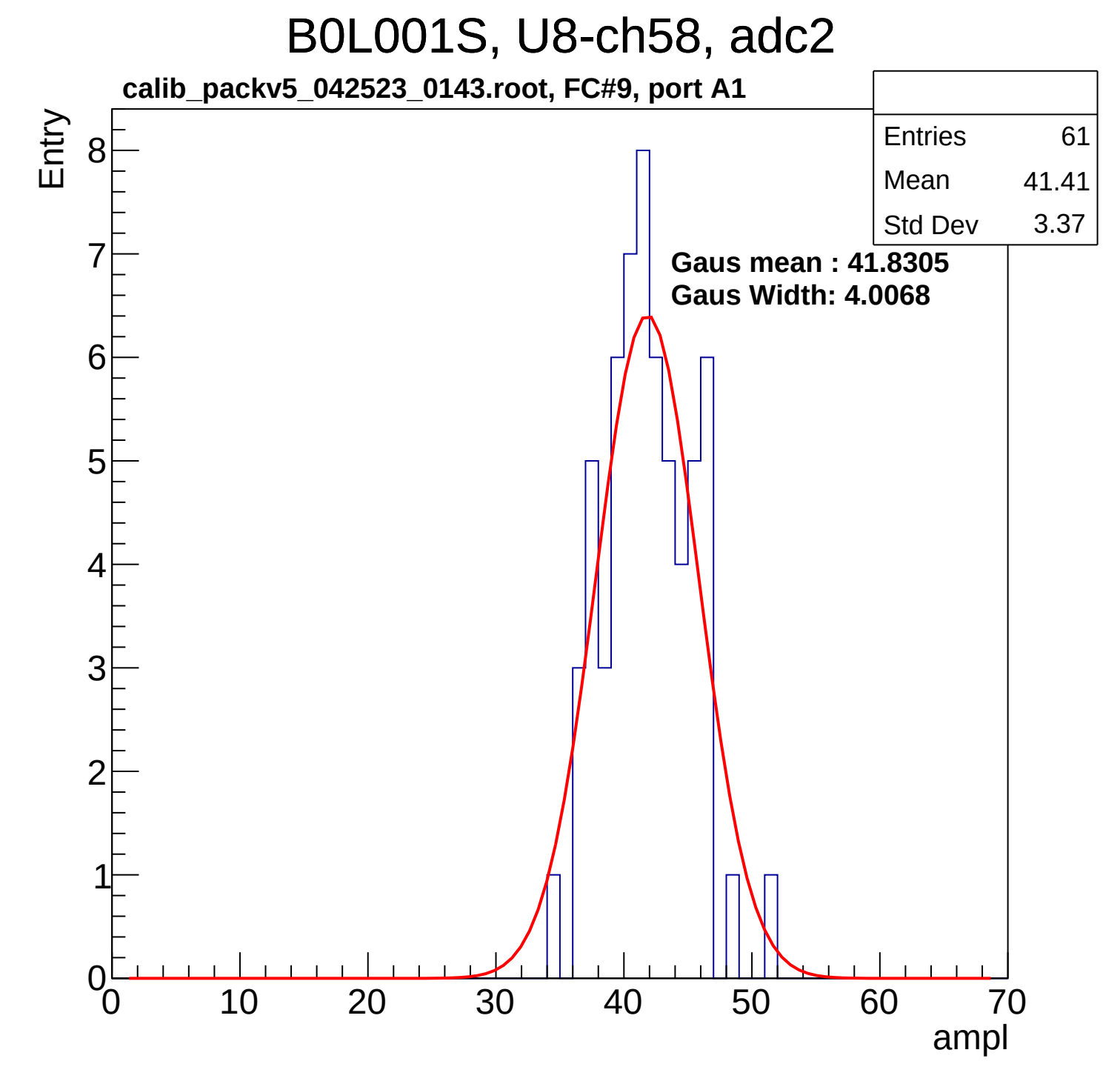
B0L001S, U8-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 41.41
Std Dev 3.37
Gaus mean : 41.8305
Gaus Width: 4.0068
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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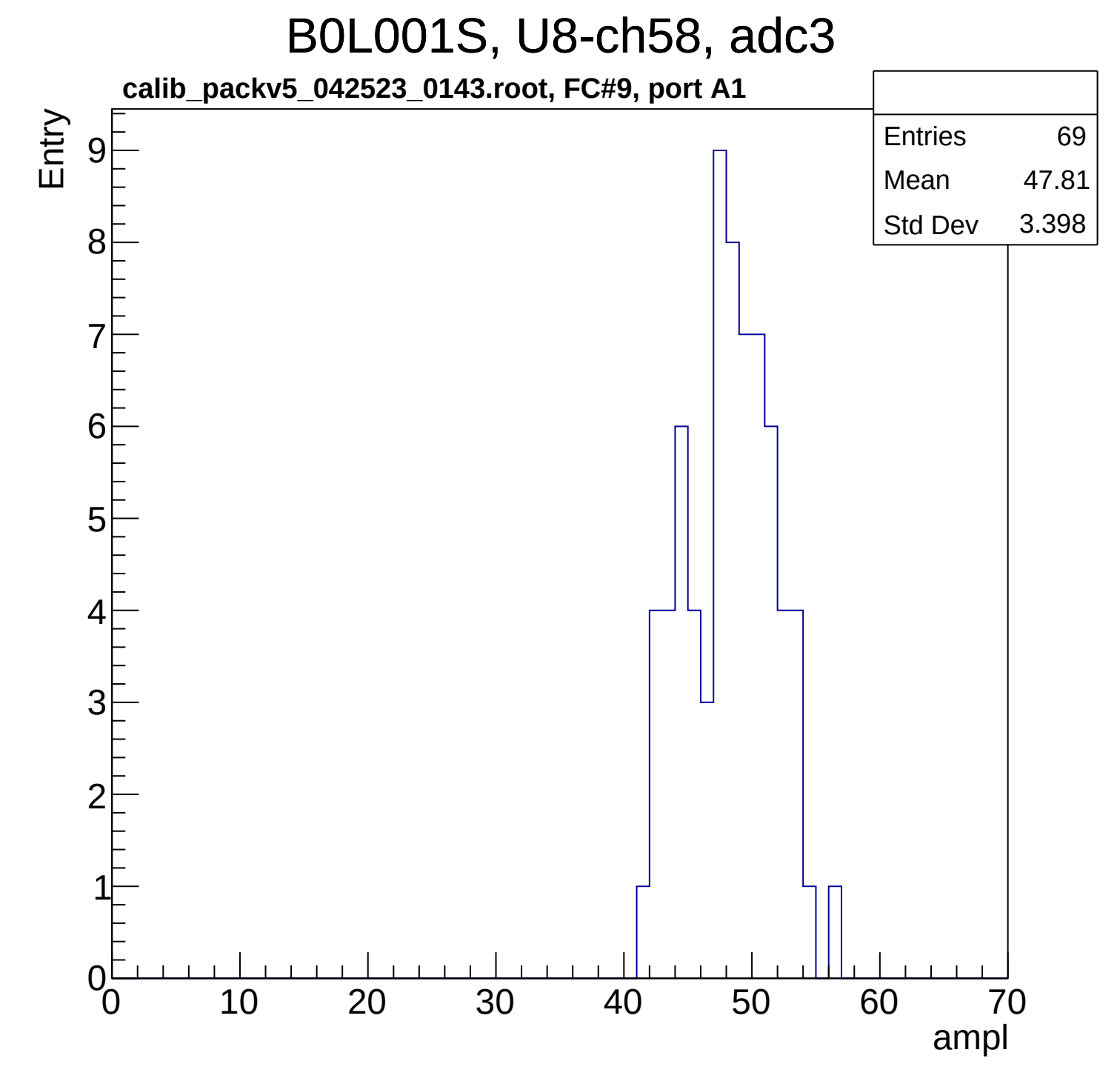
B0L001S, U8-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 47.81
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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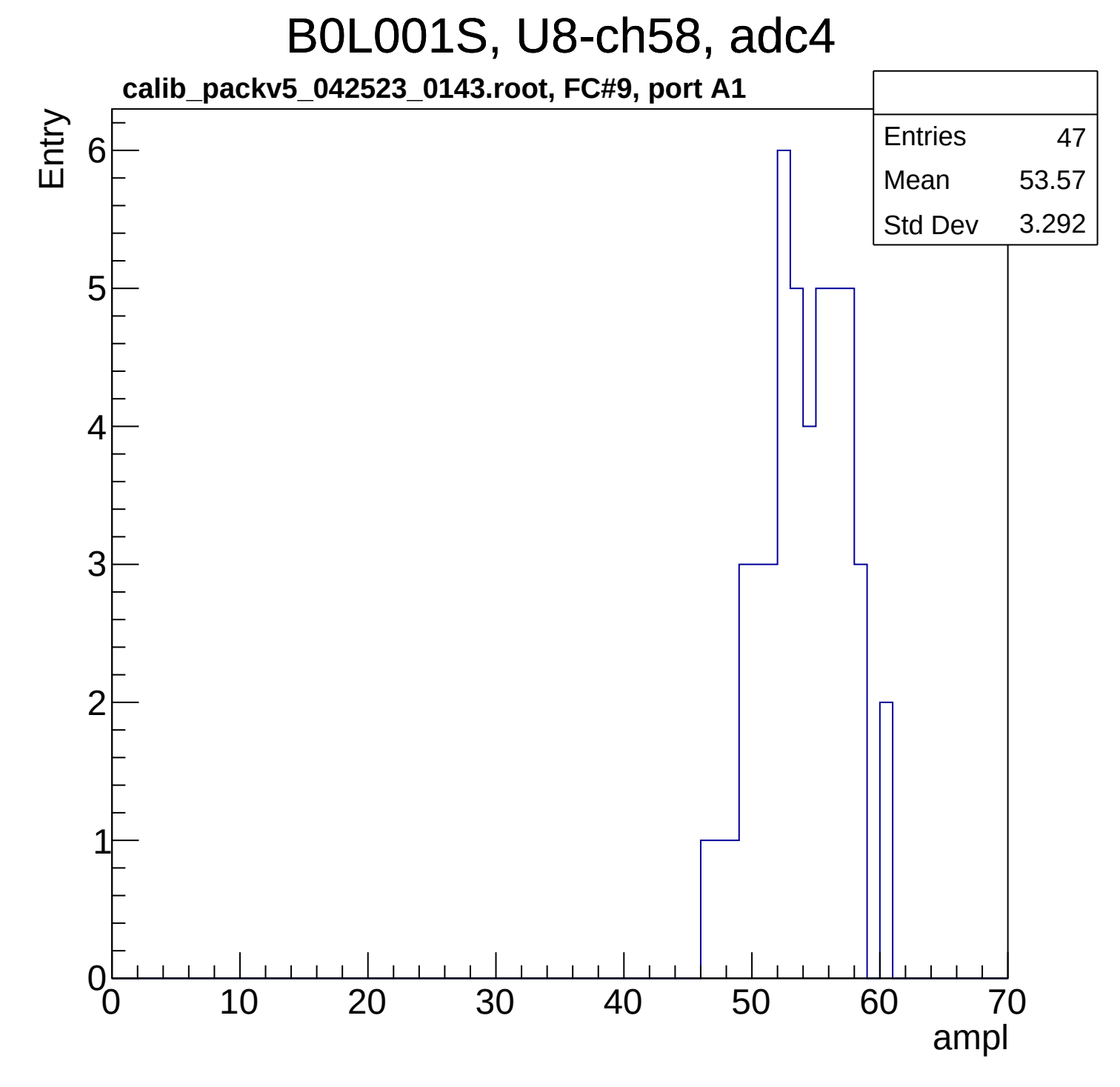
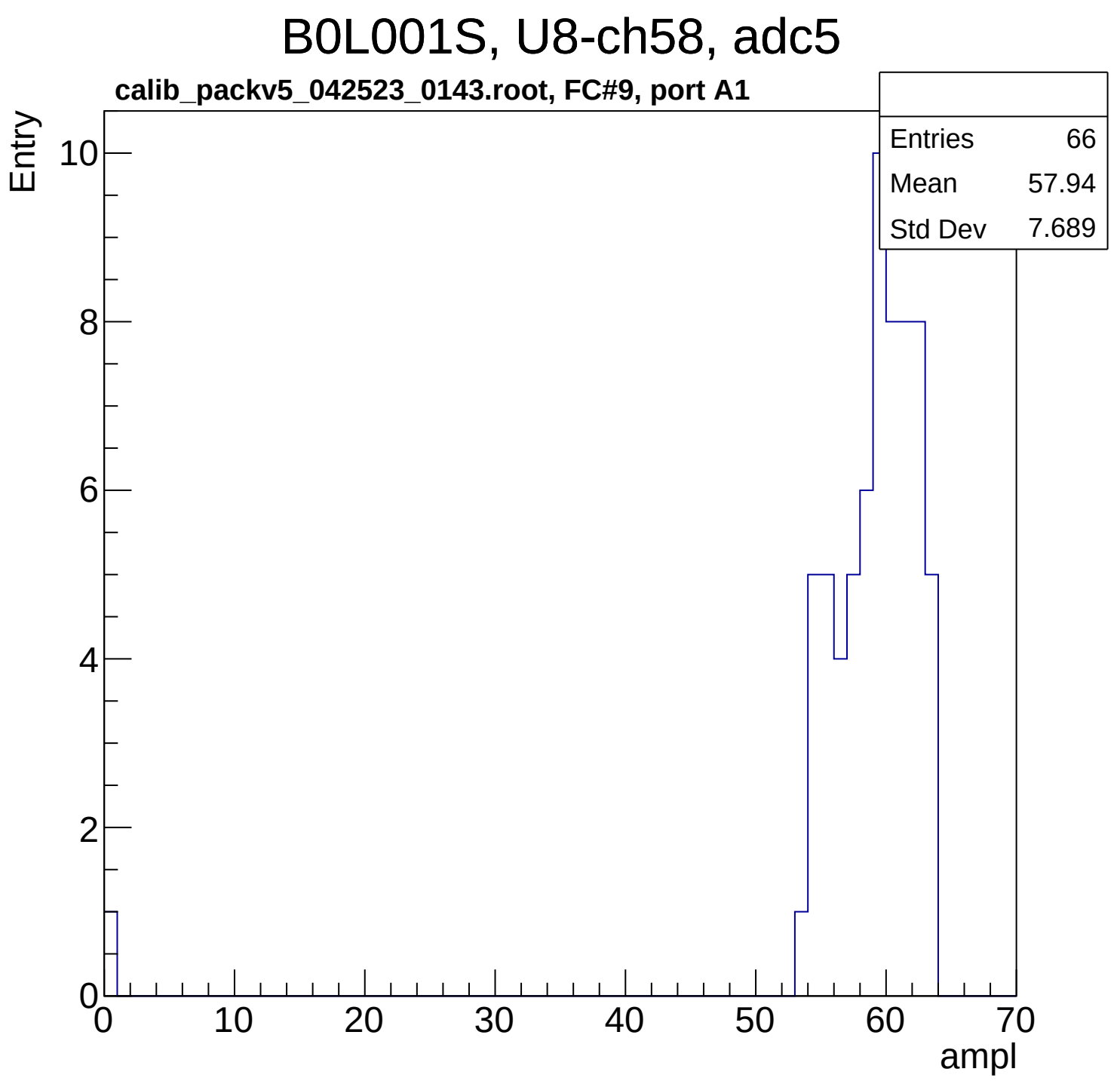
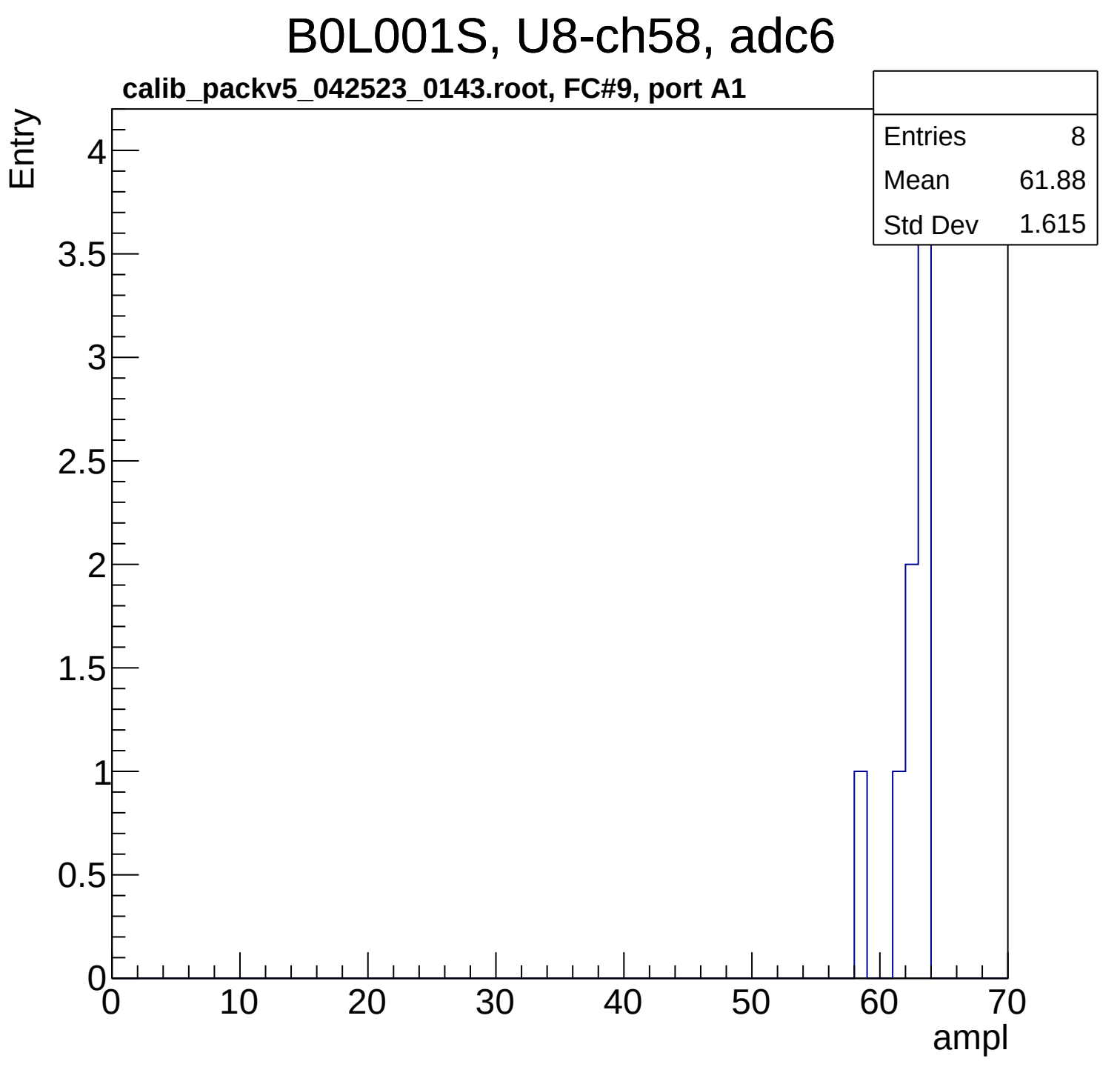

B0L001S, U8-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 53.57
Std Dev 3.292
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.94
Std Dev 7.689
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.88
Std Dev 1.615
Entry
ampl
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0.5
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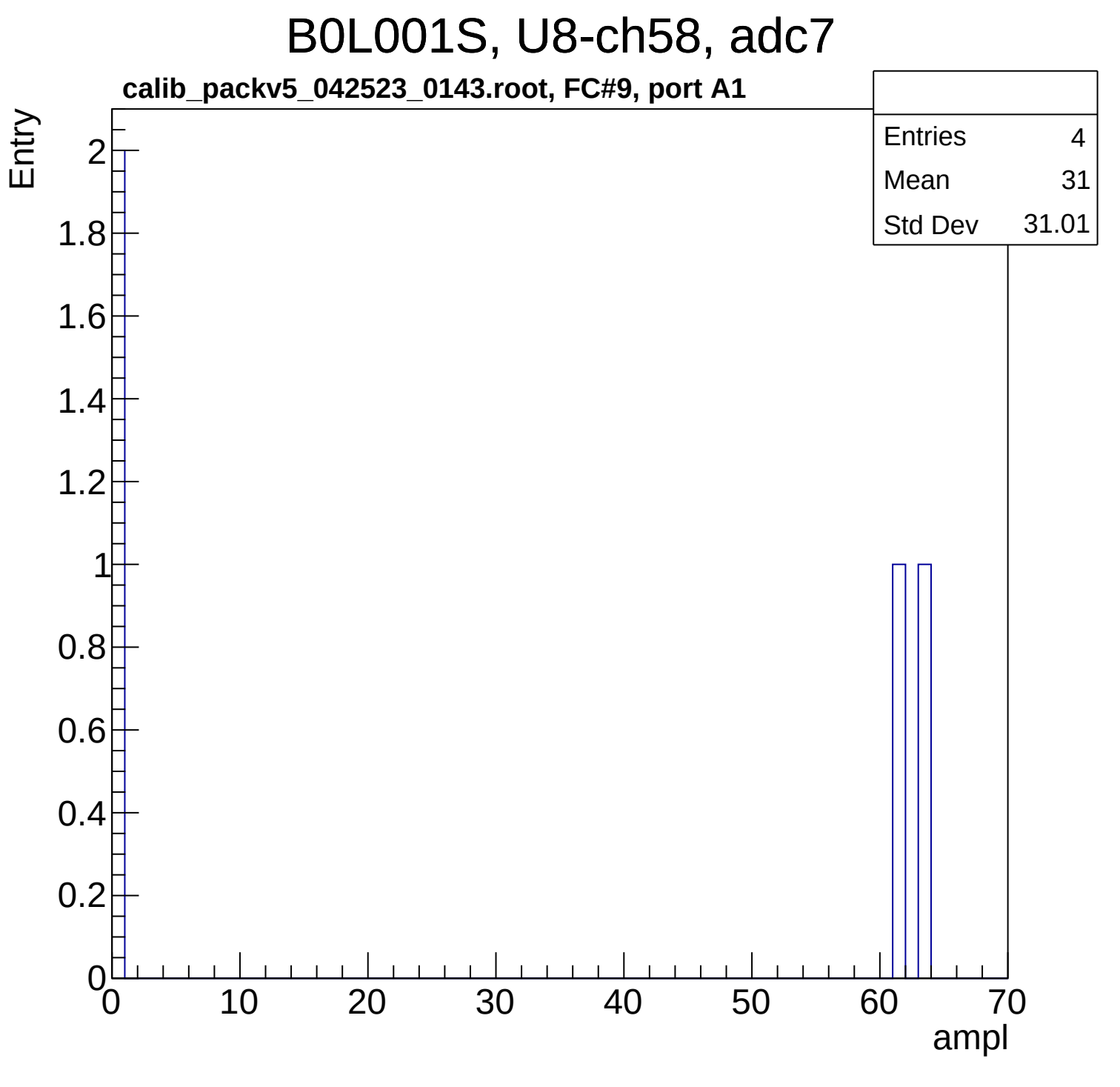
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U8-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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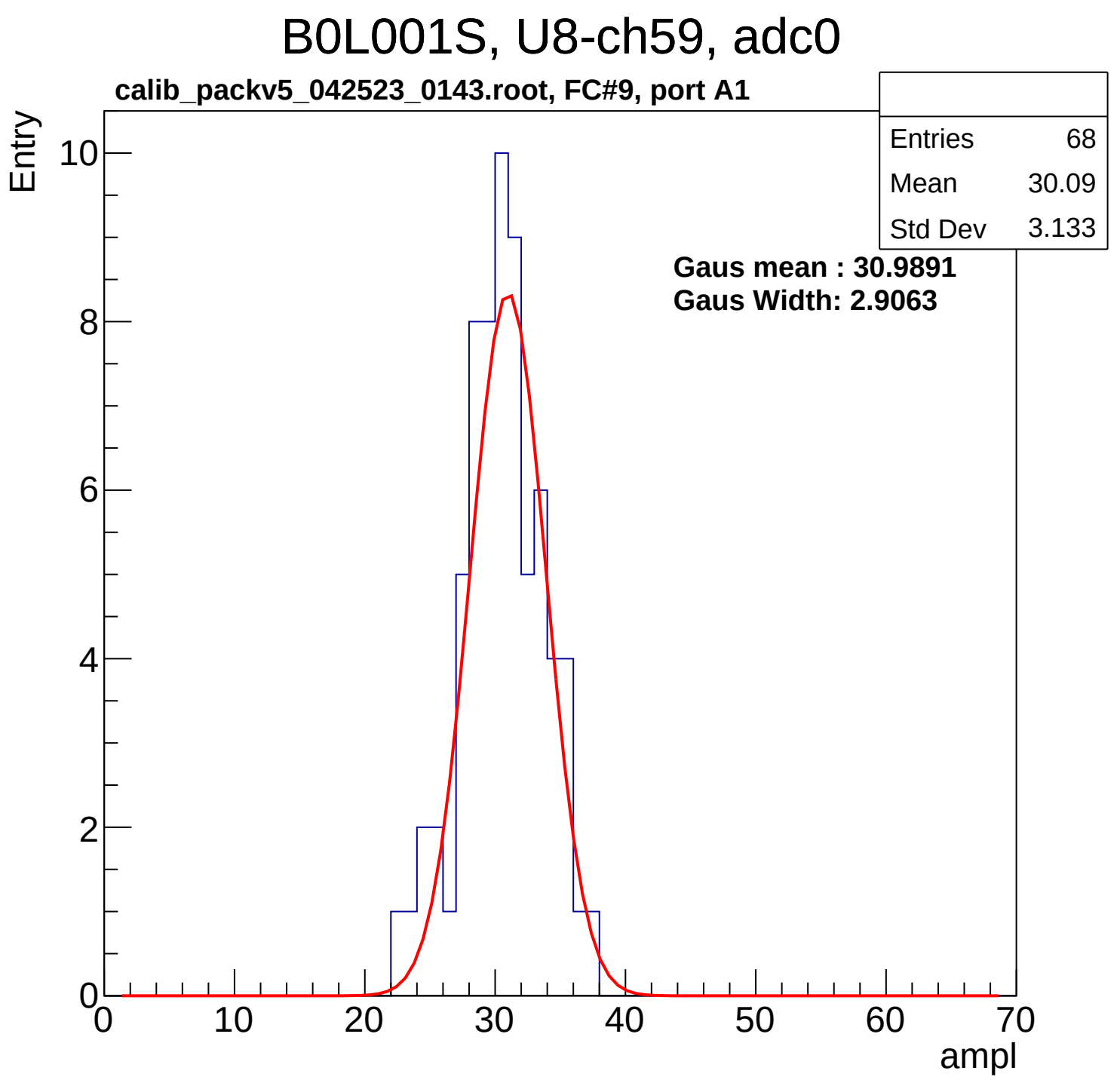
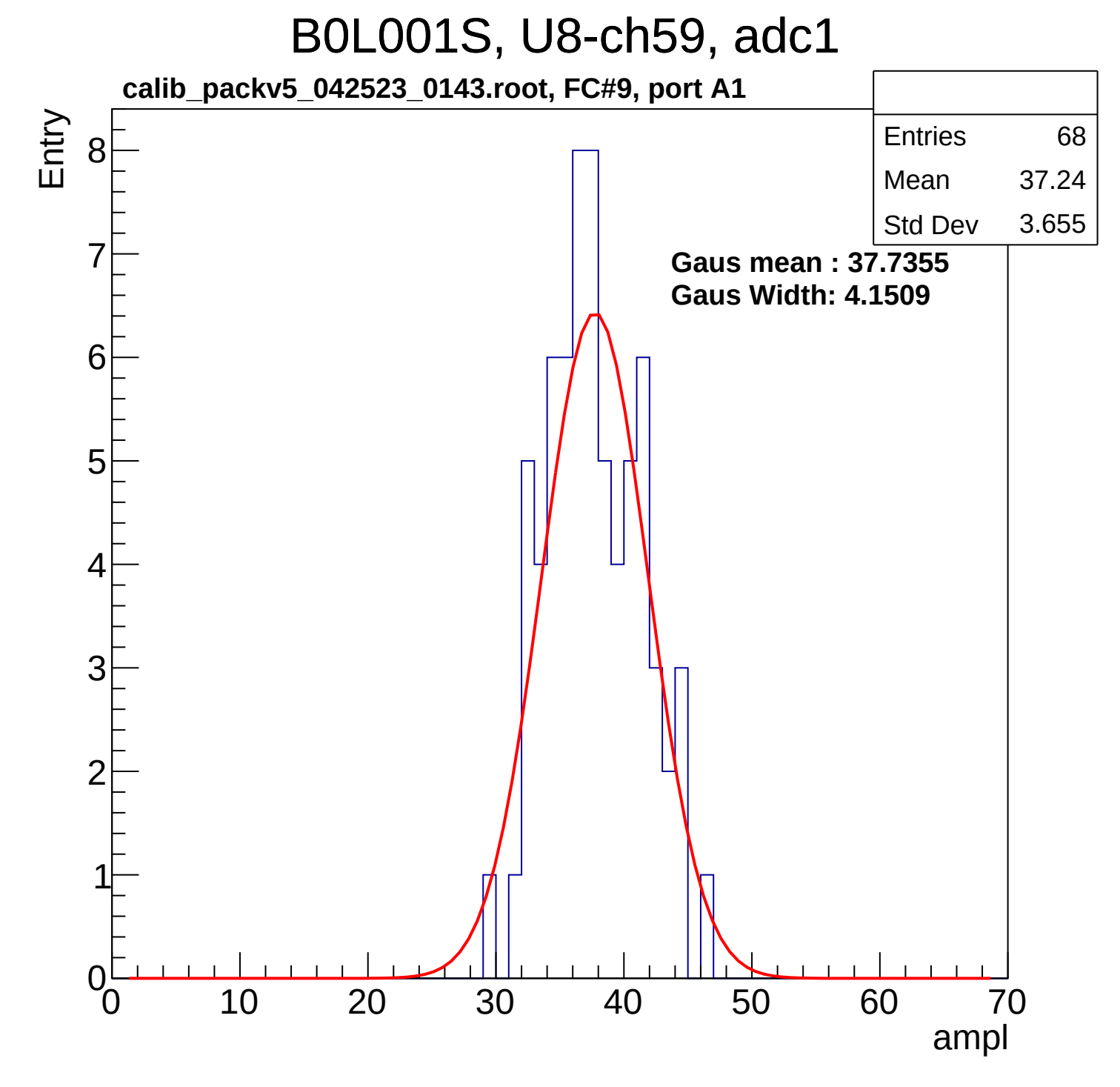

B0L001S, U8-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.09
Std Dev 3.133
Gaus mean : 30.9891
Gaus Width: 2.9063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.24
Std Dev 3.655
Gaus mean : 37.7355
Gaus Width: 4.1509
Entry
ampl
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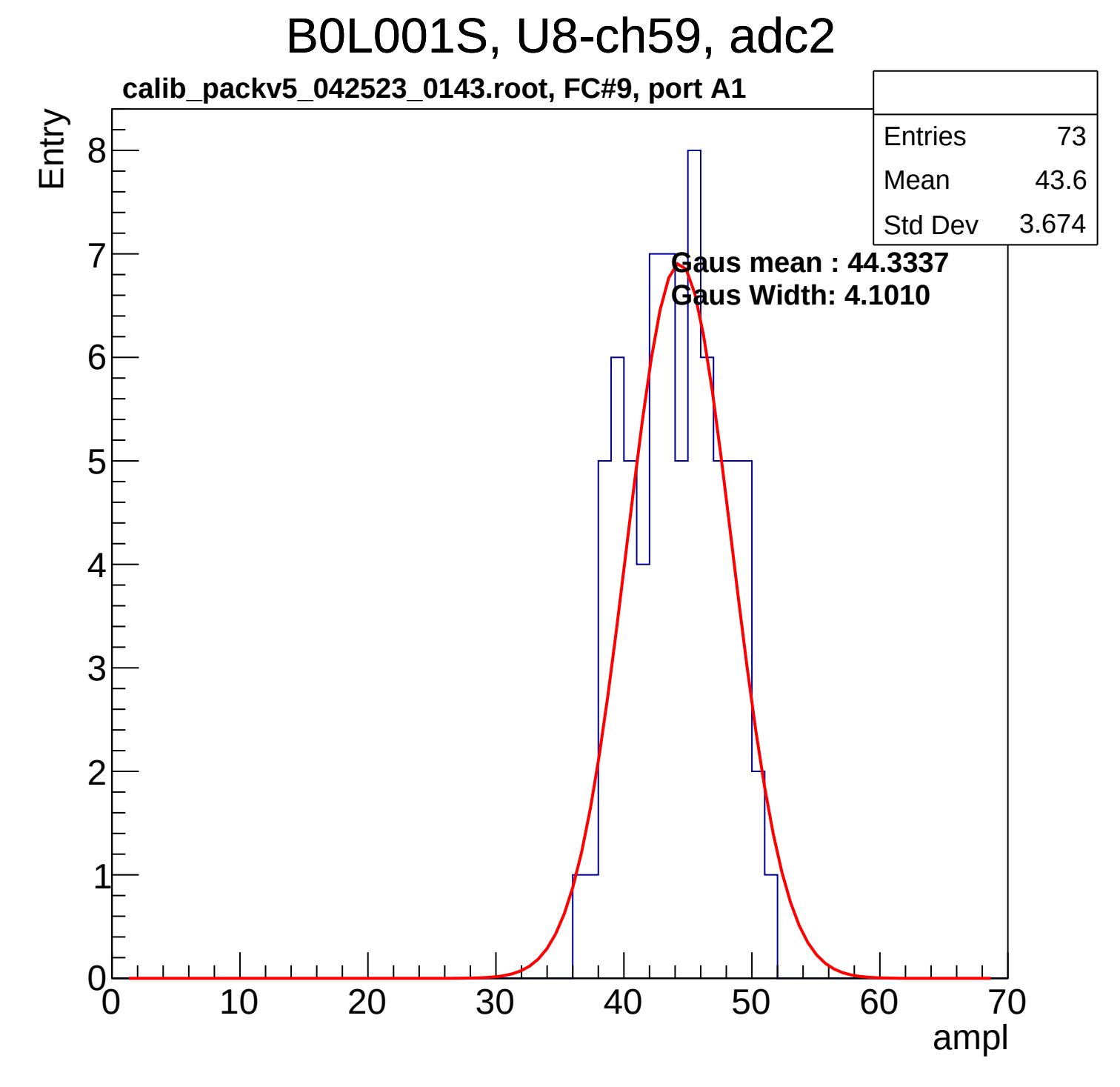
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.6
Std Dev 3.674
Gaus mean : 44.3337
Gaus Width: 4.1010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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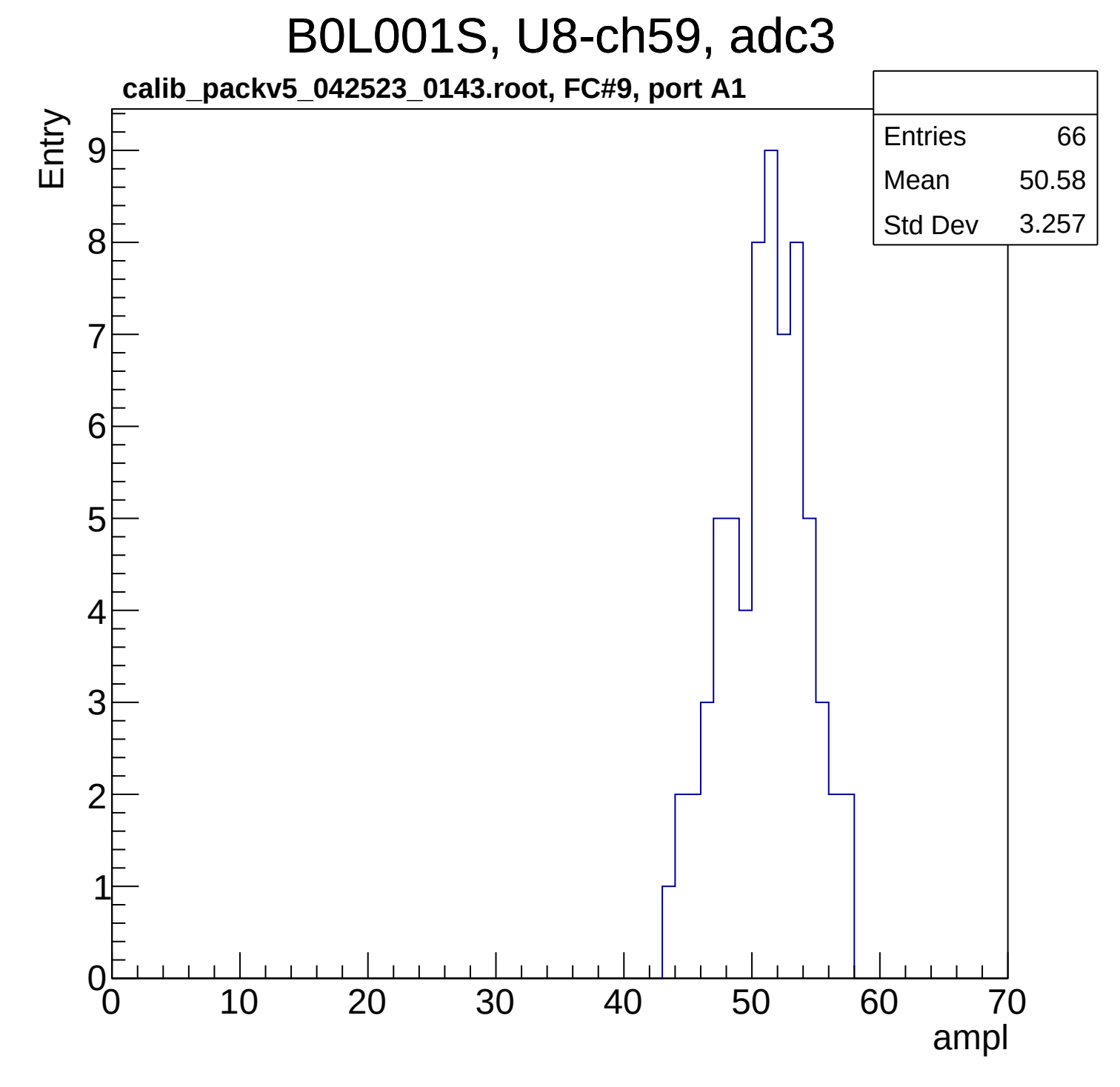
B0L001S, U8-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.58
Std Dev 3.257
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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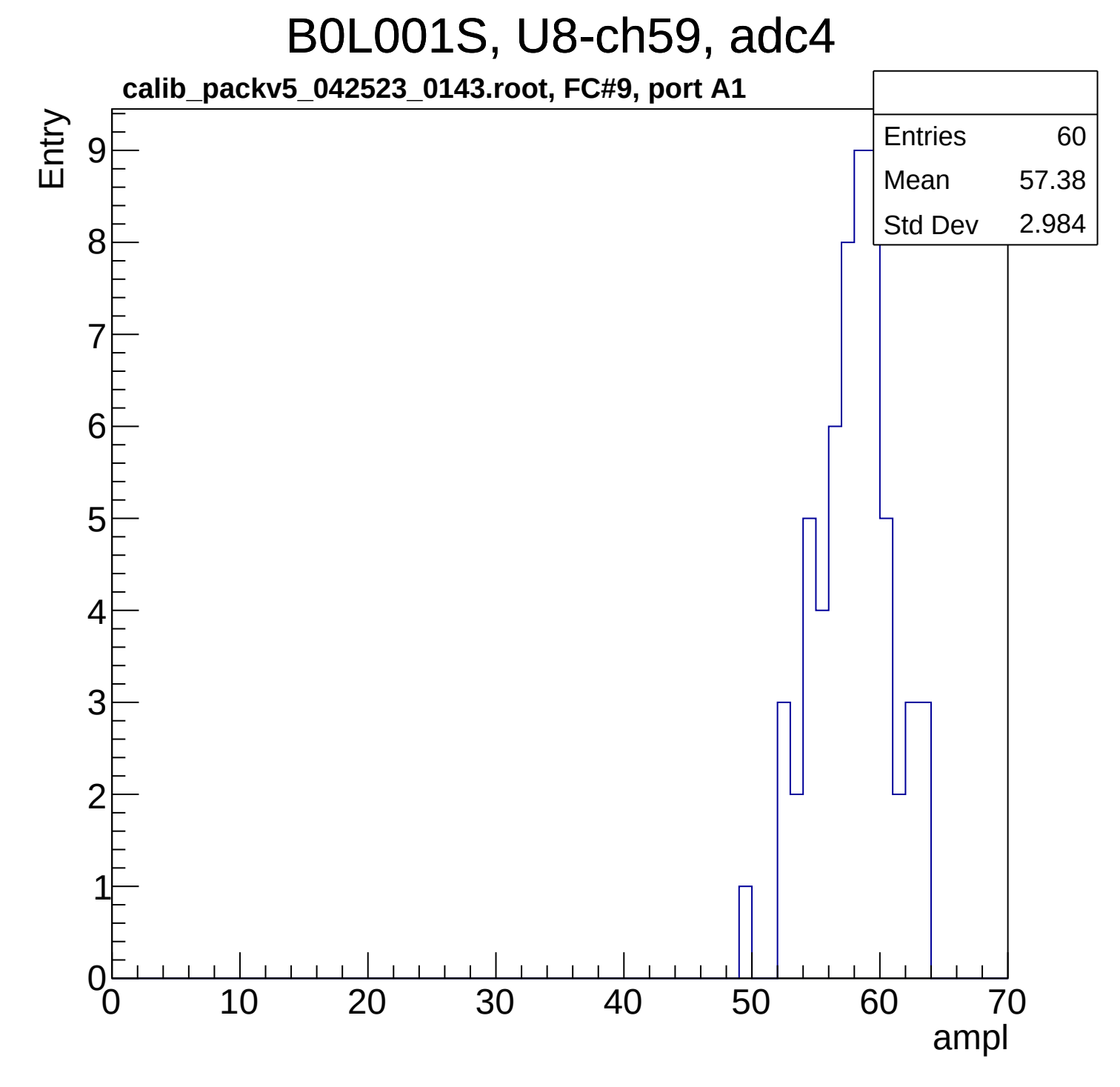
B0L001S, U8-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.38
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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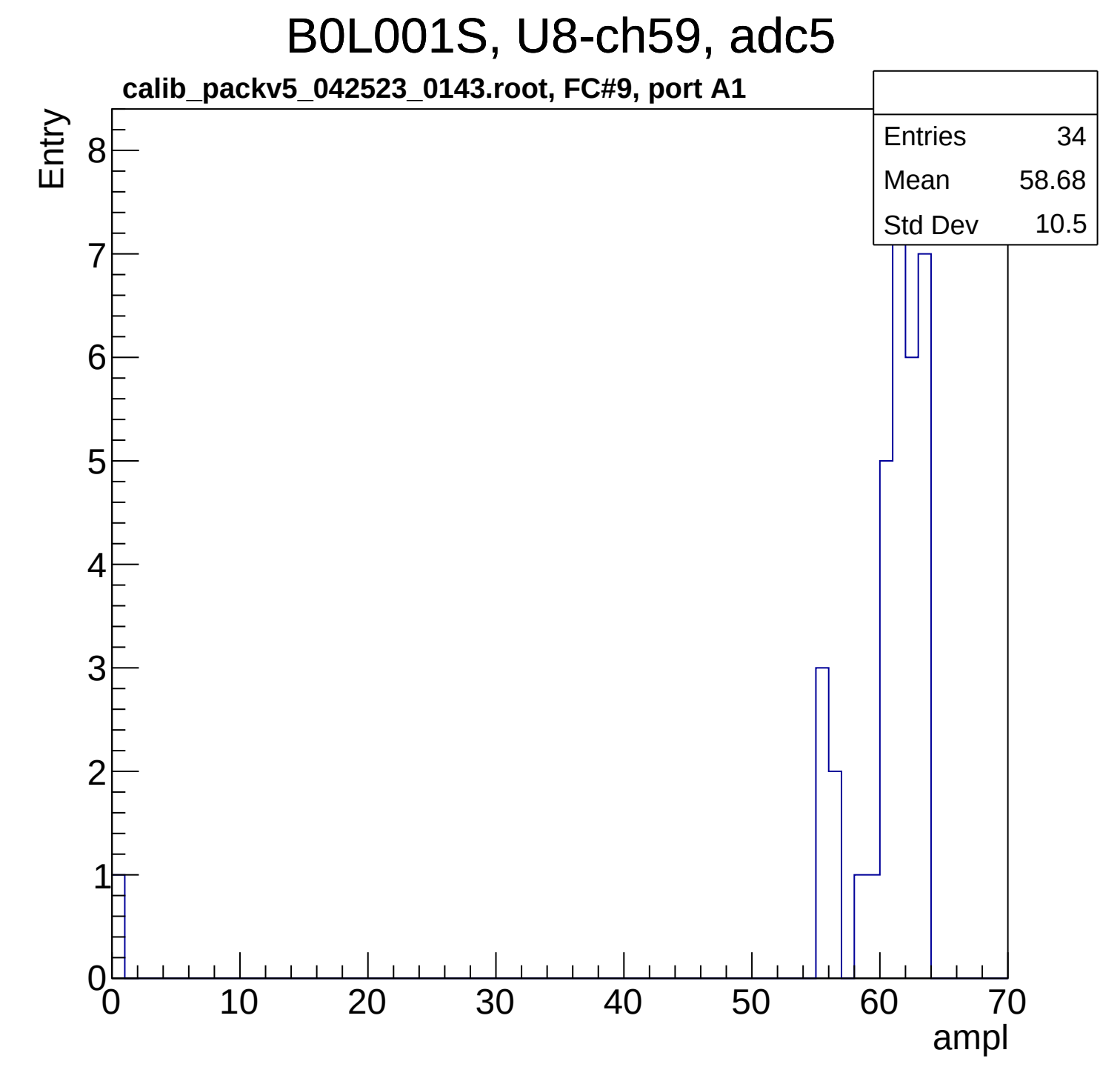
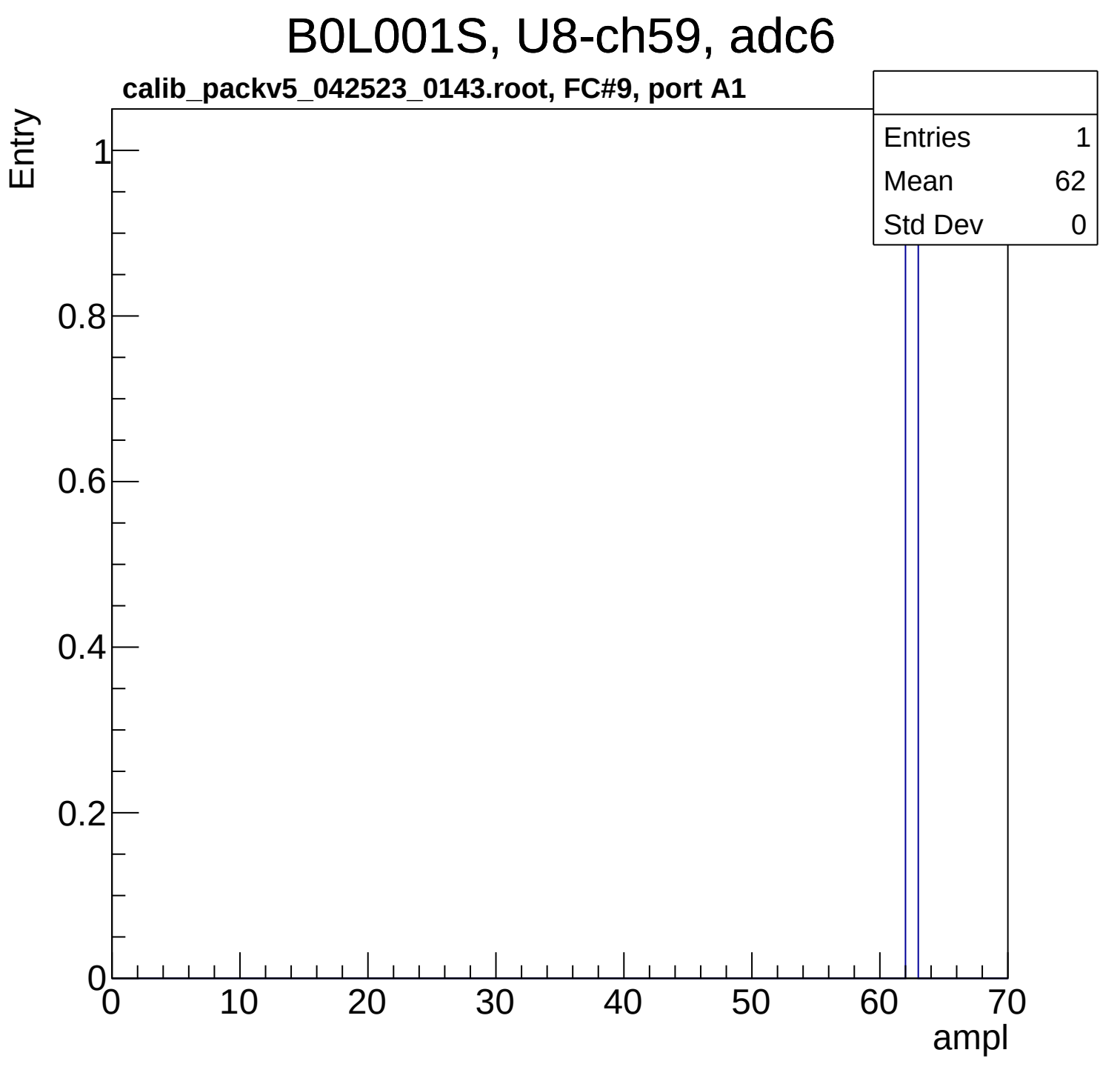
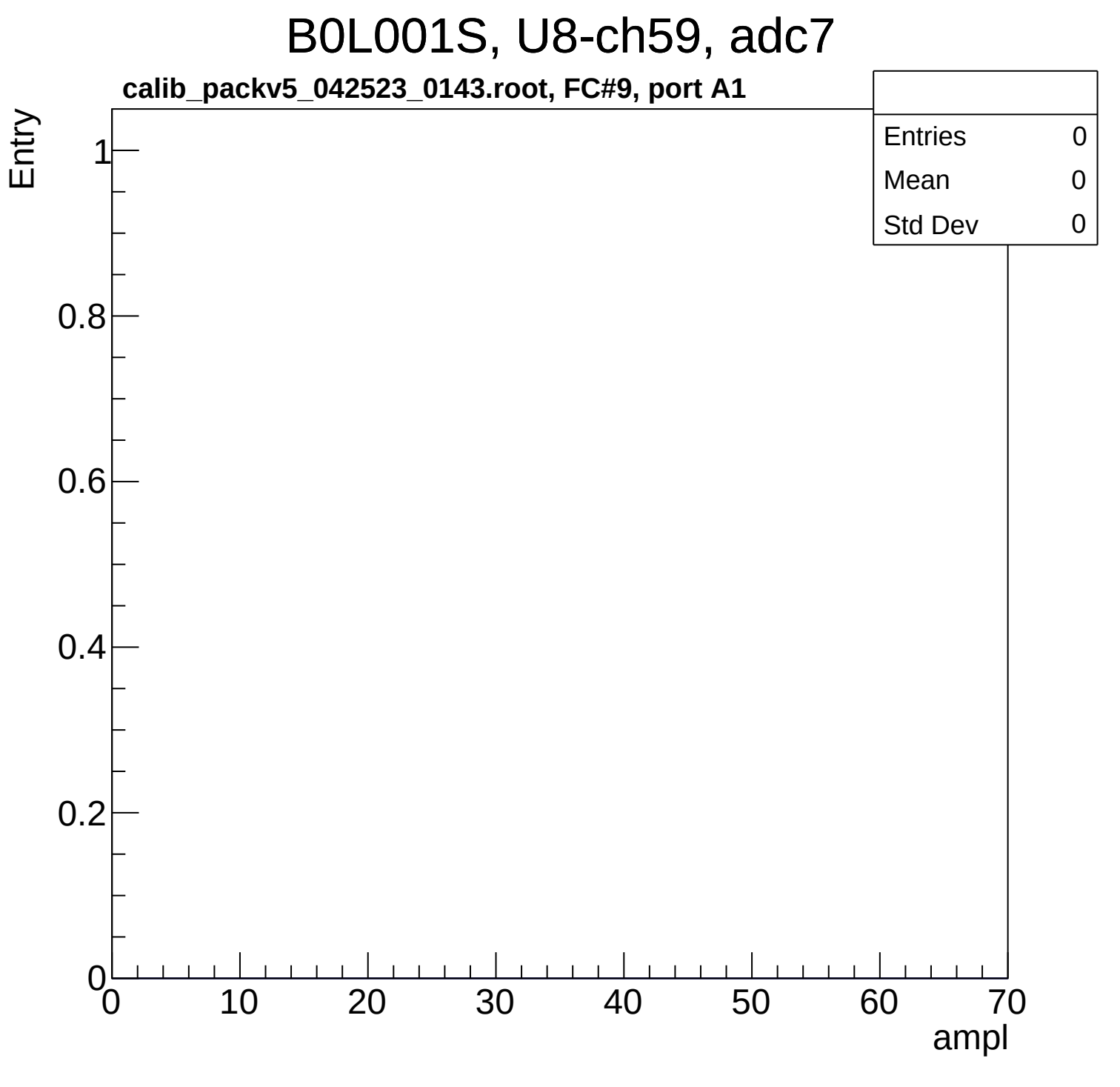
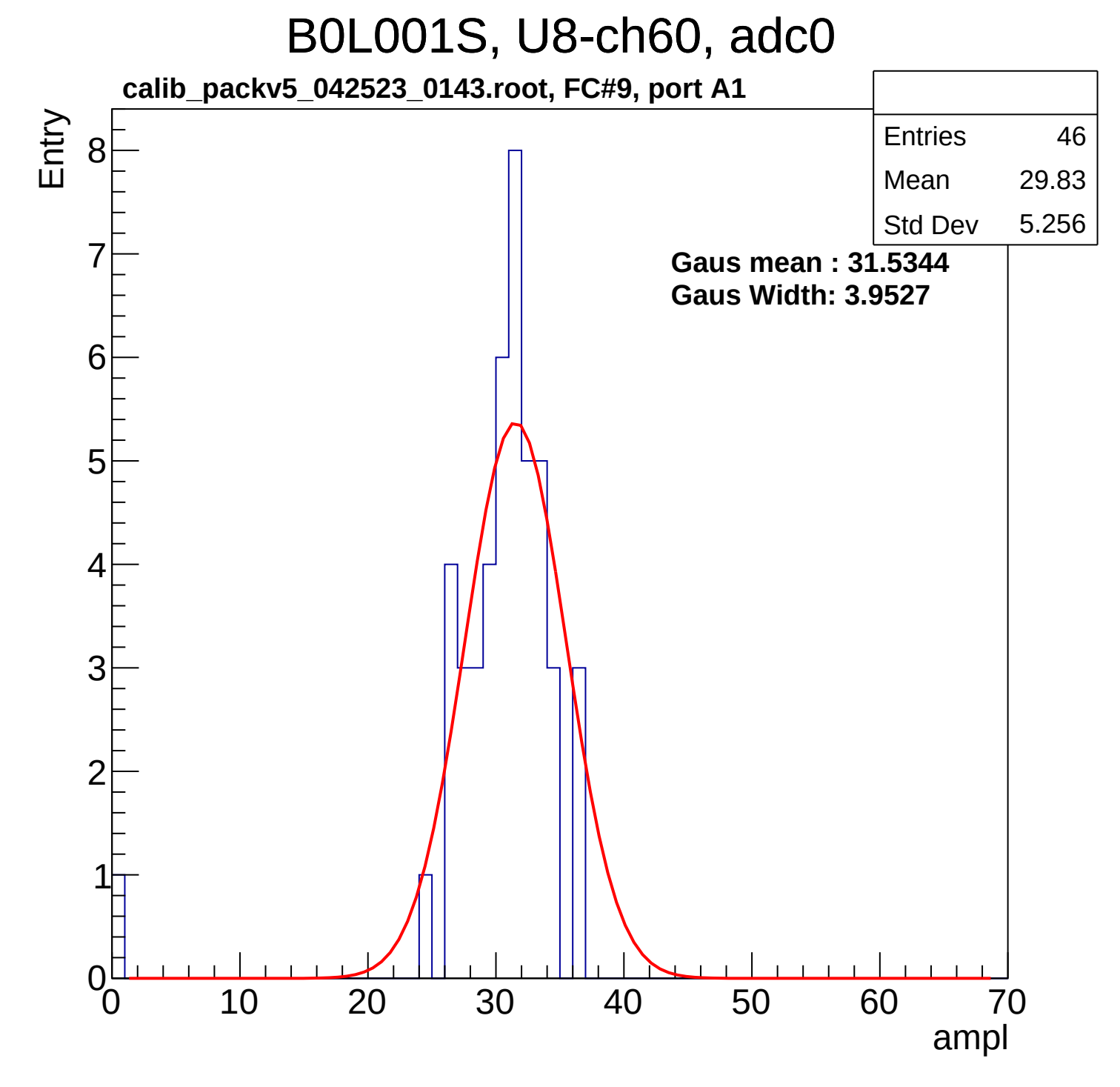

B0L001S, U8-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.68
Std Dev 10.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 29.83
Std Dev 5.256
Gaus mean : 31.5344
Gaus Width: 3.9527
Entry
ampl
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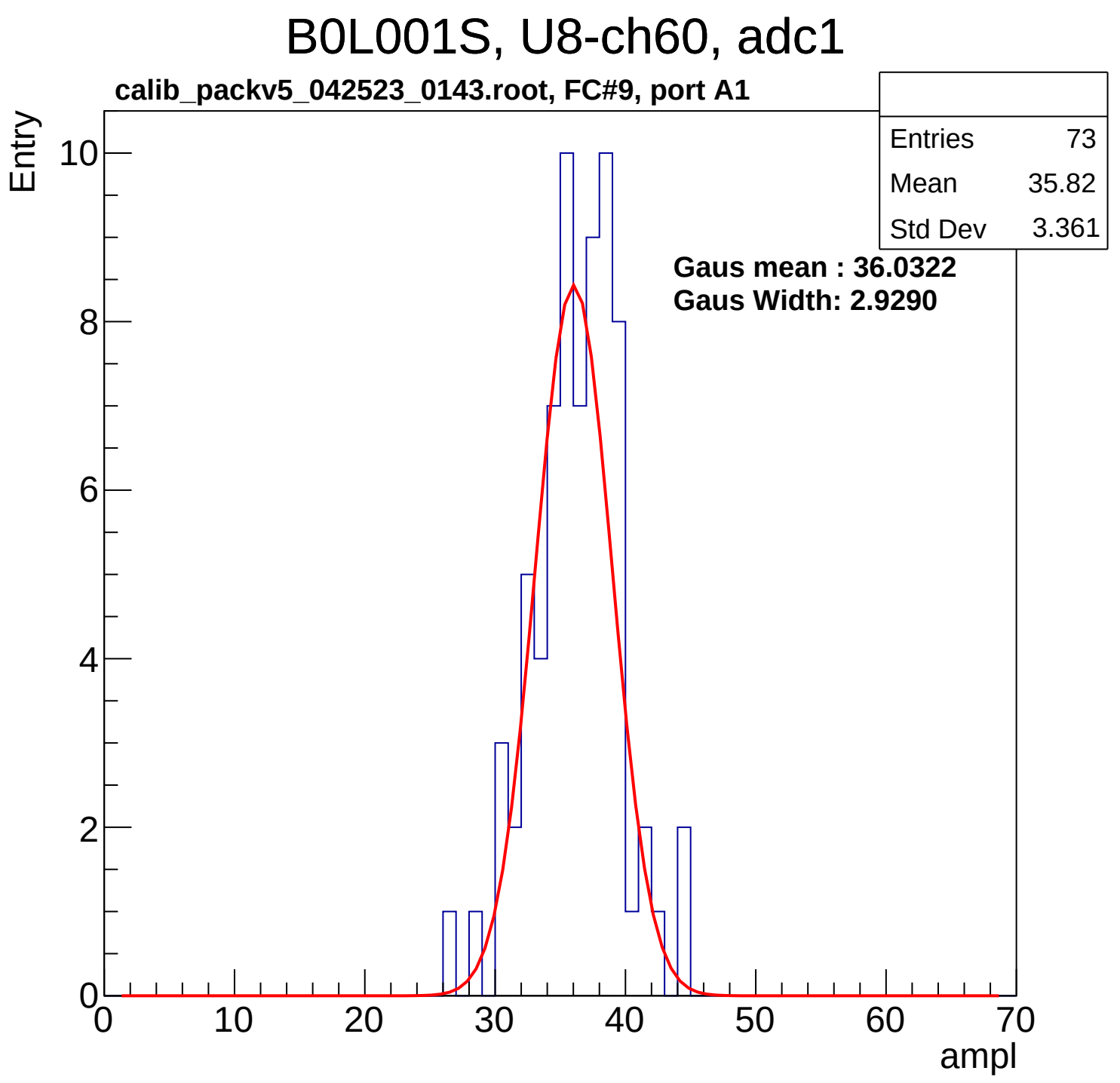
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 35.82
Std Dev 3.361
Gaus mean : 36.0322
Gaus Width: 2.9290
Entry
ampl
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2
4
6
8
10
0
10
20
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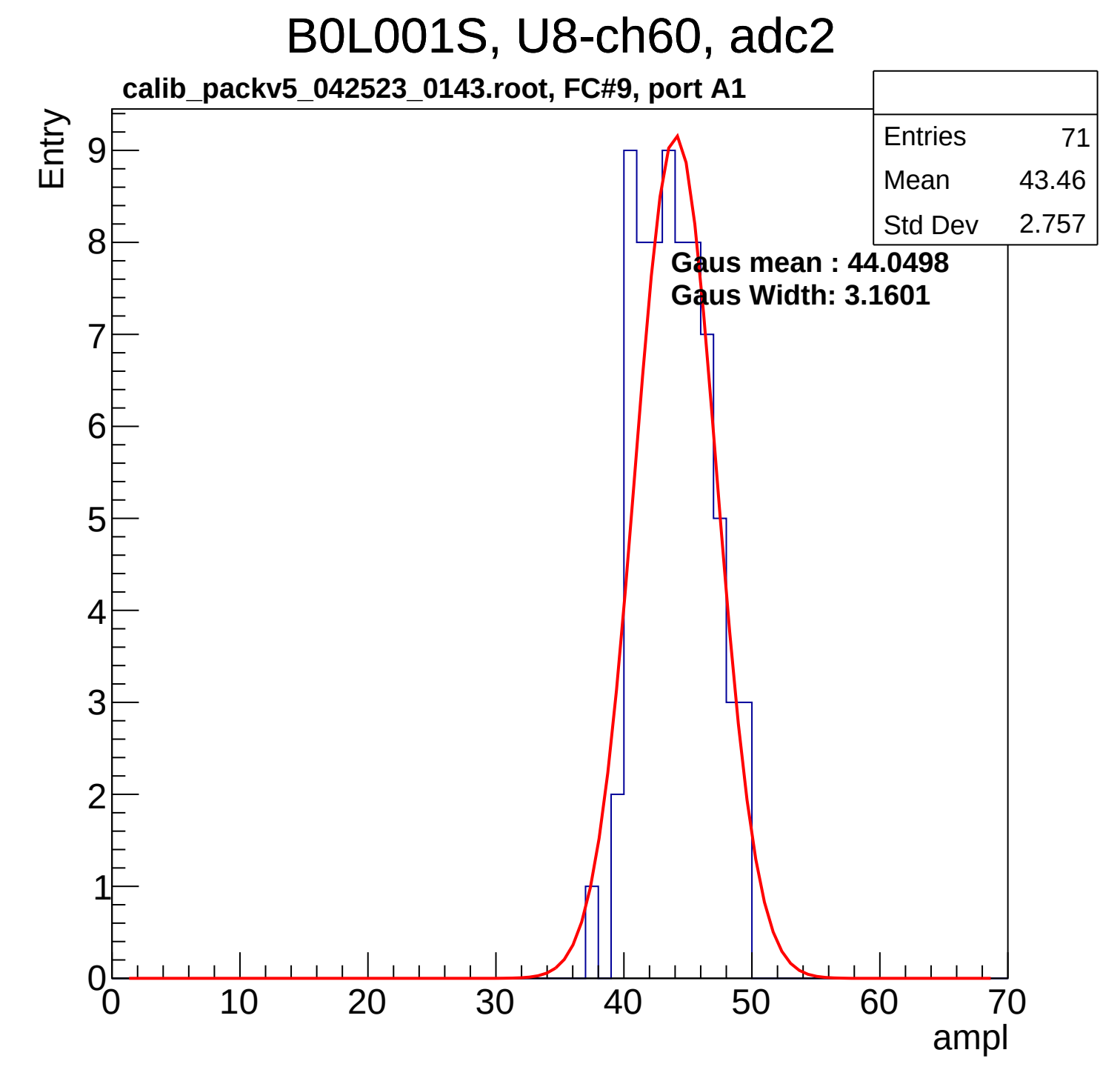
40
50
60
70


B0L001S, U8-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.46
Std Dev 2.757
Gaus mean : 44.0498
Gaus Width: 3.1601
Entry
ampl
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3
4
5
6
7
8
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0
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60
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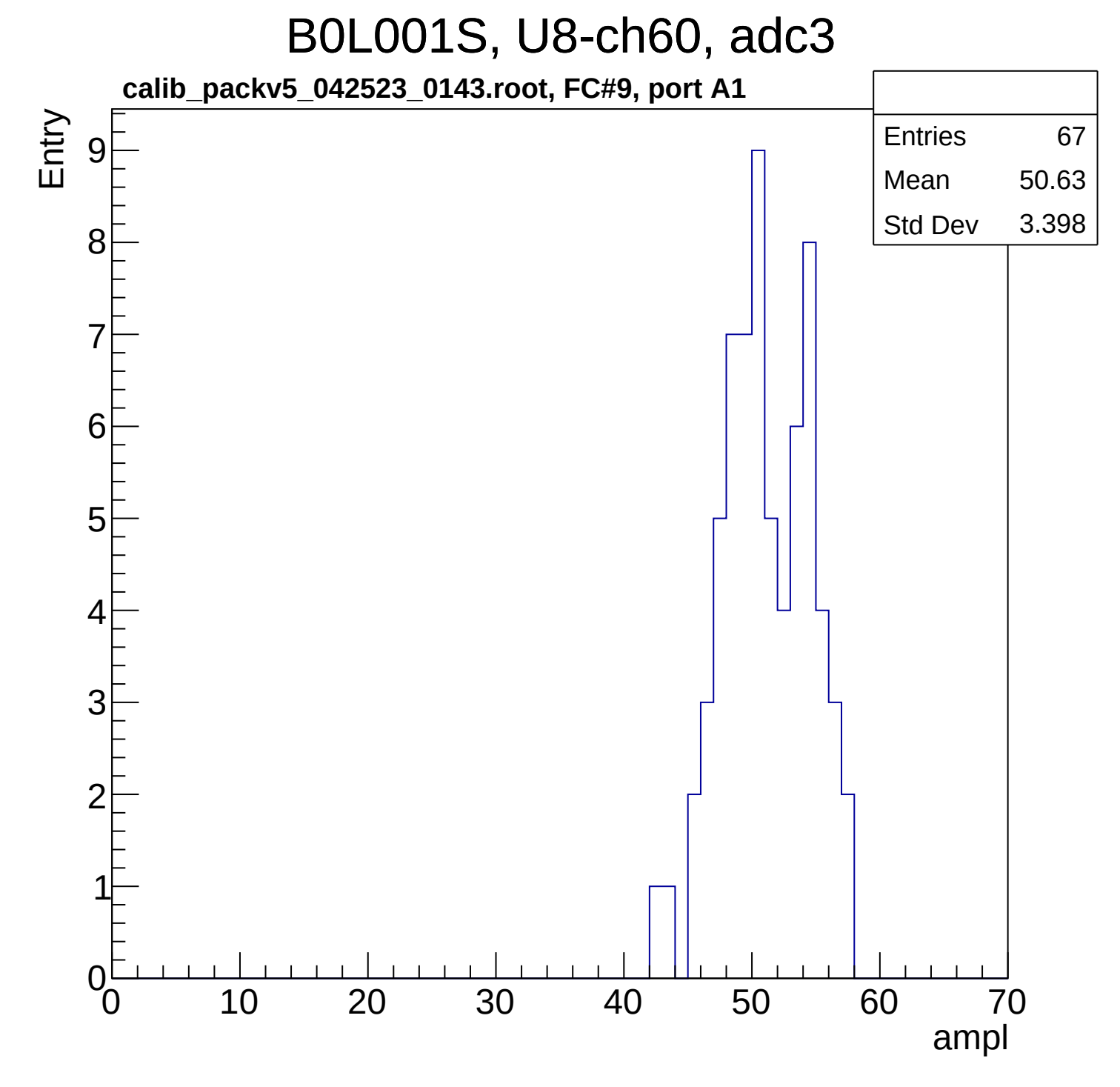
B0L001S, U8-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.63
Std Dev 3.398
Entry
ampl
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3
4
5
6
7
8
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0
10
20
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40
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60
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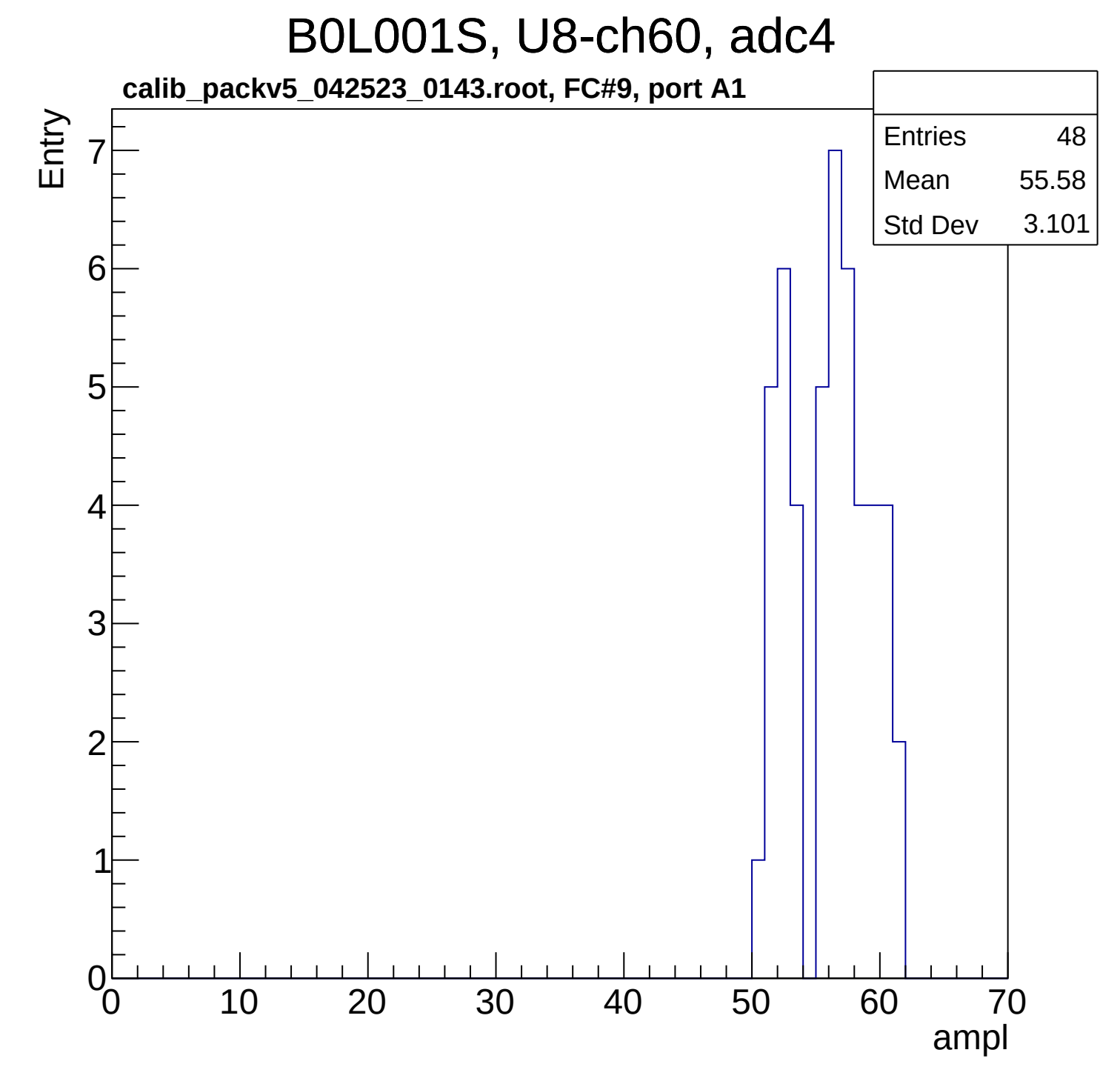
B0L001S, U8-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.58
Std Dev 3.101
Entry
ampl
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4
5
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60
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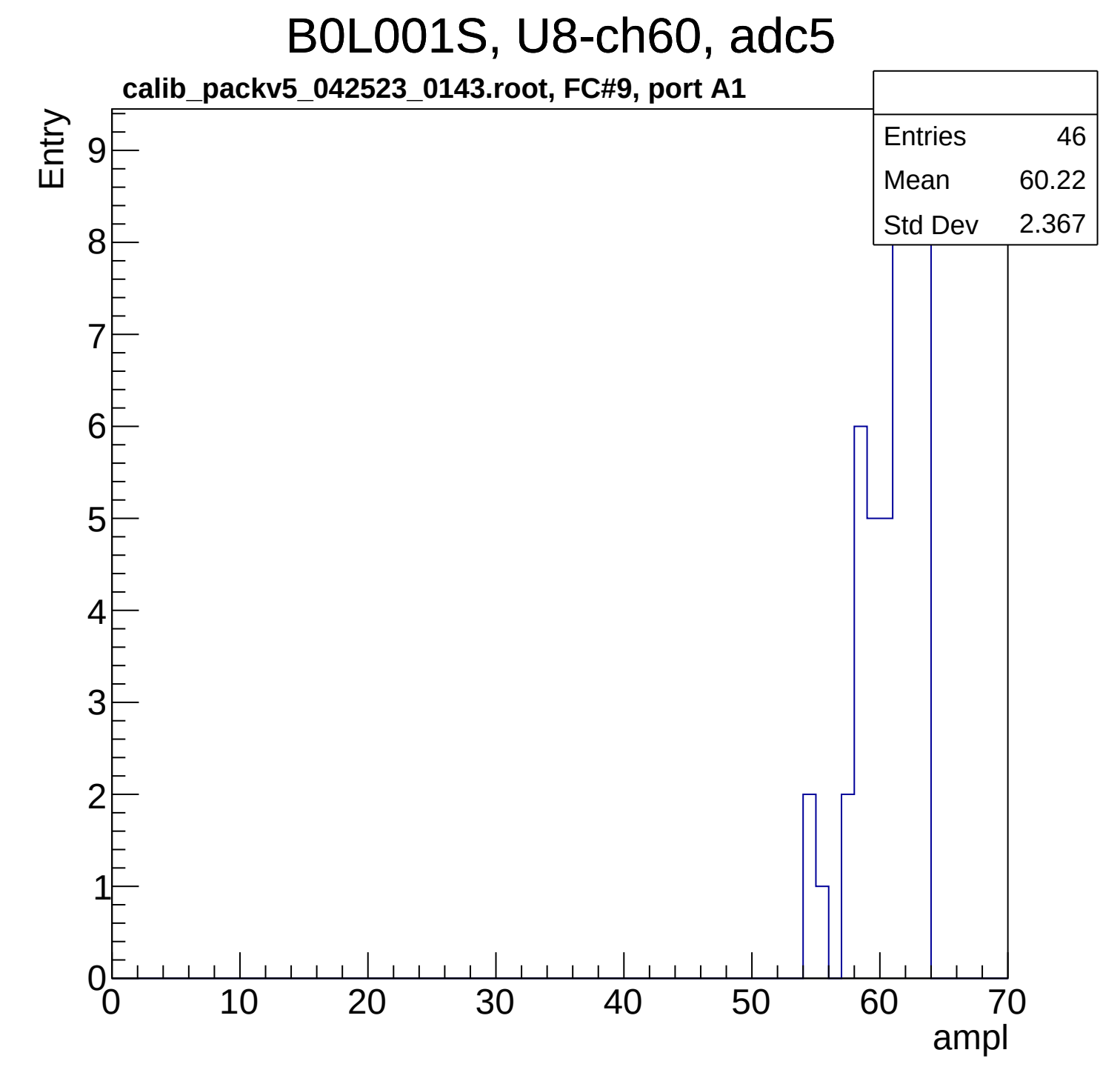
B0L001S, U8-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 60.22
Std Dev 2.367
Entry
ampl
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3
4
5
6
7
8
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0
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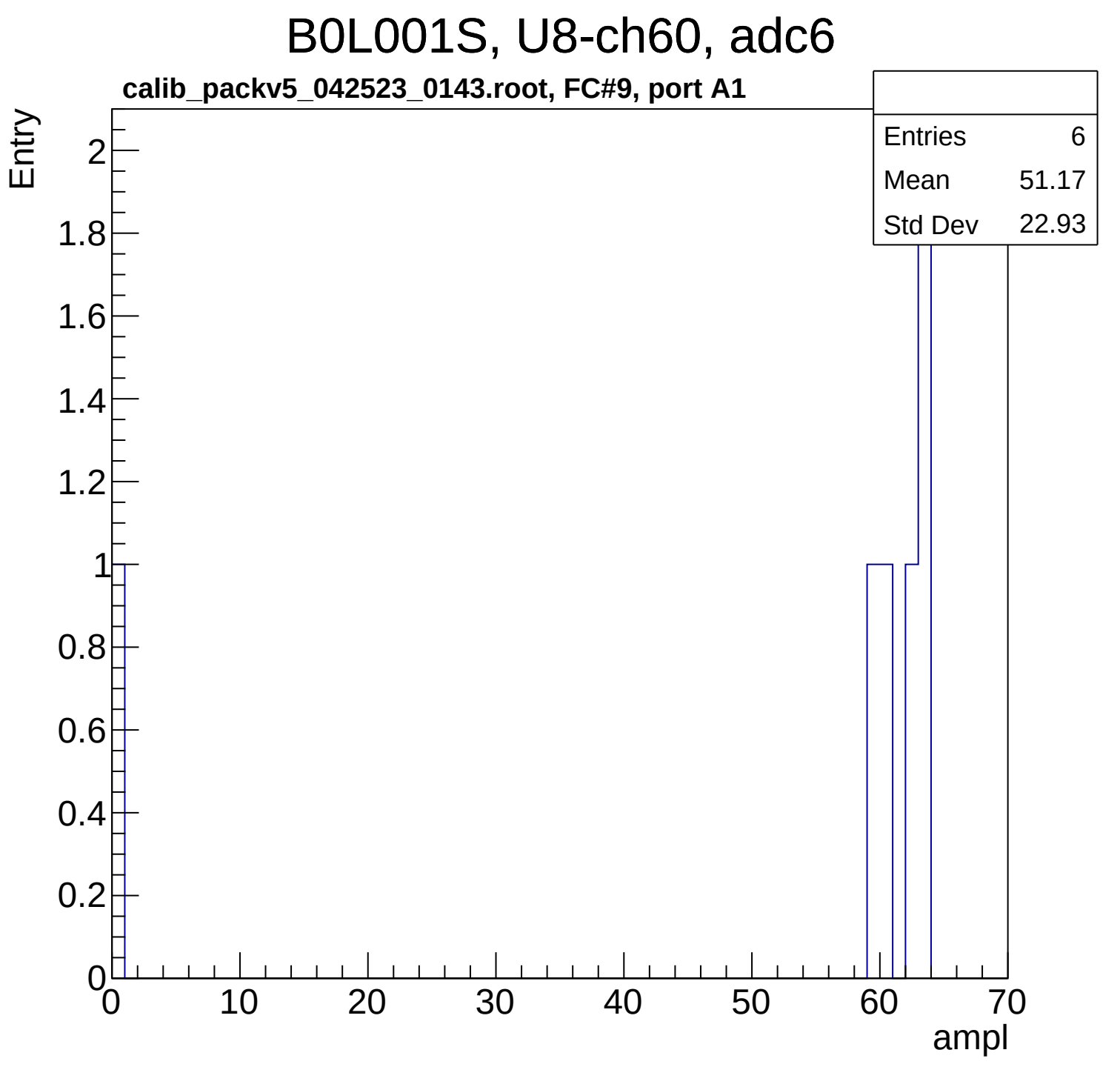
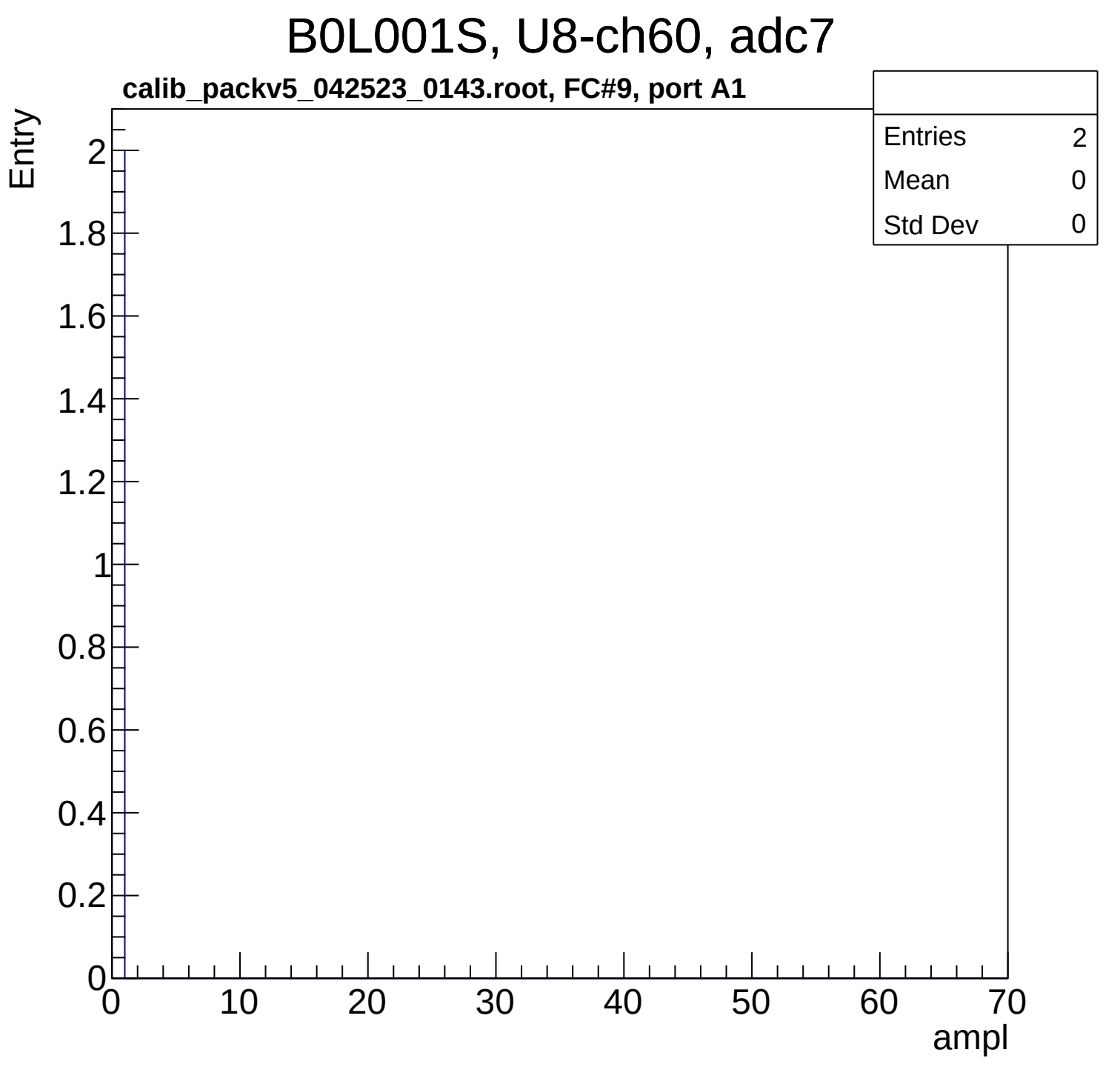

B0L001S, U8-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.17
Std Dev 22.93
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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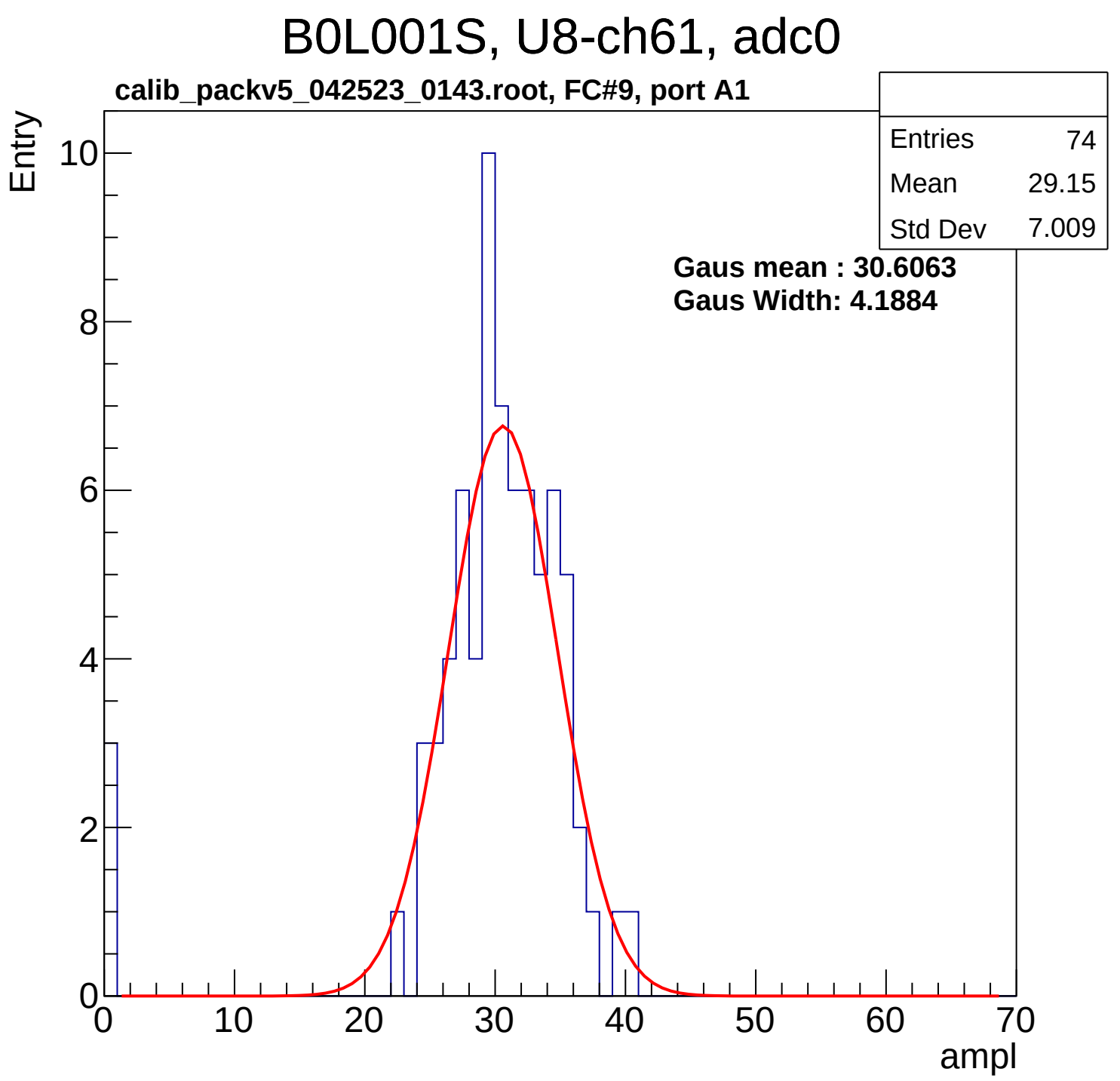
B0L001S, U8-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.15
Std Dev 7.009
Gaus mean : 30.6063
Gaus Width: 4.1884
Entry
ampl
0
2
4
6
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10
0
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20
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40
50
60
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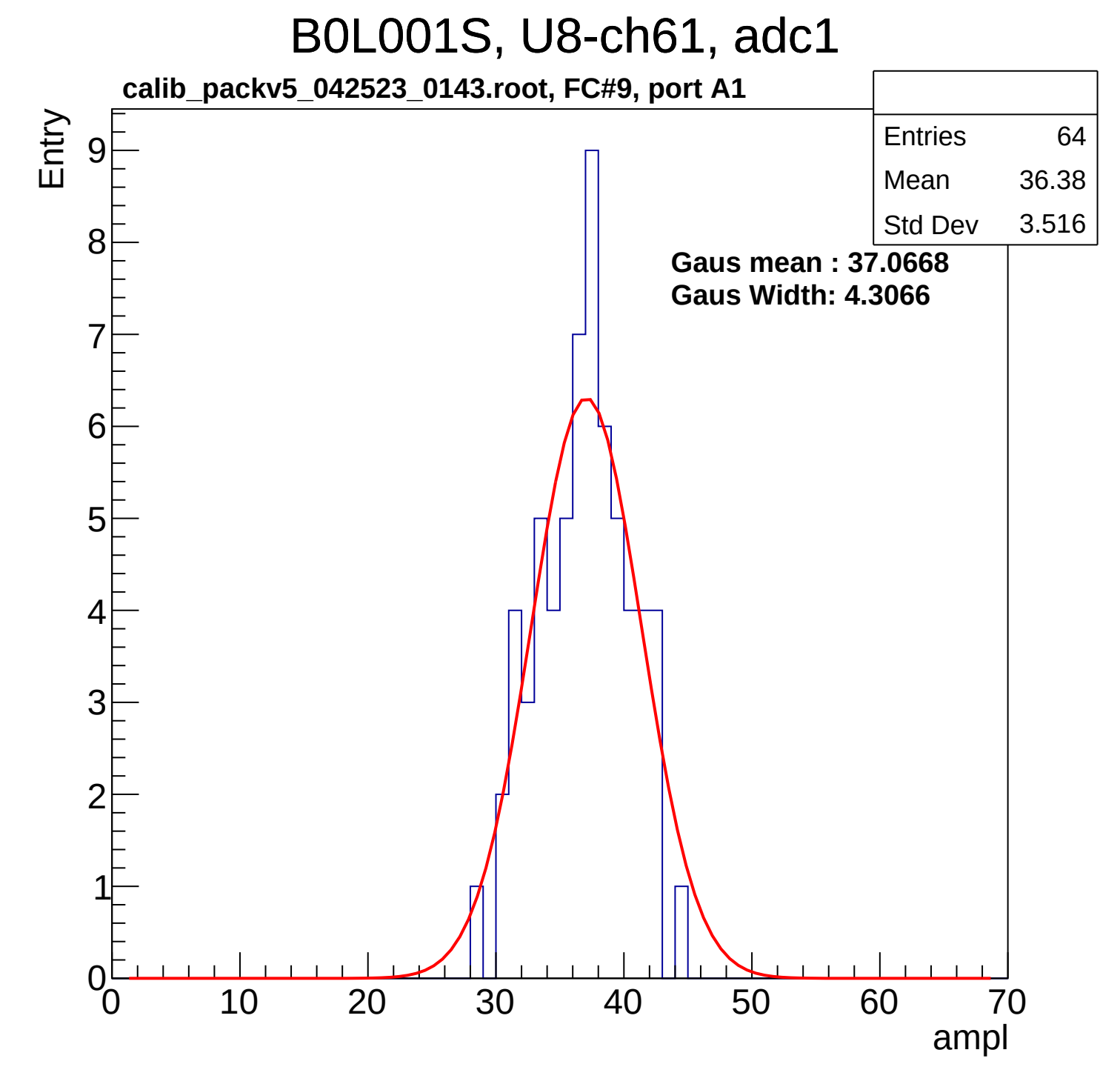
B0L001S, U8-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.38
Std Dev 3.516
Gaus mean : 37.0668
Gaus Width: 4.3066
Entry
ampl
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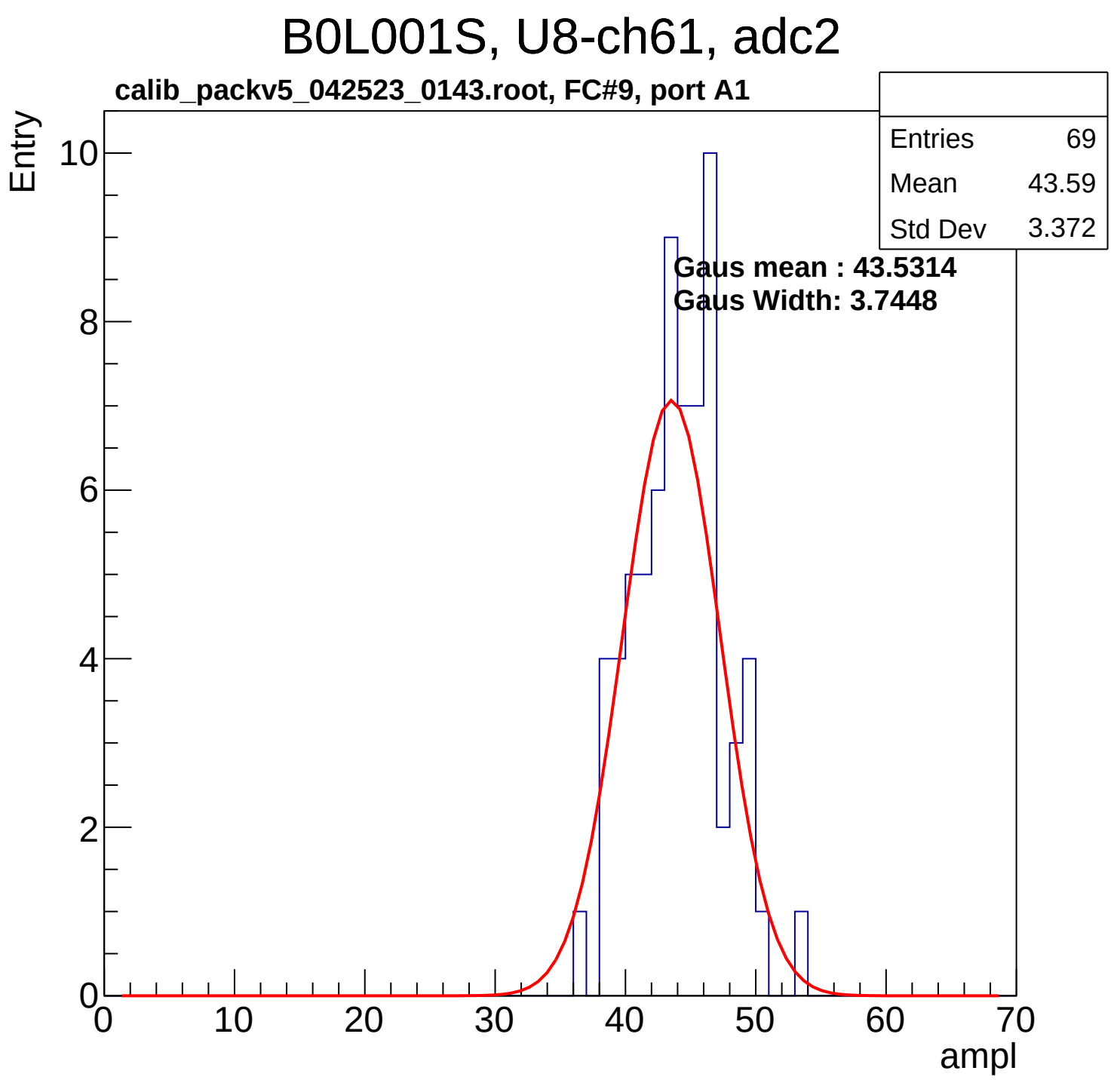
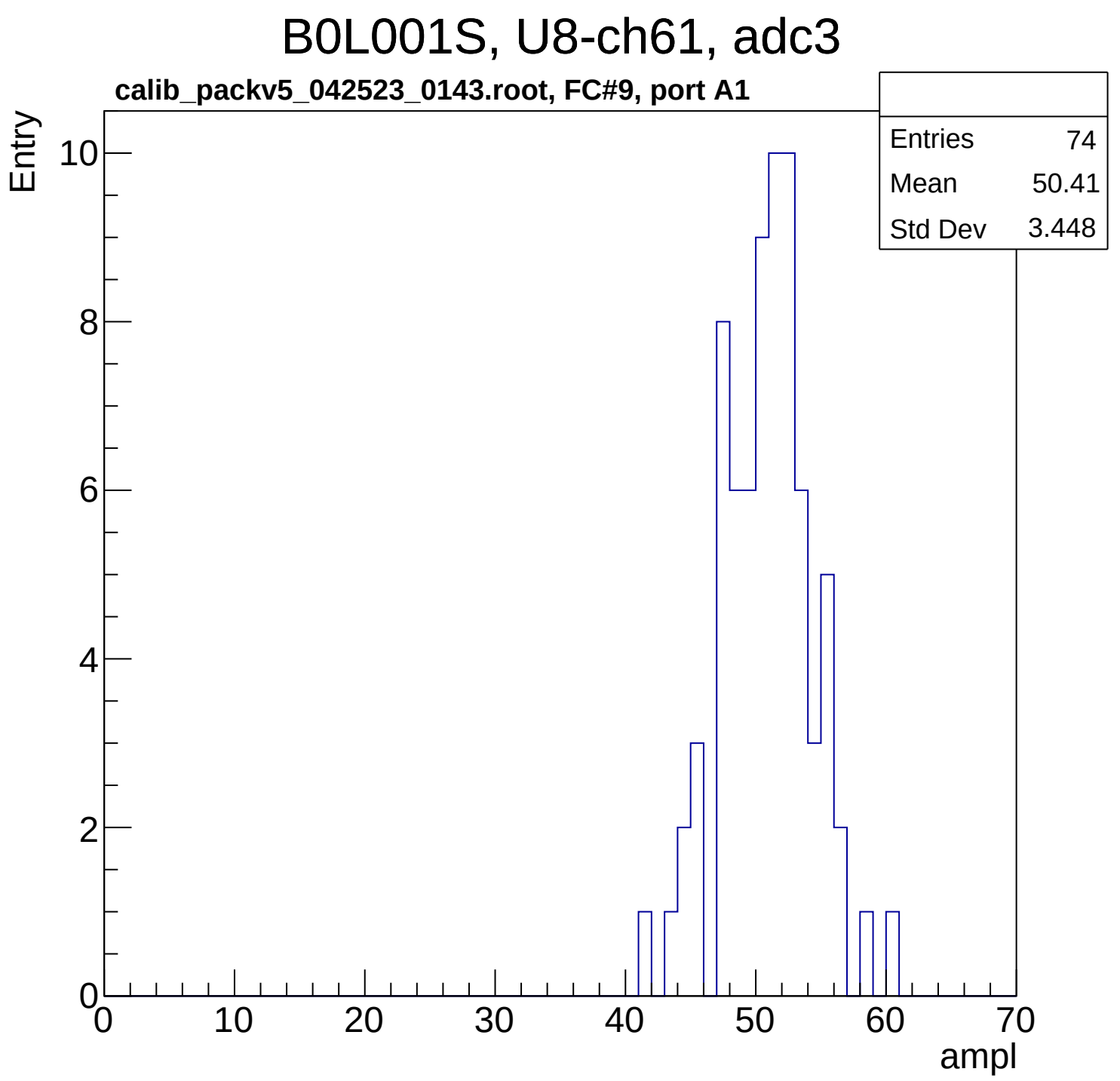
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U8-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.59
Std Dev 3.372
Gaus mean : 43.5314
Gaus Width: 3.7448
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.41
Std Dev 3.448
Entry
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2
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10
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10
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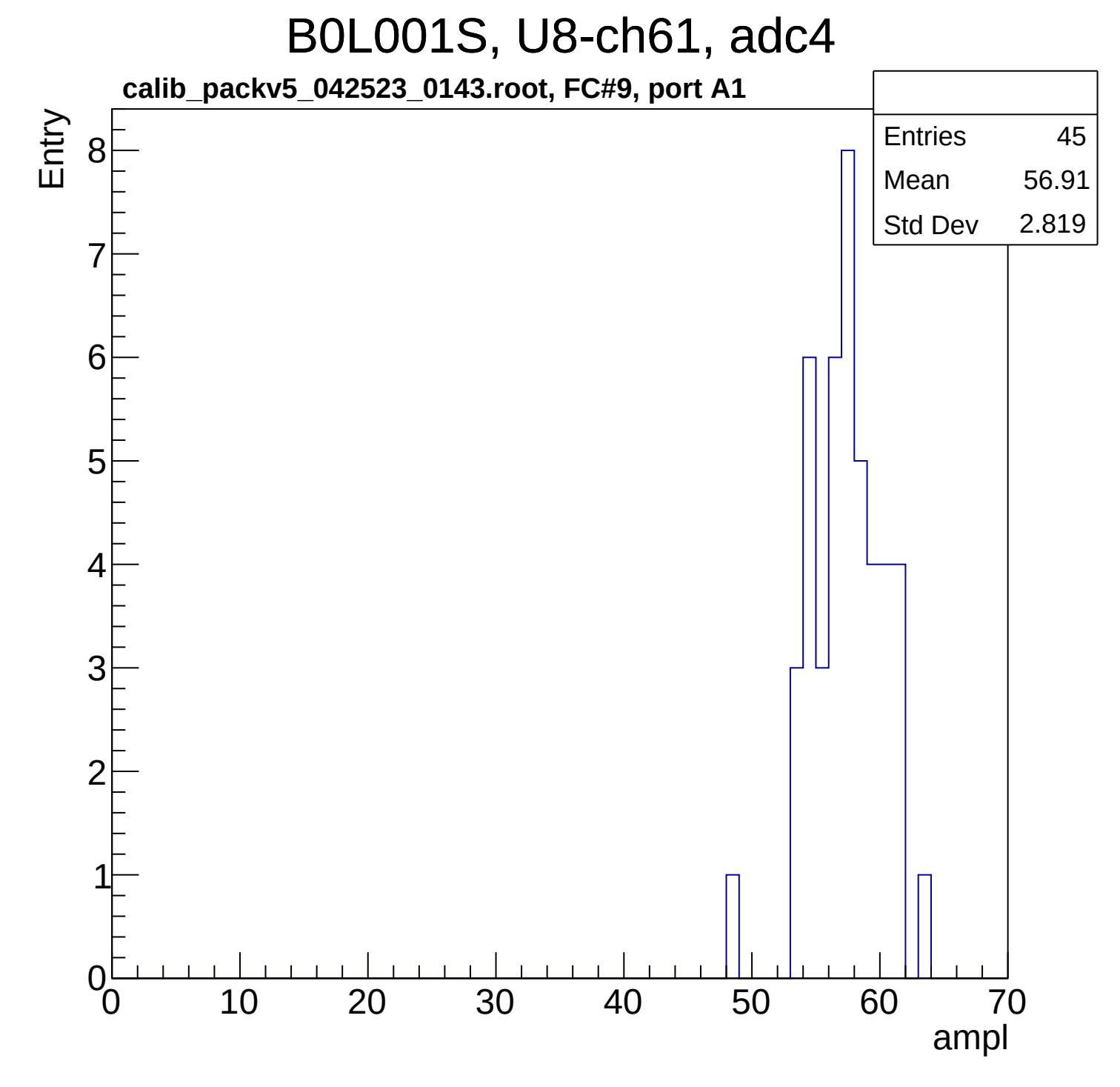
70
ampl


B0L001S, U8-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.91
Std Dev 2.819
Entry
ampl
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2
3
4
5
6
7
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10
20
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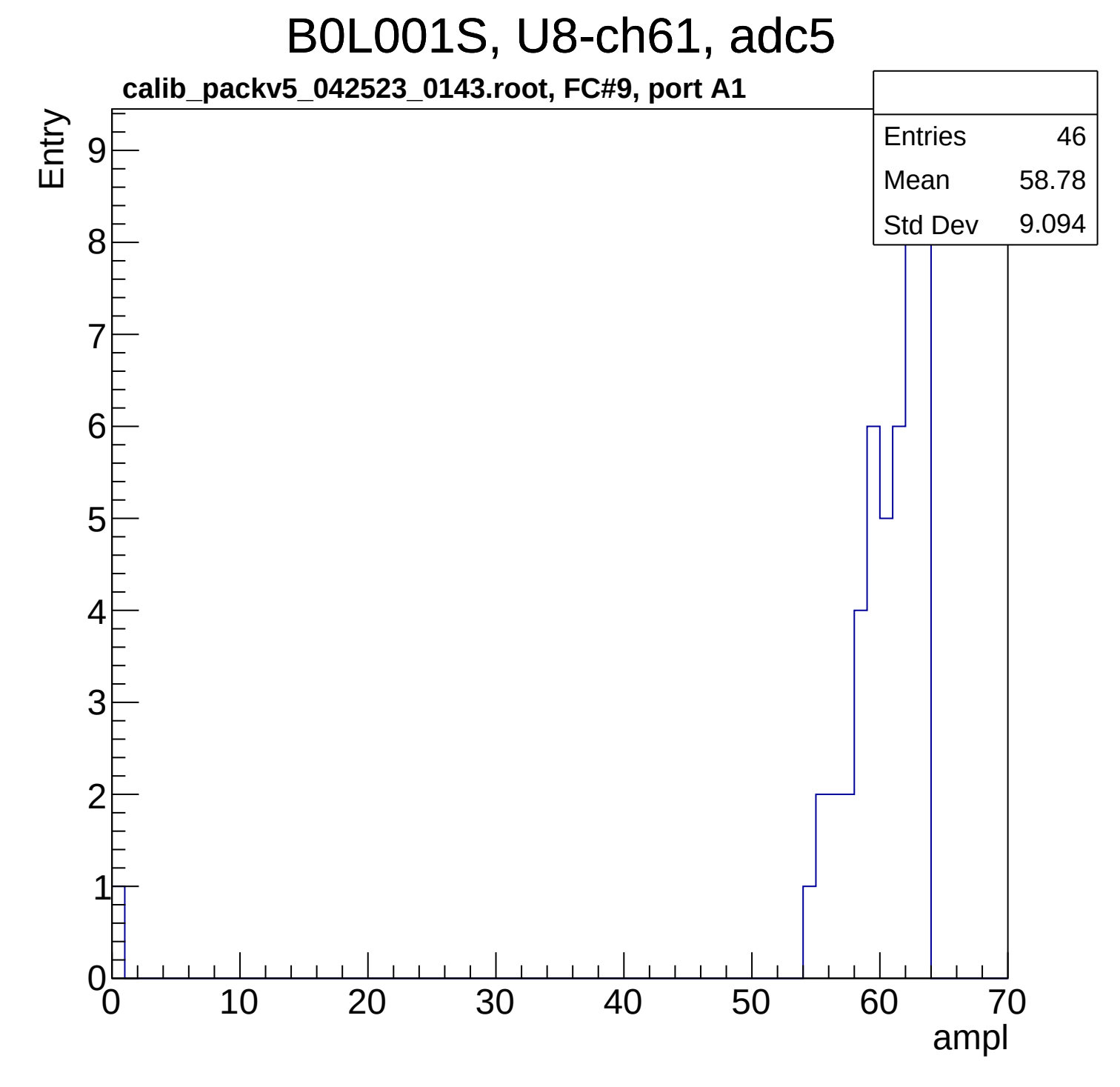
B0L001S, U8-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.78
Std Dev 9.094
Entry
ampl
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2
3
4
5
6
7
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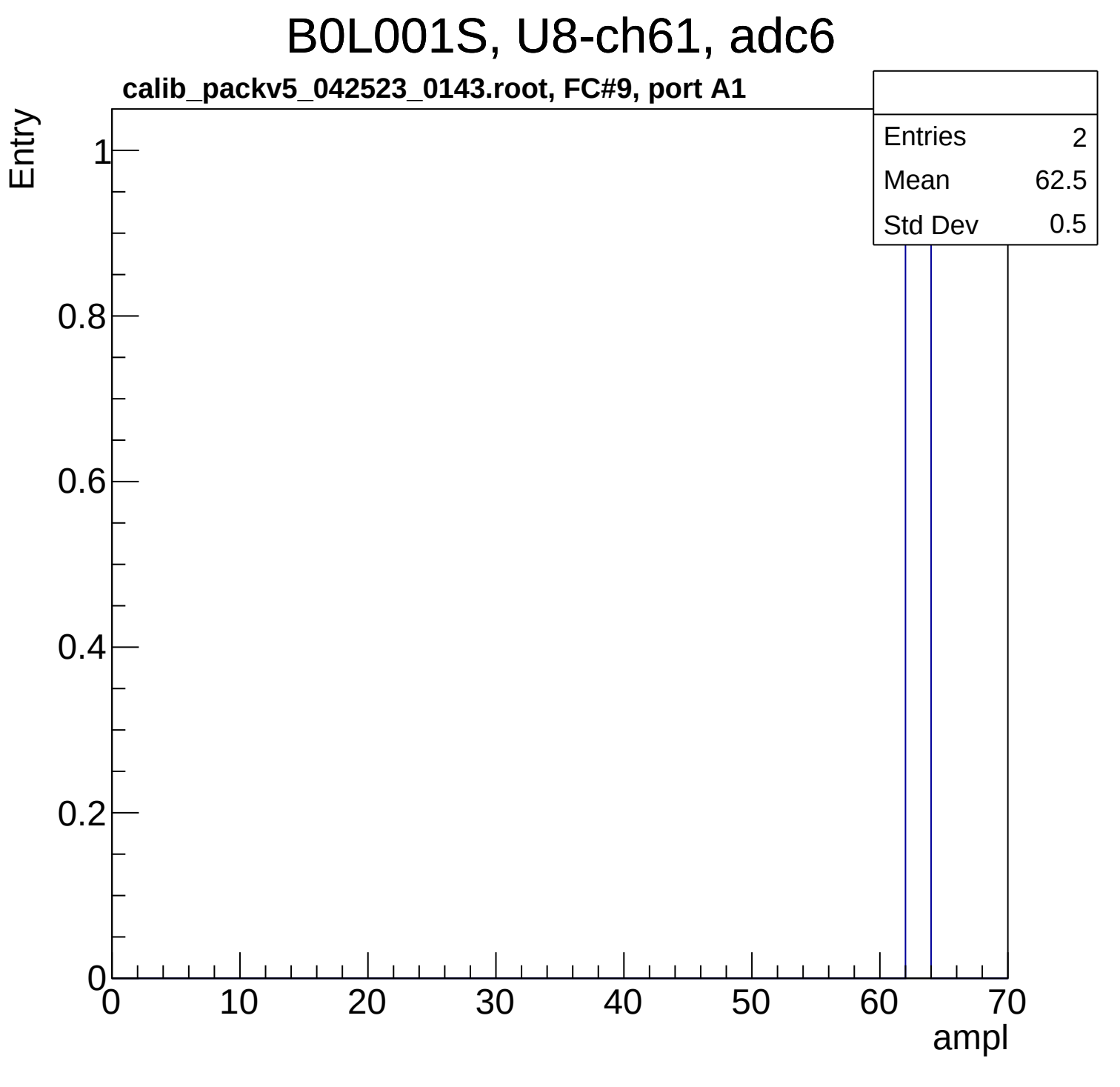
B0L001S, U8-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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50
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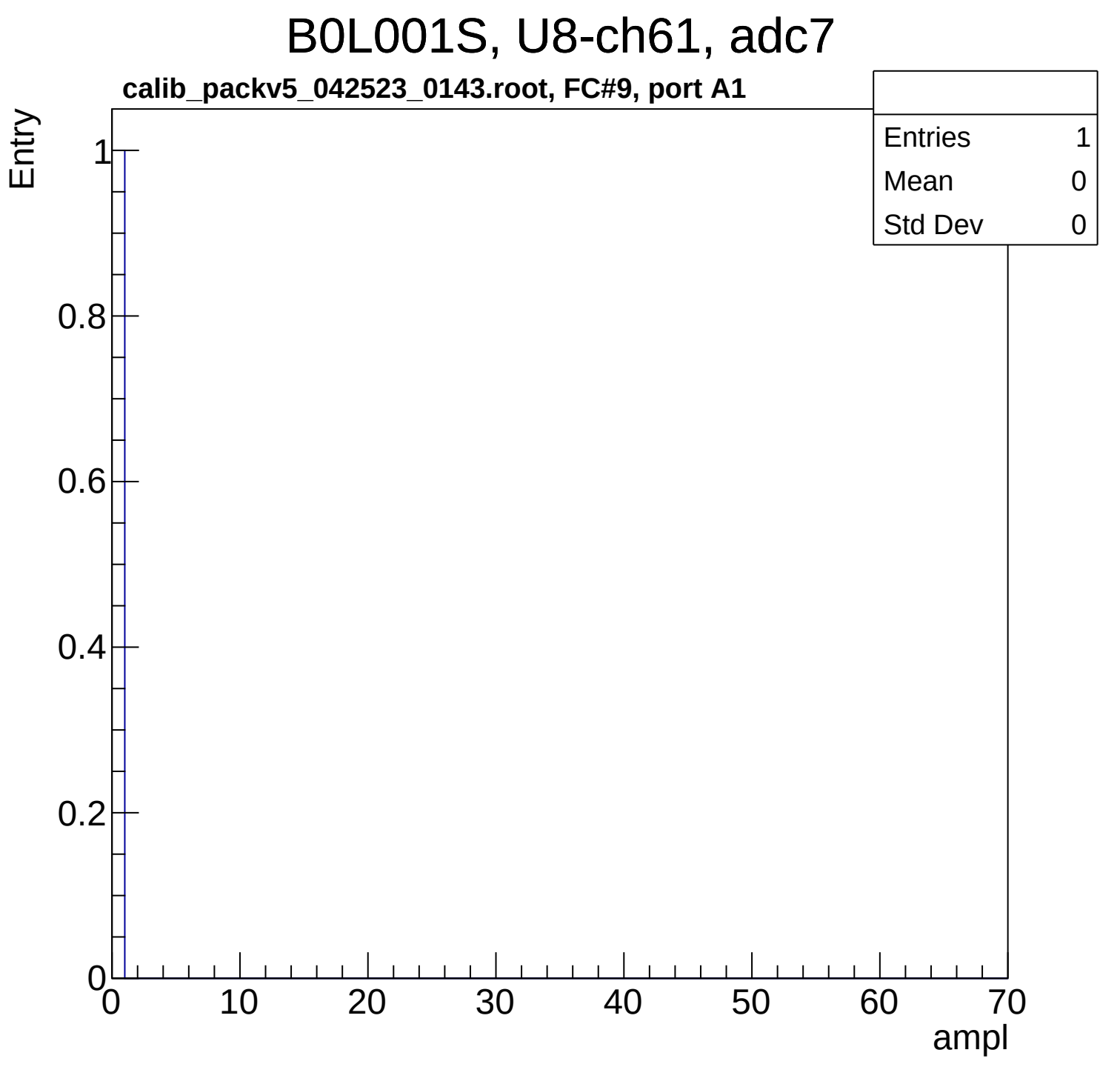
70
ampl


B0L001S, U8-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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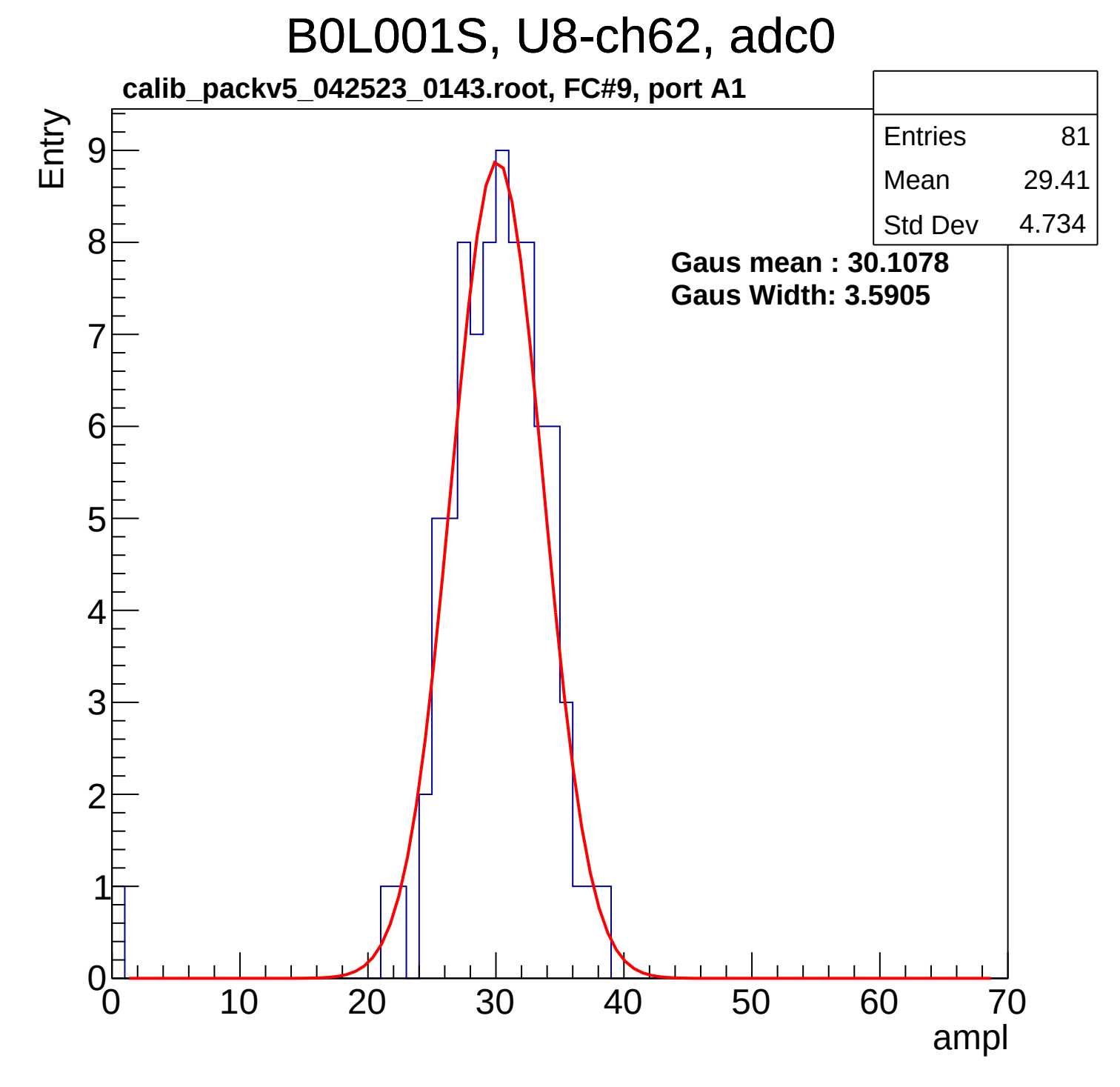
50
60
70
ampl


B0L001S, U8-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 29.41
Std Dev 4.734
Gaus mean : 30.1078
Gaus Width: 3.5905
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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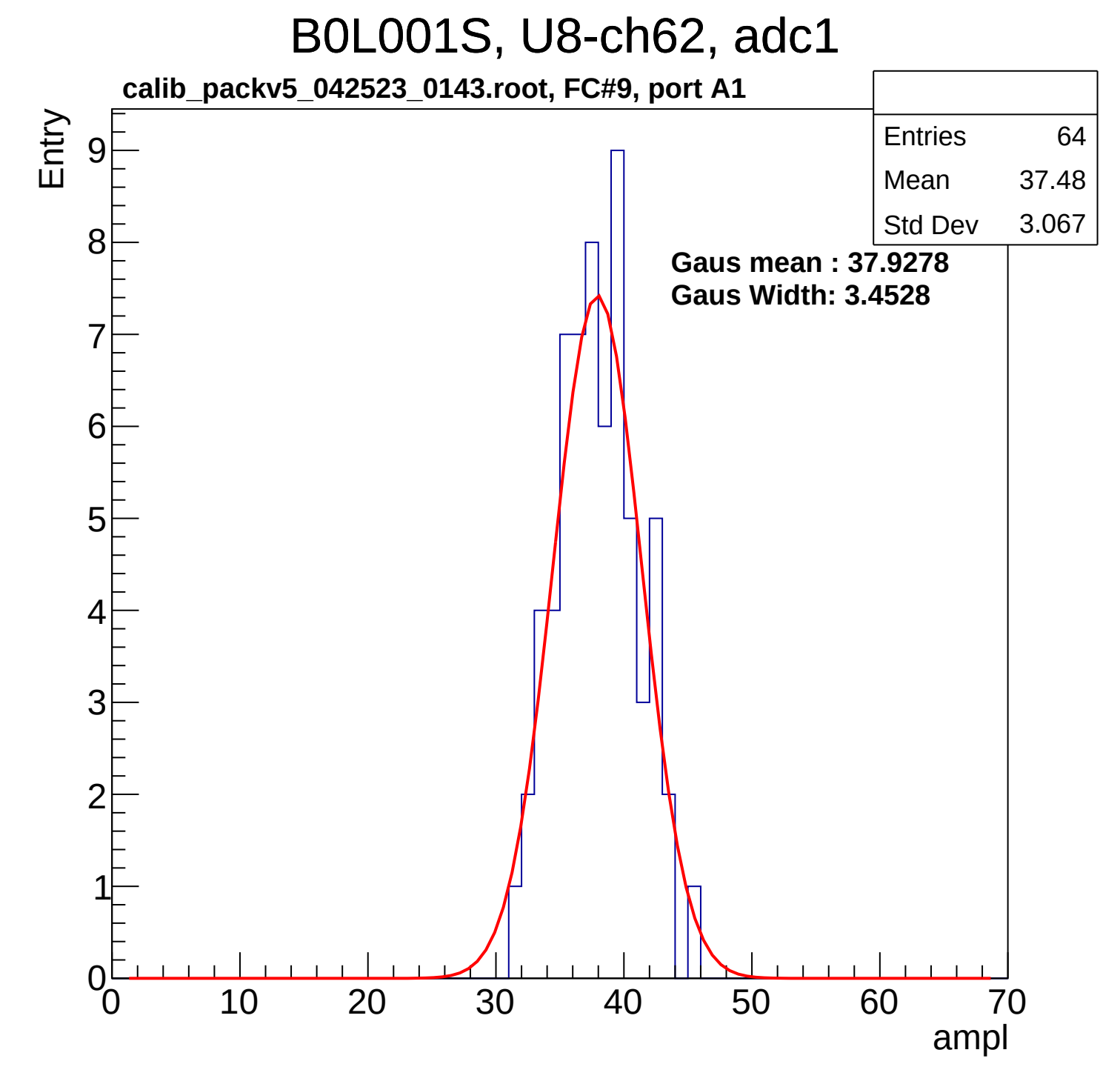
B0L001S, U8-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.48
Std Dev 3.067
Gaus mean : 37.9278
Gaus Width: 3.4528
Entry
ampl
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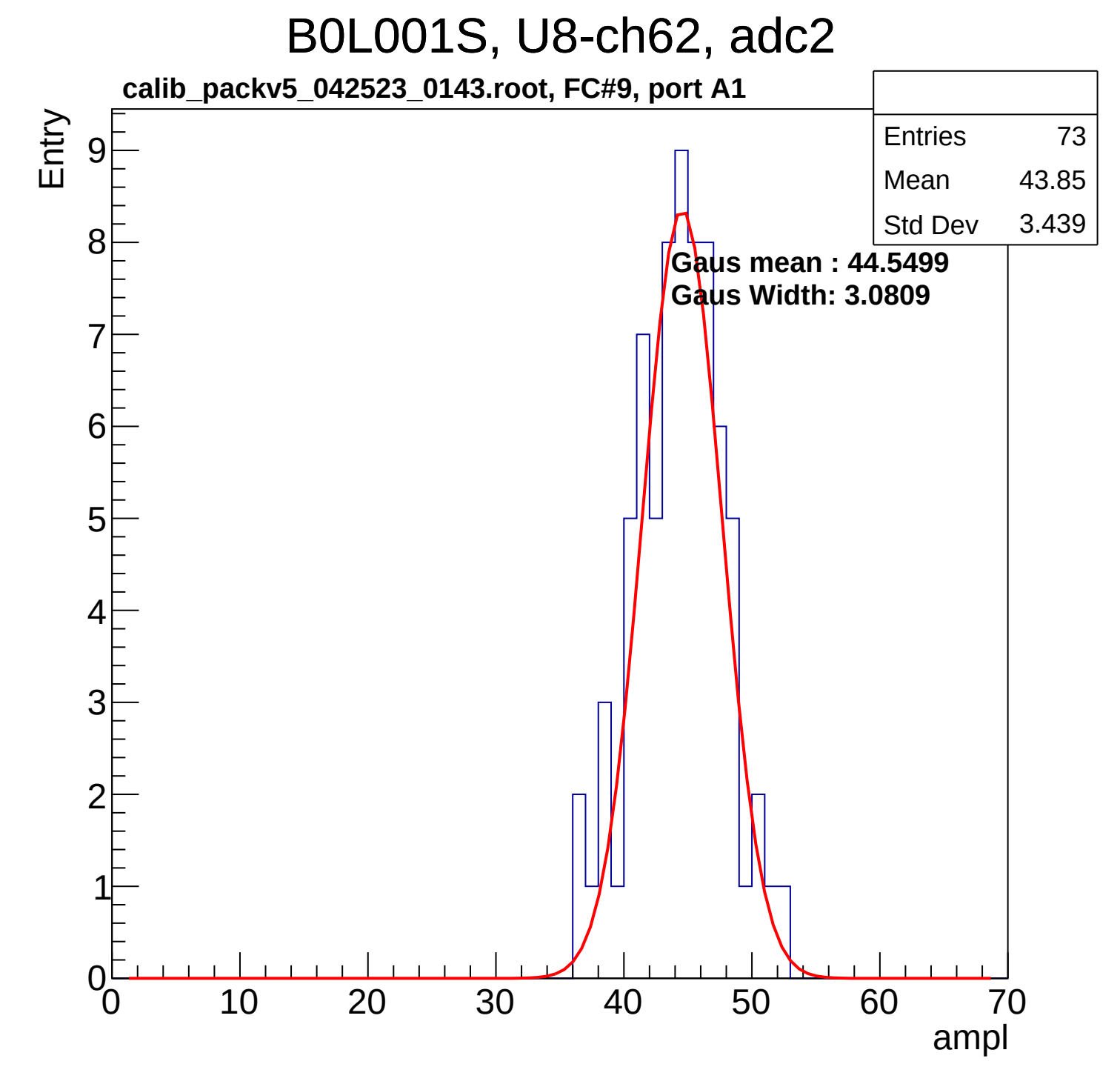
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U8-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.85
Std Dev 3.439
Gaus mean : 44.5499
Gaus Width: 3.0809
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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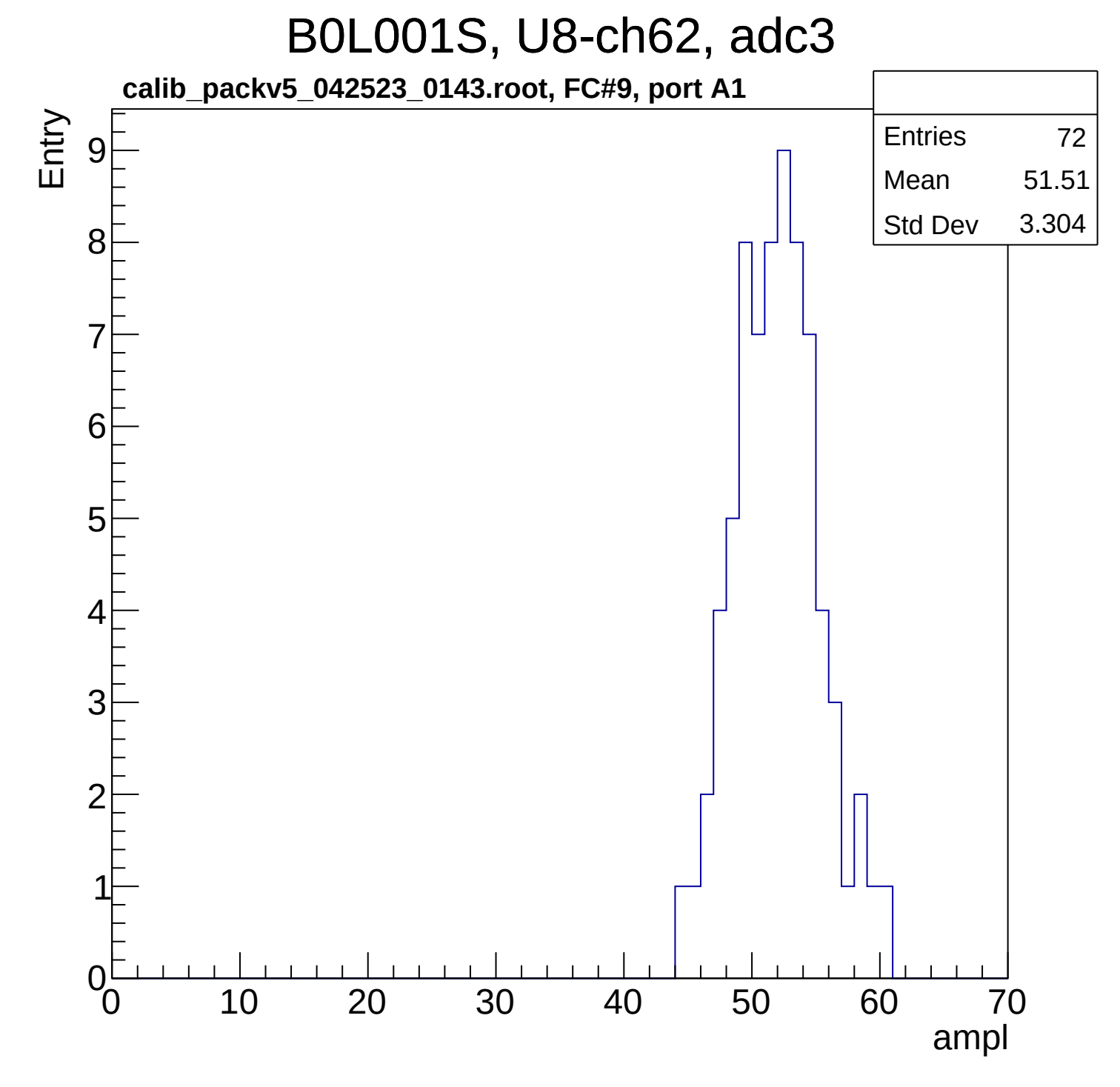
B0L001S, U8-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.51
Std Dev 3.304
Entry
ampl
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2
3
4
5
6
7
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0
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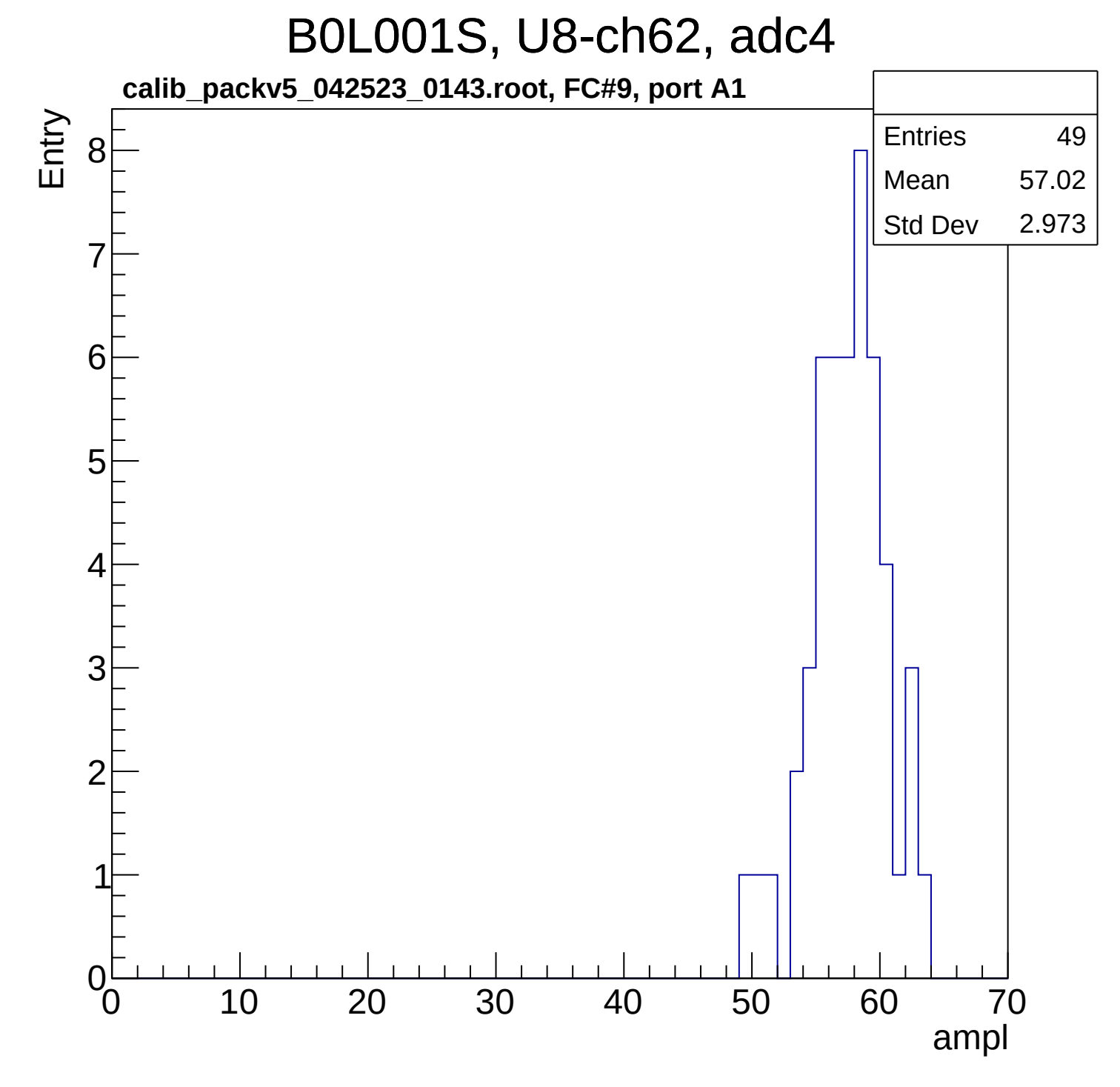
B0L001S, U8-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.02
Std Dev 2.973
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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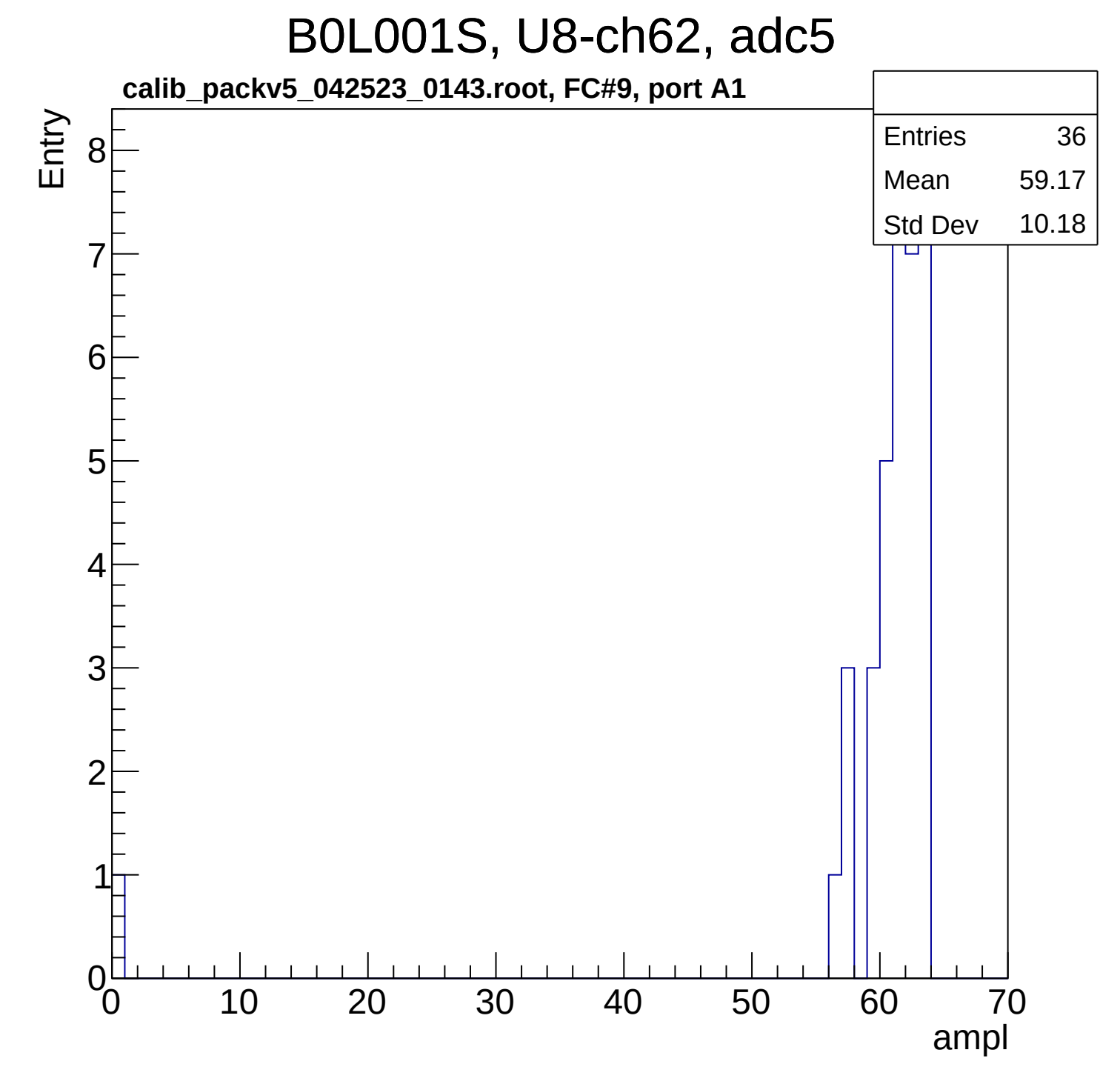
B0L001S, U8-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.17
Std Dev 10.18
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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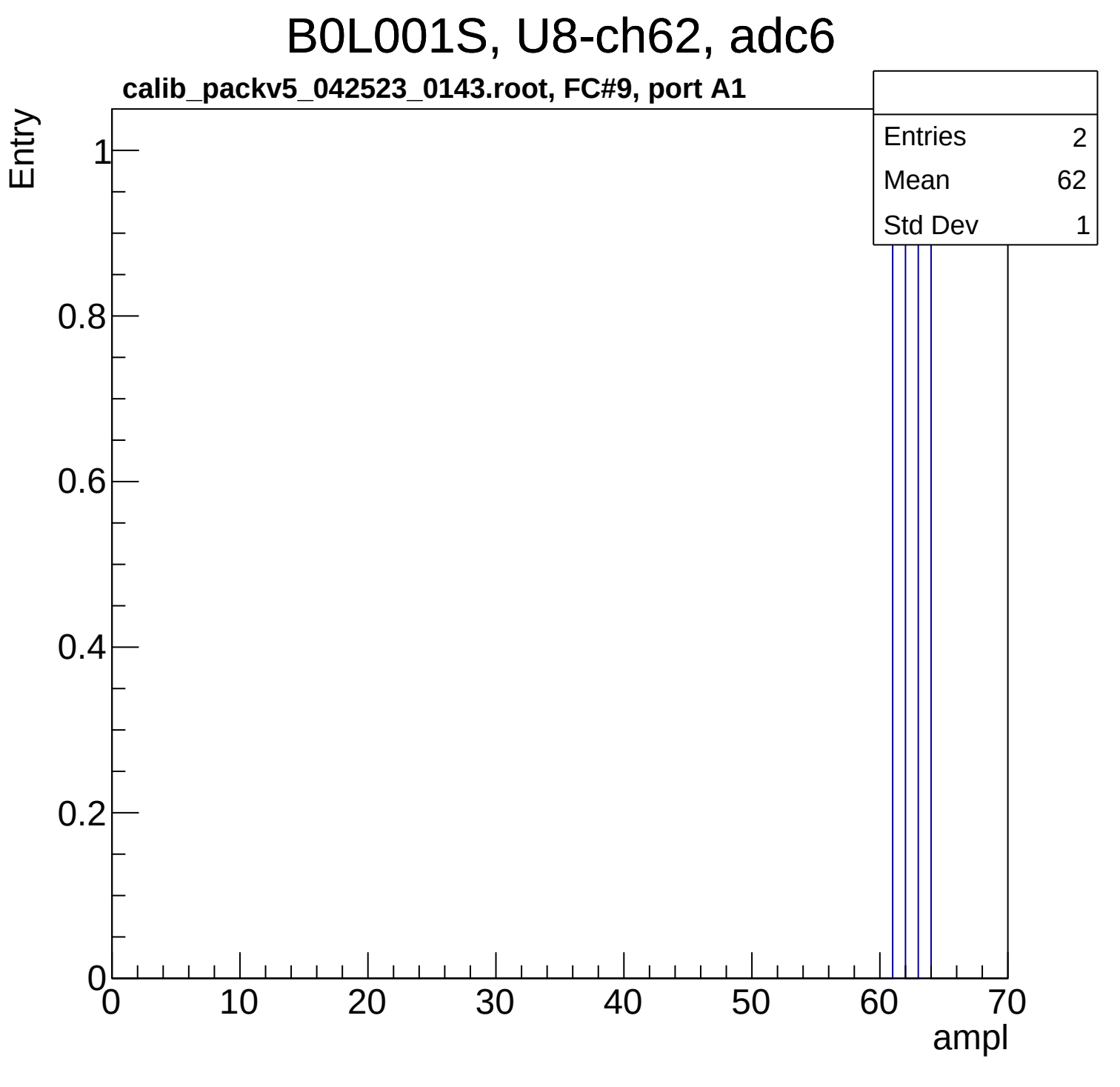
B0L001S, U8-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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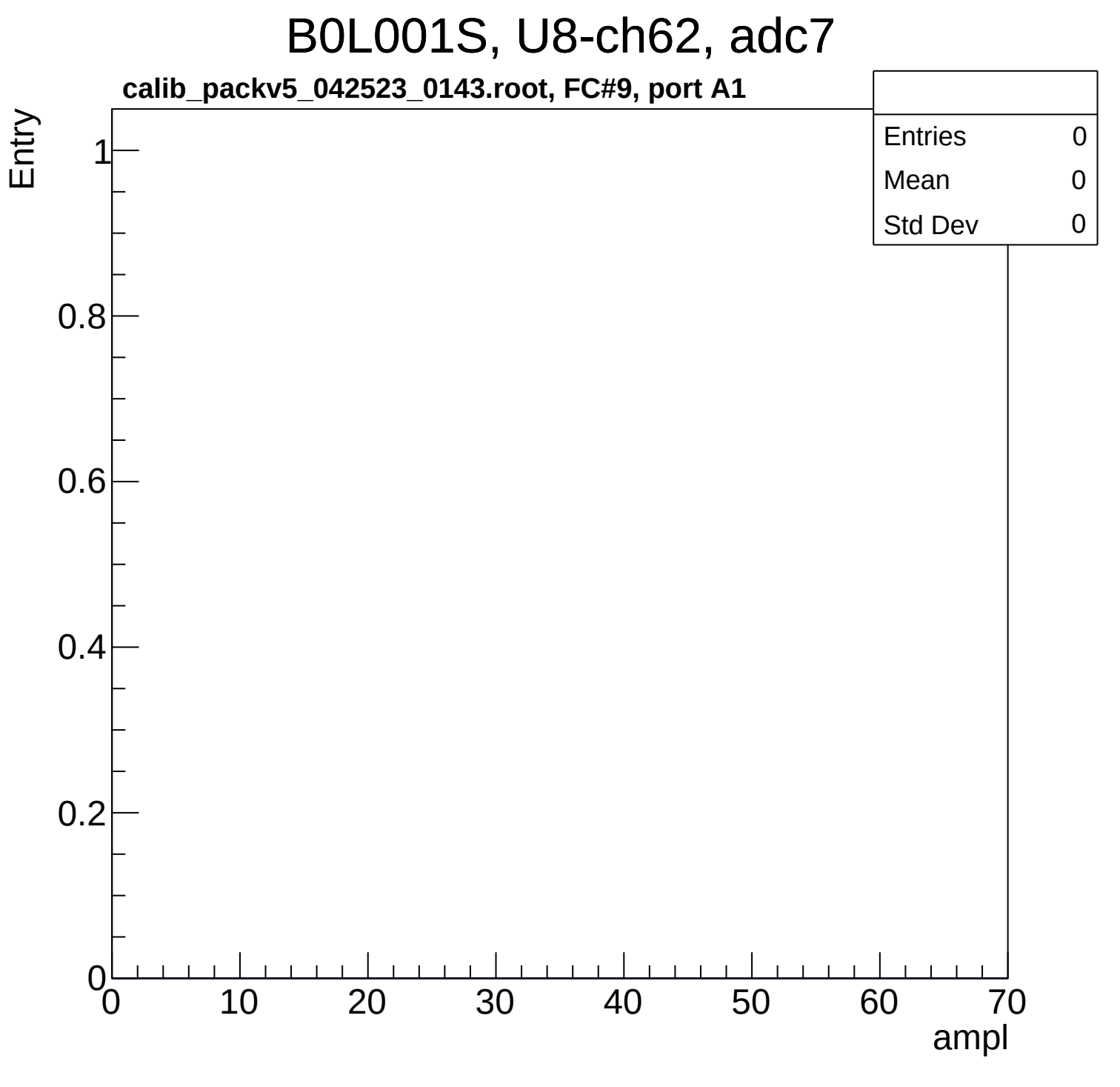
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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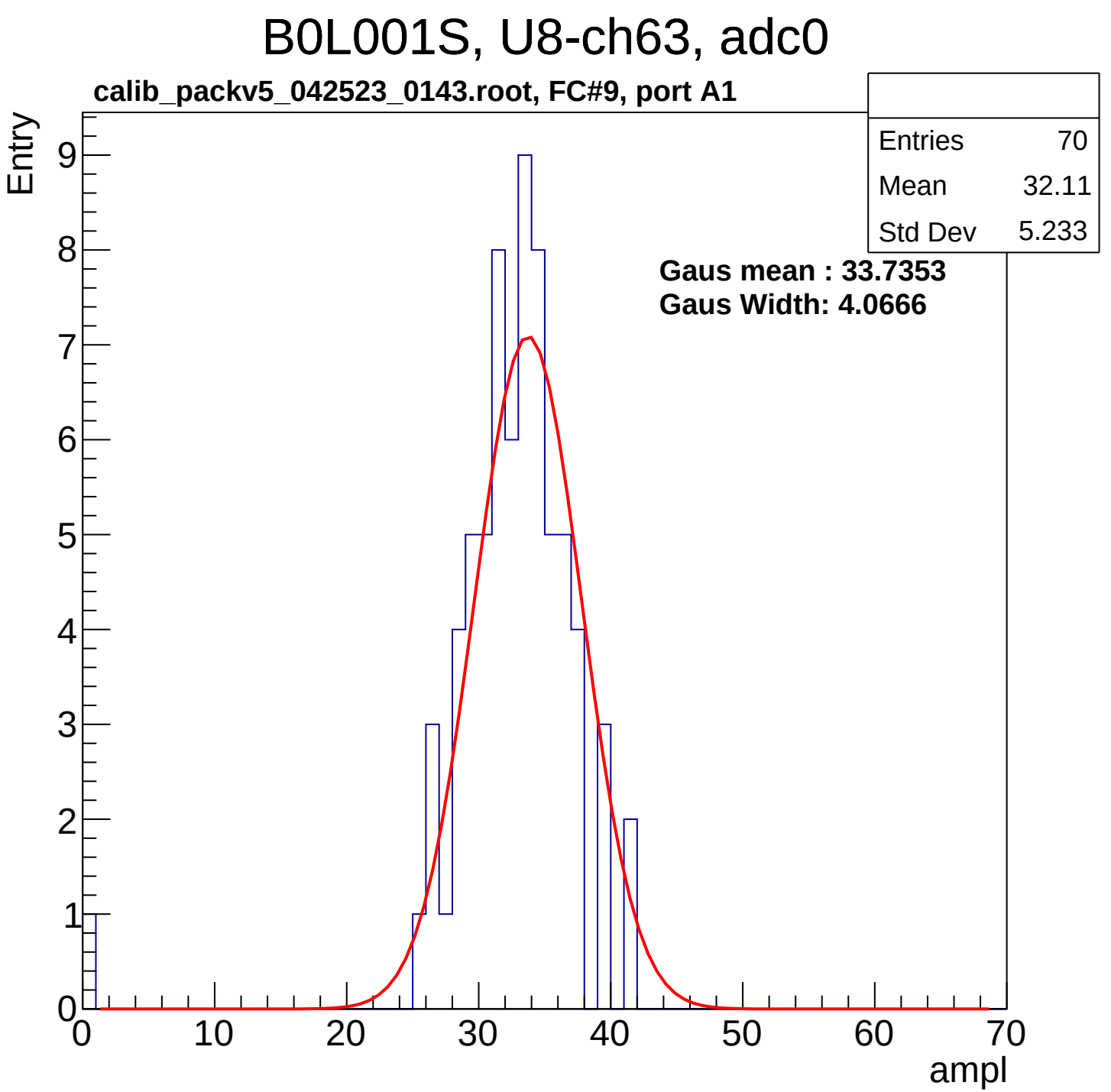
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 32.11
Std Dev 5.233
Gaus mean : 33.7353
Gaus Width: 4.0666
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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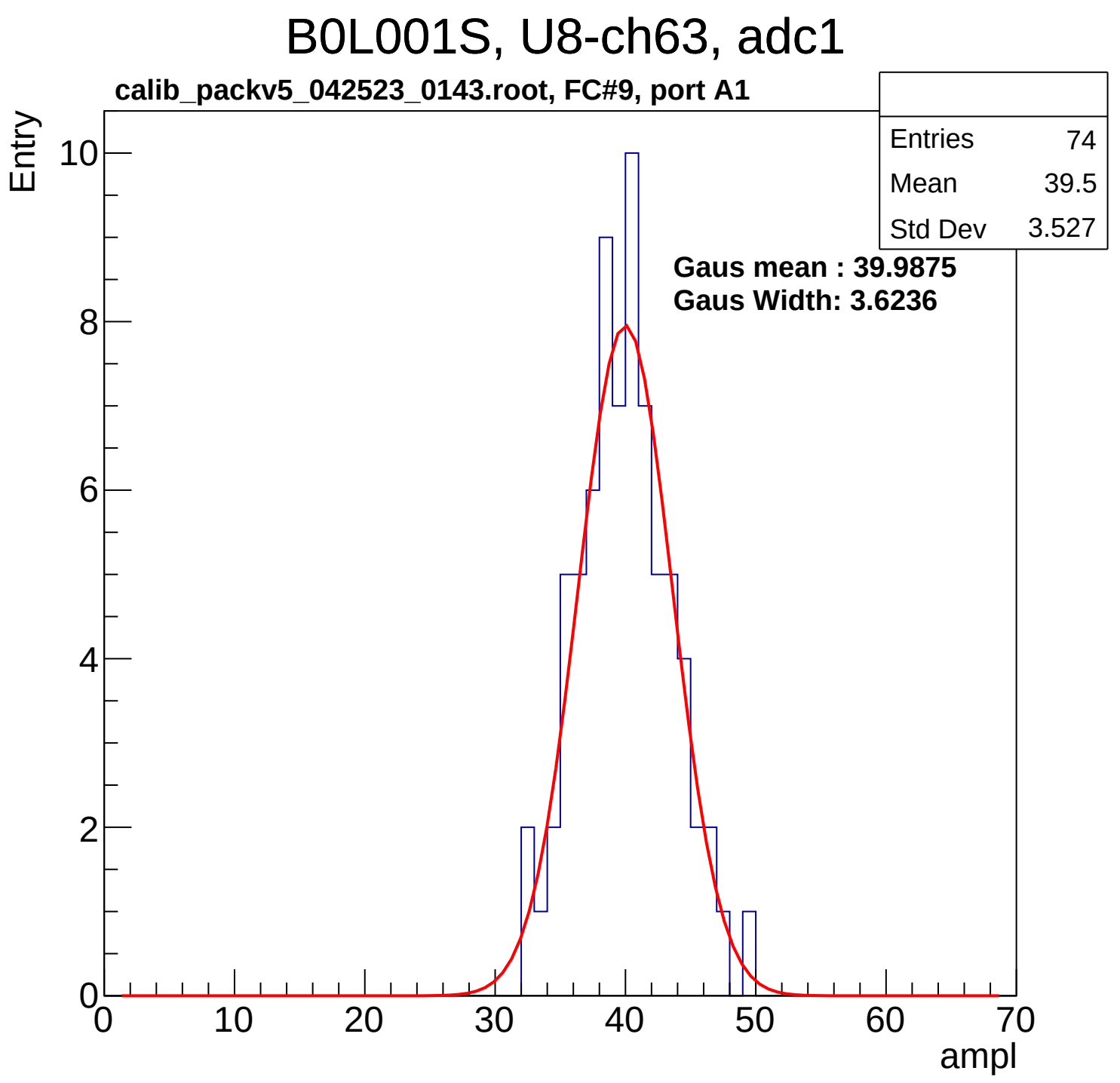
B0L001S, U8-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 39.5
Std Dev 3.527
Gaus mean : 39.9875
Gaus Width: 3.6236
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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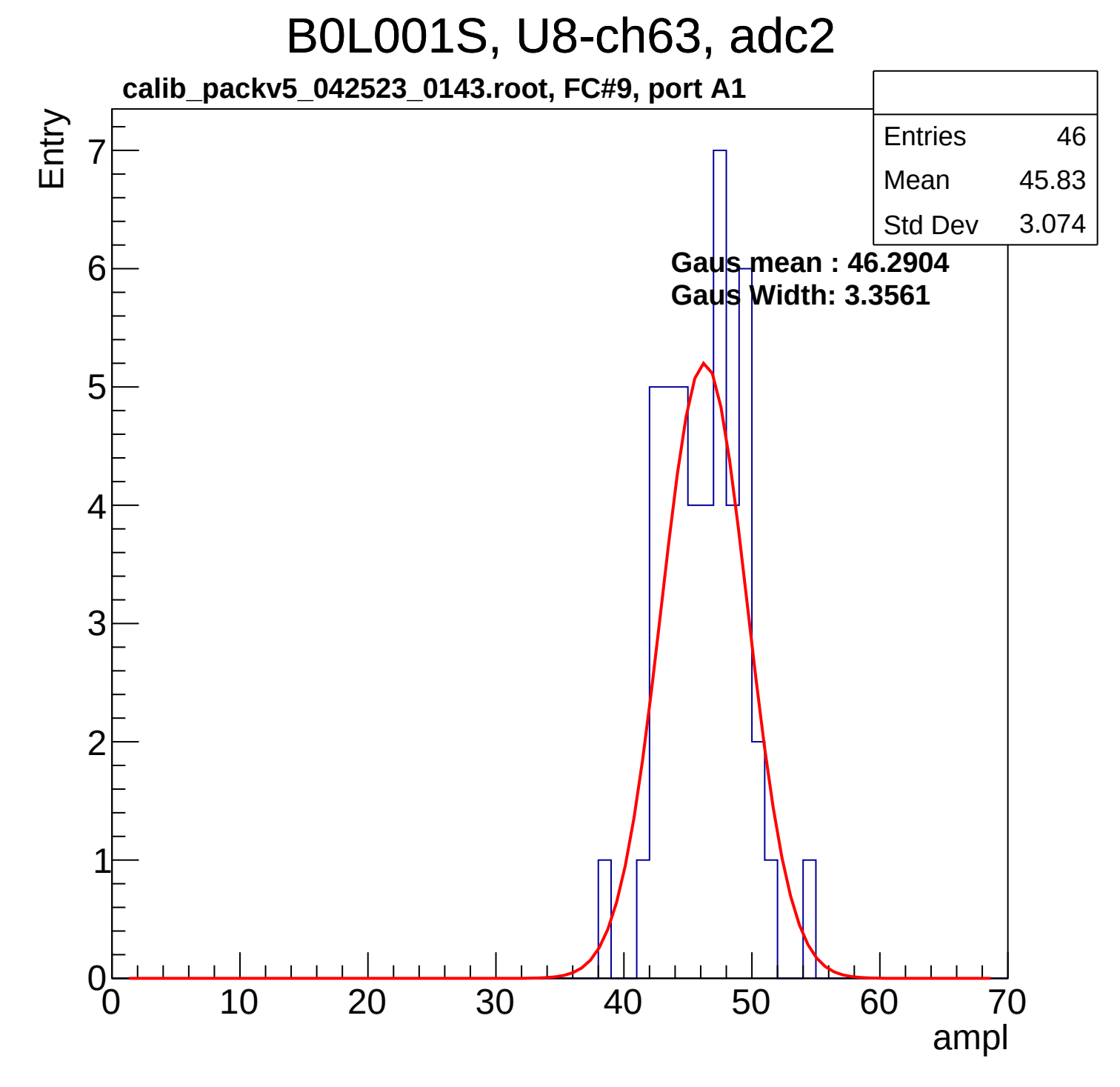
B0L001S, U8-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 45.83
Std Dev 3.074
Gaus mean : 46.2904
Gaus Width: 3.3561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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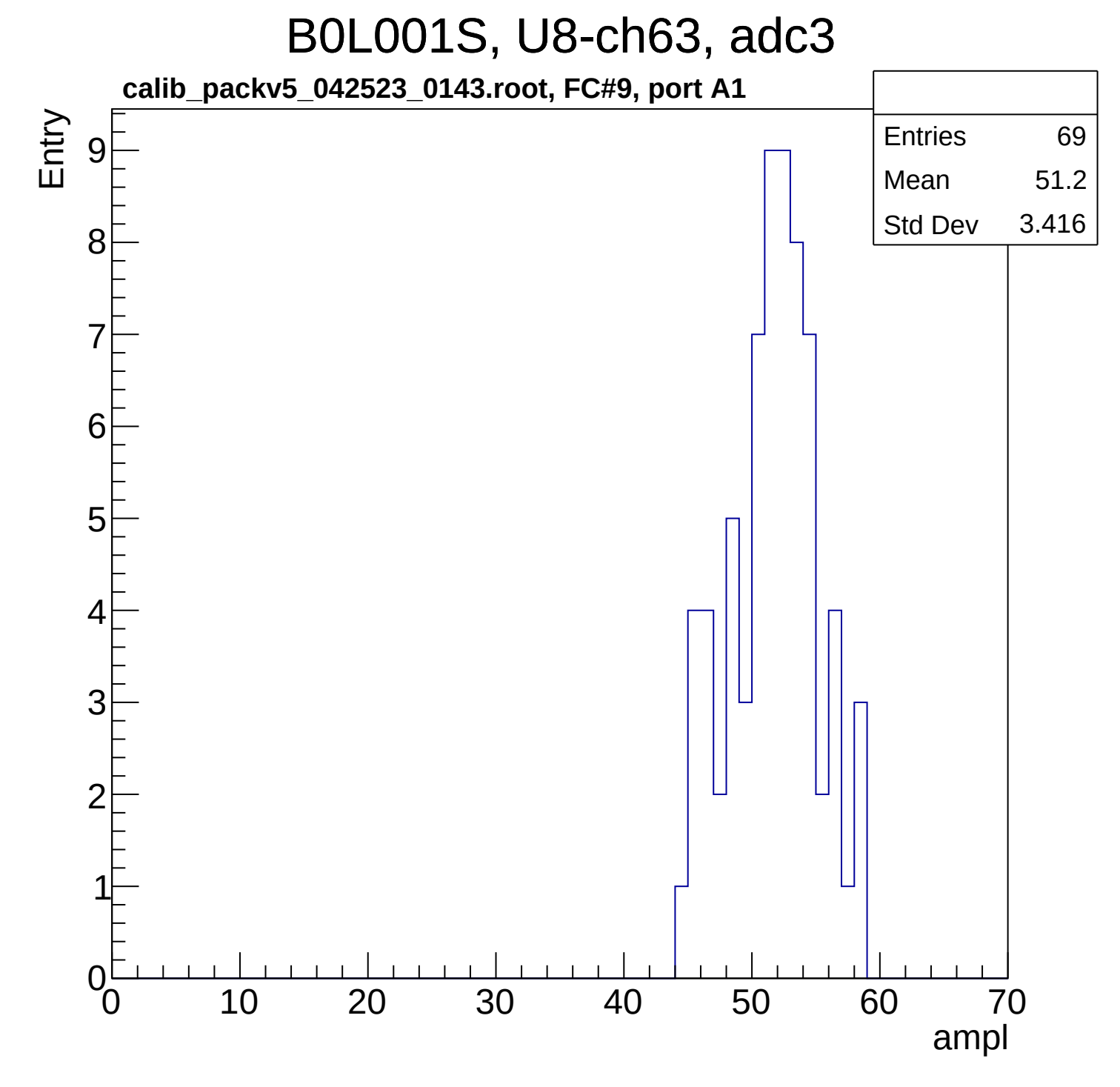
B0L001S, U8-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.2
Std Dev 3.416
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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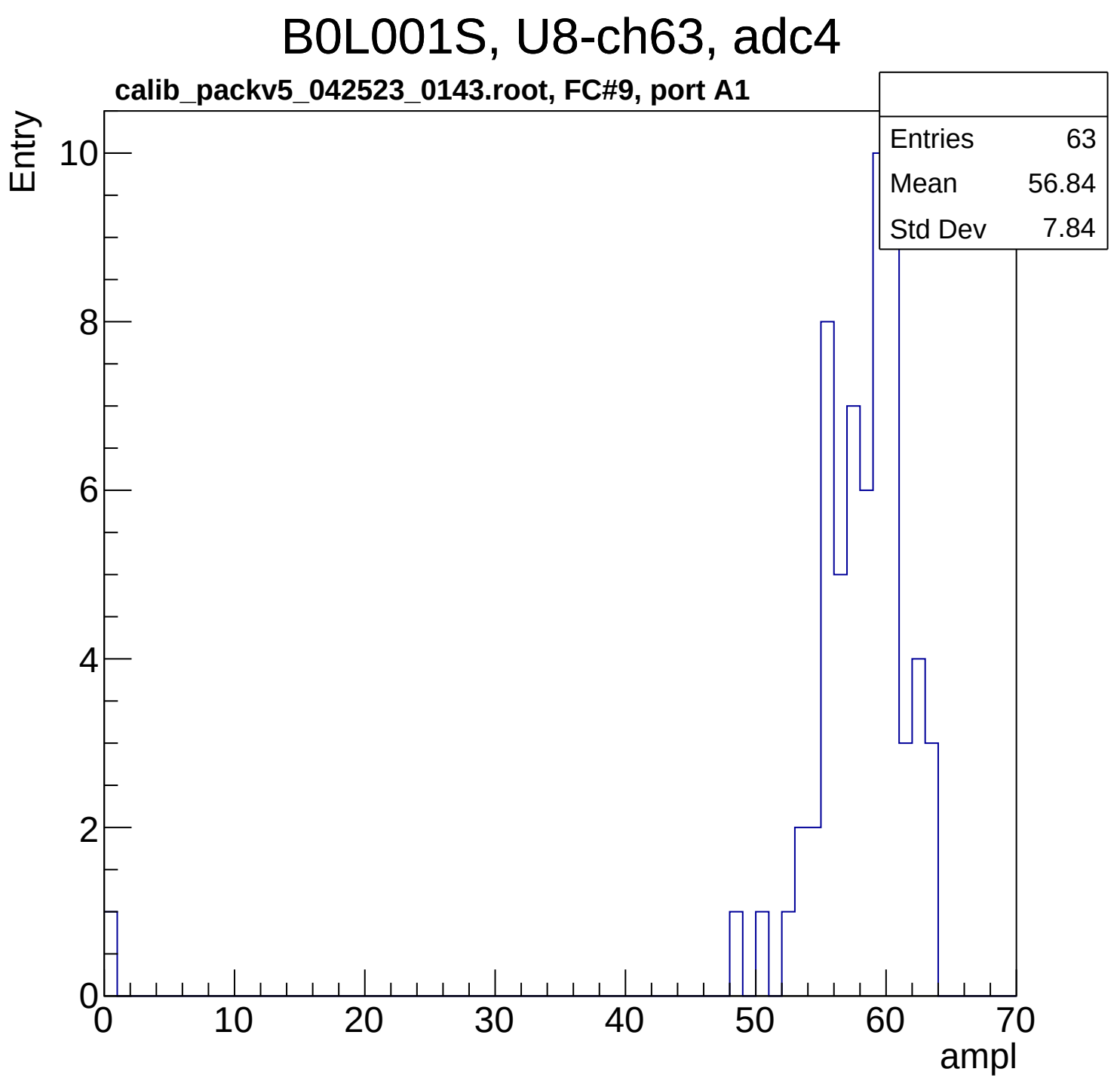
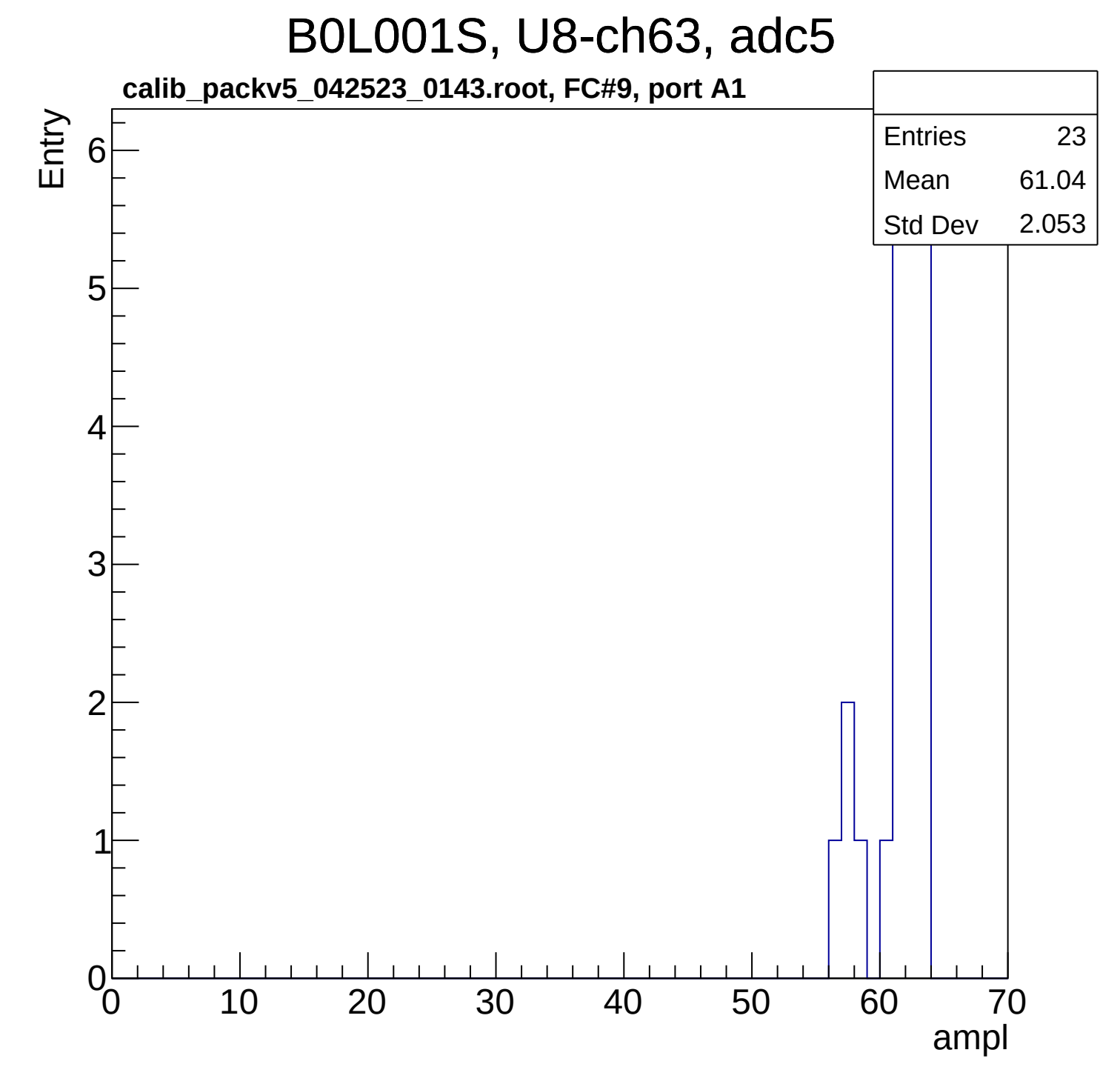

B0L001S, U8-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.84
Std Dev 7.84
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.04
Std Dev 2.053
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
50
60
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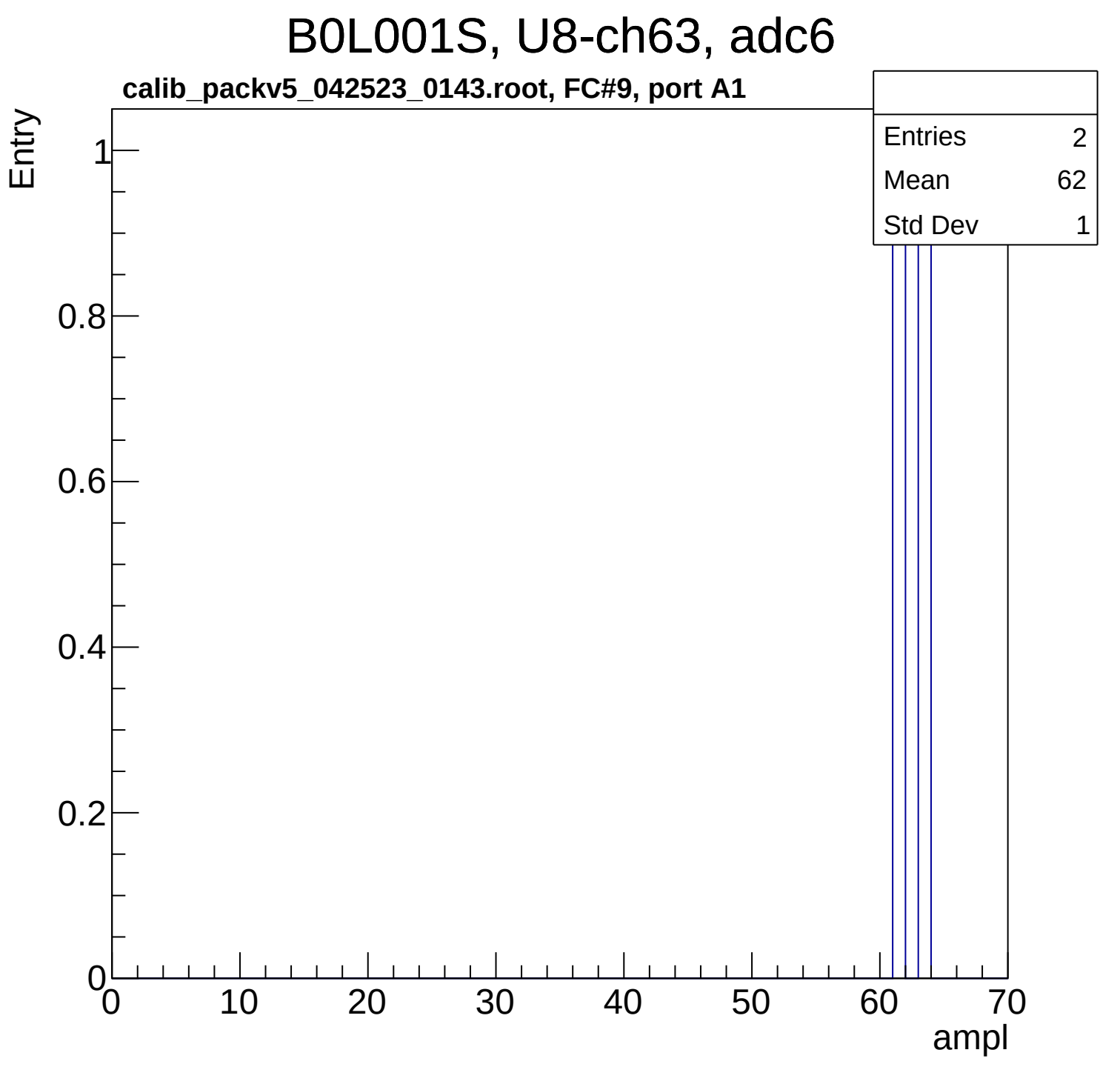
B0L001S, U8-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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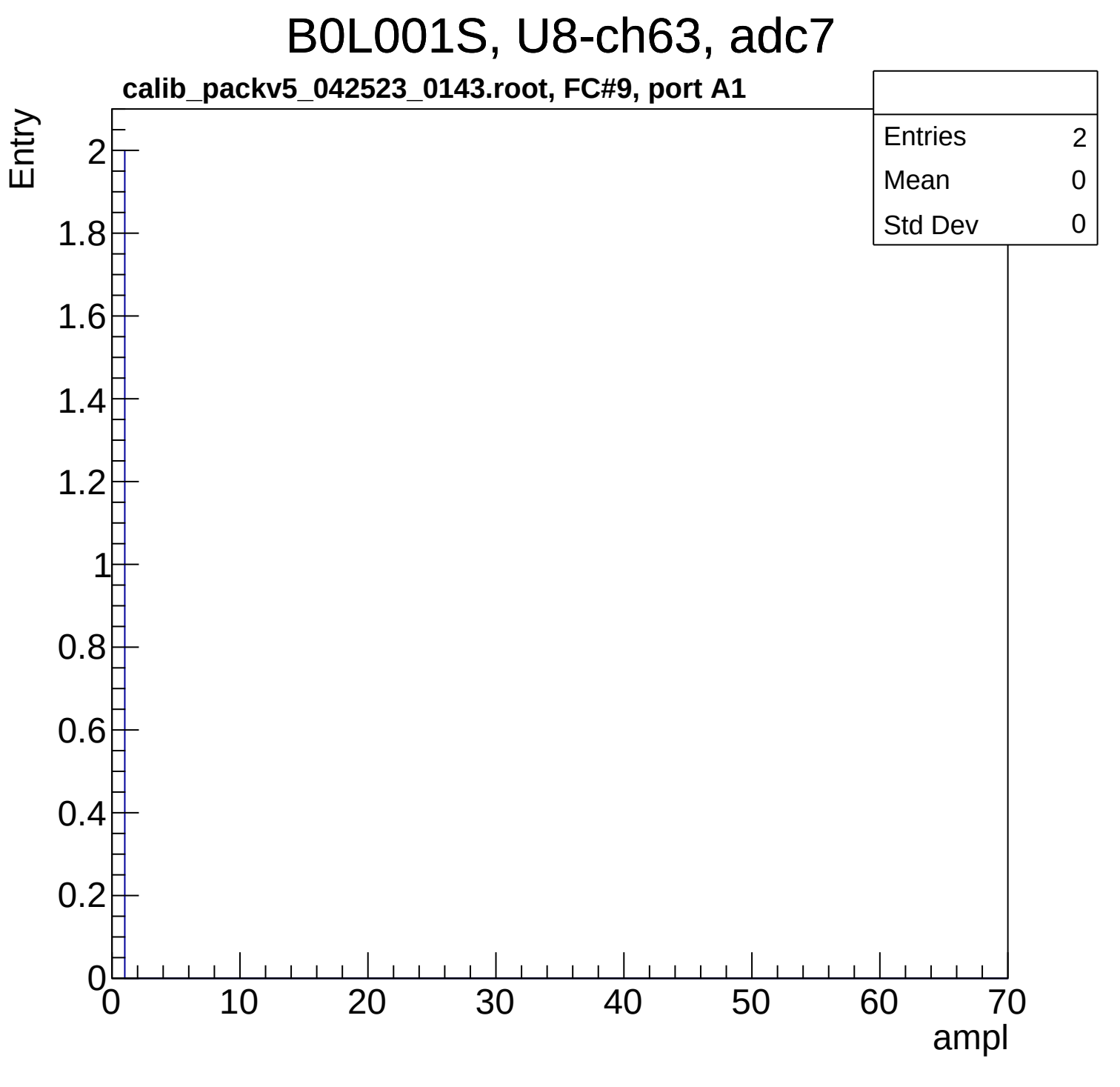
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
0
10
20
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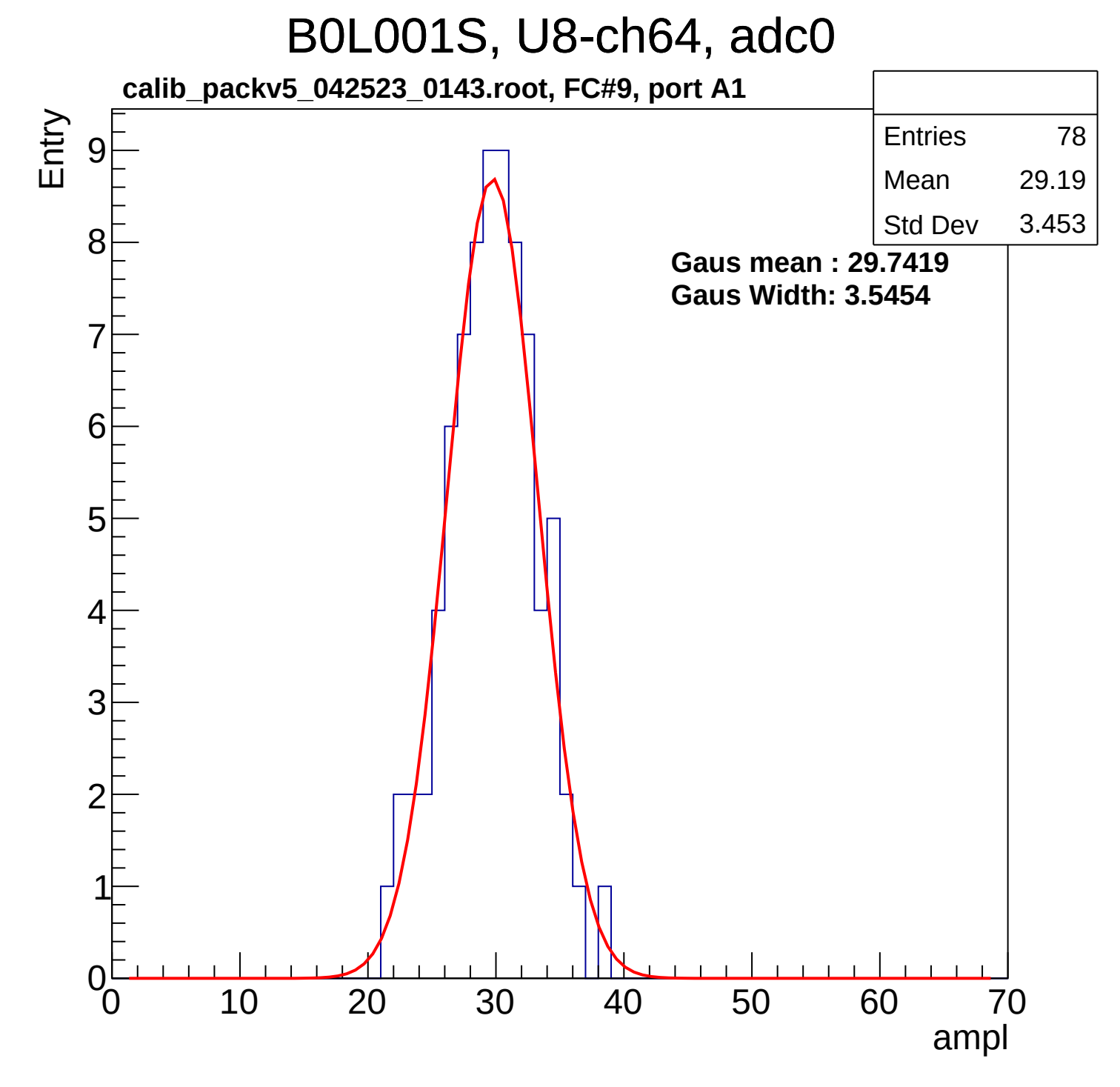
50
60
70
ampl


B0L001S, U8-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.19
Std Dev 3.453
Gaus mean : 29.7419
Gaus Width: 3.5454
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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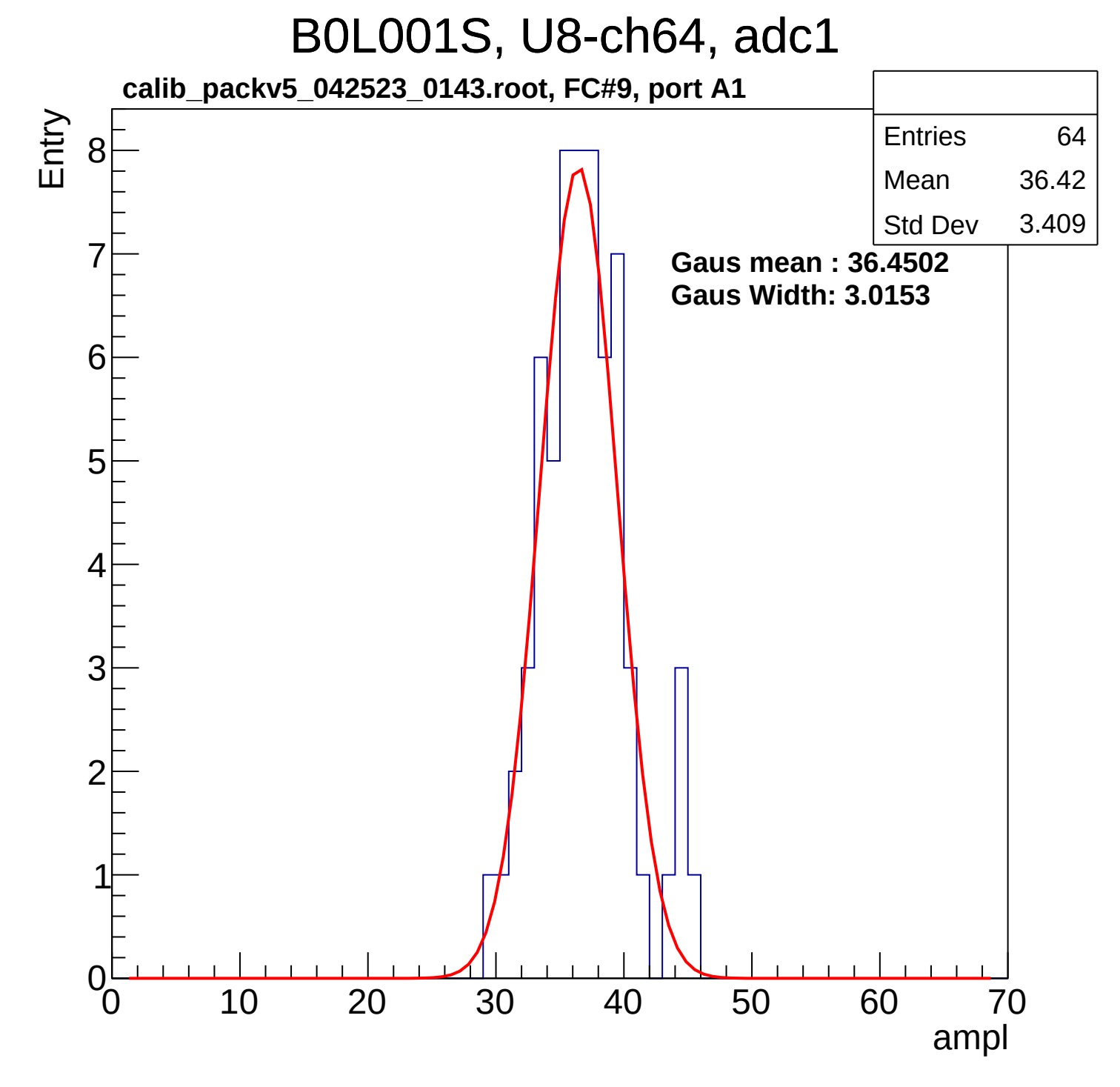
B0L001S, U8-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.42
Std Dev 3.409
Gaus mean : 36.4502
Gaus Width: 3.0153
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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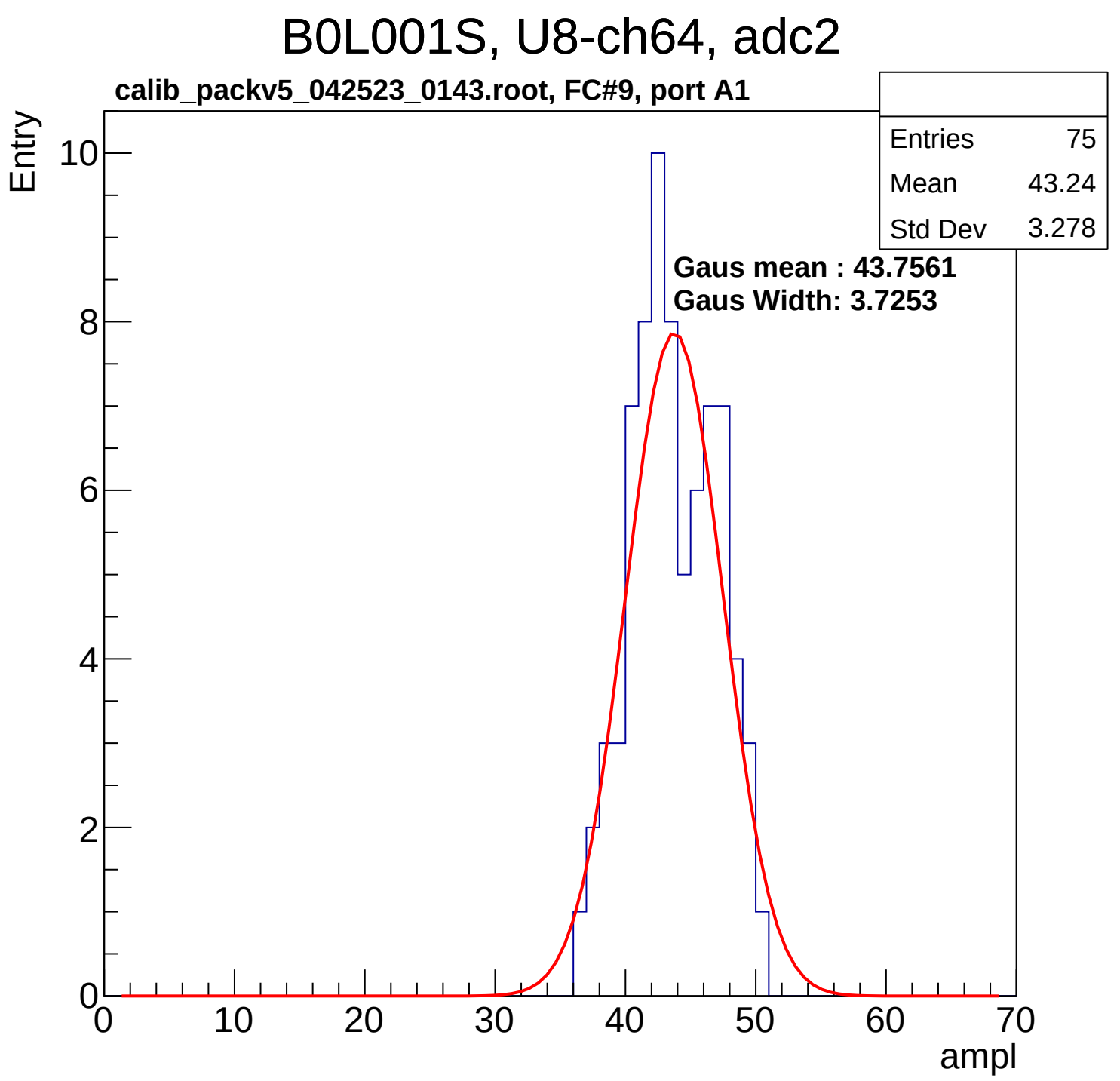
B0L001S, U8-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 43.24
Std Dev 3.278
Gaus mean : 43.7561
Gaus Width: 3.7253
Entry
ampl
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8
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0
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40
50
60
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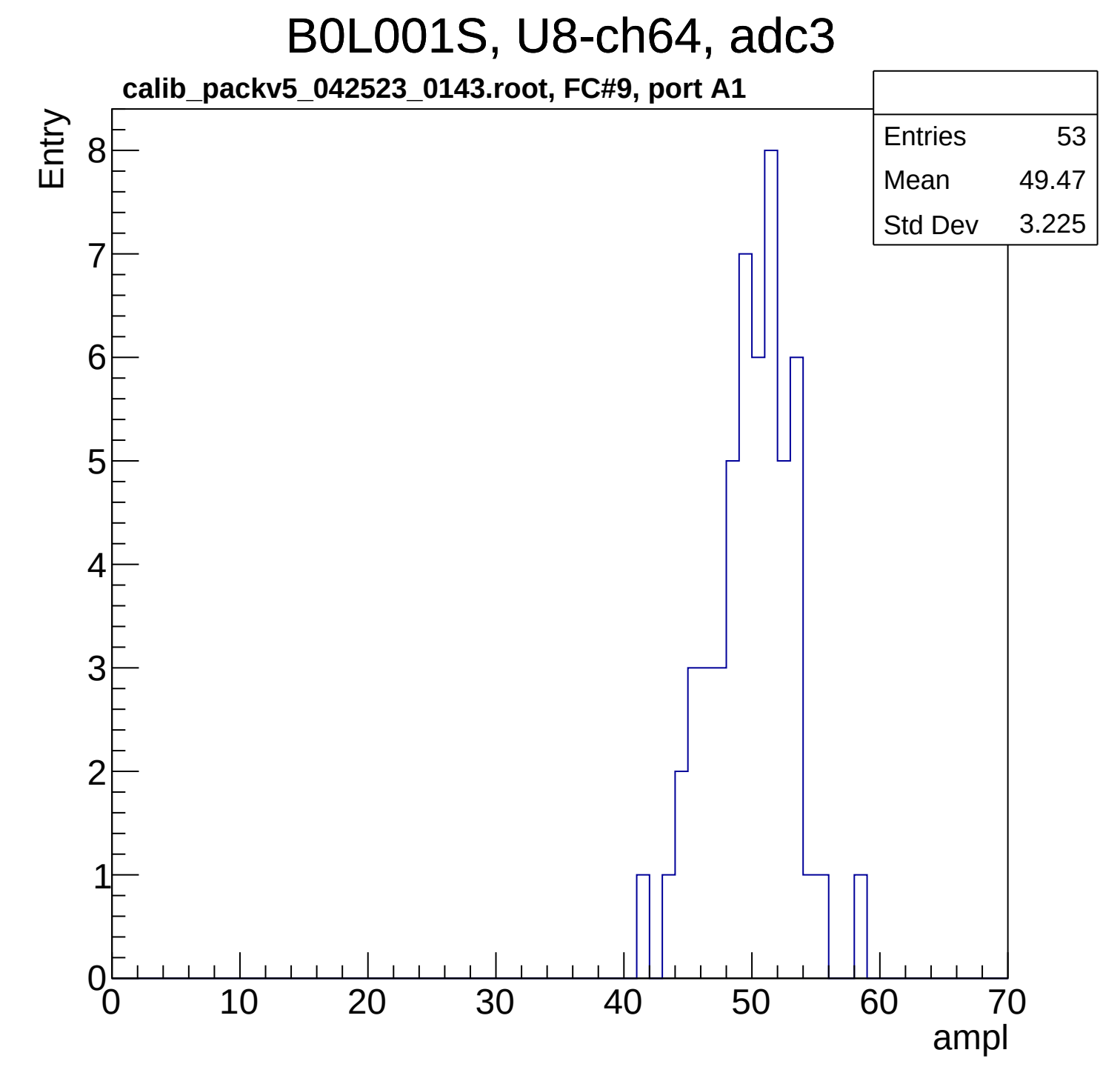
B0L001S, U8-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 49.47
Std Dev 3.225
Entry
ampl
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0
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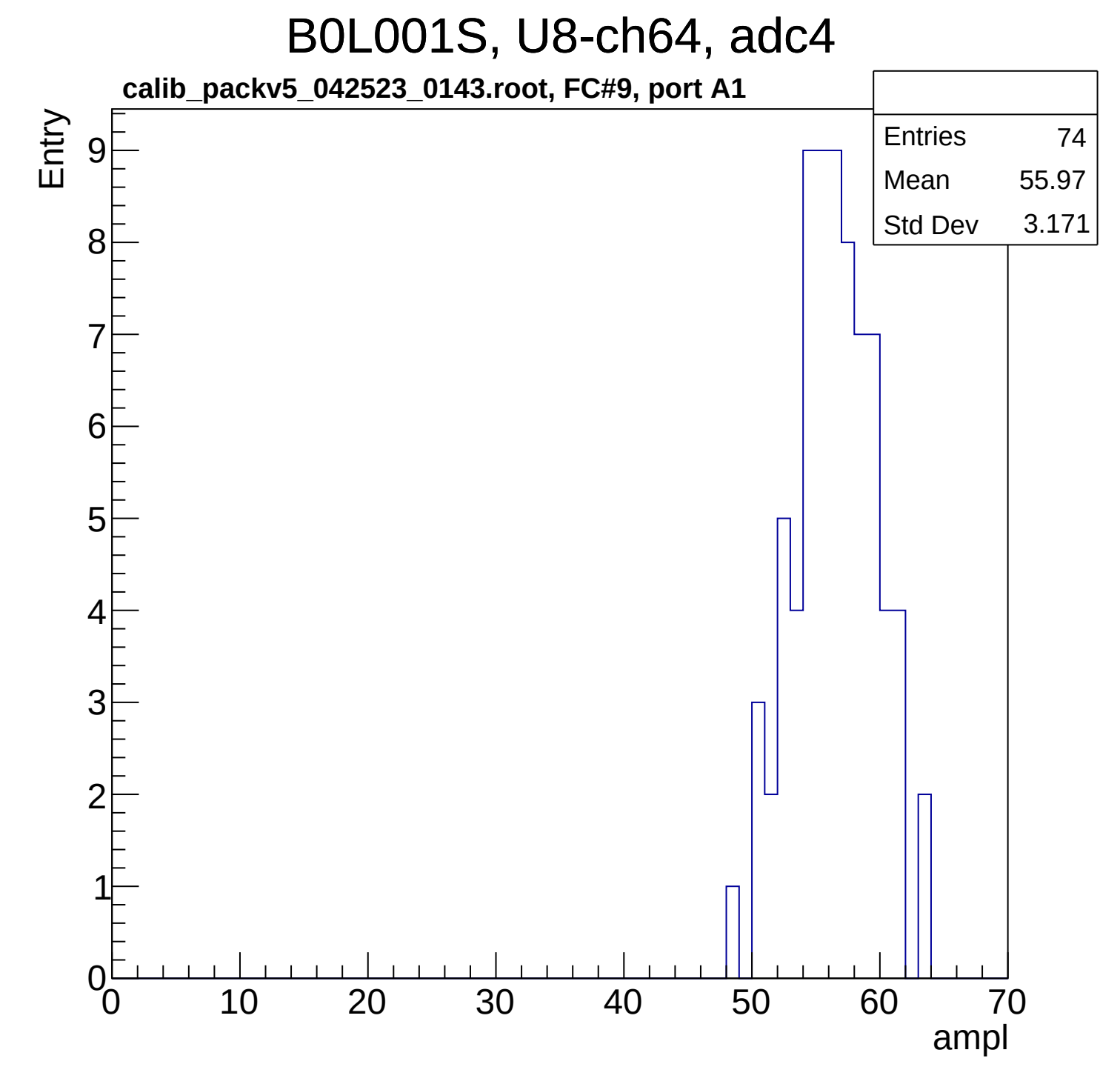
B0L001S, U8-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 55.97
Std Dev 3.171
Entry
ampl
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7
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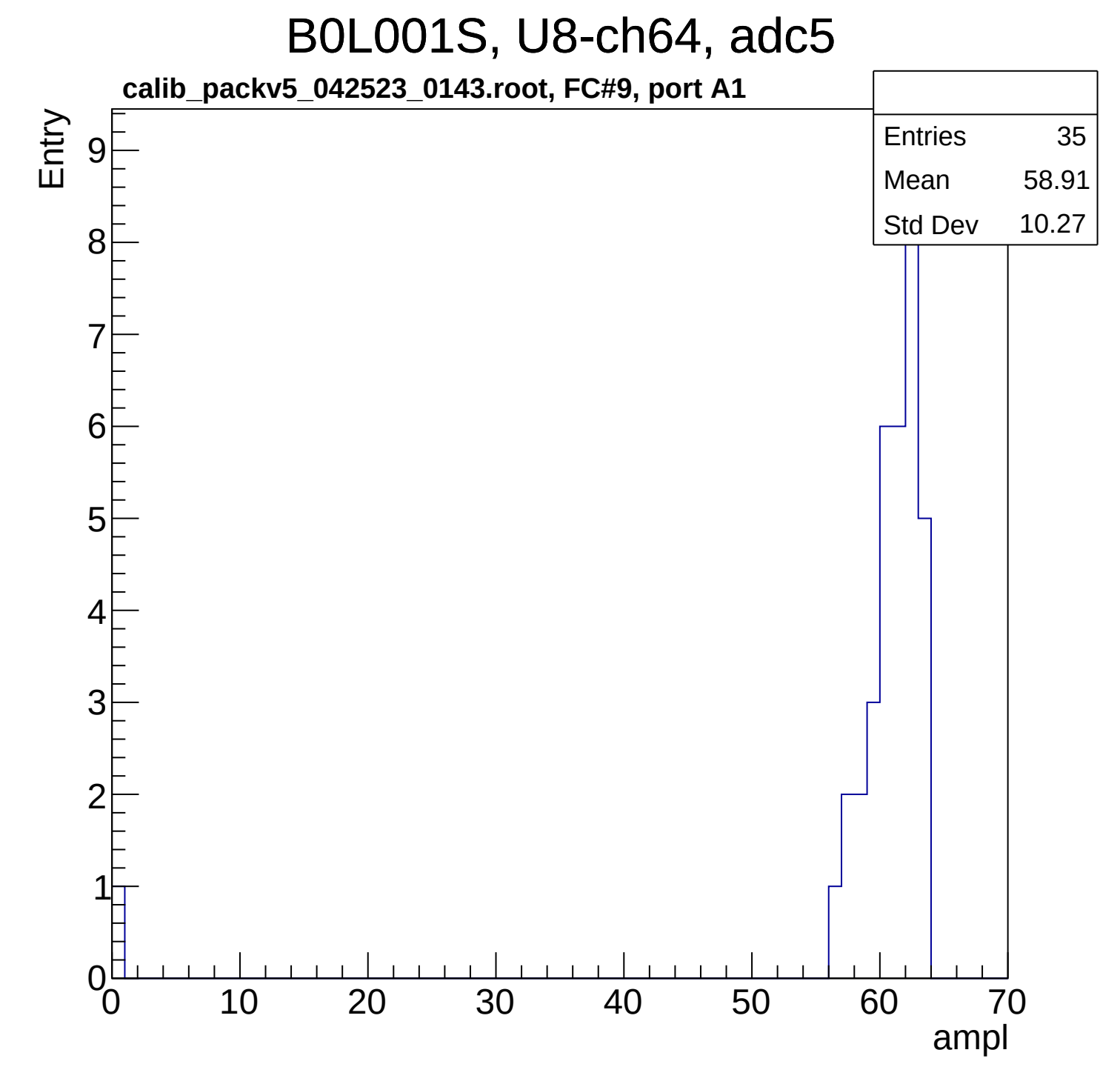
B0L001S, U8-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.91
Std Dev 10.27
Entry
ampl
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3
4
5
6
7
8
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0
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60
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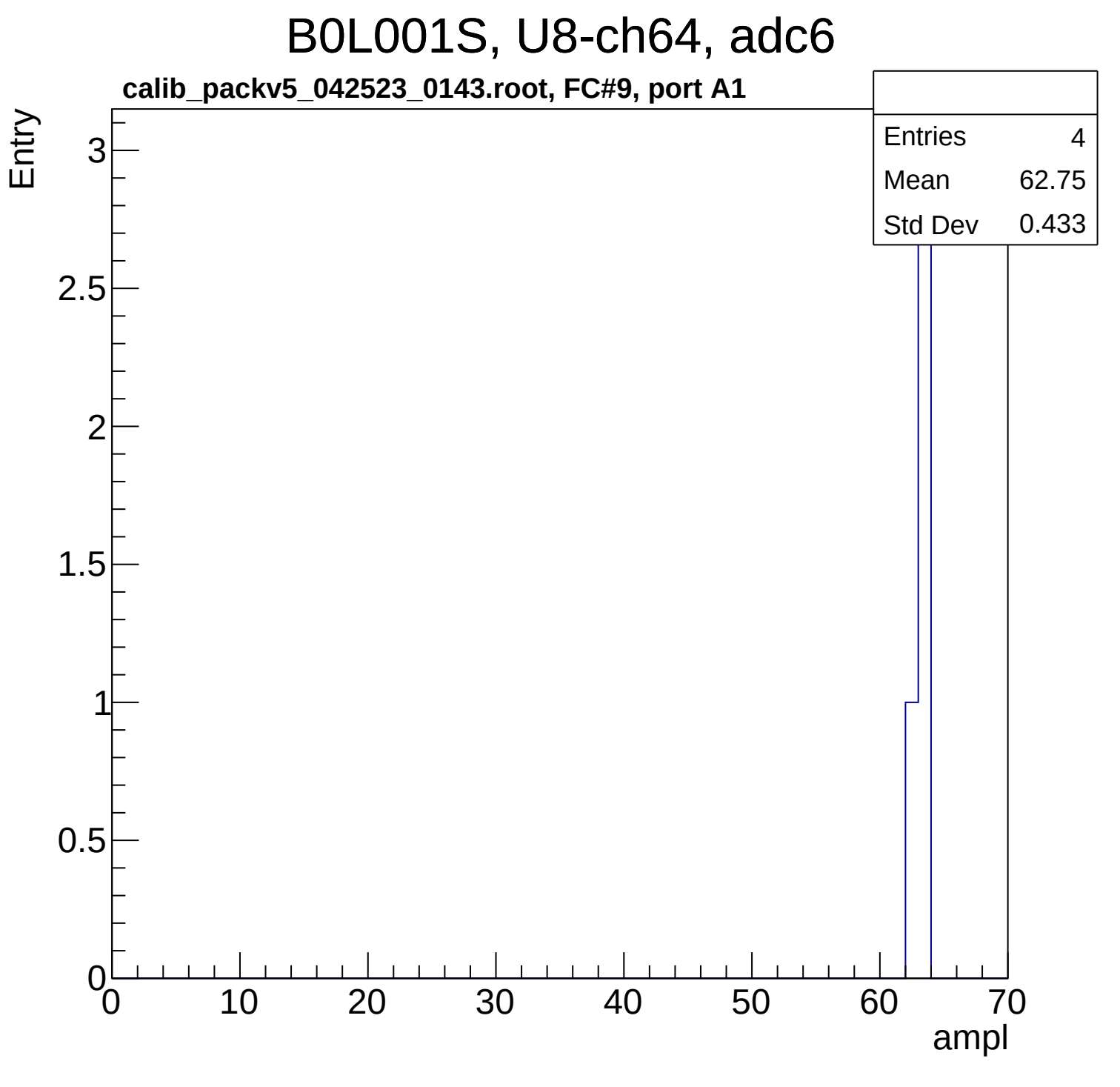
B0L001S, U8-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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60
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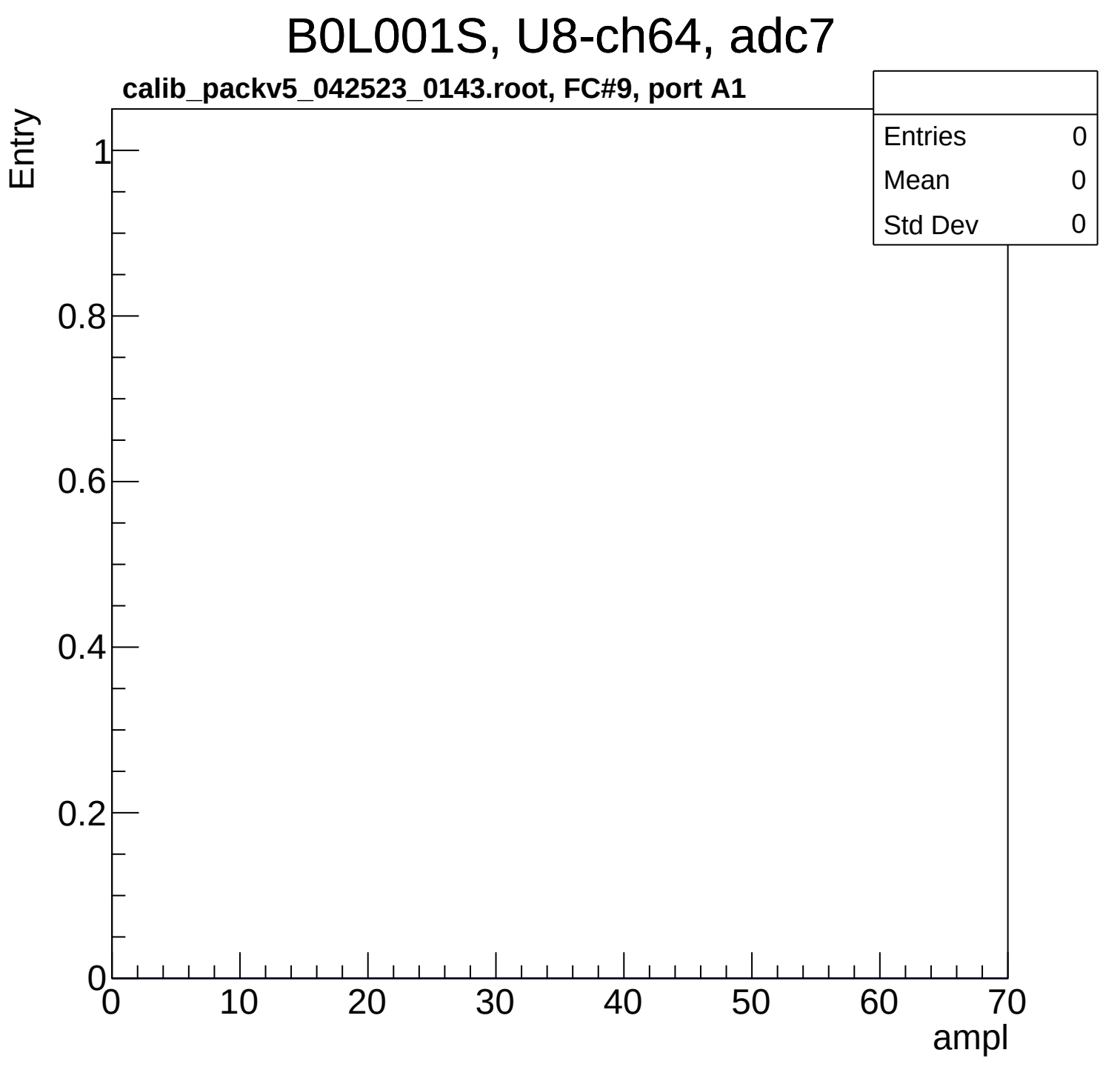
B0L001S, U8-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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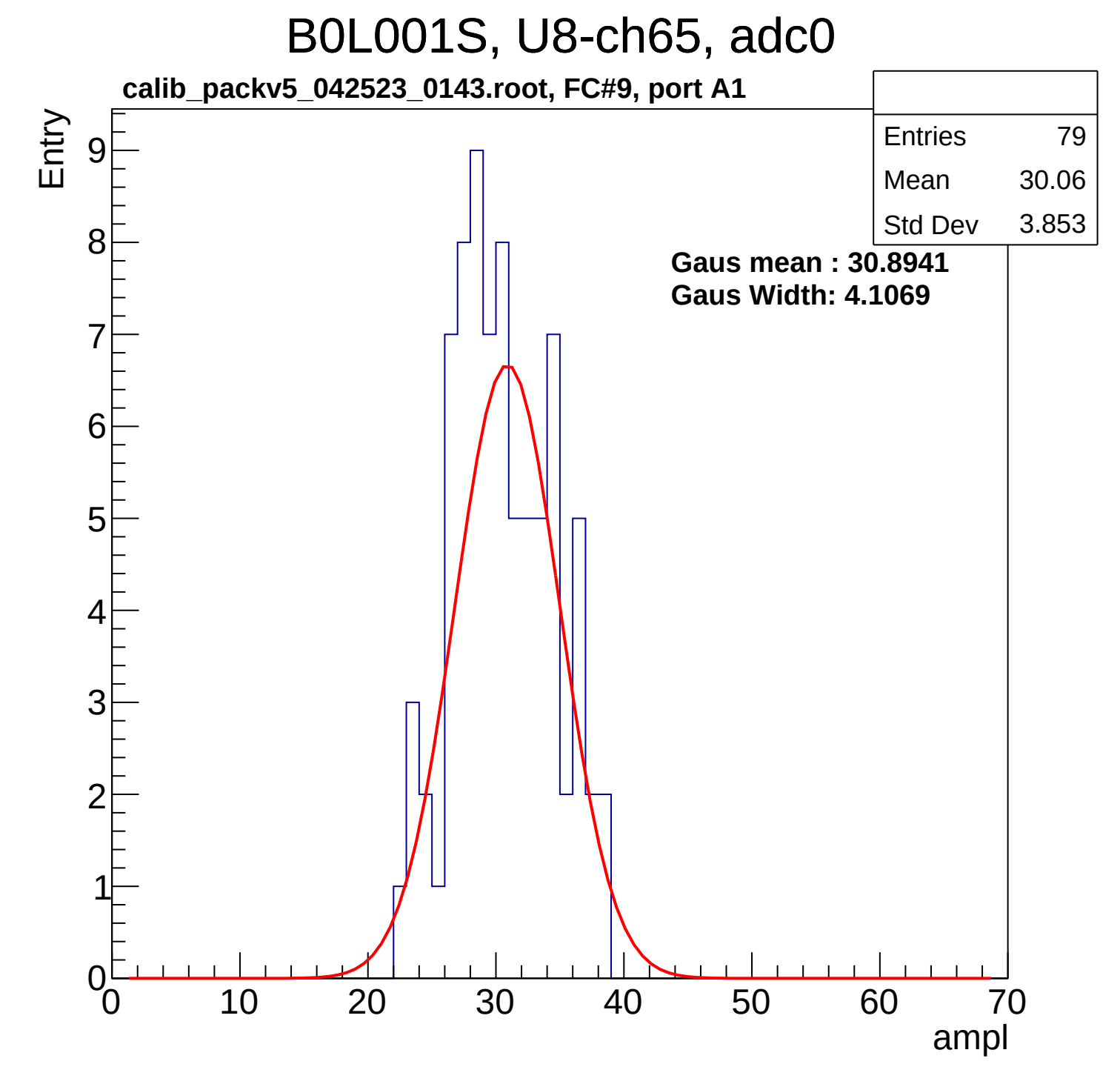
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.06
Std Dev 3.853
Gaus mean : 30.8941
Gaus Width: 4.1069
Entry
ampl
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2
3
4
5
6
7
8
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0
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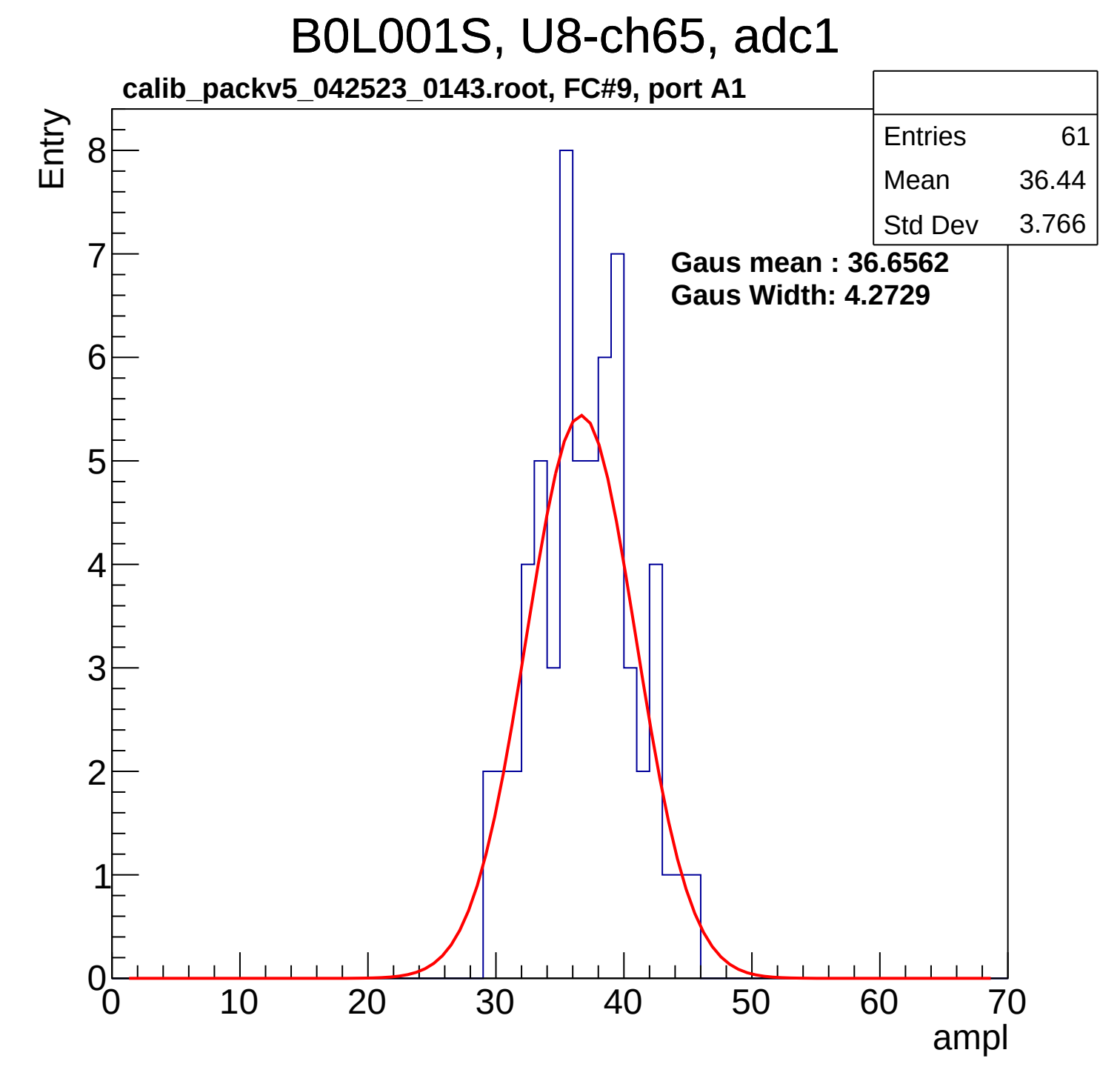
B0L001S, U8-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.44
Std Dev 3.766
Gaus mean : 36.6562
Gaus Width: 4.2729
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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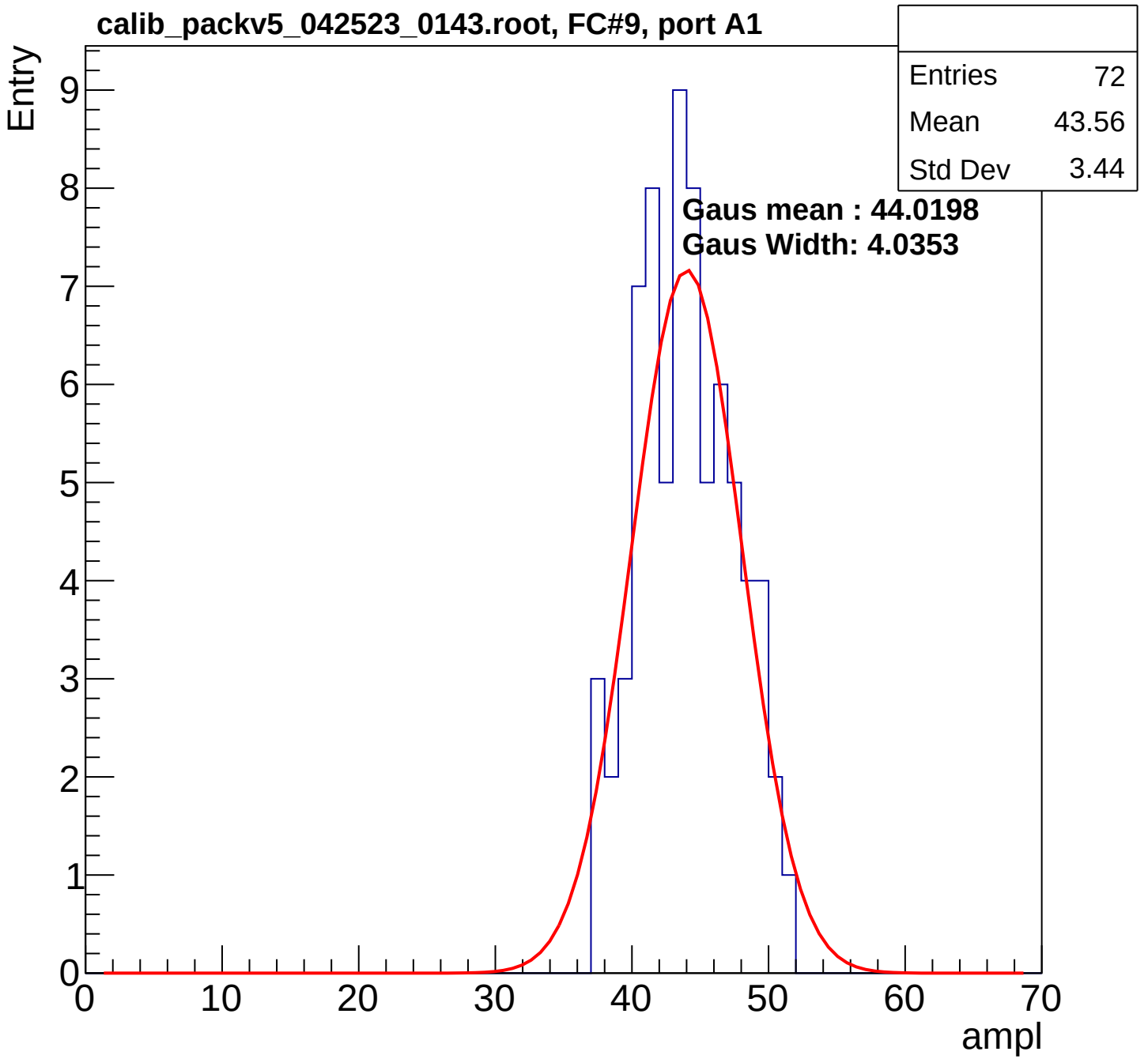
B0L001S, U8-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 43.56
Std Dev 3.44
Gaus mean : 44.0198
Gaus Width: 4.0353
Entry
ampl
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2
3
4
5
6
7
8
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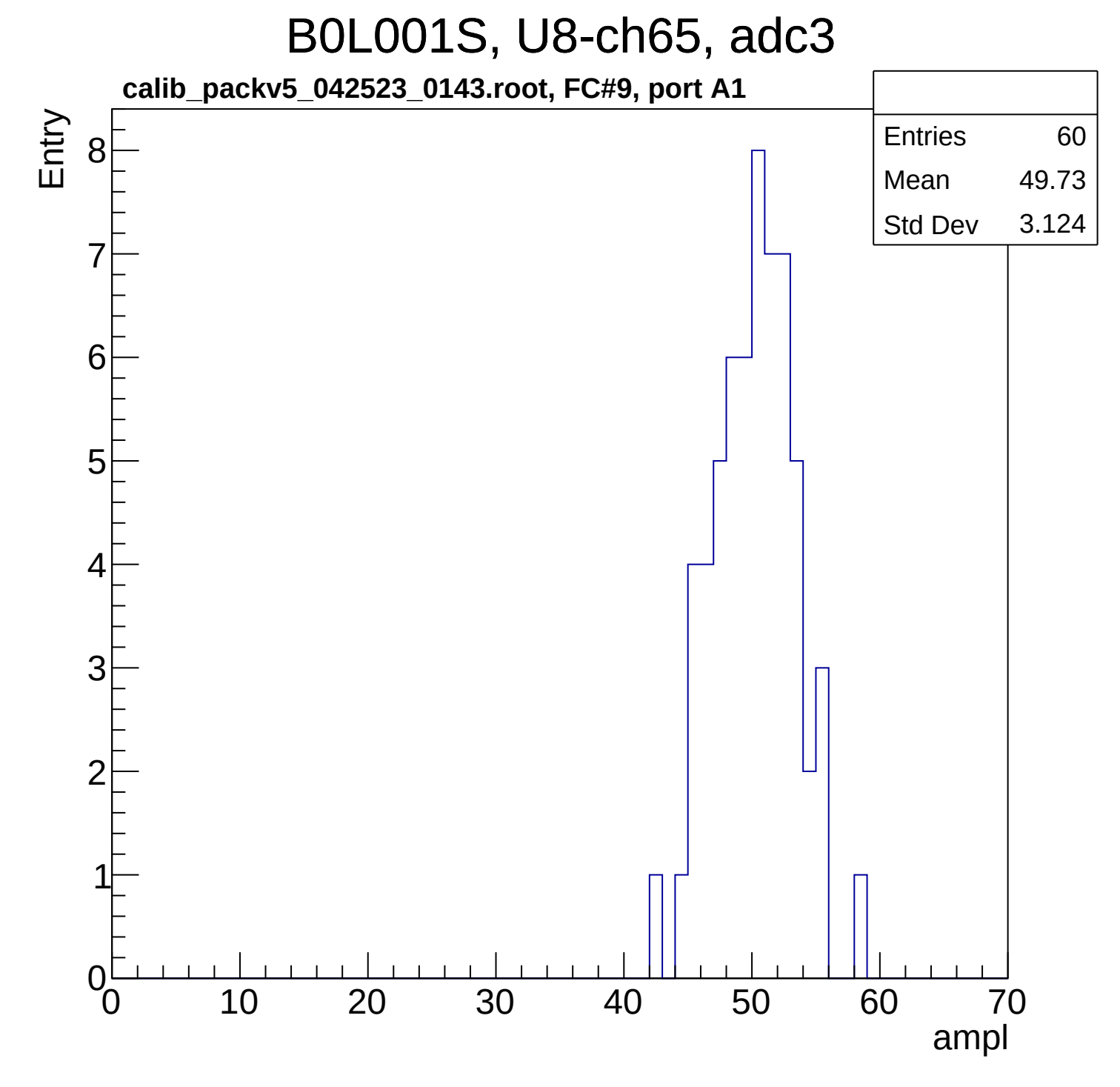
B0L001S, U8-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.73
Std Dev 3.124
Entry
ampl
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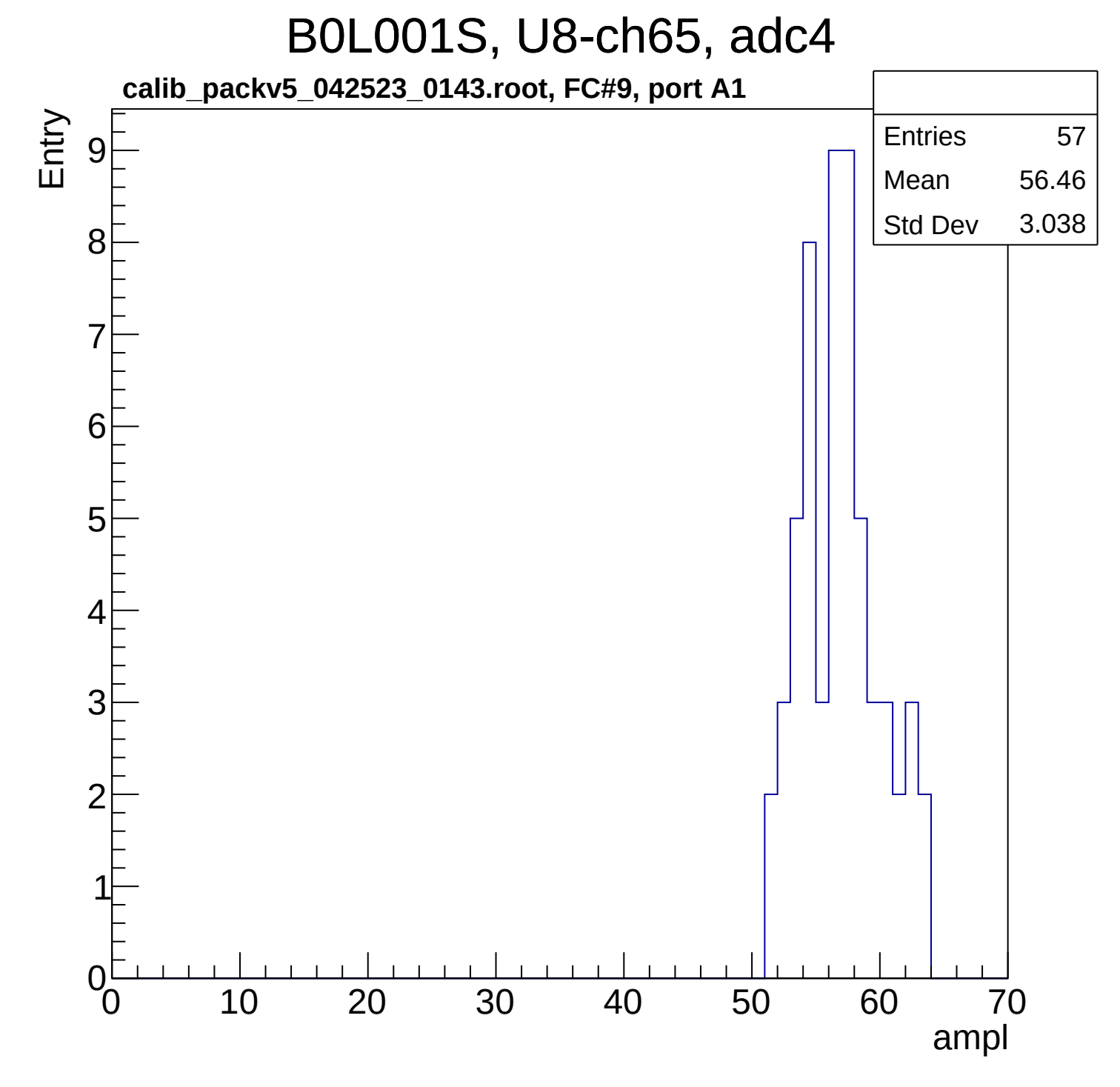
B0L001S, U8-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.46
Std Dev 3.038
Entry
ampl
0
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2
3
4
5
6
7
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9
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60
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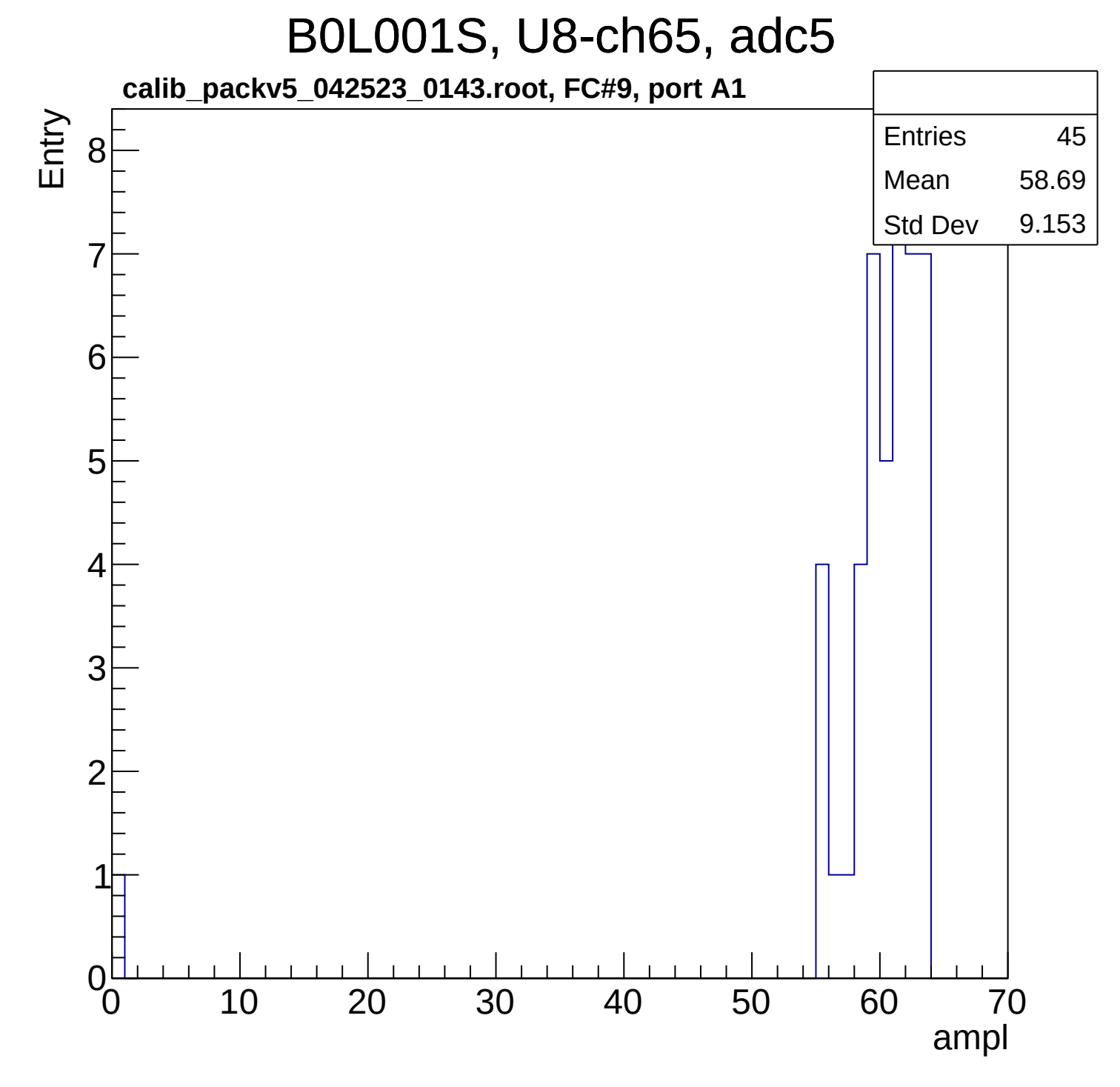
B0L001S, U8-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.69
Std Dev 9.153
Entry
ampl
0
1
2
3
4
5
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7
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40
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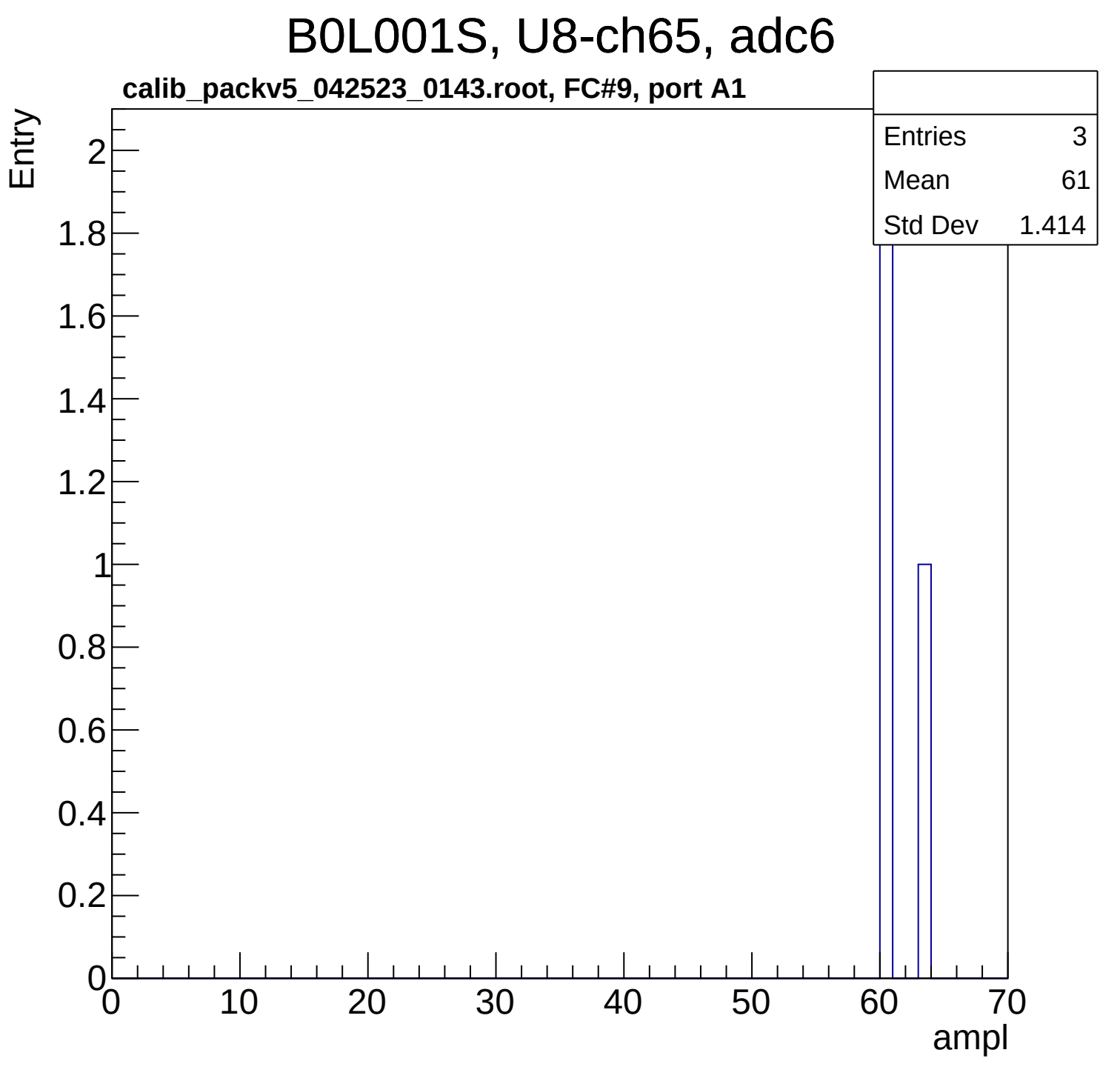
B0L001S, U8-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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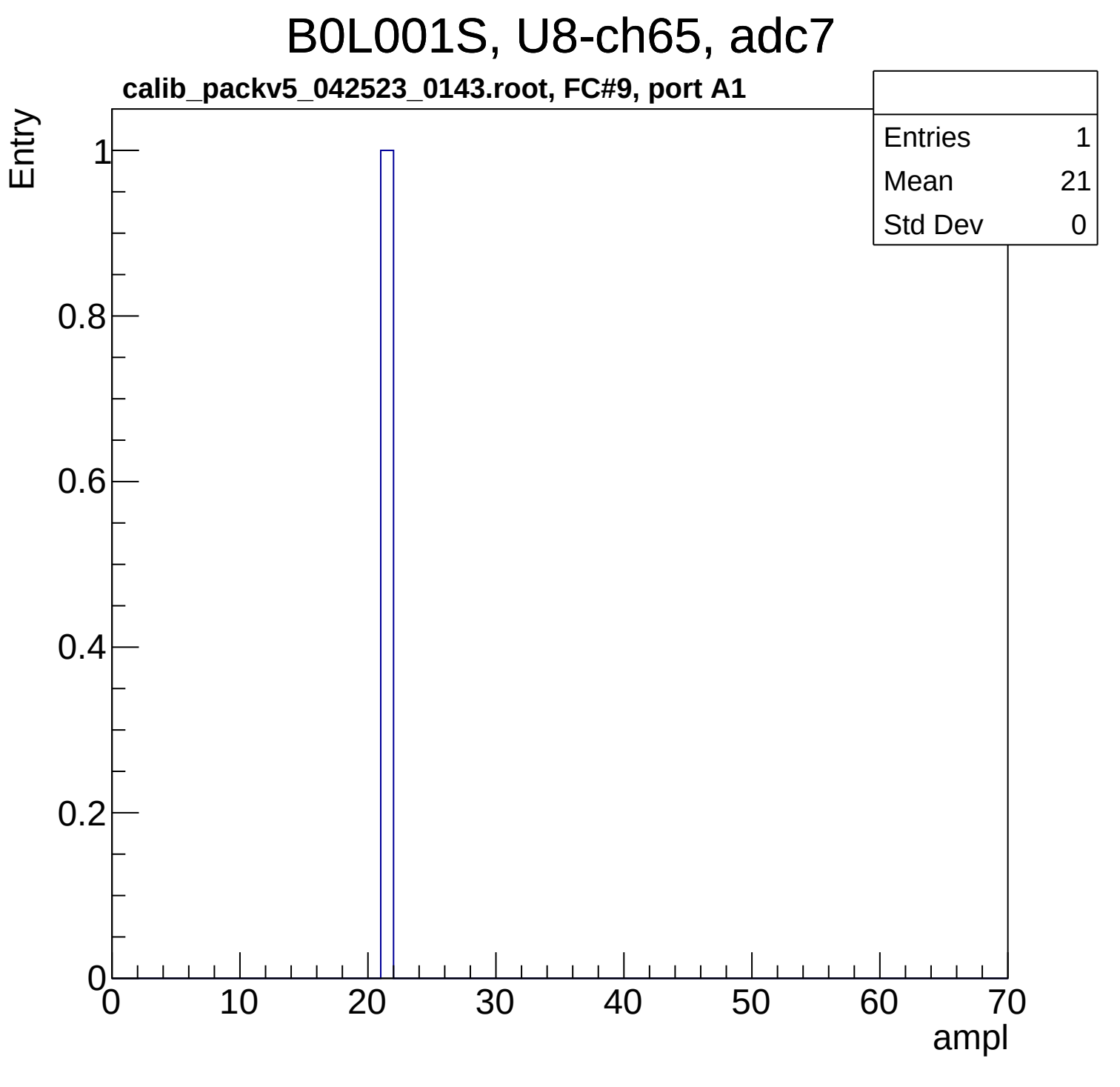
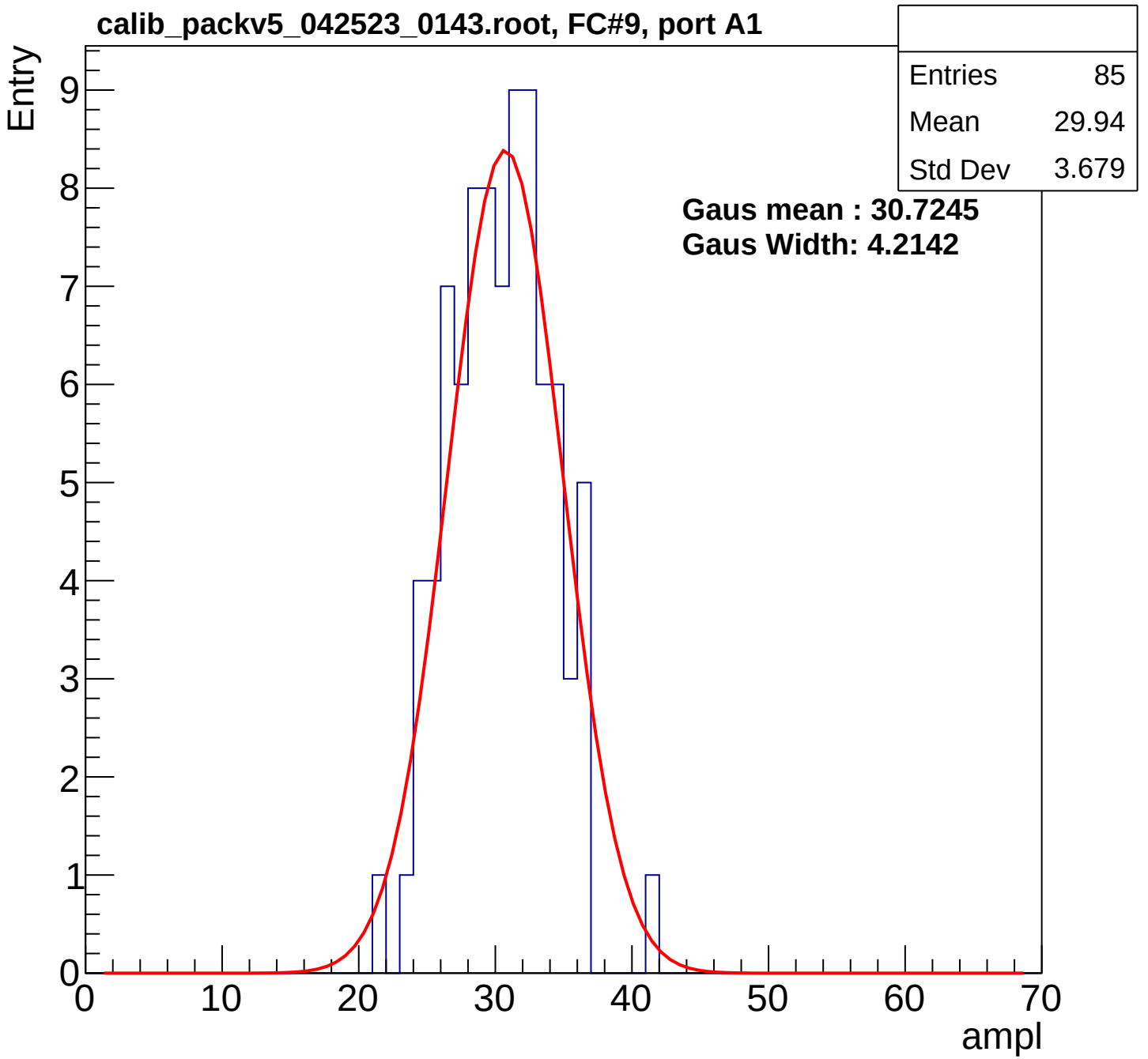

B0L001S, U8-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 29.94
Std Dev 3.679
Gaus mean : 30.7245
Gaus Width: 4.2142
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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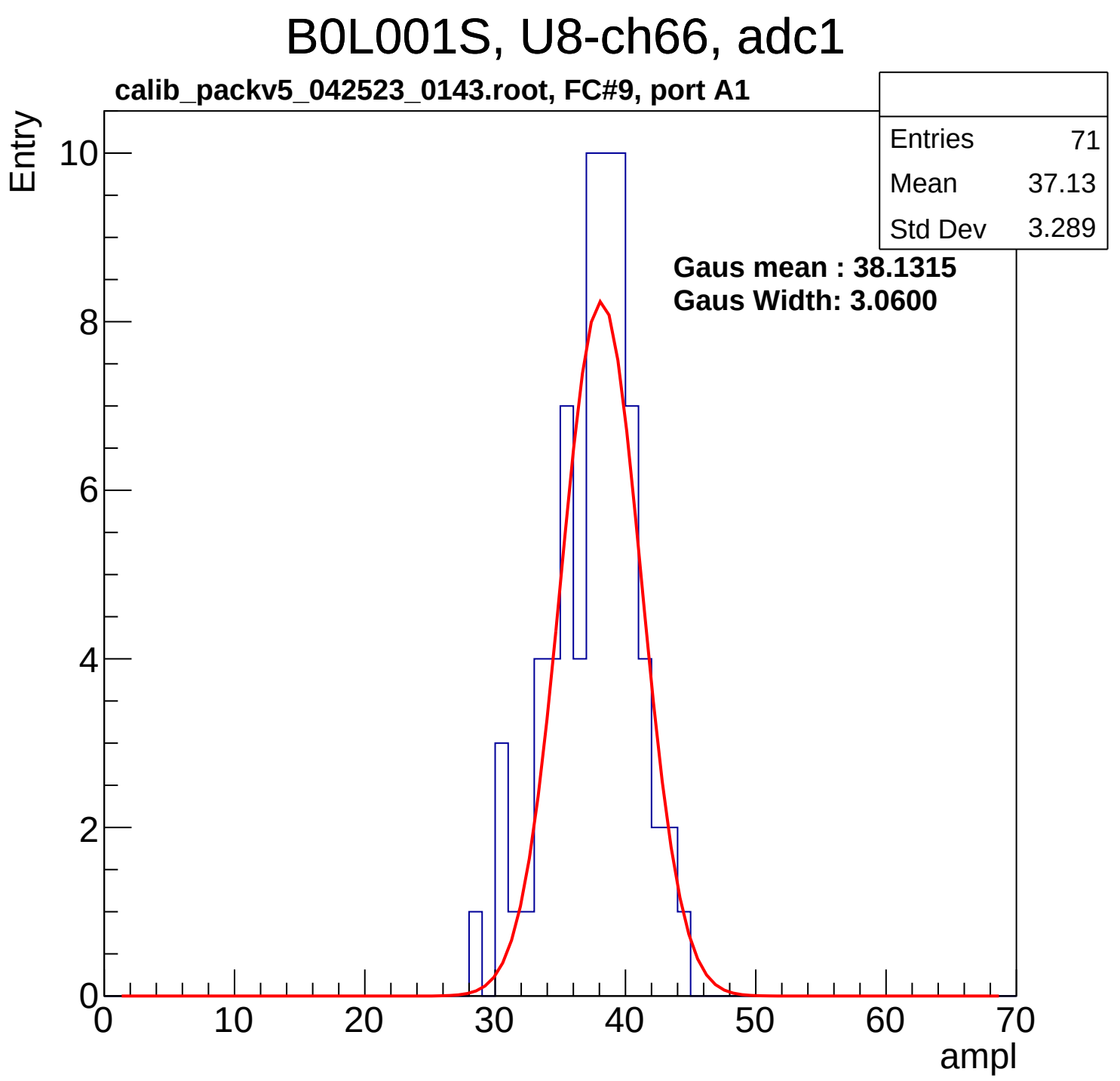
B0L001S, U8-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.13
Std Dev 3.289
Gaus mean : 38.1315
Gaus Width: 3.0600
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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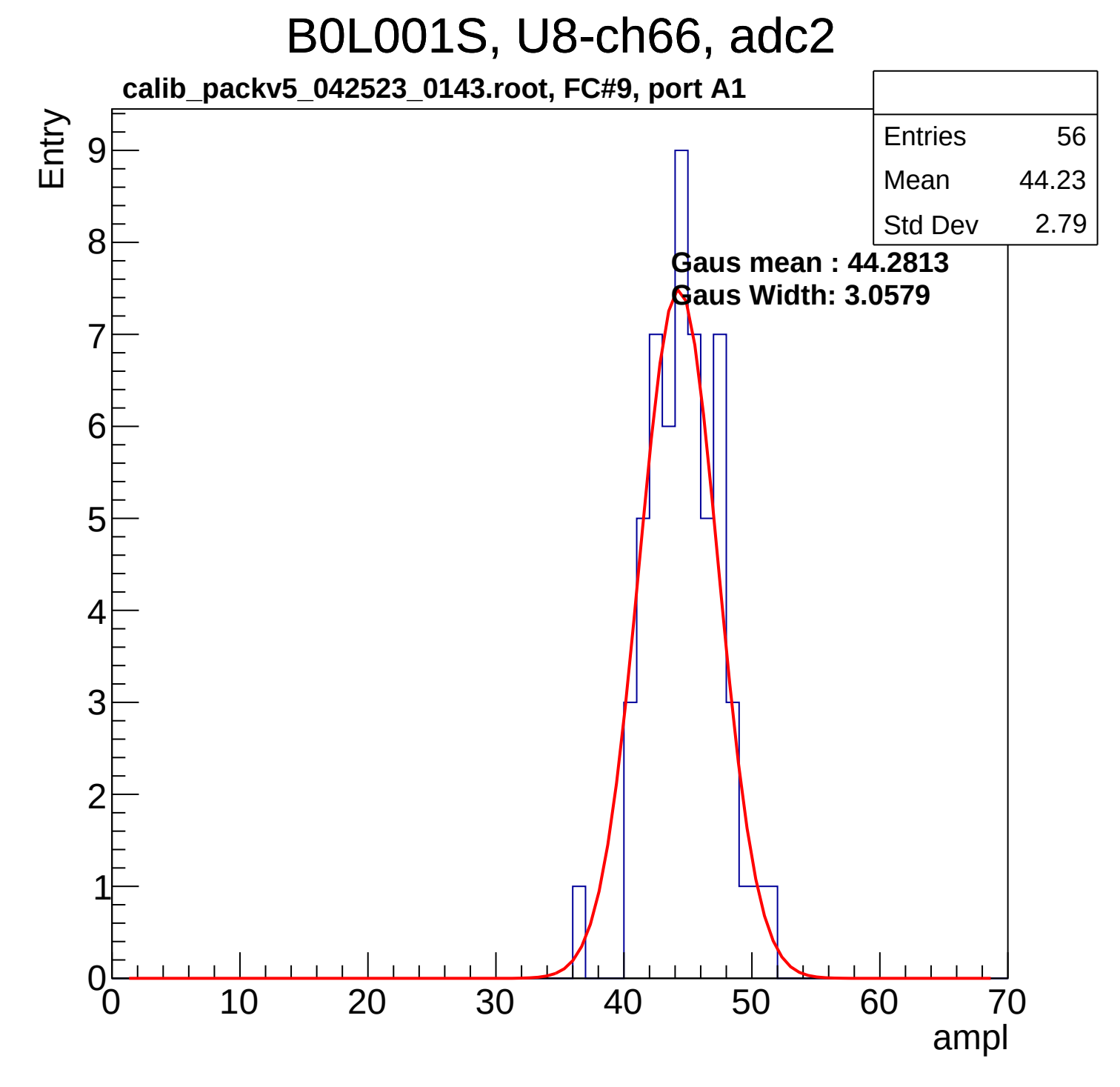
B0L001S, U8-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.23
Std Dev 2.79
Gaus mean : 44.2813
Gaus Width: 3.0579
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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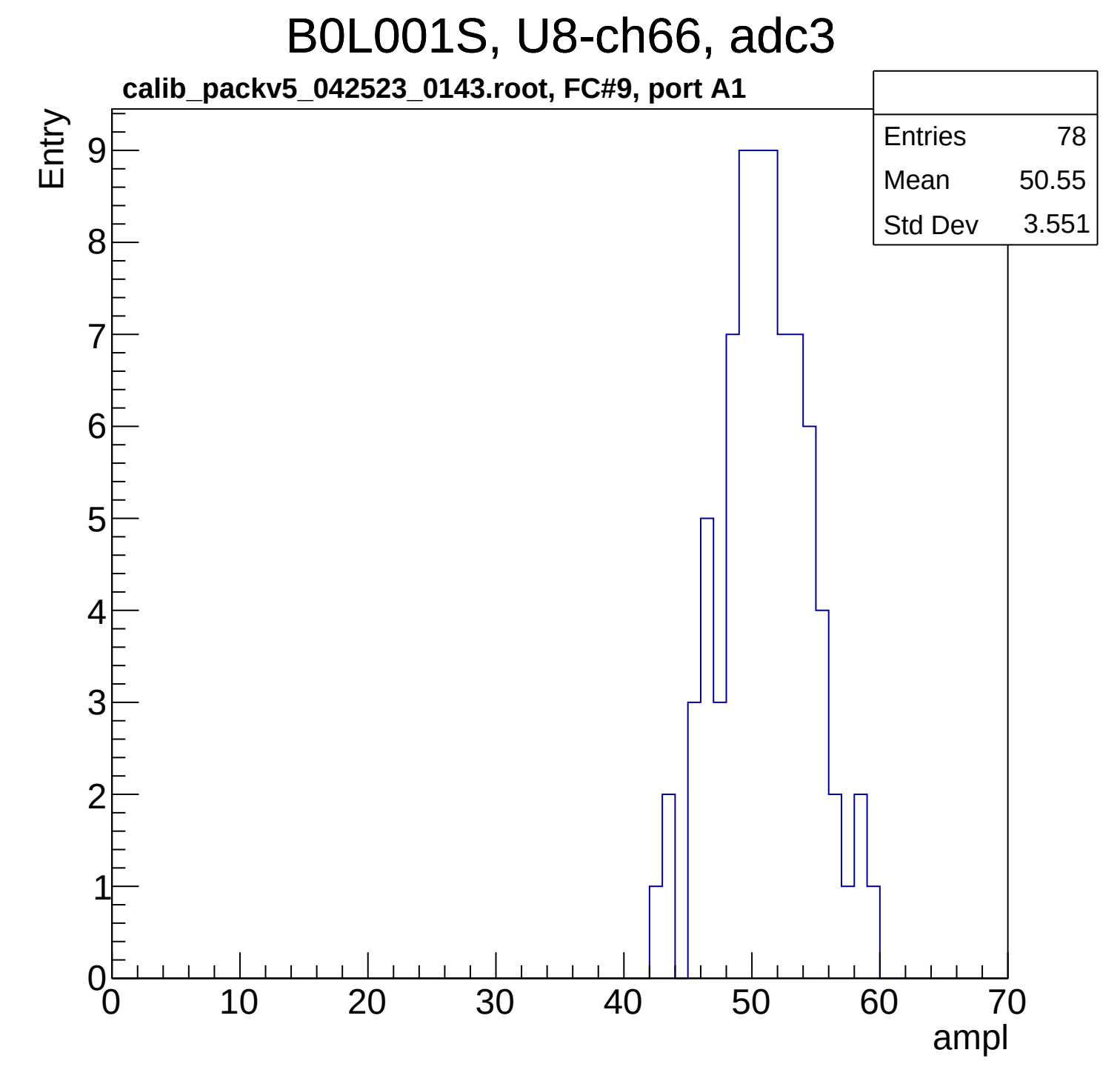
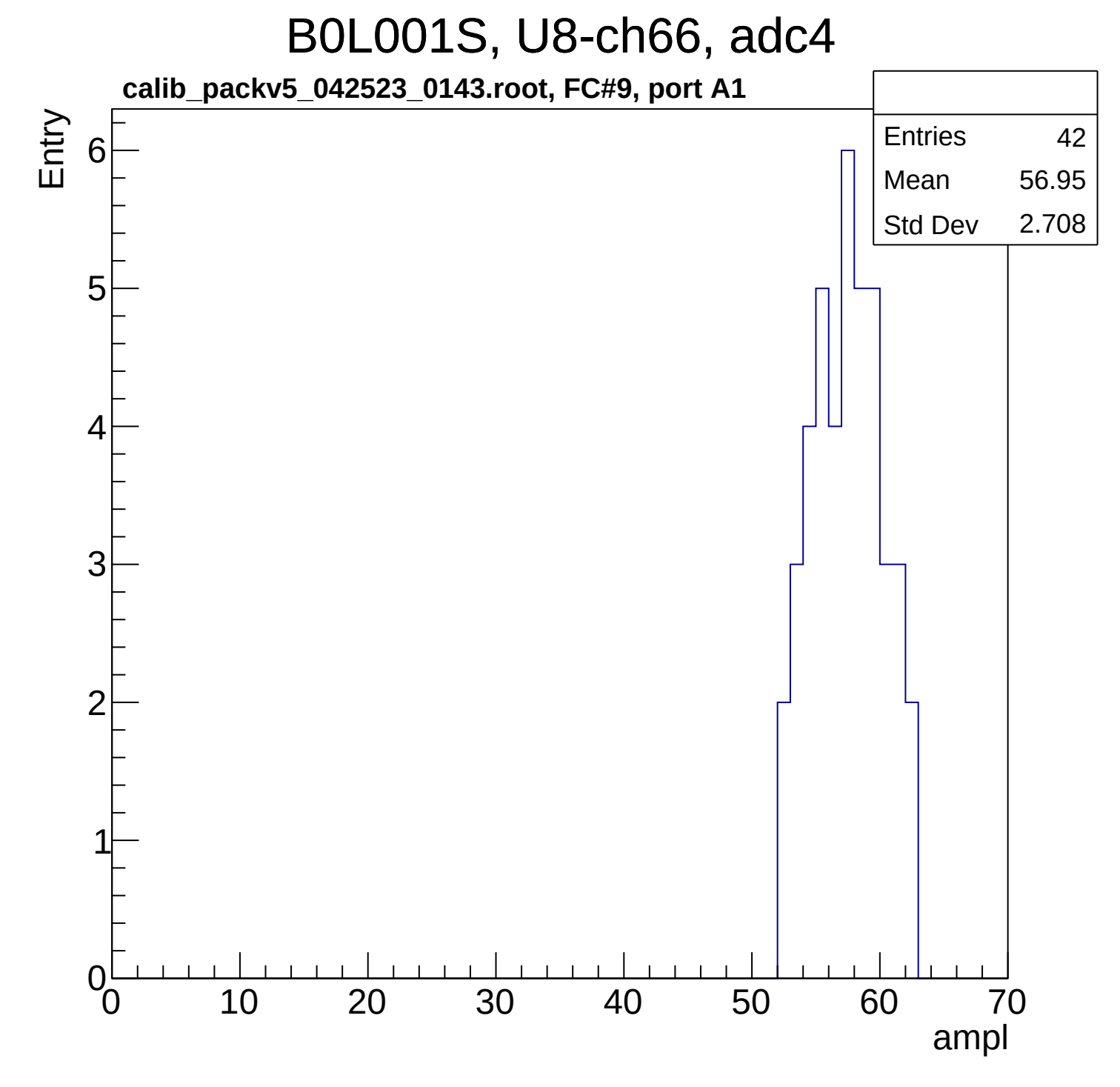
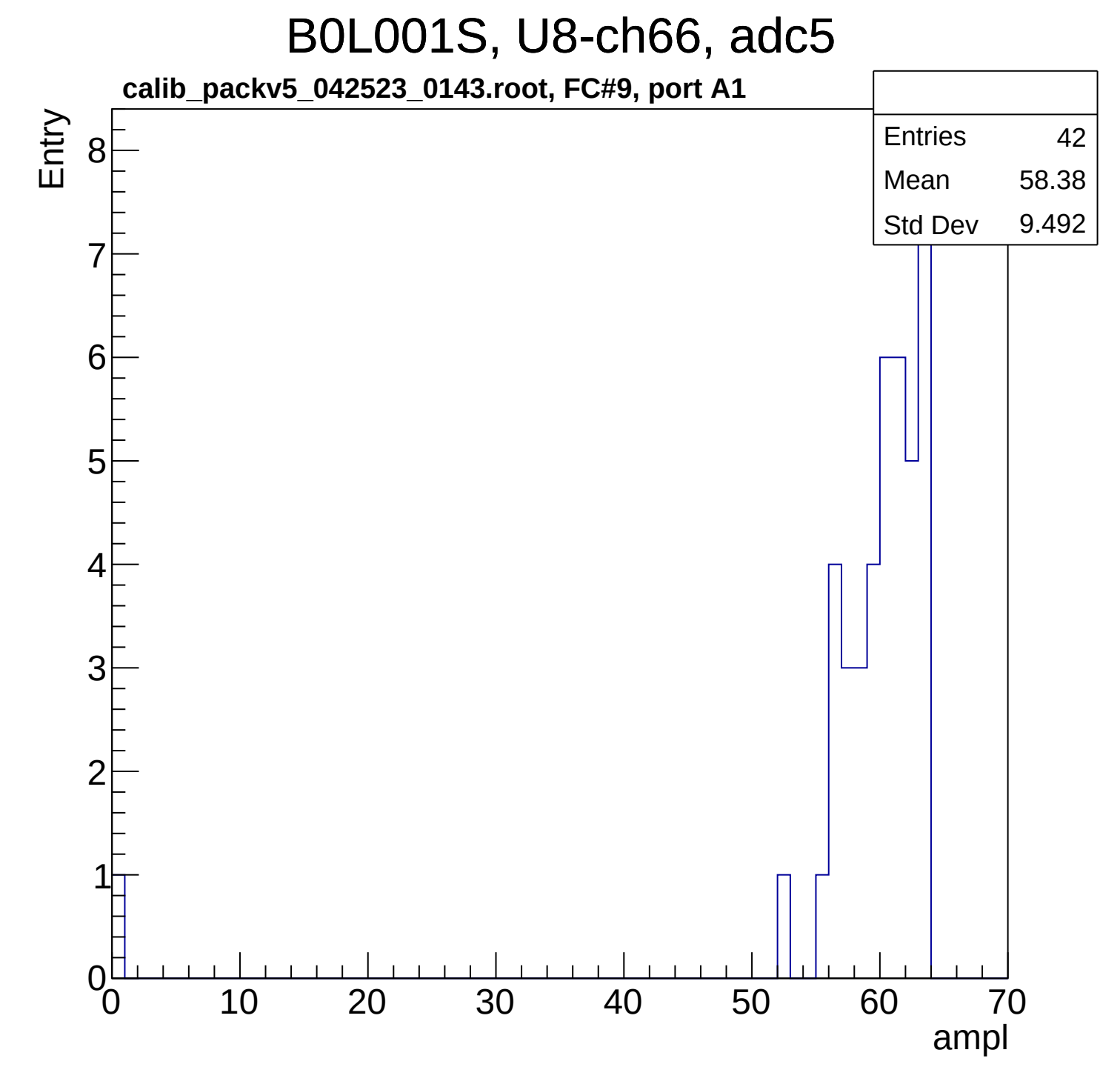

B0L001S, U8-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 50.55
Std Dev 3.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 56.95
Std Dev 2.708
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.38
Std Dev 9.492
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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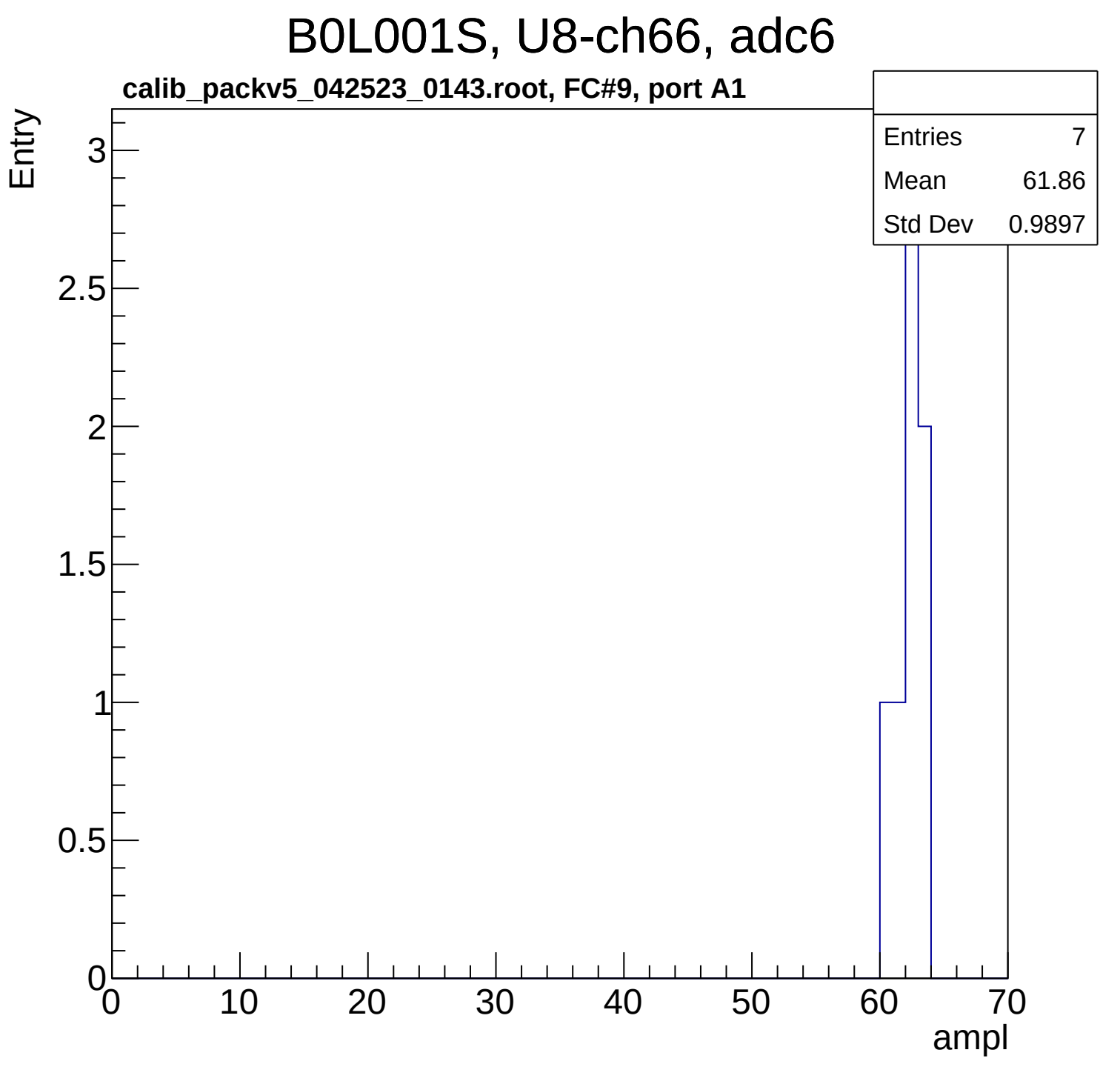
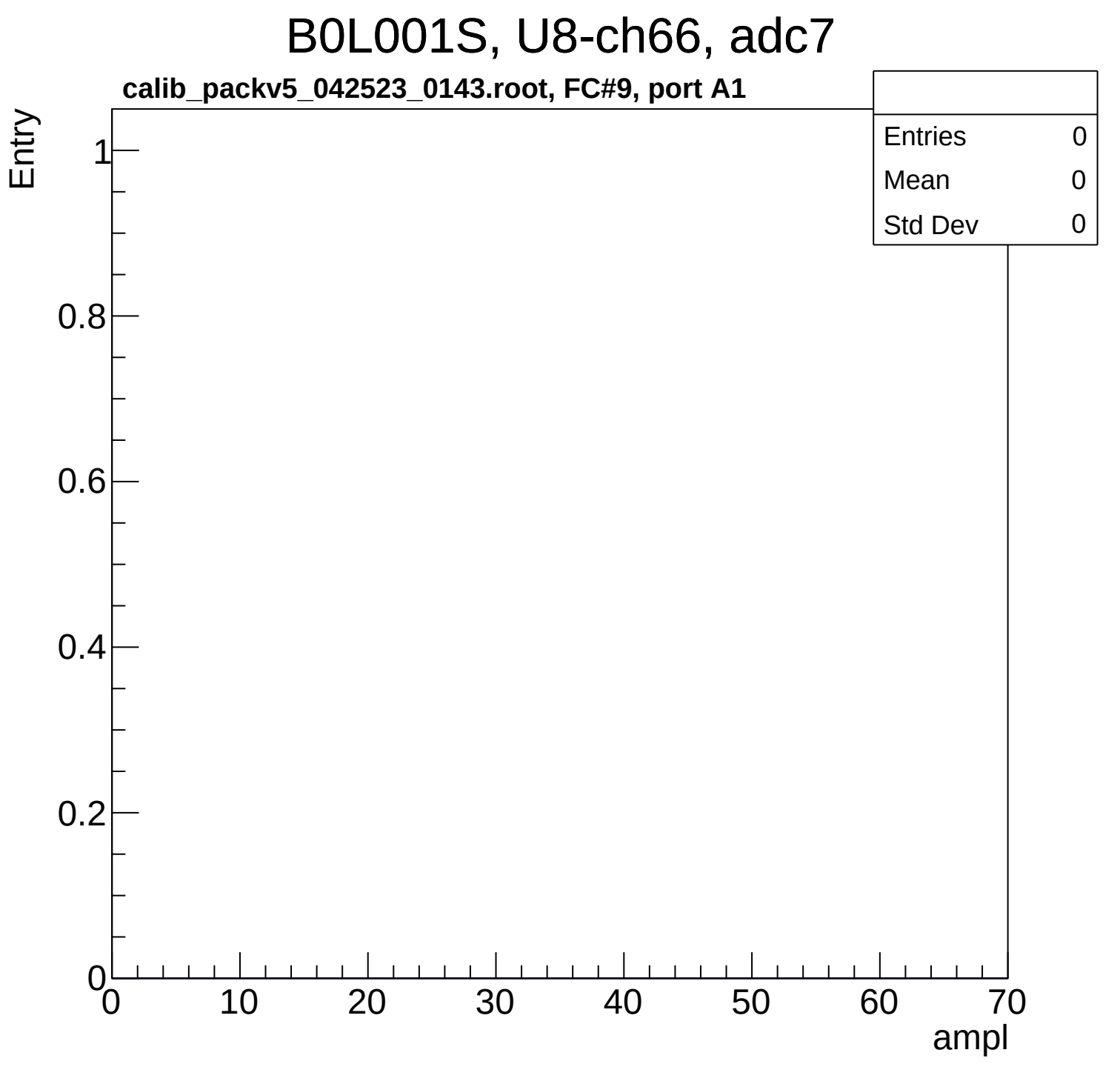
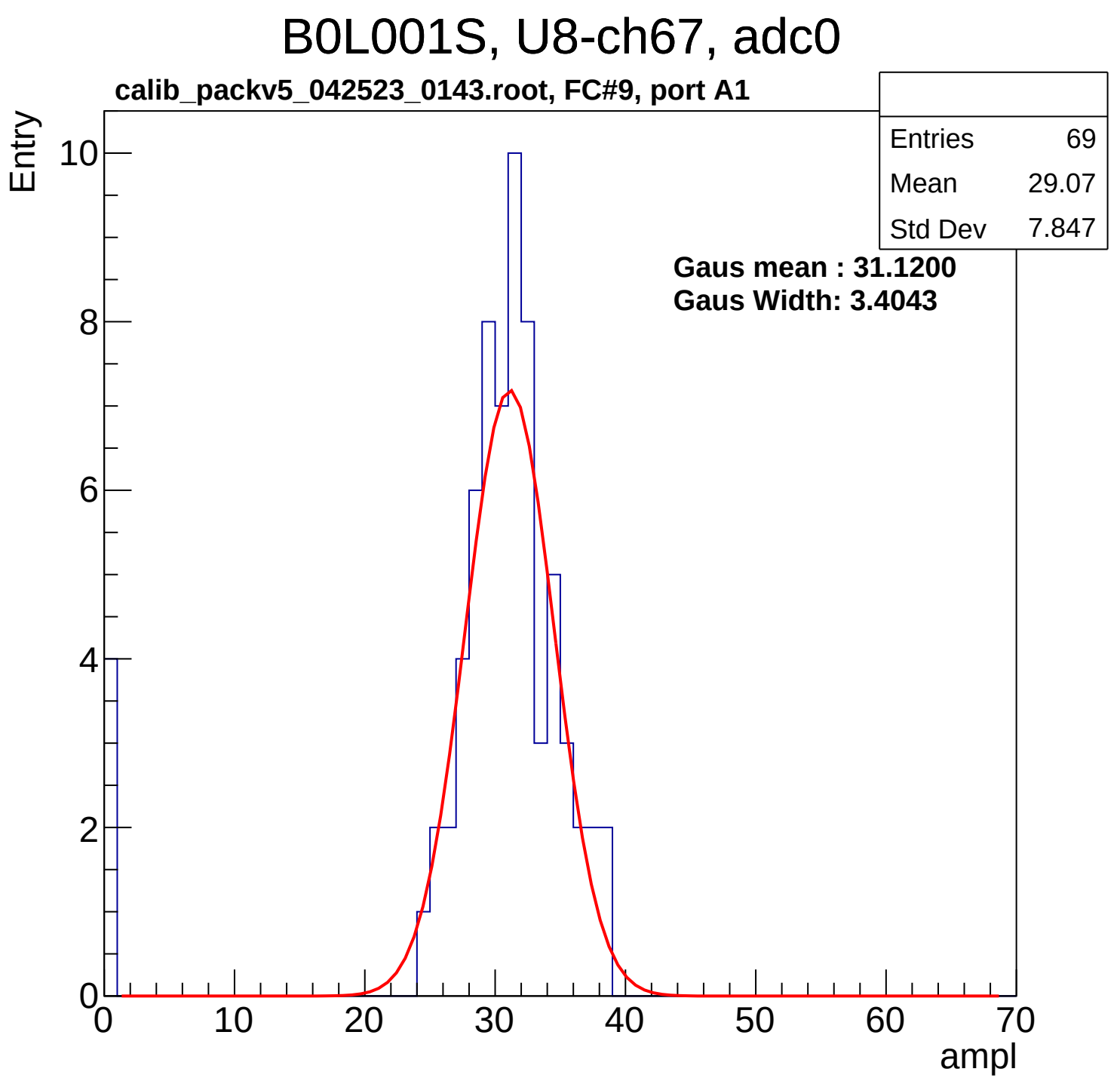
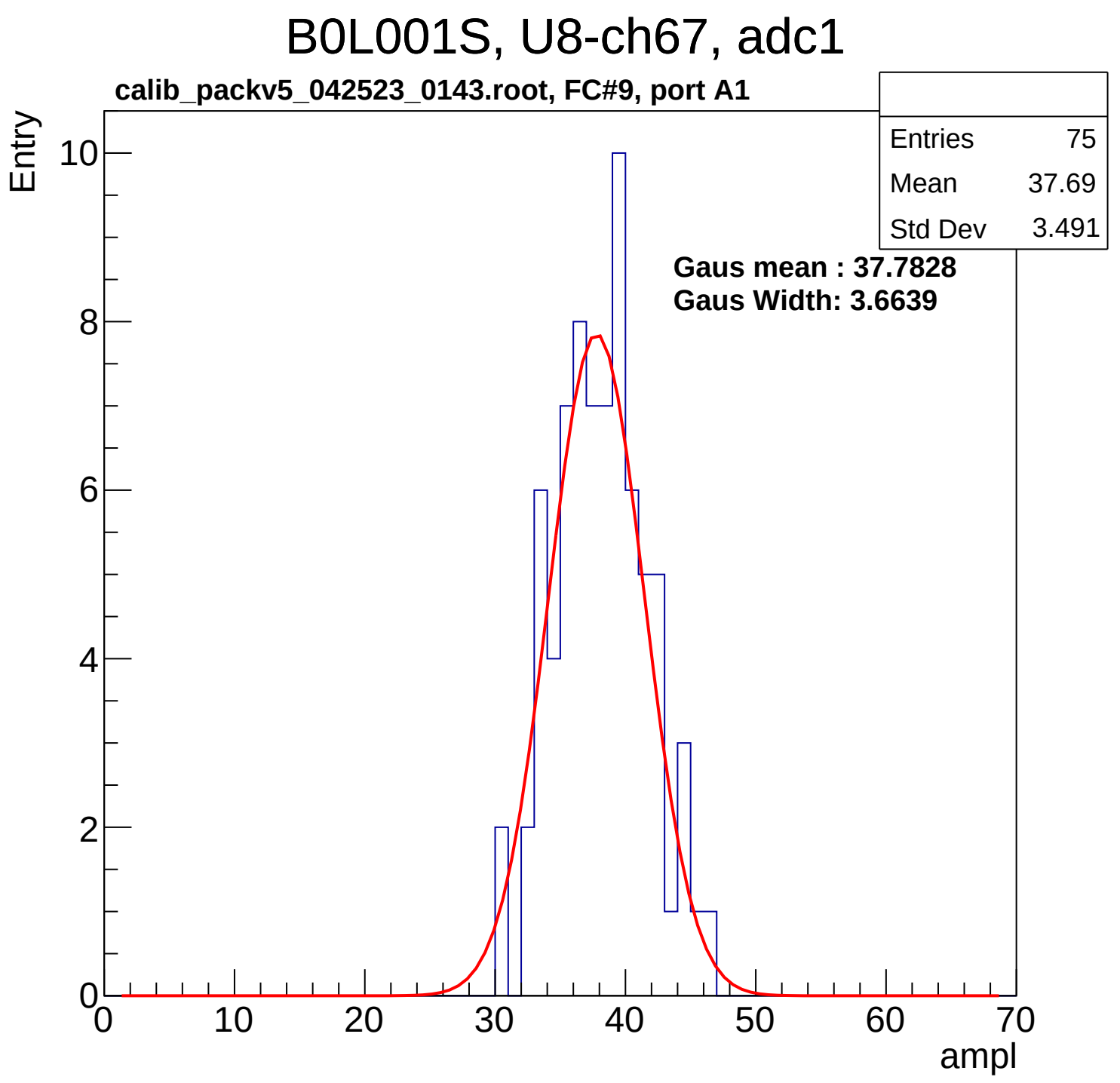
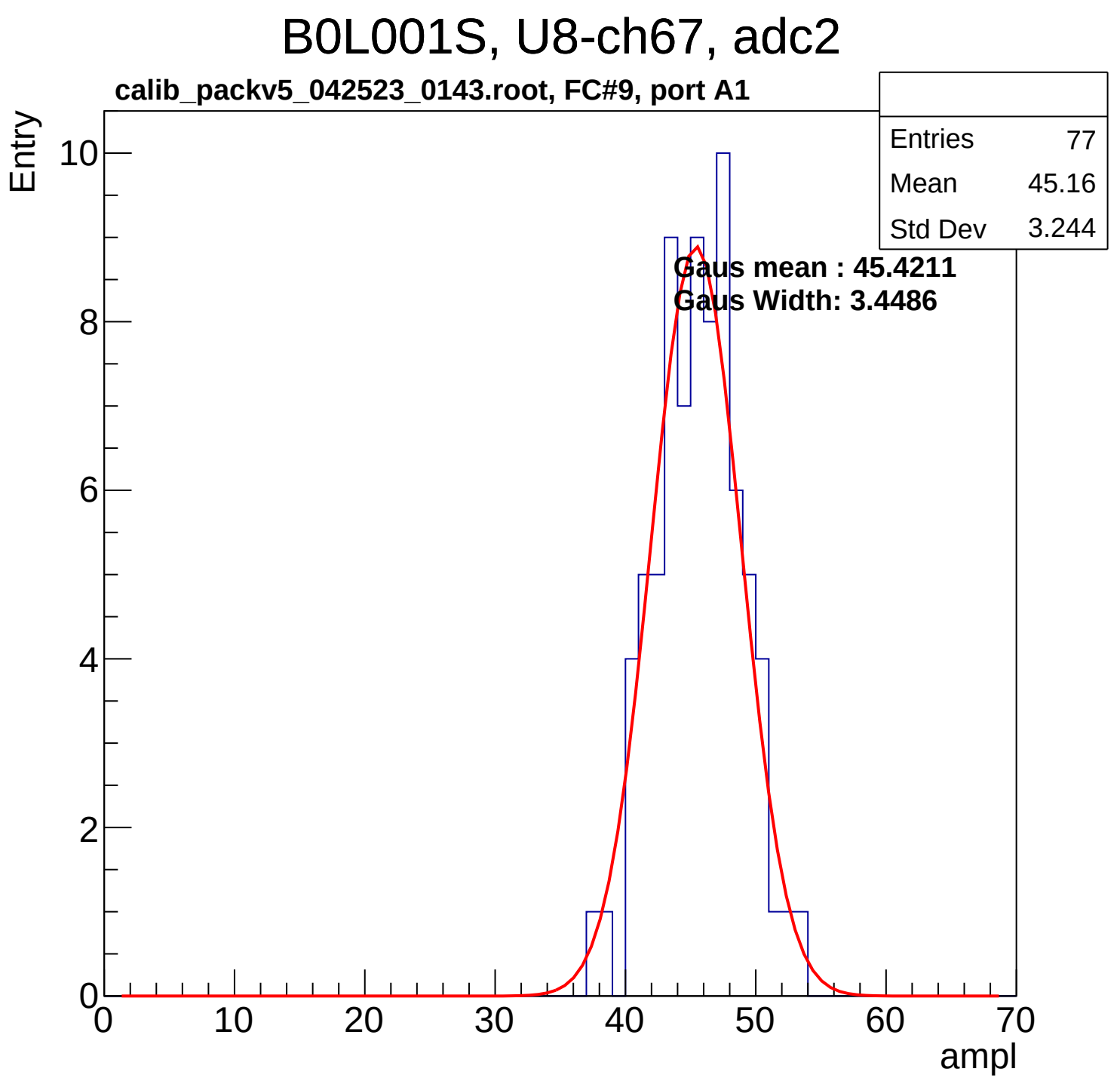
40
50
60
70

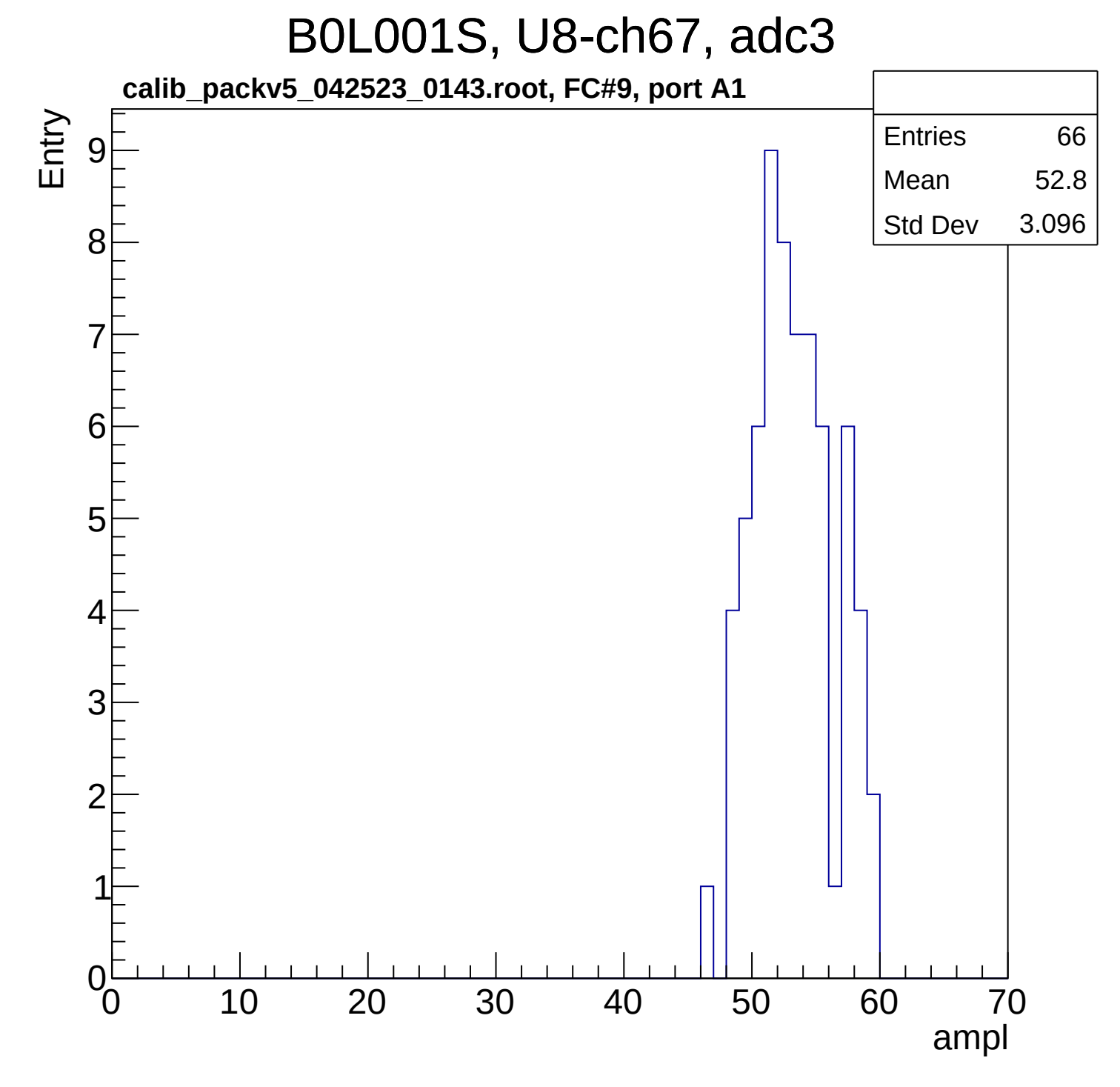

B0L001S, U8-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.07
Std Dev 7.847
Gaus mean : 31.1200
Gaus Width: 3.4043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.69
Std Dev 3.491
Gaus mean : 37.7828
Gaus Width: 3.6639
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 45.16
Std Dev 3.244
Gaus mean : 45.4211
Gaus Width: 3.4486
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.8
Std Dev 3.096
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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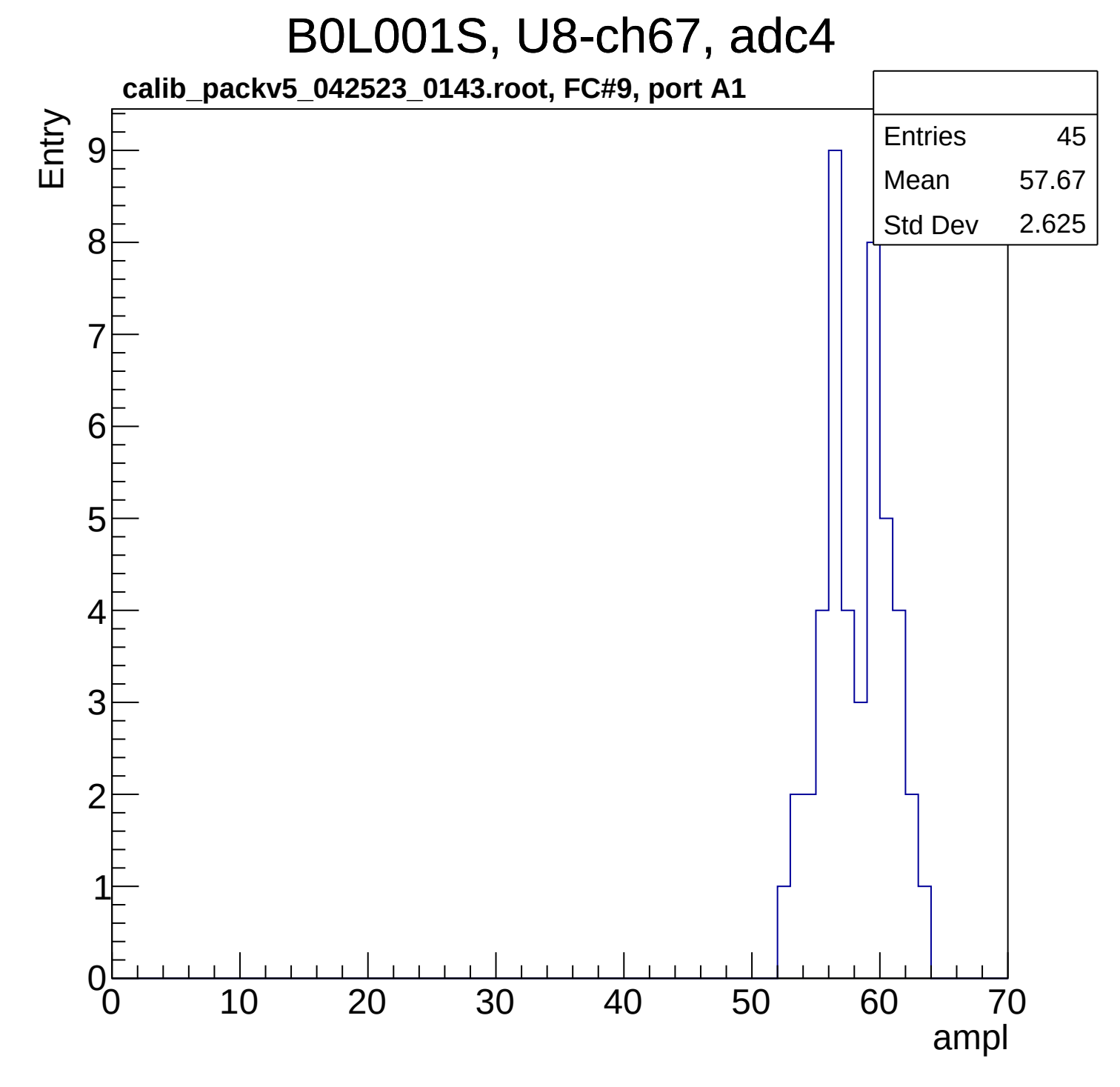
B0L001S, U8-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 57.67
Std Dev 2.625
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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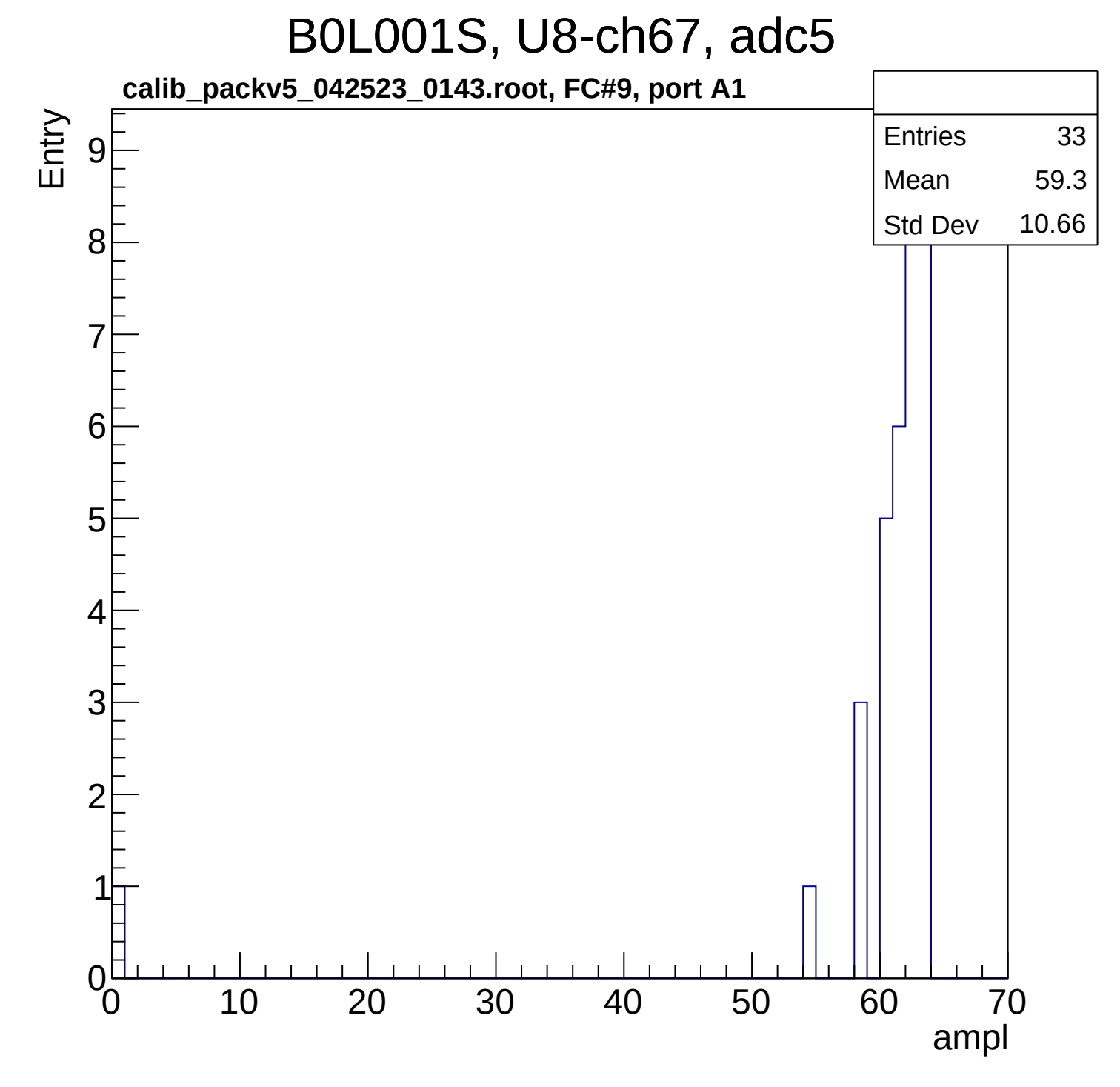
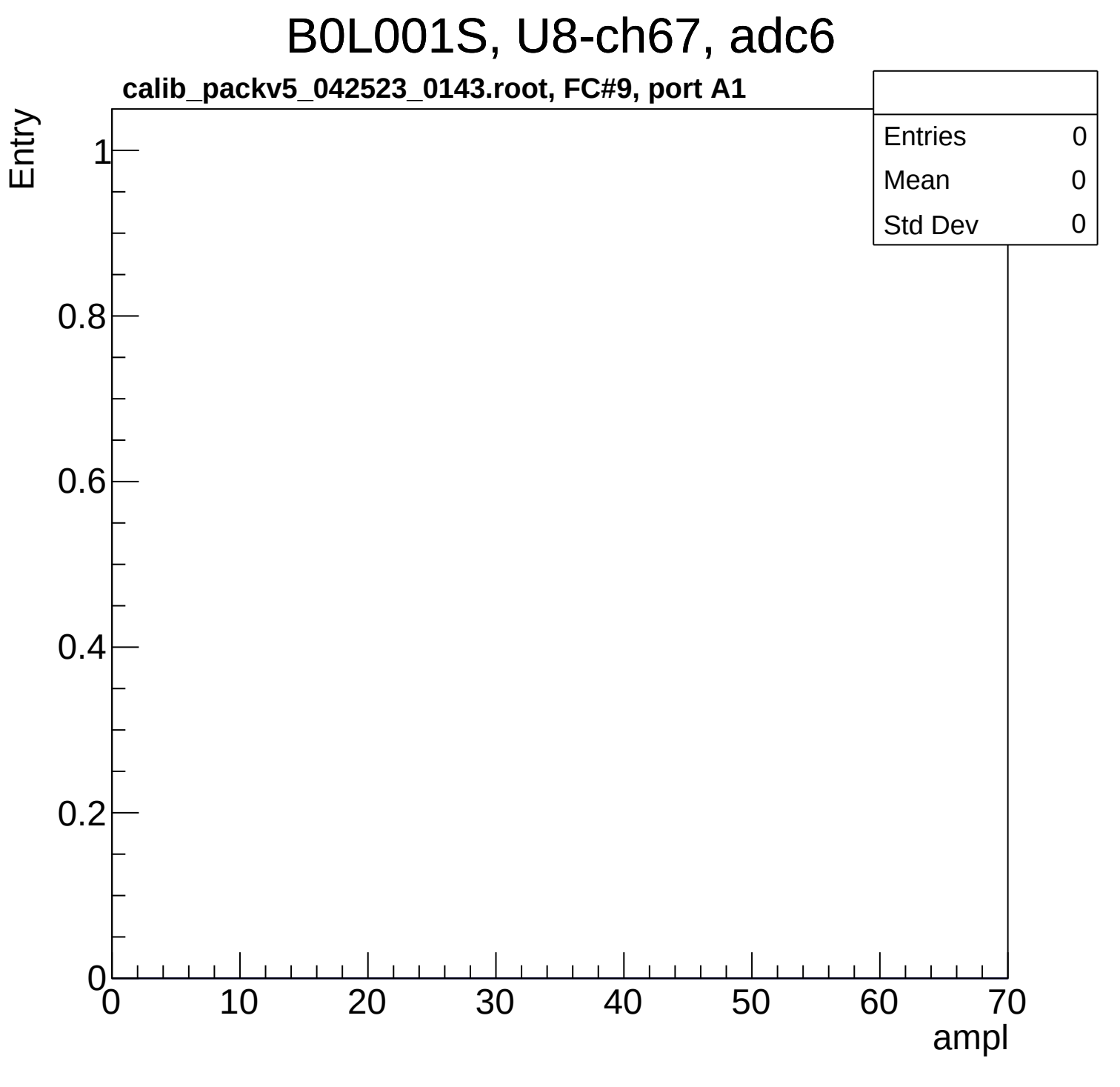
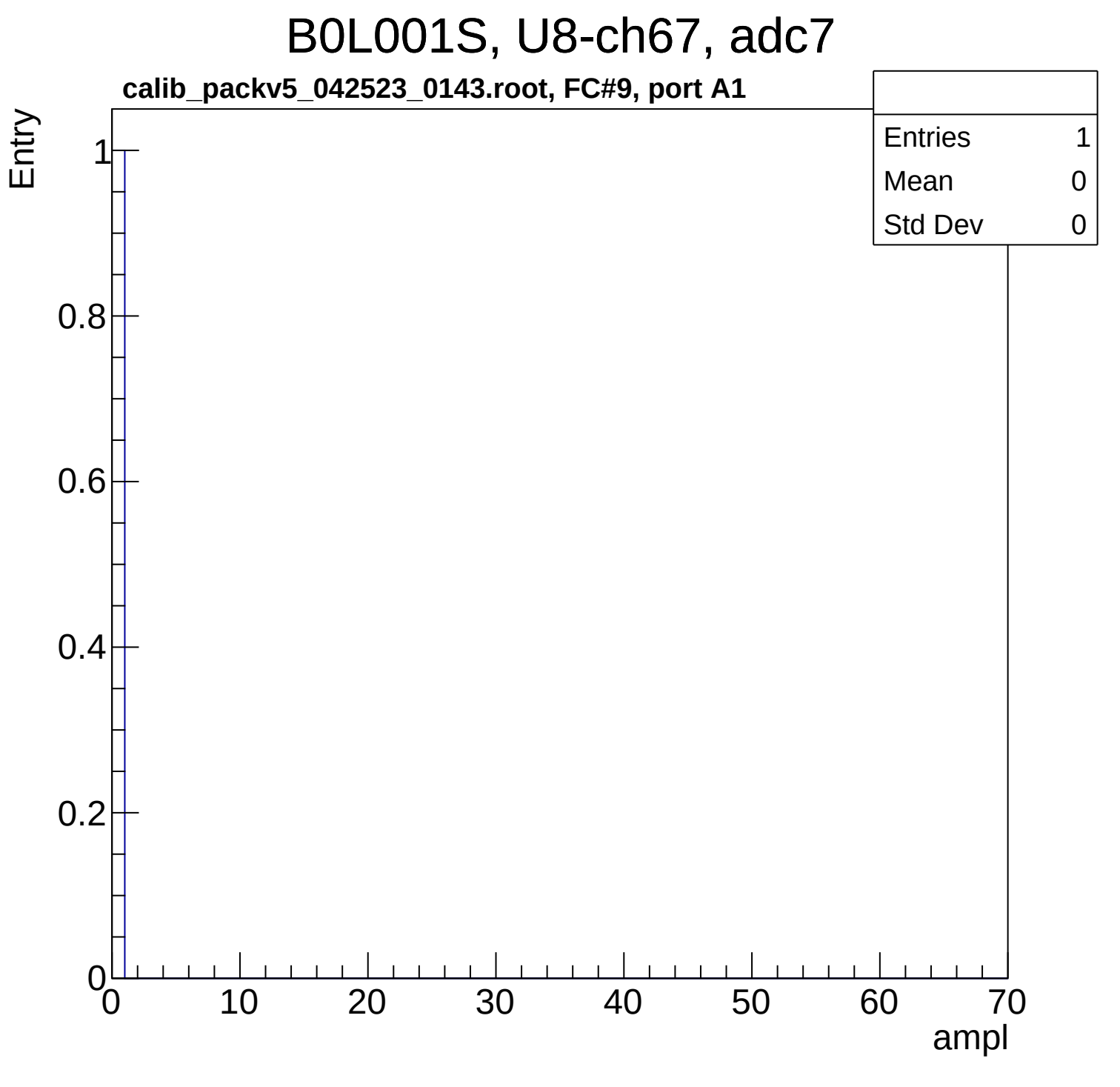
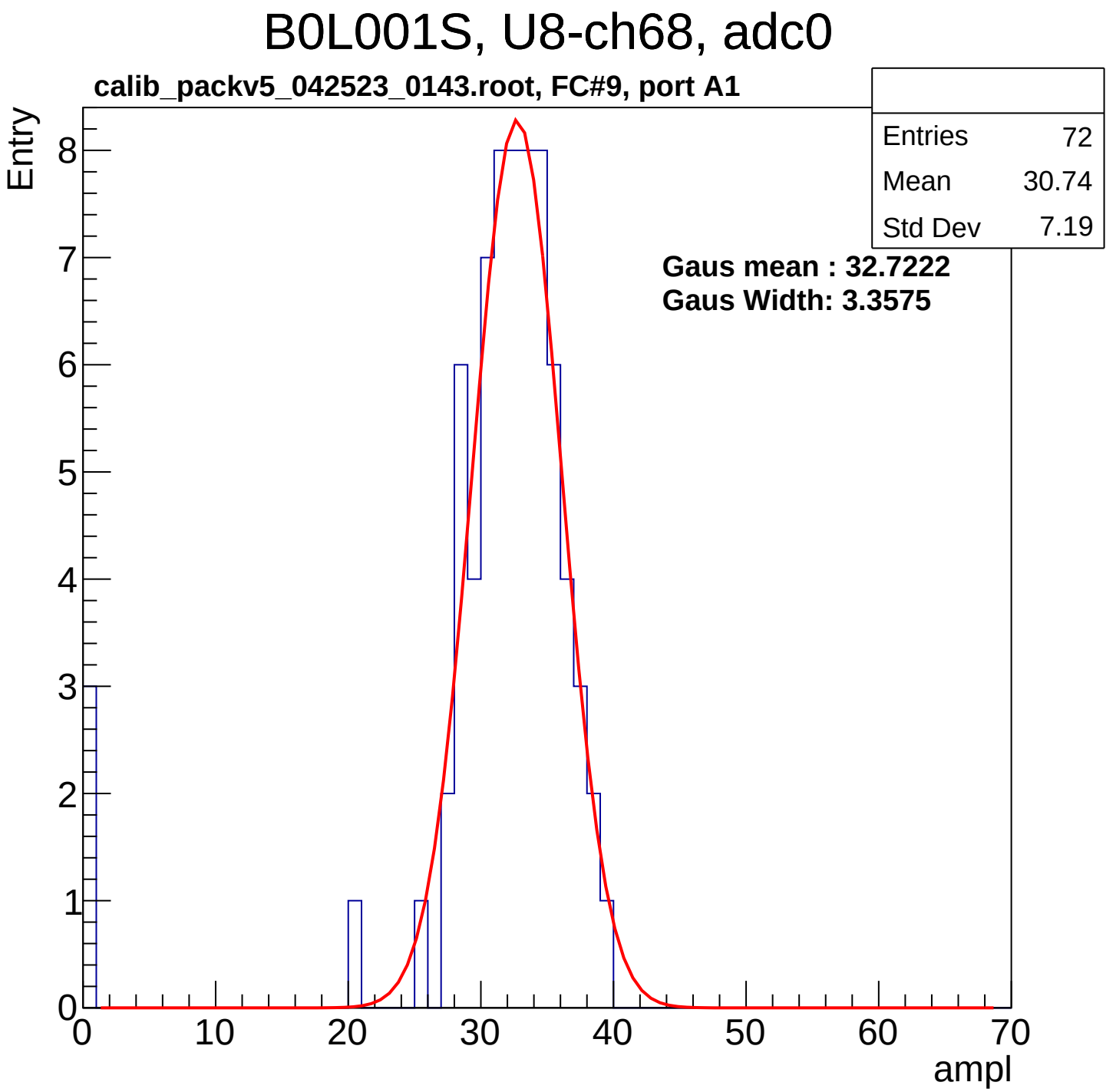

B0L001S, U8-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 59.3
Std Dev 10.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.74
Std Dev 7.19
Gaus mean : 32.7222
Gaus Width: 3.3575
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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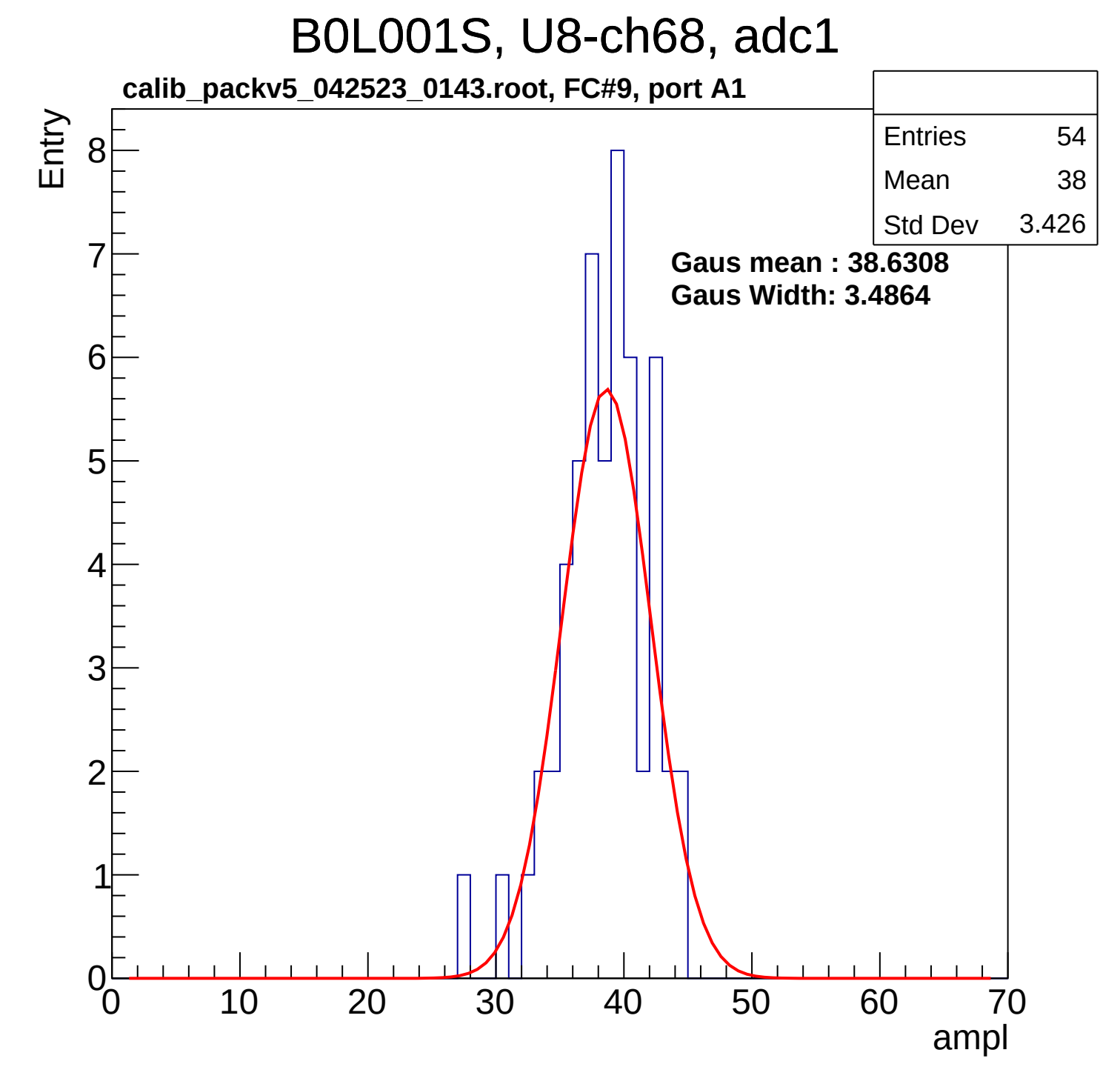
40
50
60
70


B0L001S, U8-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38
Std Dev 3.426
Gaus mean : 38.6308
Gaus Width: 3.4864
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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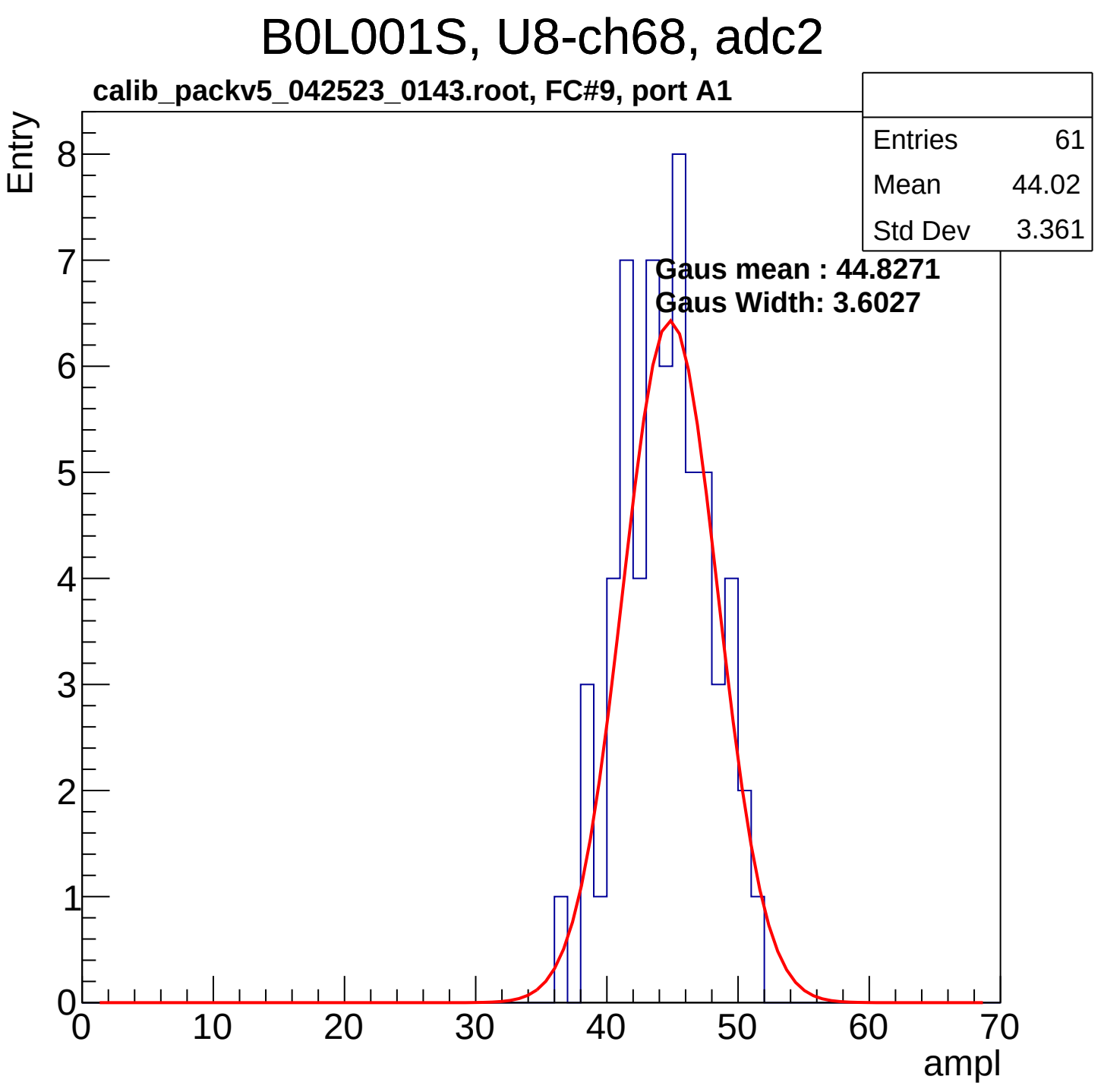
40
50
60
70


B0L001S, U8-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.02
Std Dev 3.361
Gaus mean : 44.8271
Gaus Width: 3.6027
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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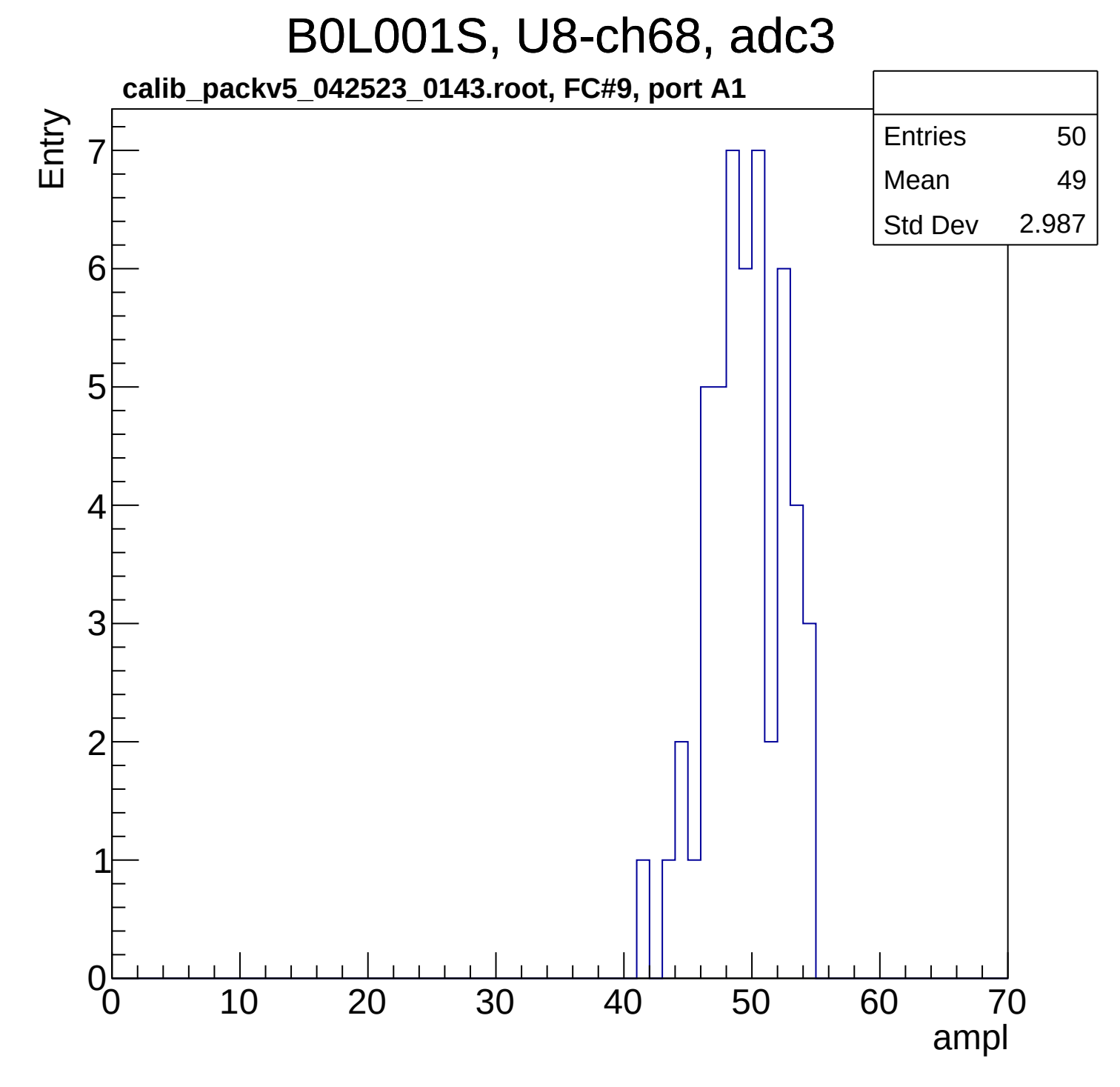
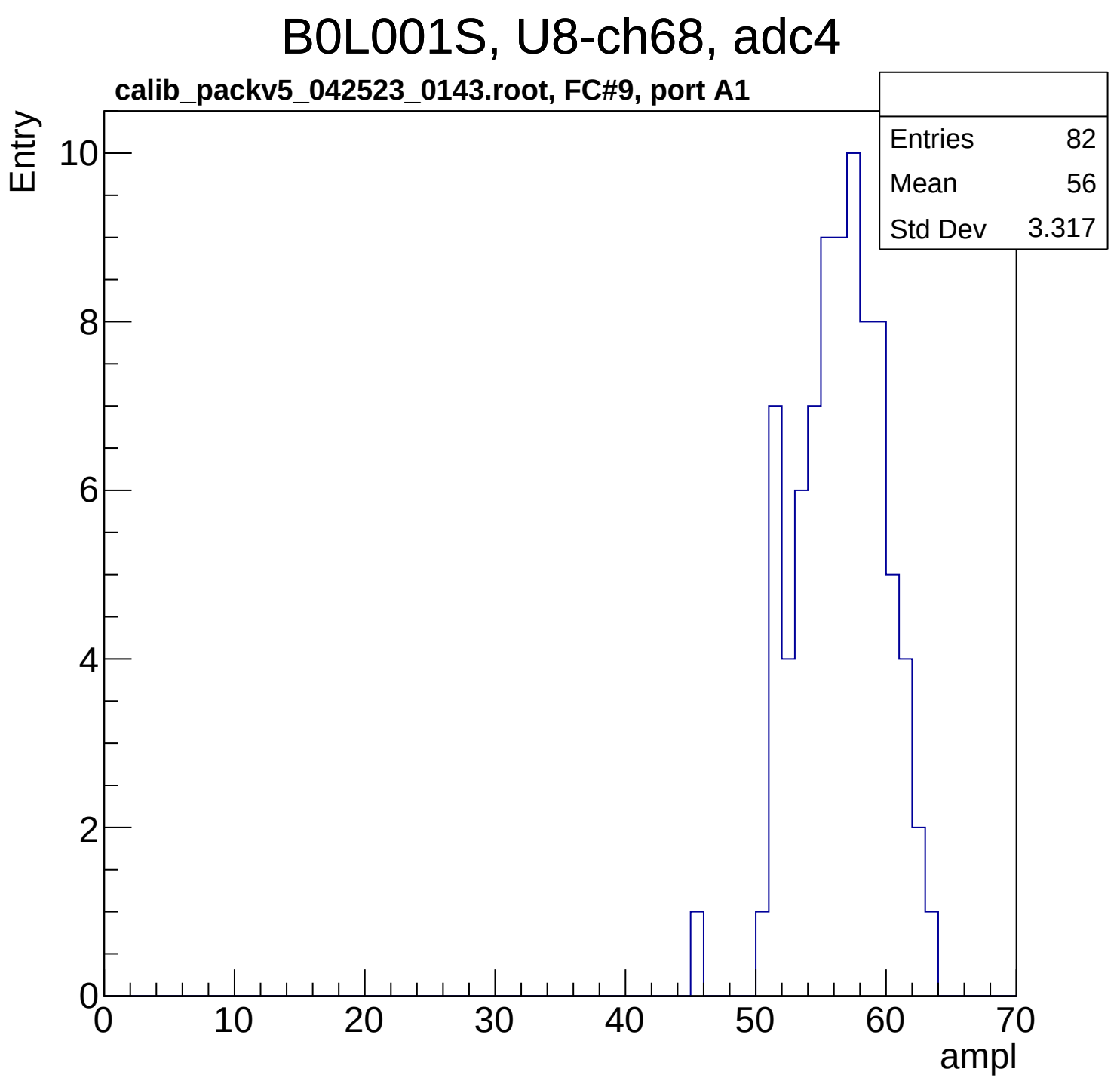
50
60
70


B0L001S, U8-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 49
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 56
Std Dev 3.317
Entry
ampl
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2
4
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10
0
10
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40
50
60
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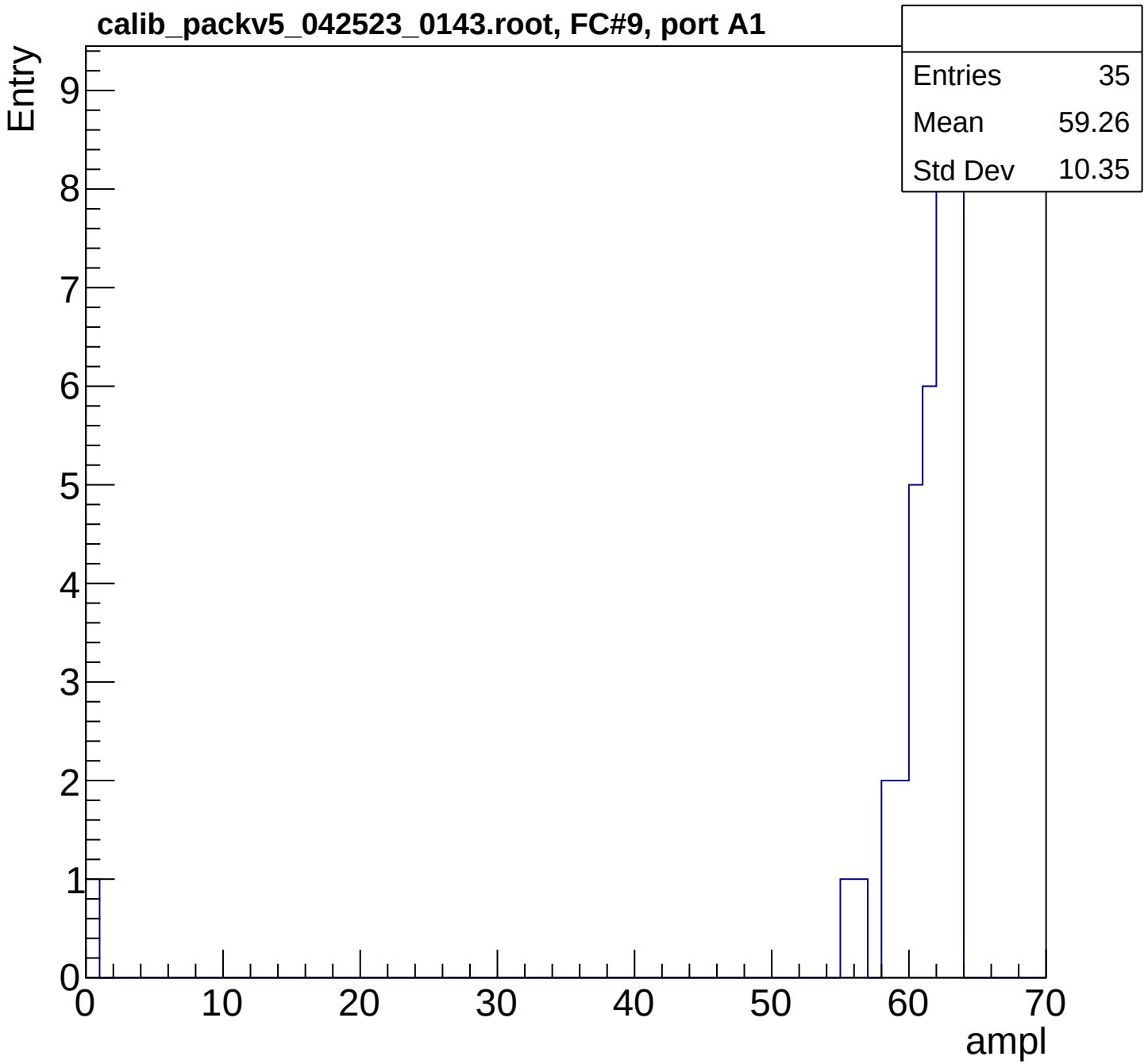
B0L001S, U8-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.26
Std Dev 10.35
Entry
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3
4
5
6
7
8
9
0
10
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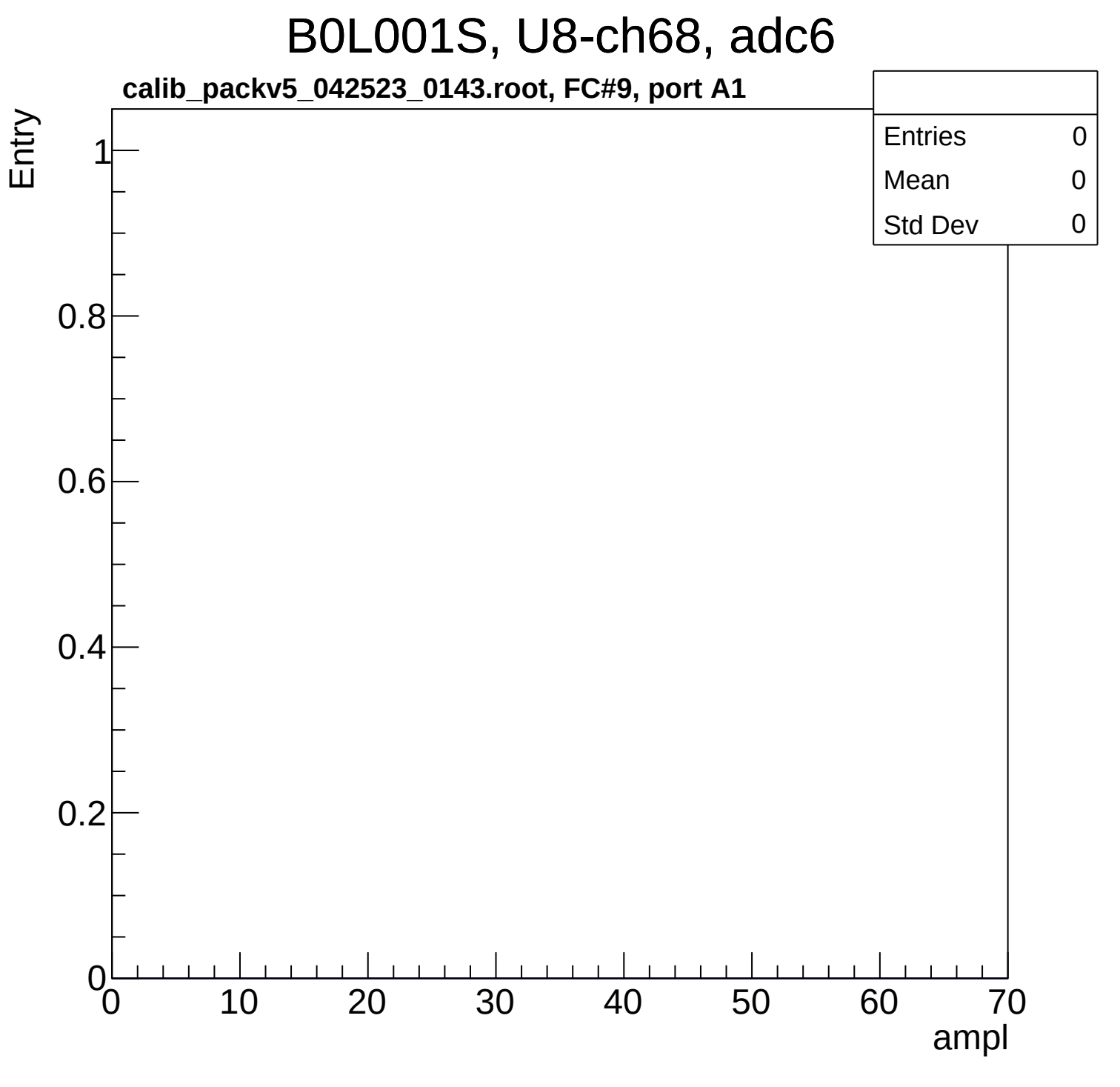
50
60
70
ampl


B0L001S, U8-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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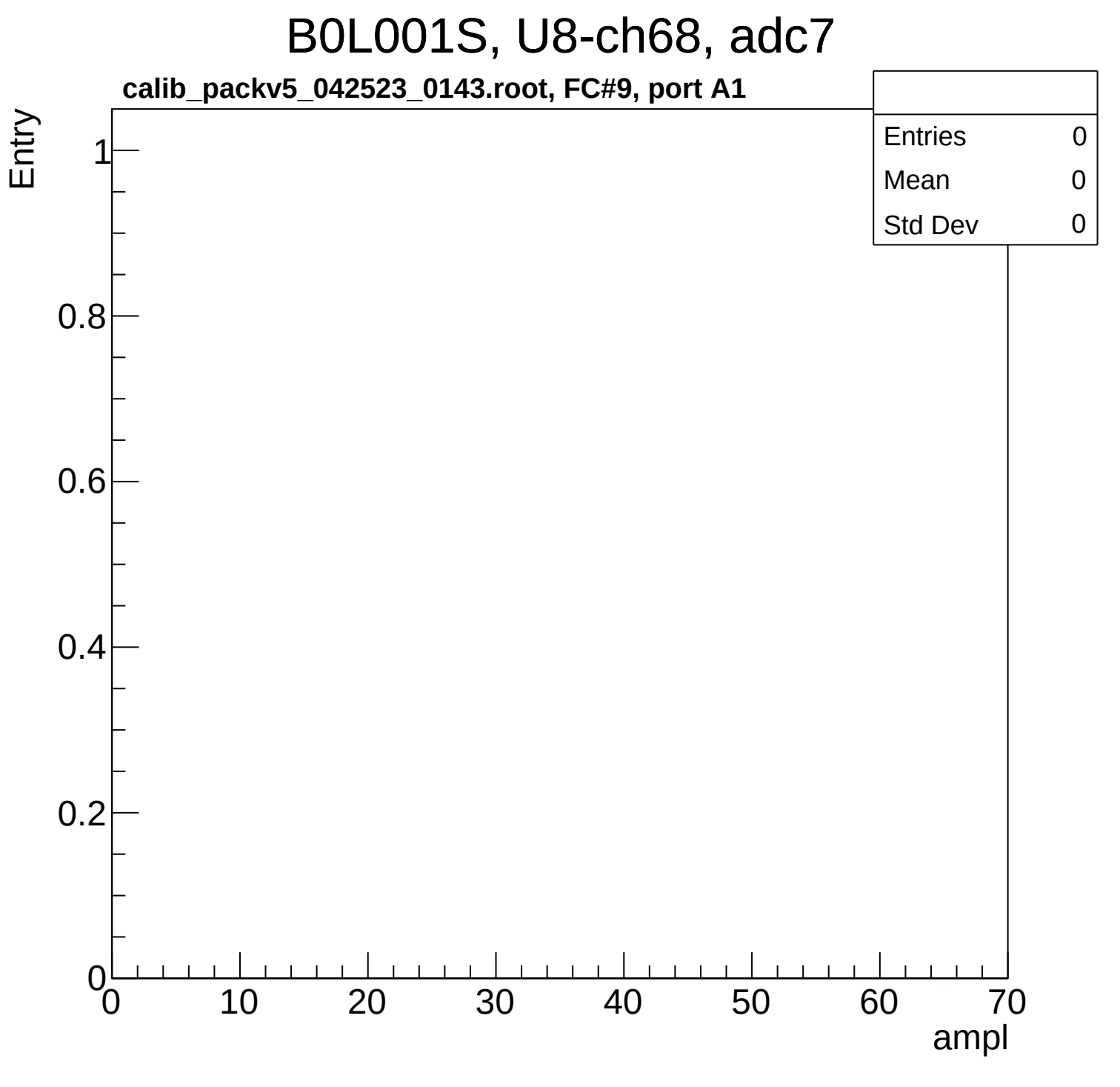
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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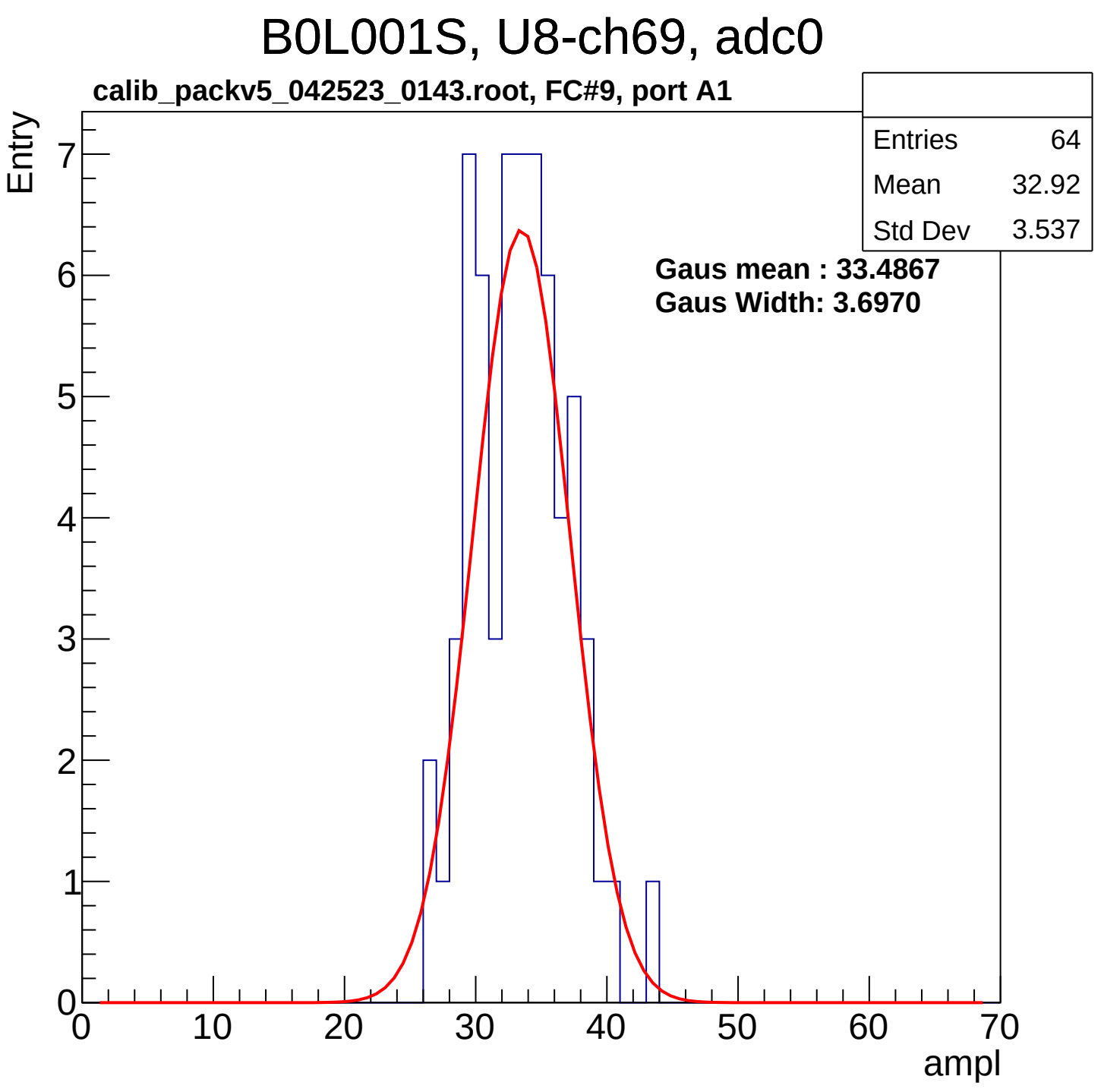
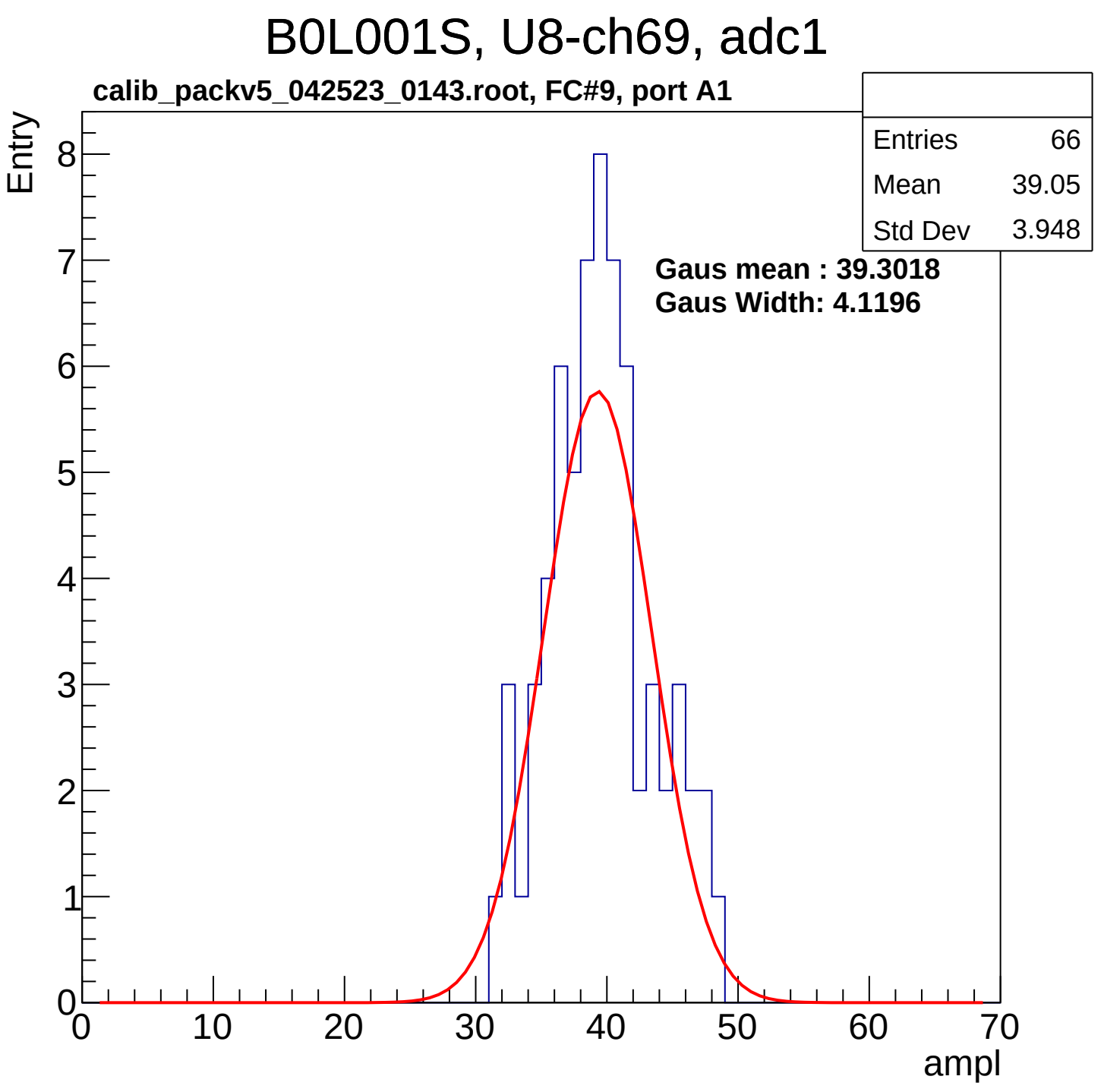
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 32.92
Std Dev 3.537
Gaus mean : 33.4867
Gaus Width: 3.6970
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 39.05
Std Dev 3.948
Gaus mean : 39.3018
Gaus Width: 4.1196
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
50
60
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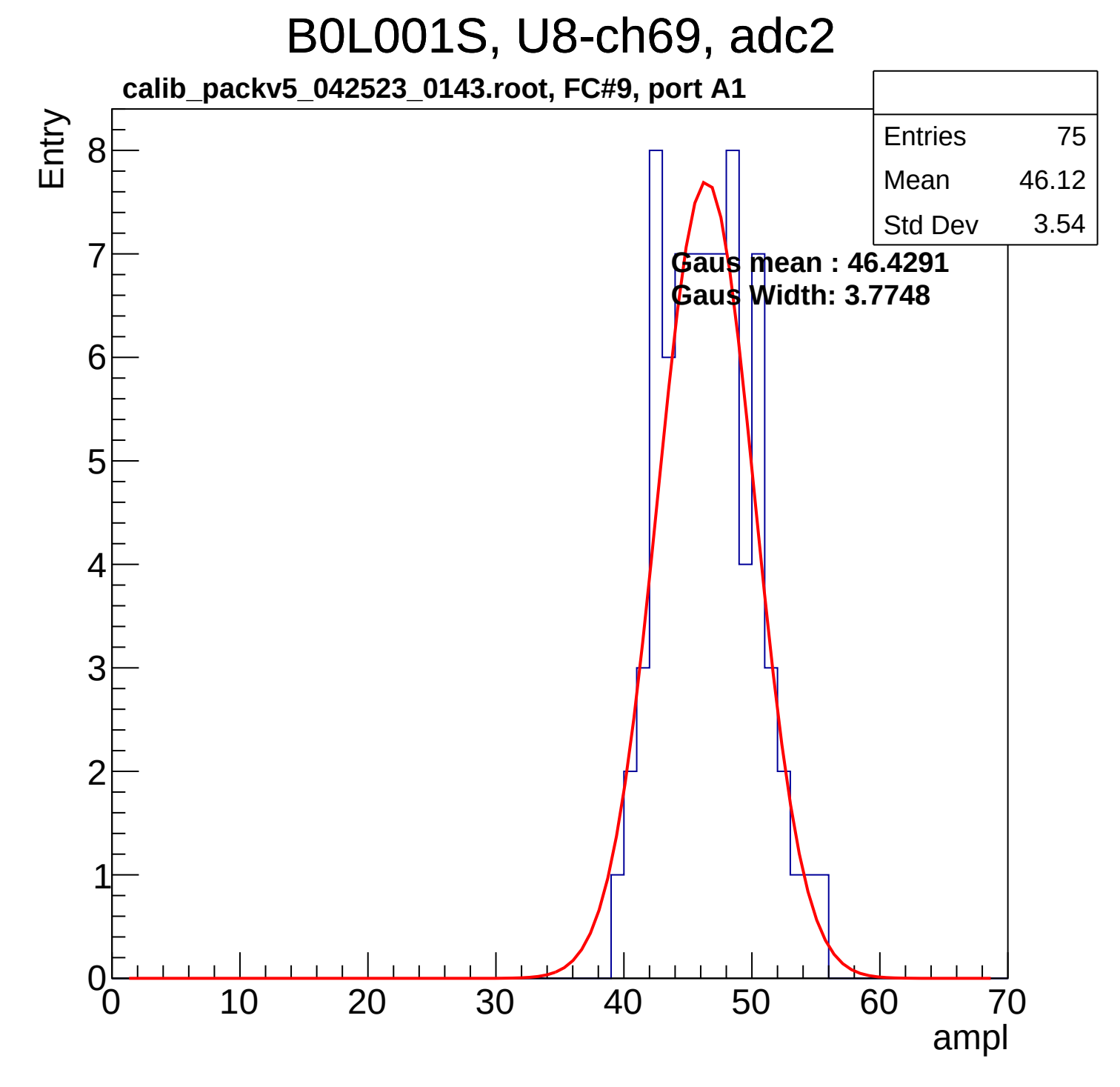
B0L001S, U8-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 46.12
Std Dev 3.54
Gaus mean : 46.4291
Gaus Width: 3.7748
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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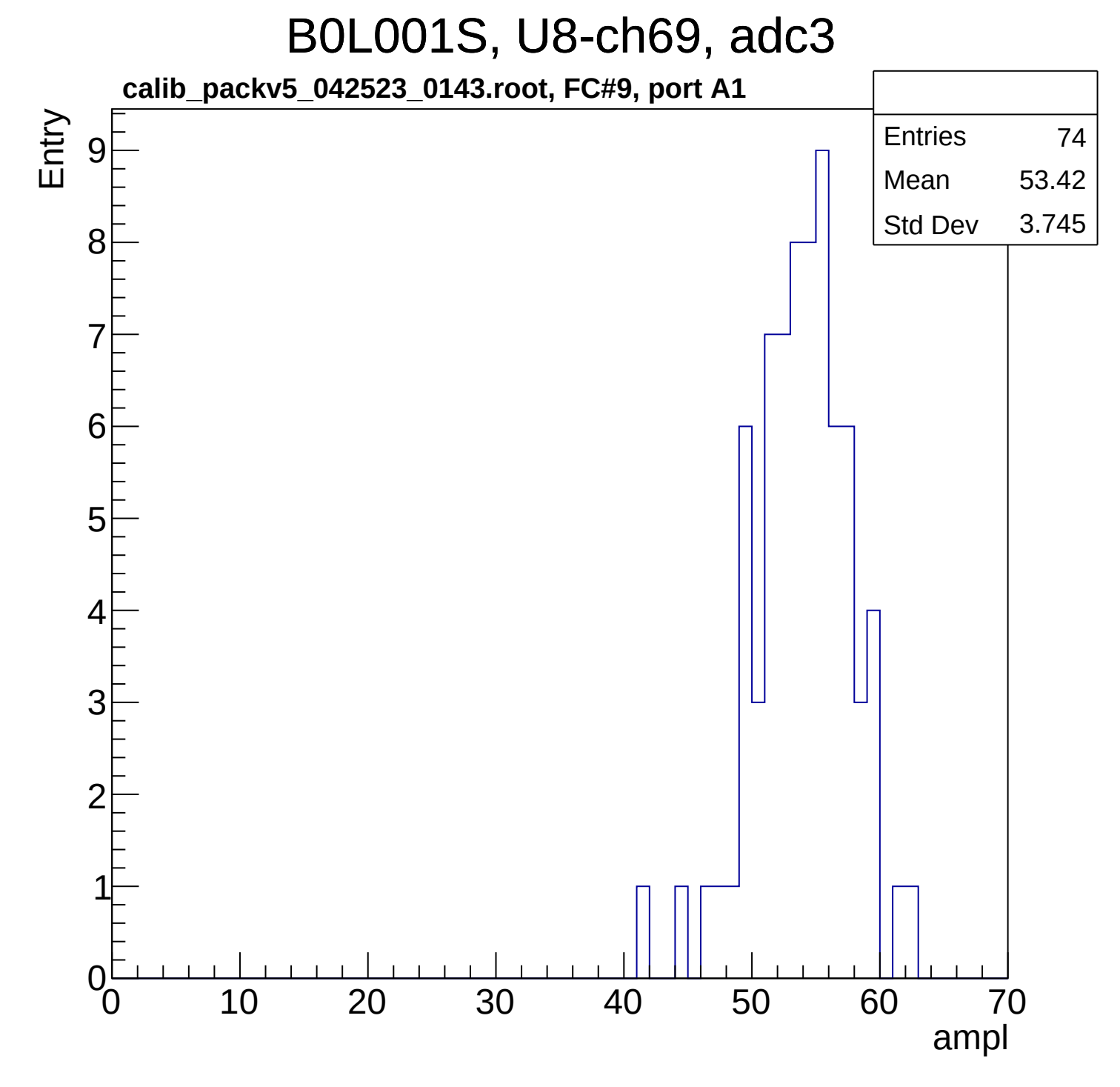
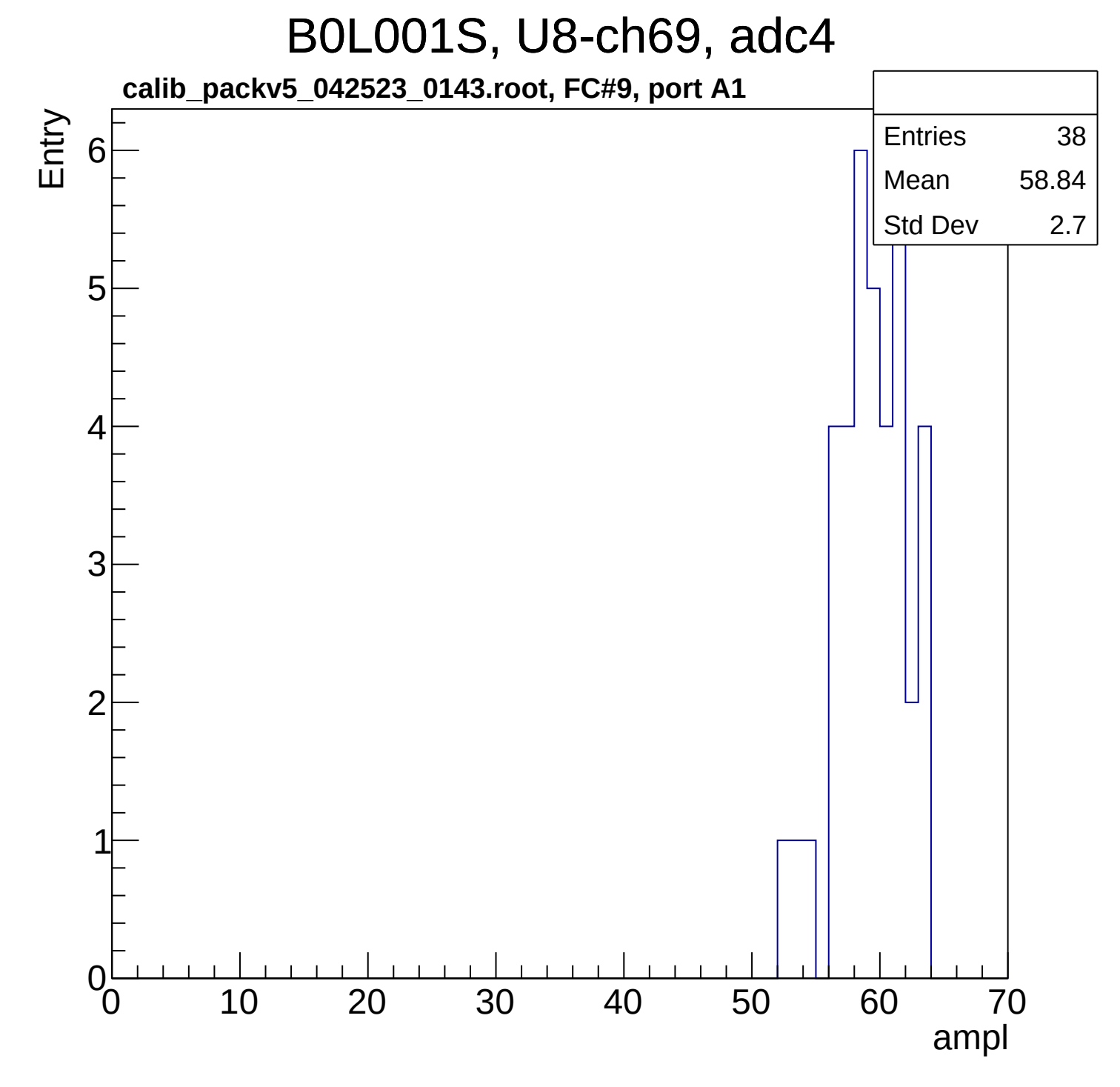
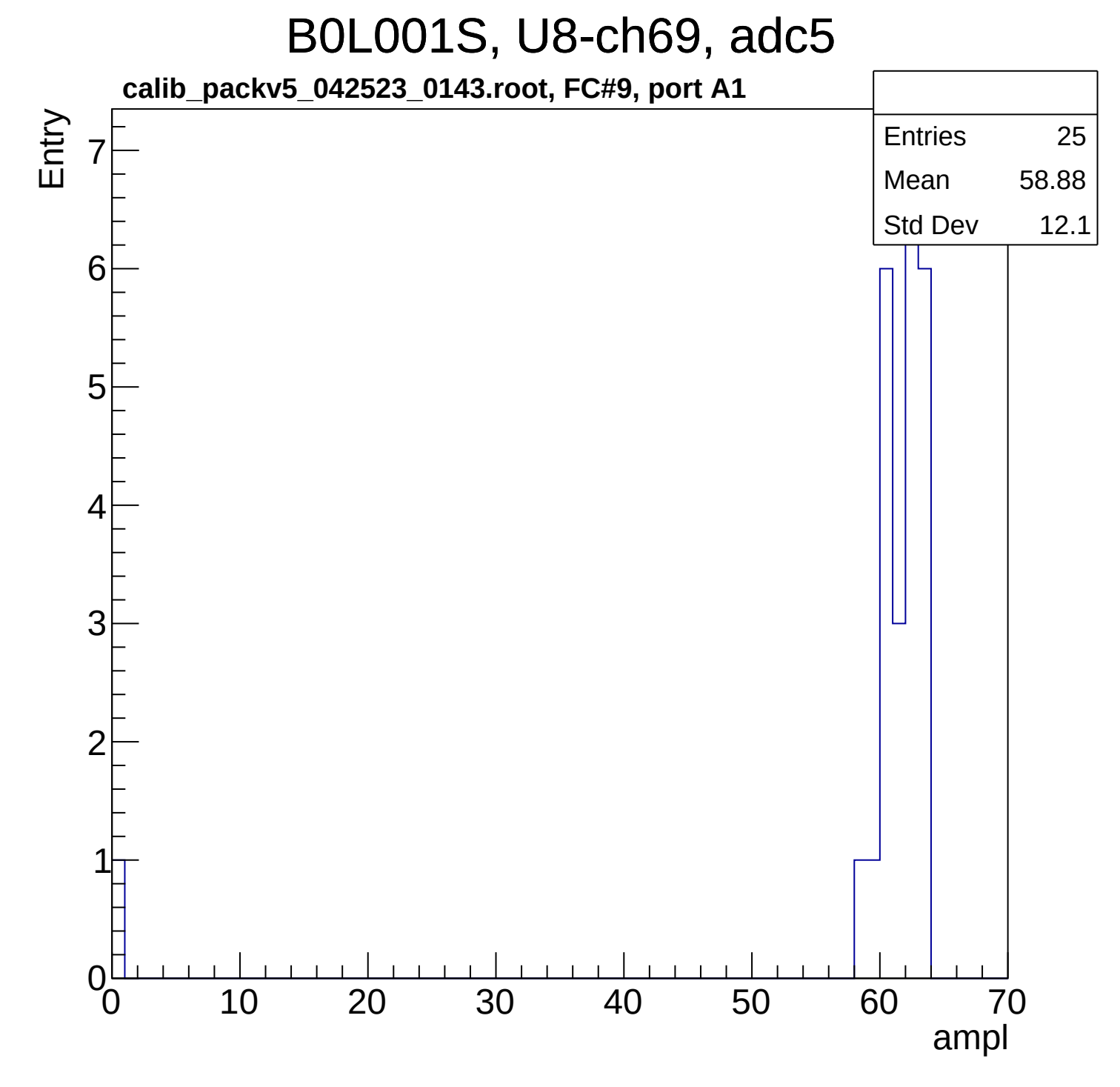
60
70


B0L001S, U8-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 53.42
Std Dev 3.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.84
Std Dev 2.7
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.88
Std Dev 12.1
Entry
ampl
0
1
2
3
4
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7
0
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20
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40
50
60
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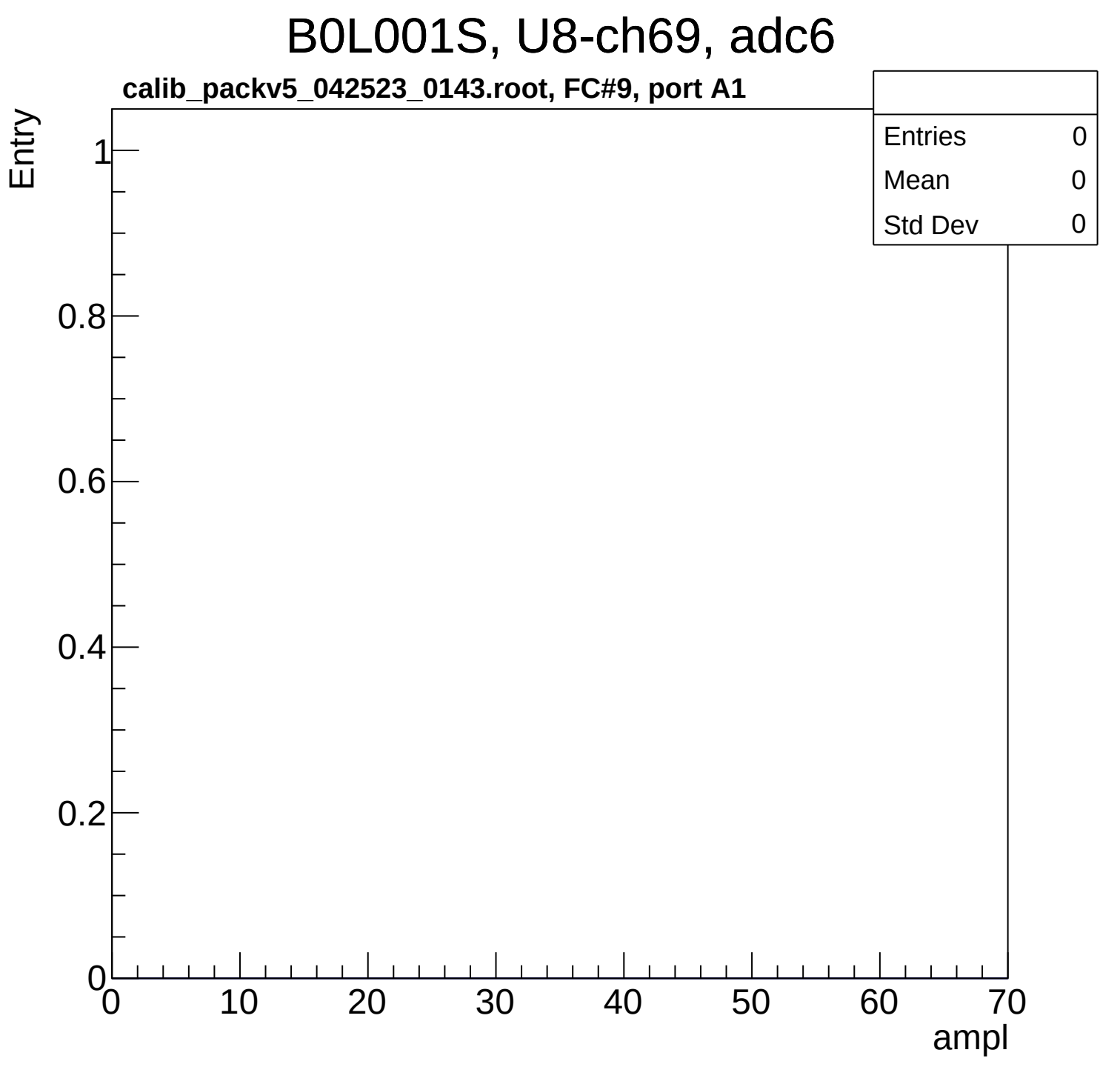
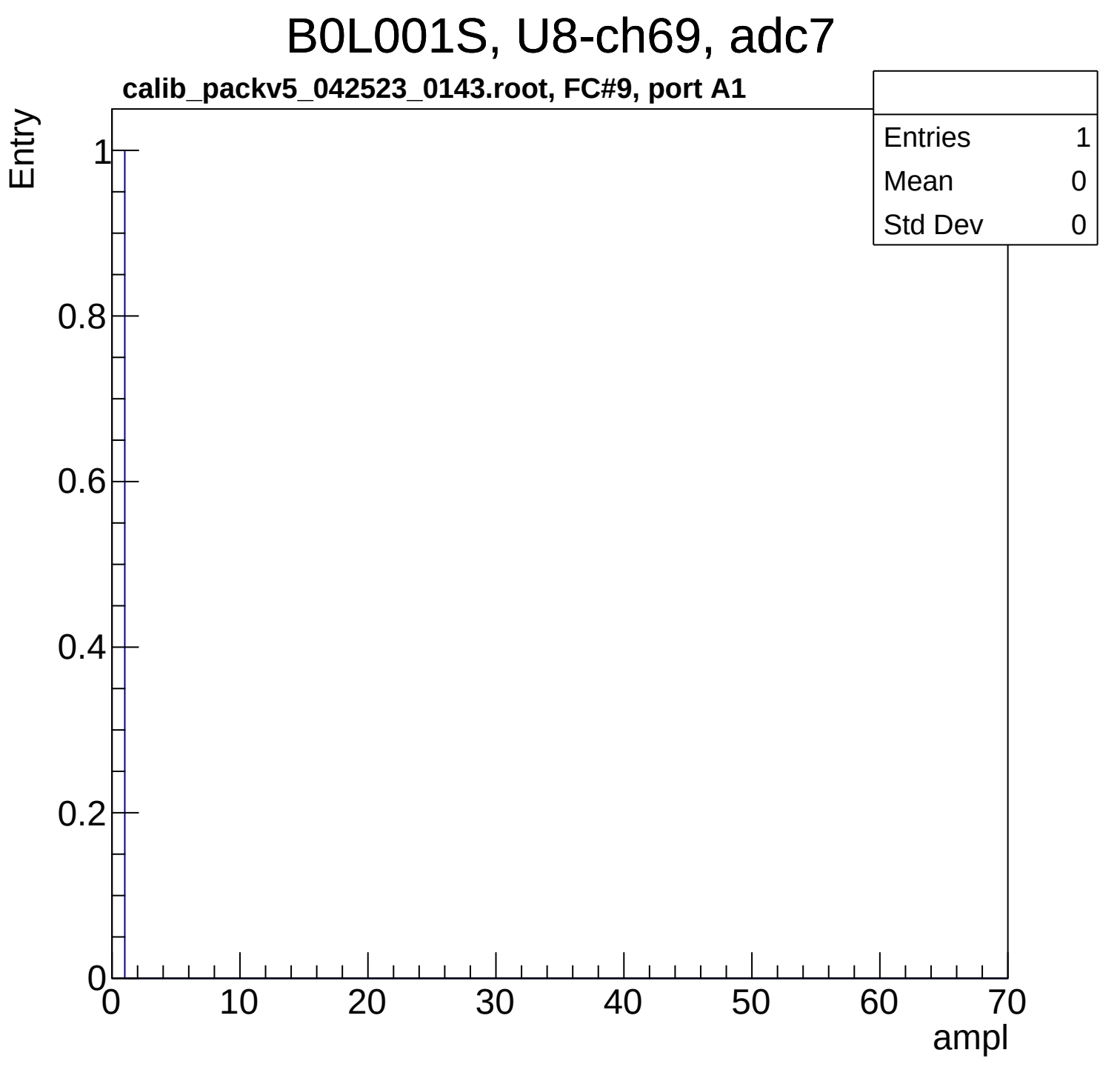
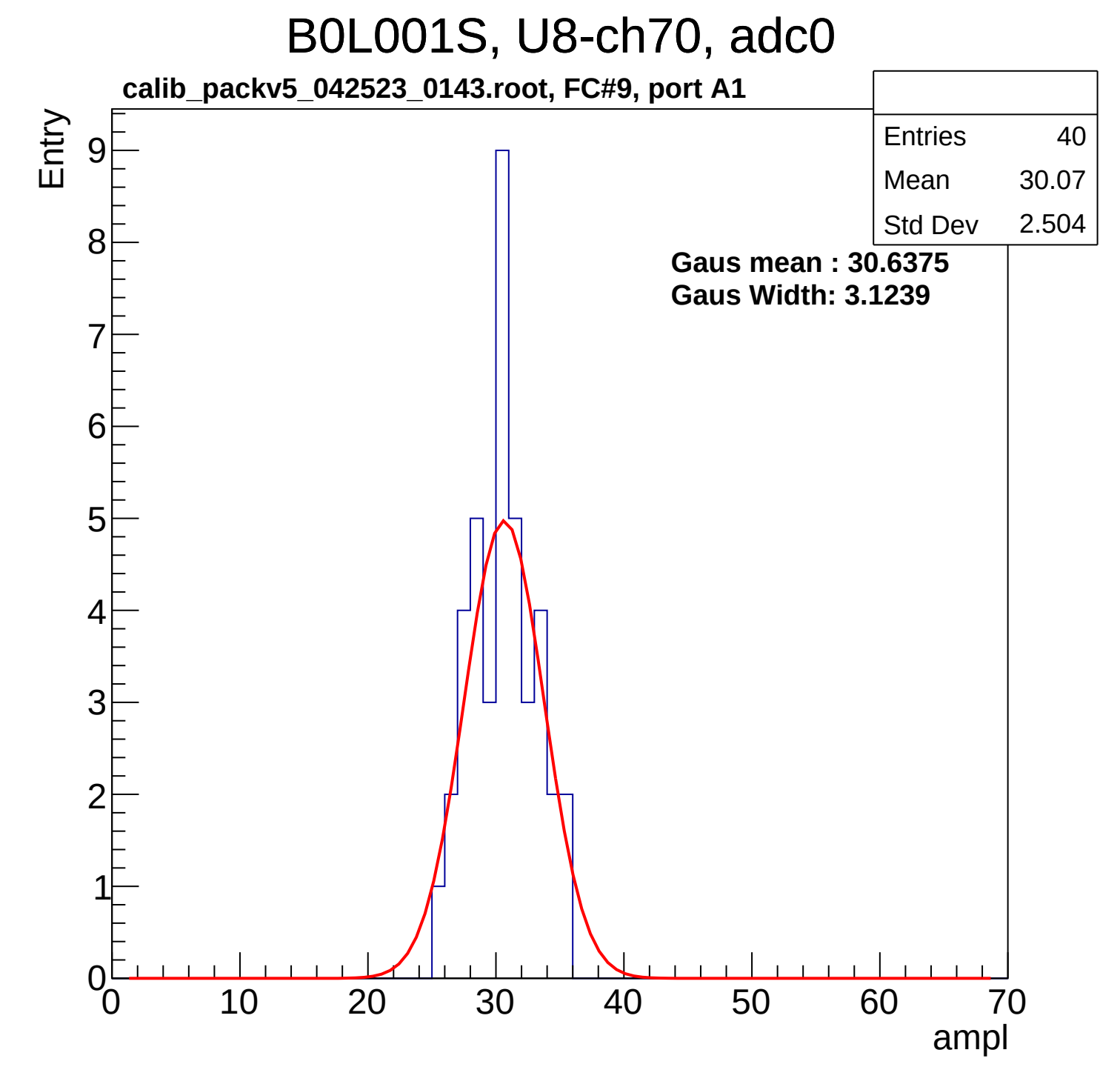
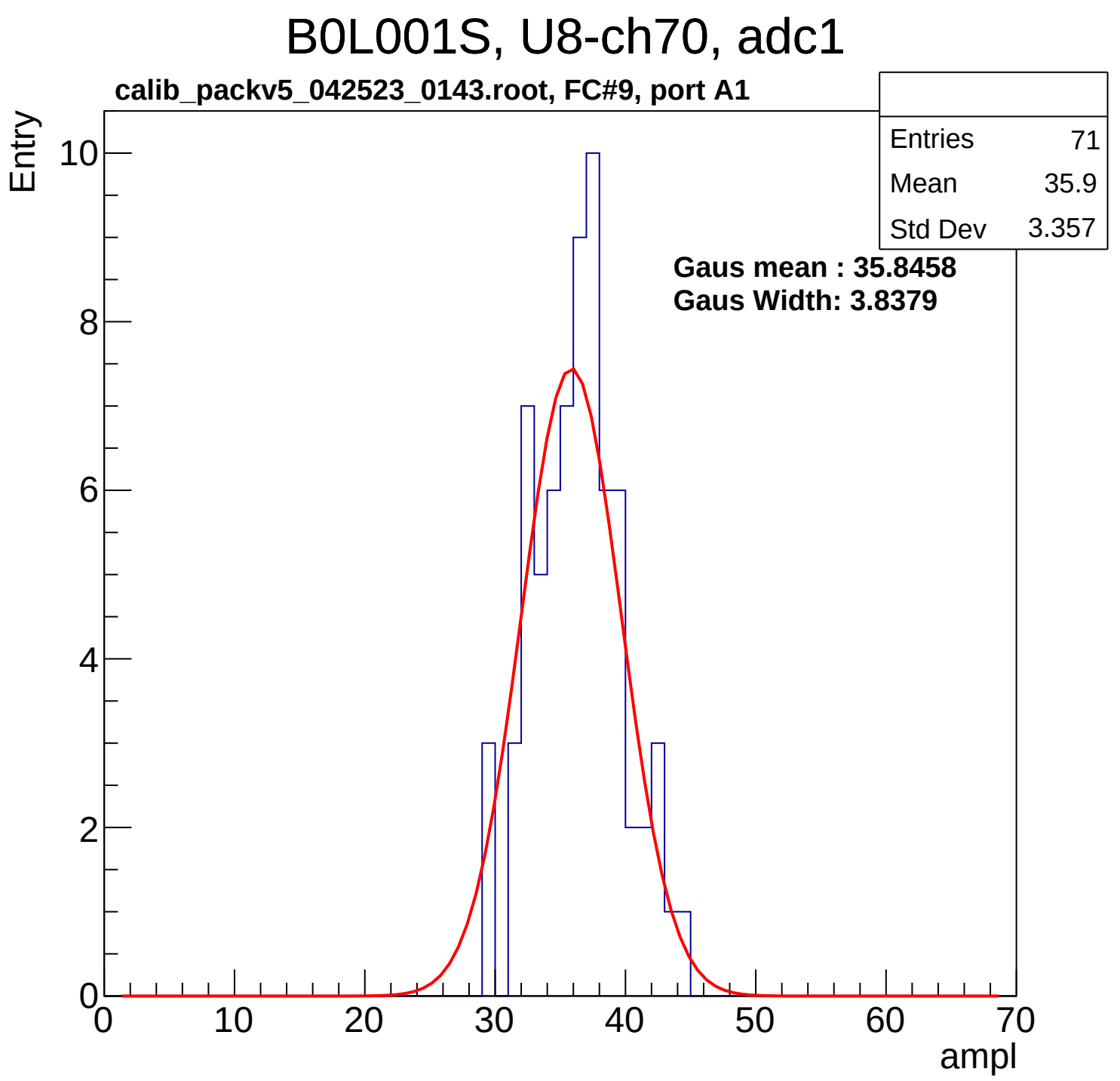
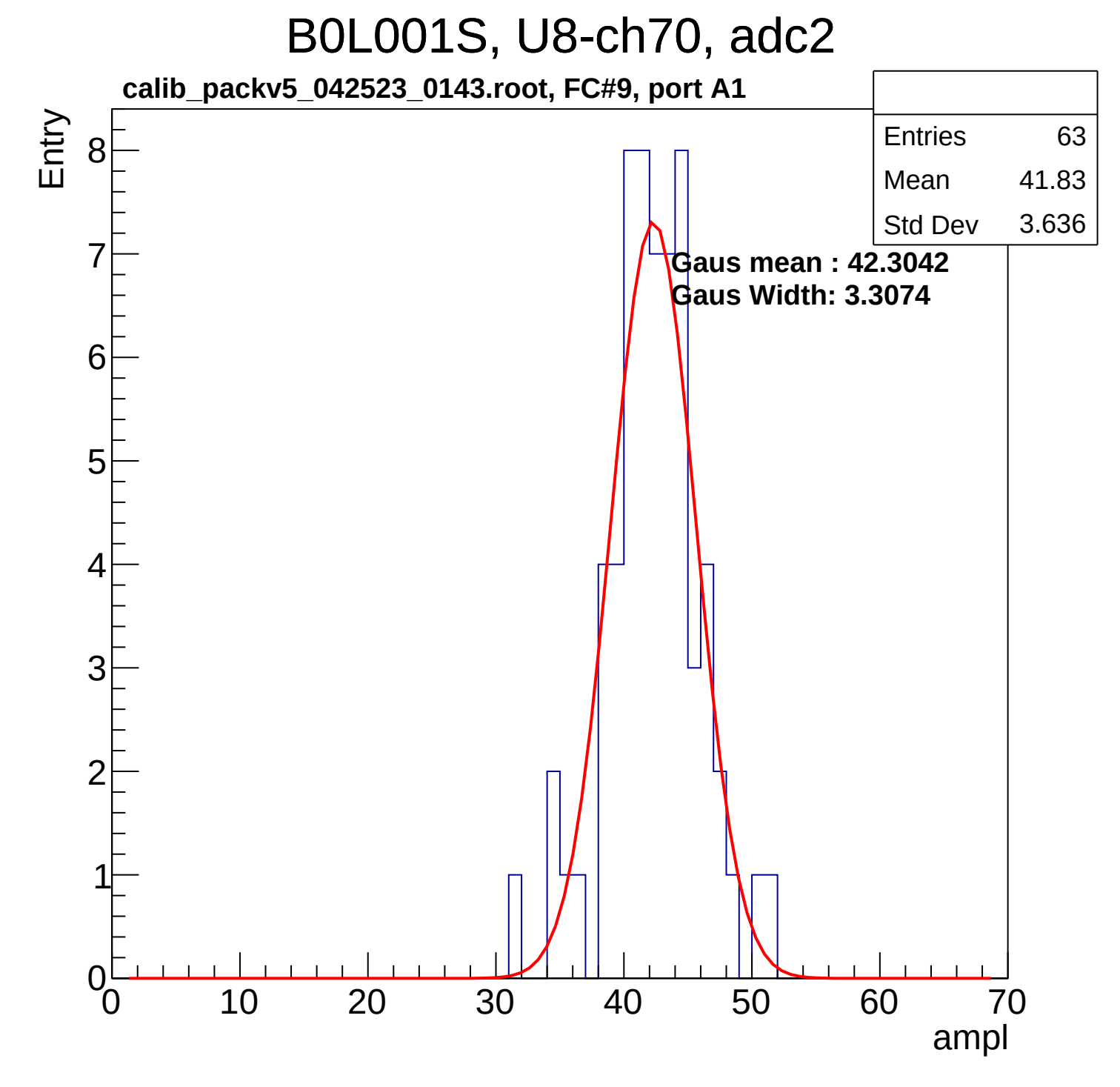
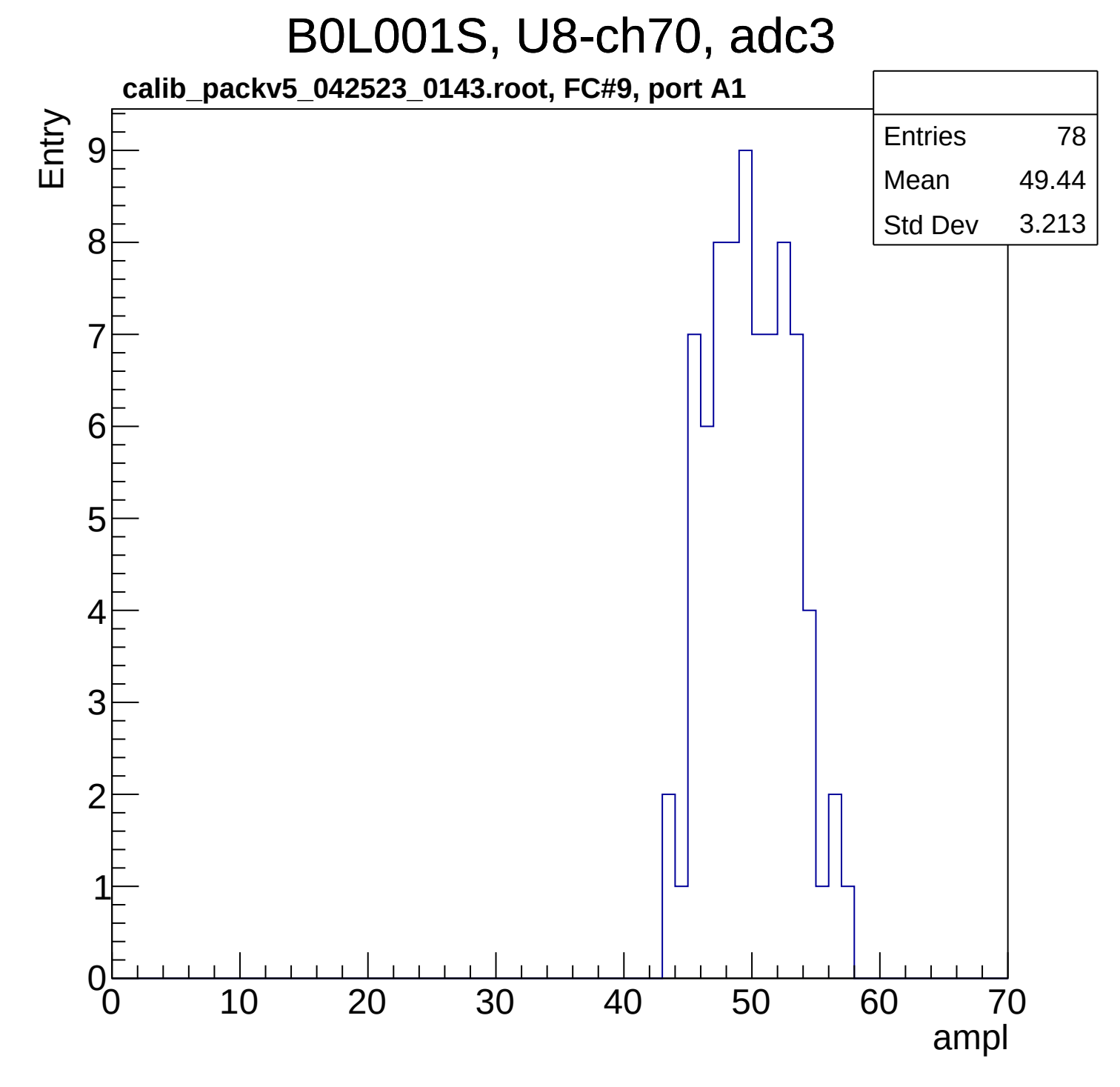

B0L001S, U8-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

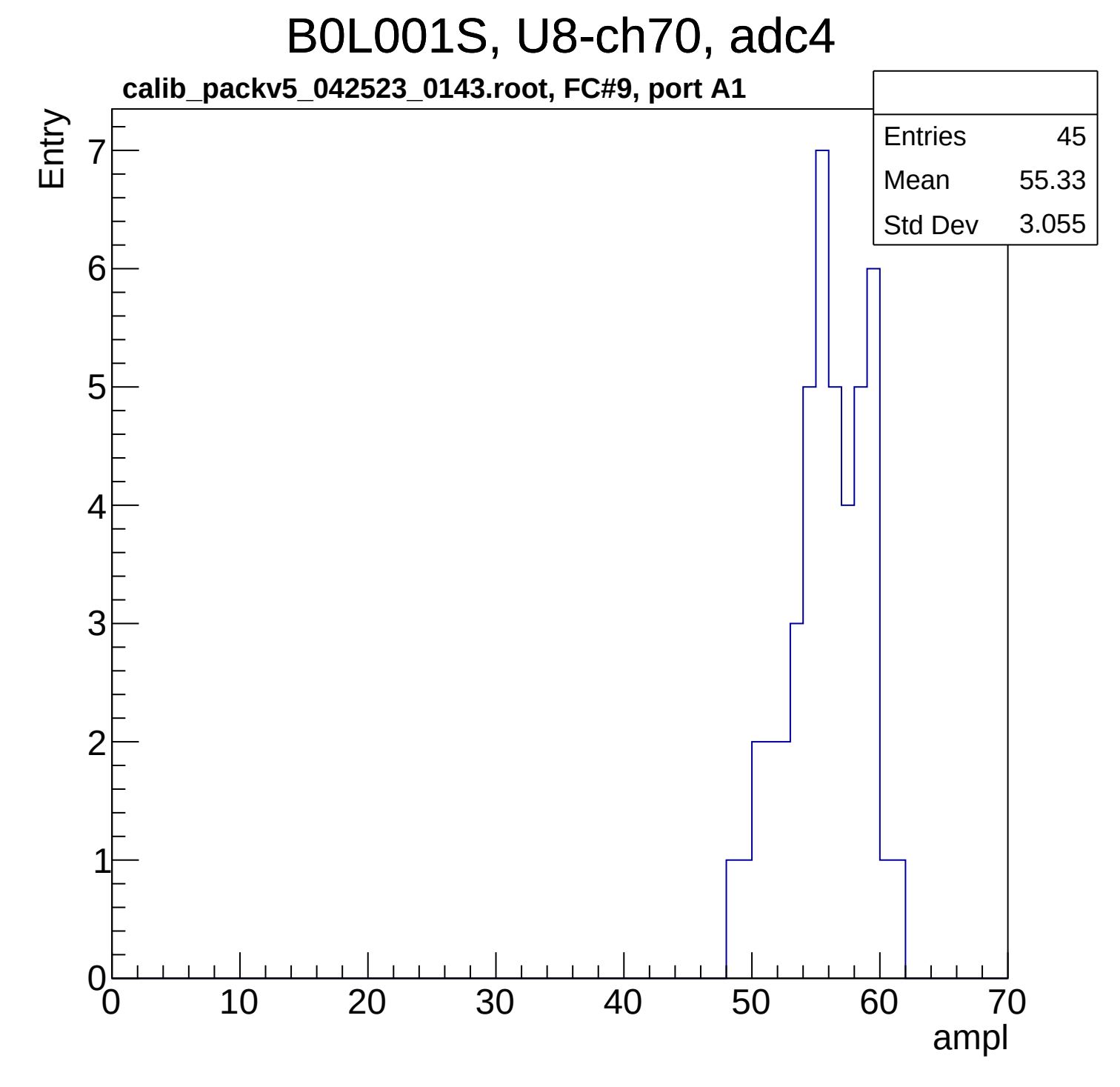

B0L001S, U8-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

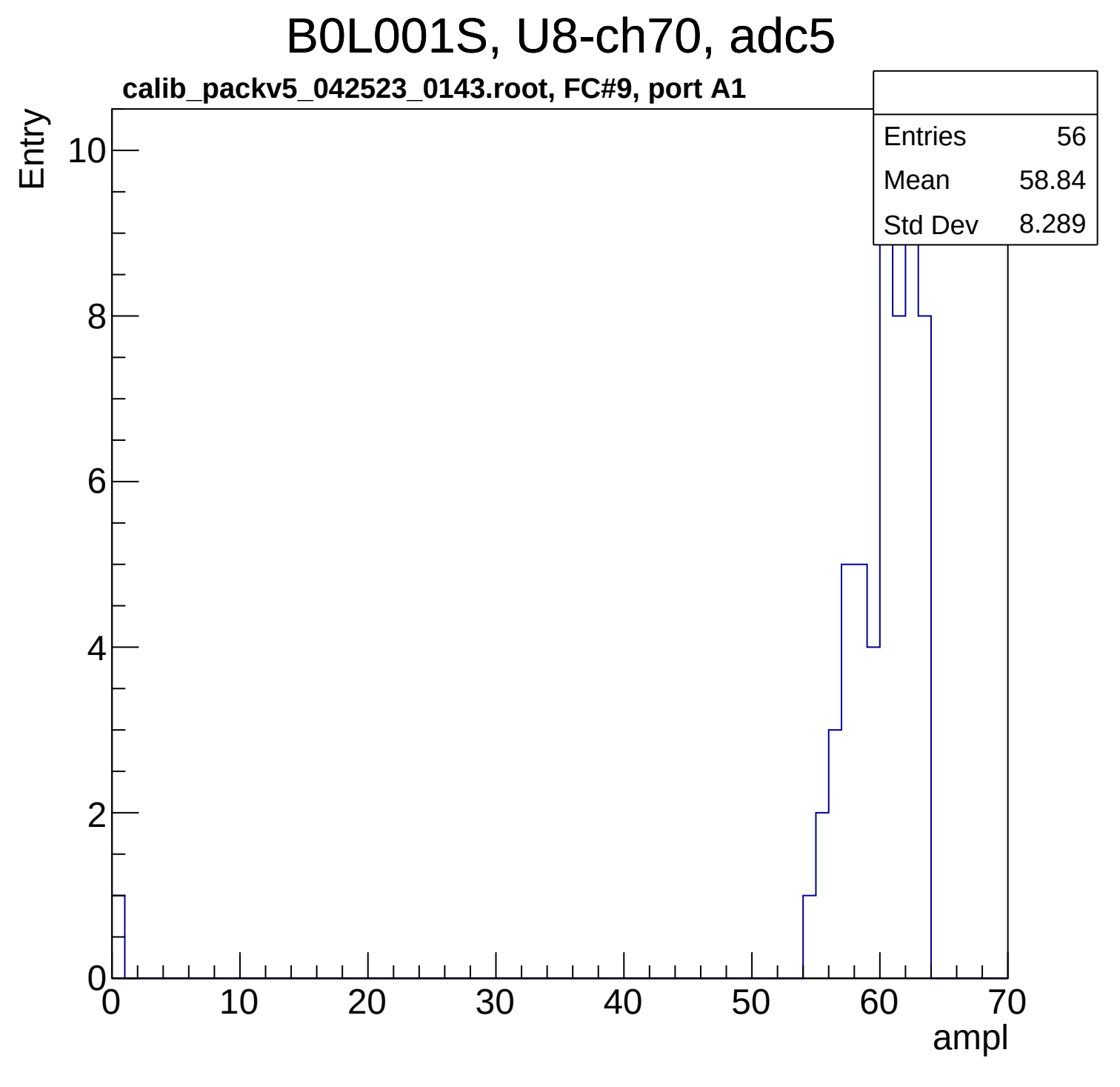

B0L001S, U8-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 30.07
Std Dev 2.504
Gaus mean : 30.6375
Gaus Width: 3.1239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

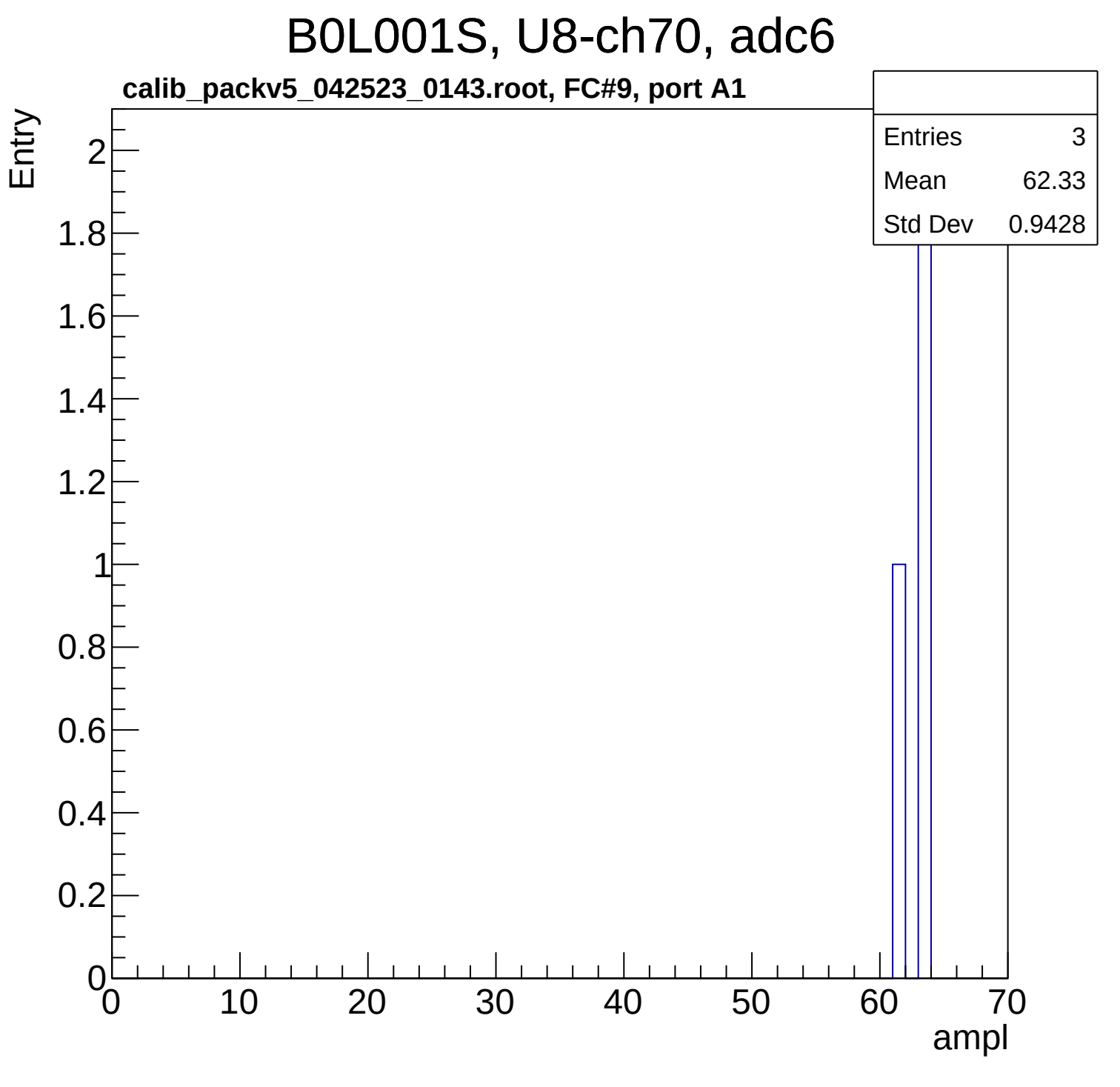

B0L001S, U8-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 35.9
Std Dev 3.357
Gaus mean : 35.8458
Gaus Width: 3.8379
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

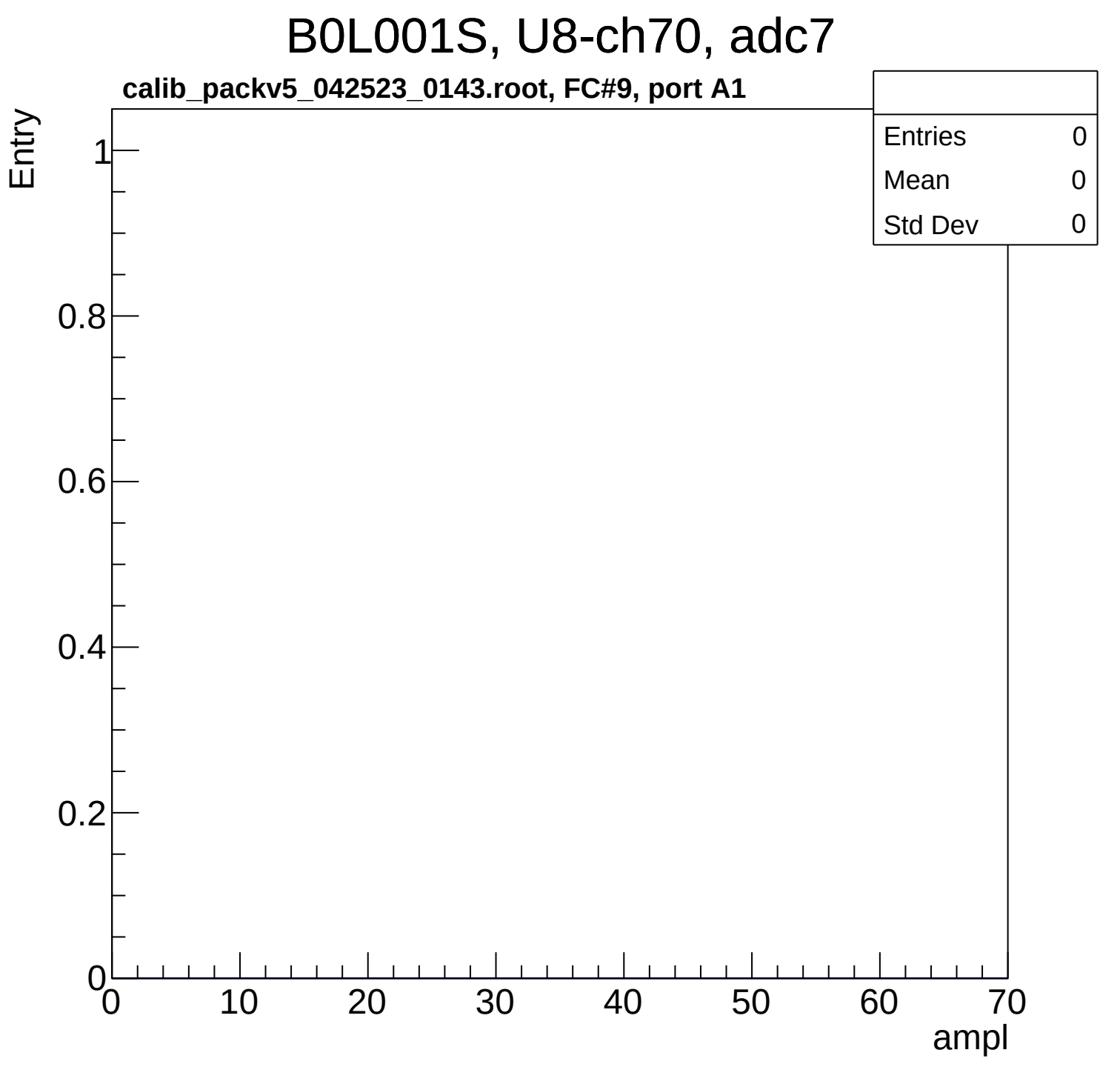

B0L001S, U8-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 41.83
Std Dev 3.636
Gaus mean : 42.3042
Gaus Width: 3.3074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

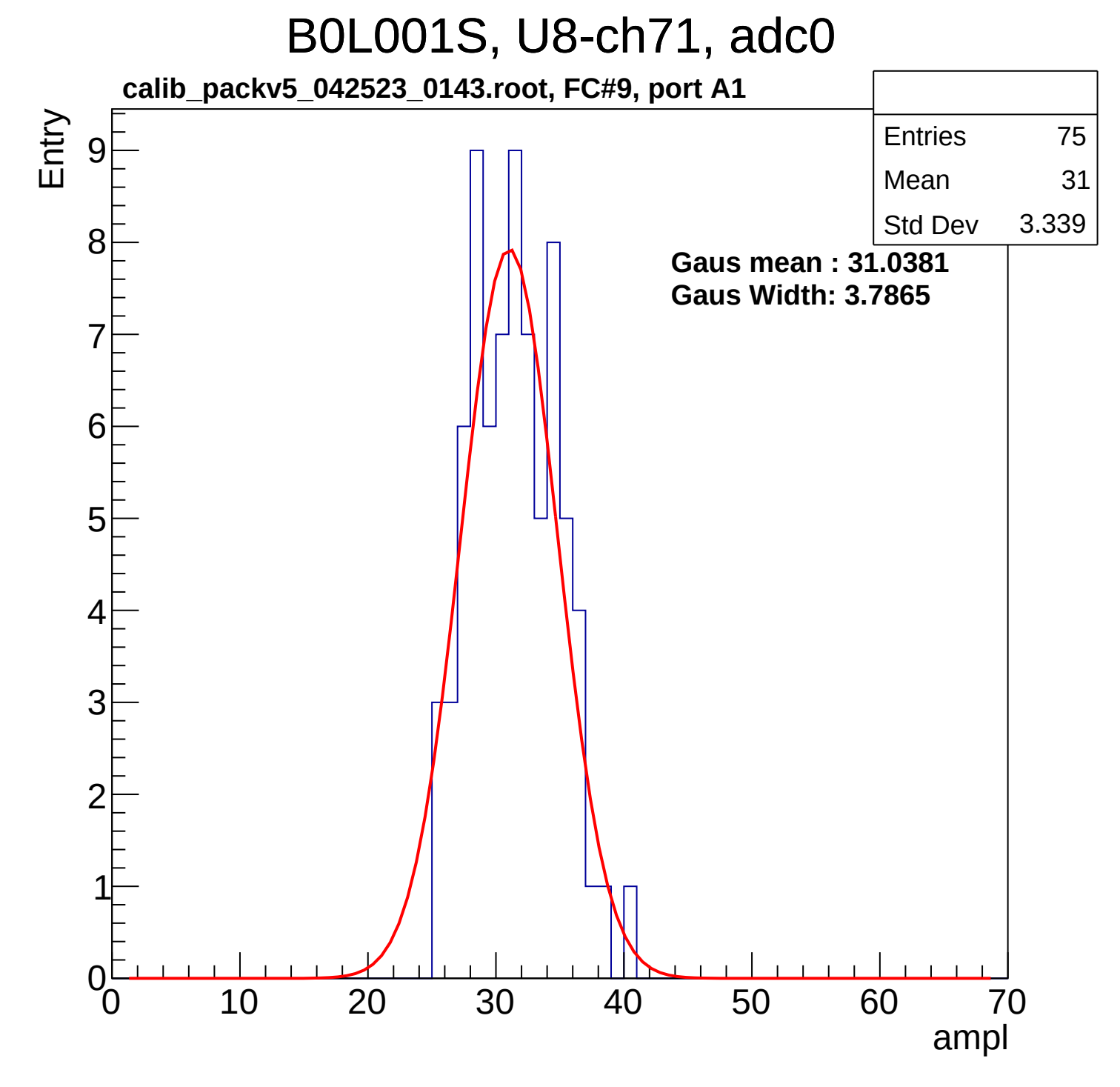

B0L001S, U8-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 49.44
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

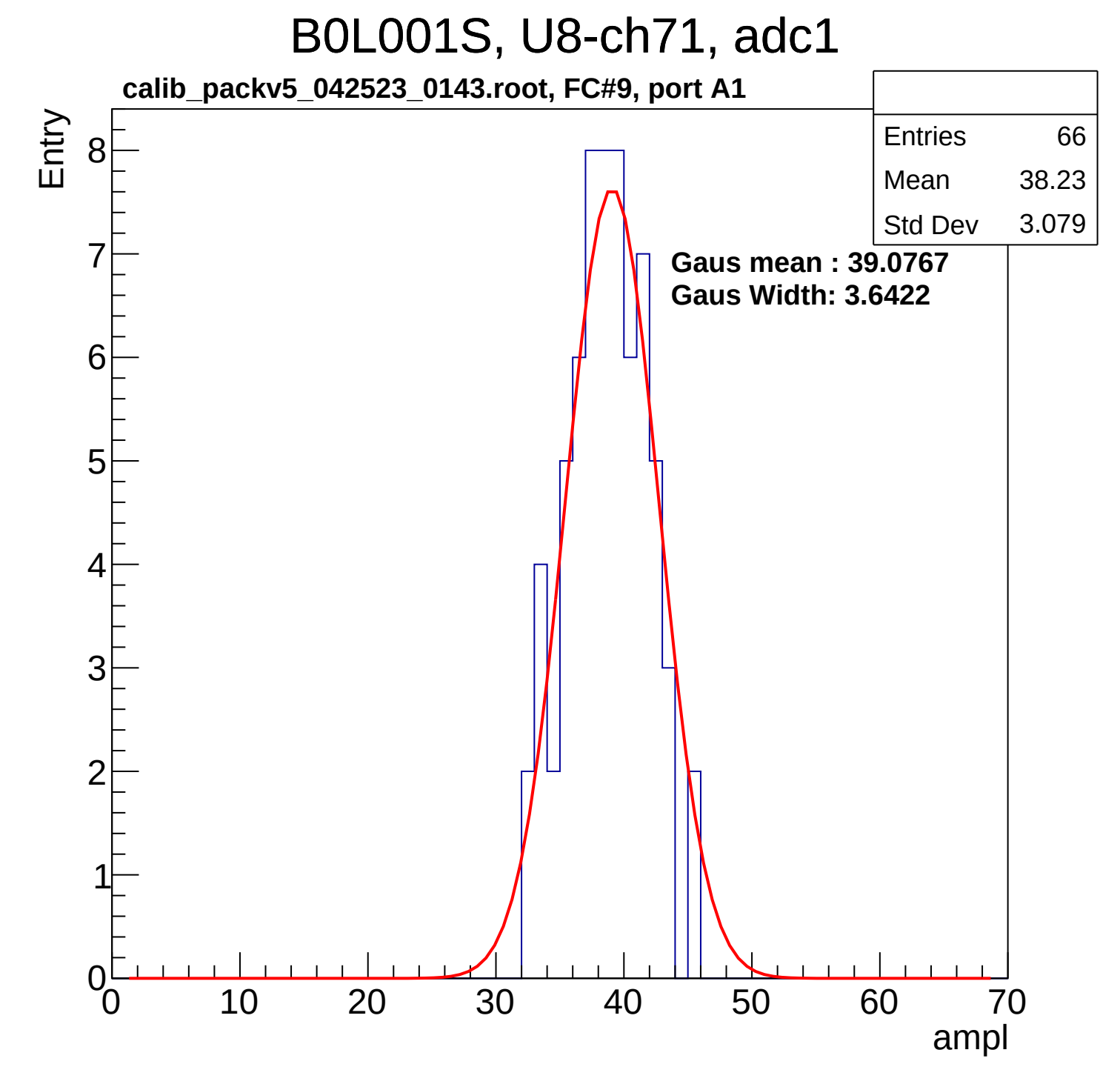

B0L001S, U8-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.33
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

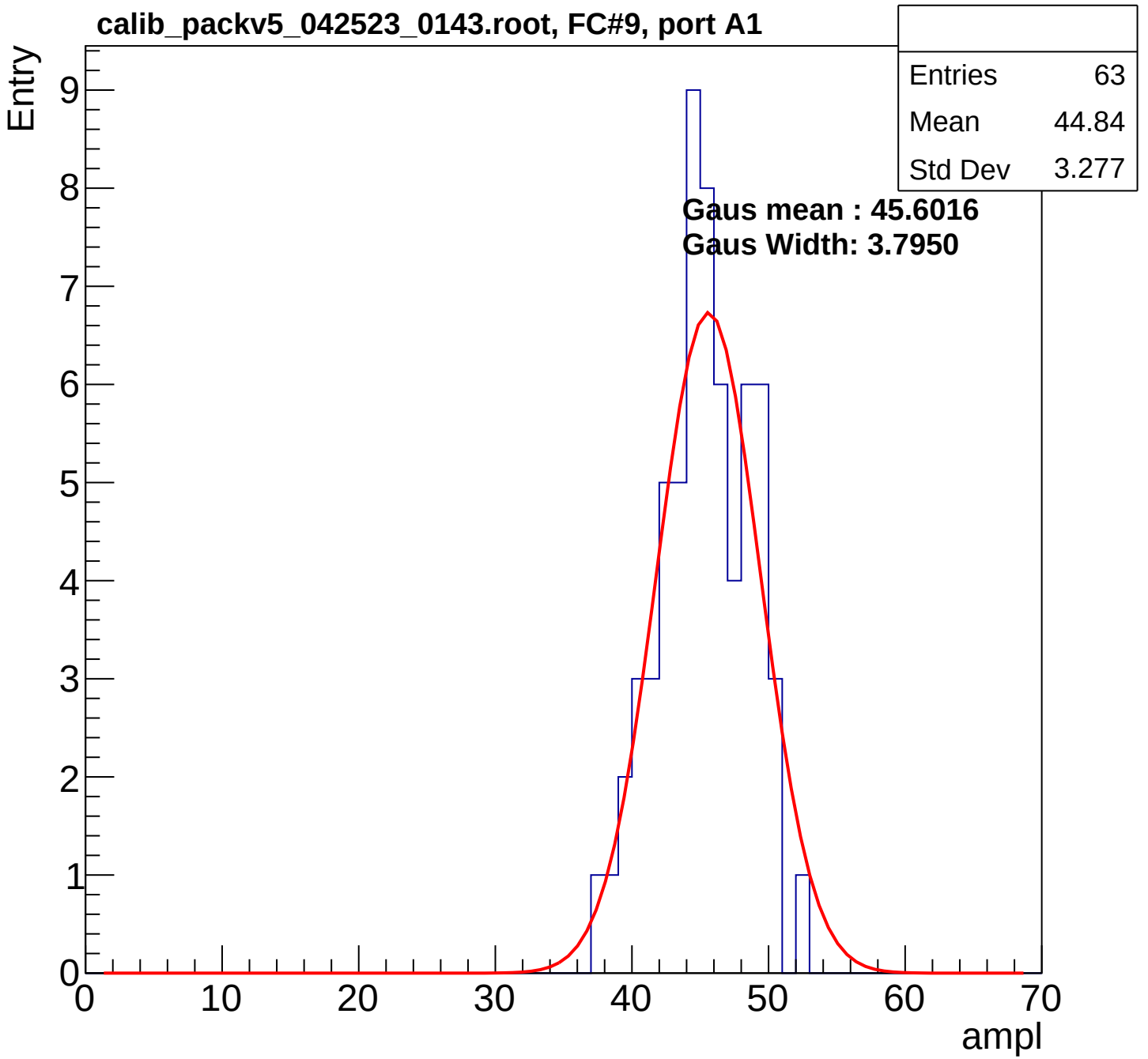

B0L001S, U8-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.84
Std Dev 8.289
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 31
Std Dev 3.339
Gaus mean : 31.0381
Gaus Width: 3.7865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.23
Std Dev 3.079
Gaus mean : 39.0767
Gaus Width: 3.6422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.84
Std Dev 3.277
Gaus mean : 45.6016
Gaus Width: 3.7950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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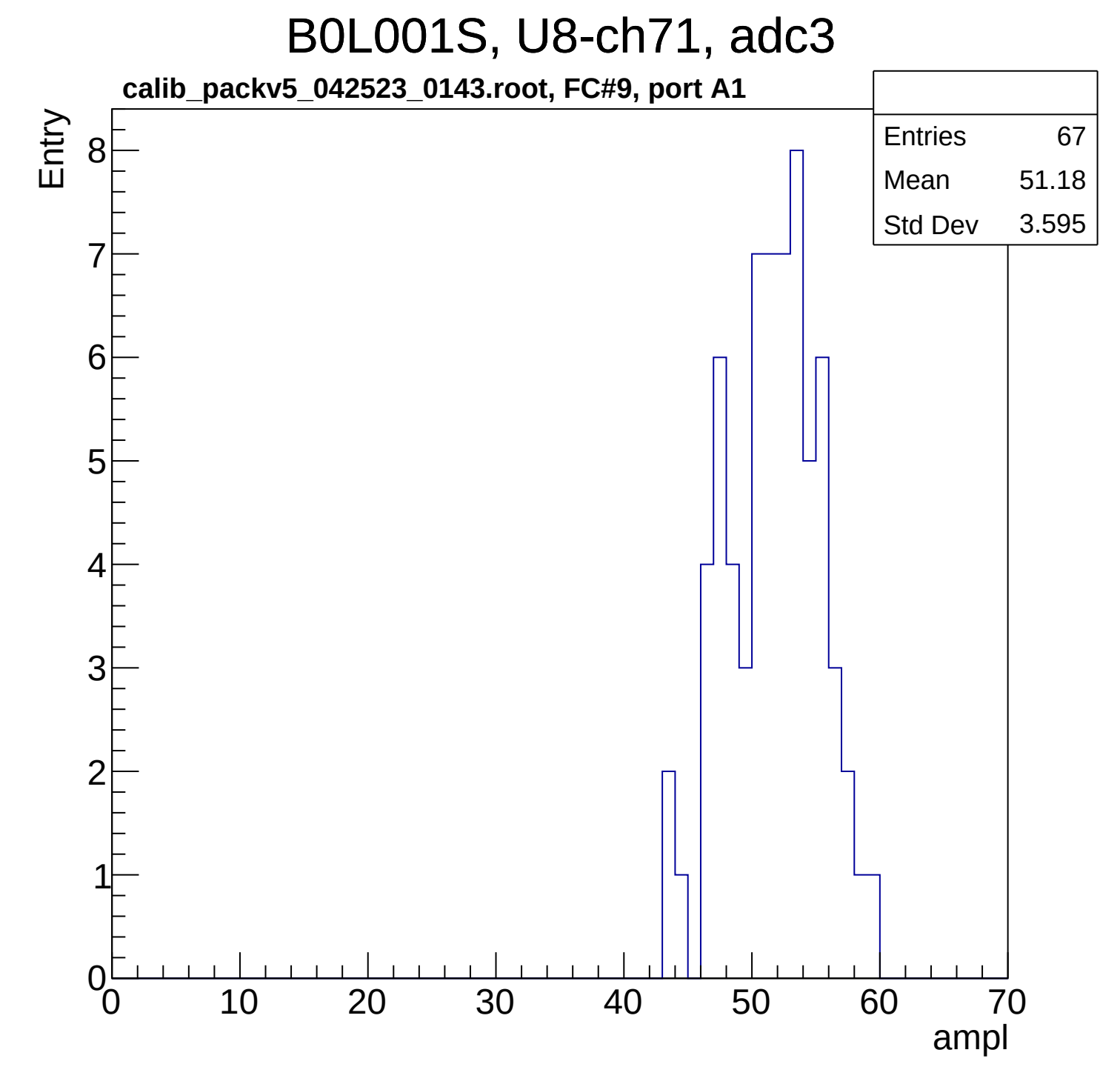
B0L001S, U8-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.18
Std Dev 3.595
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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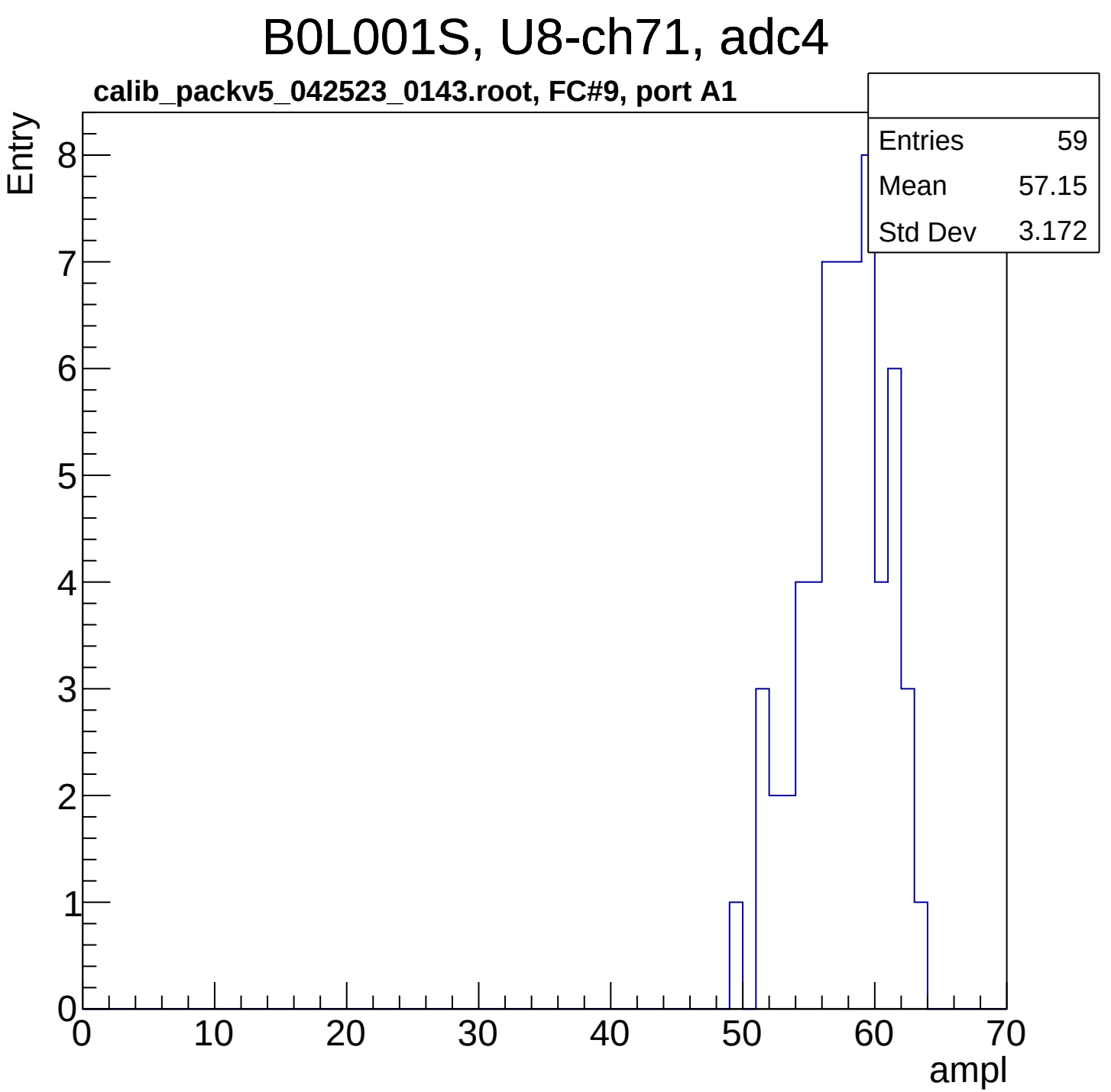
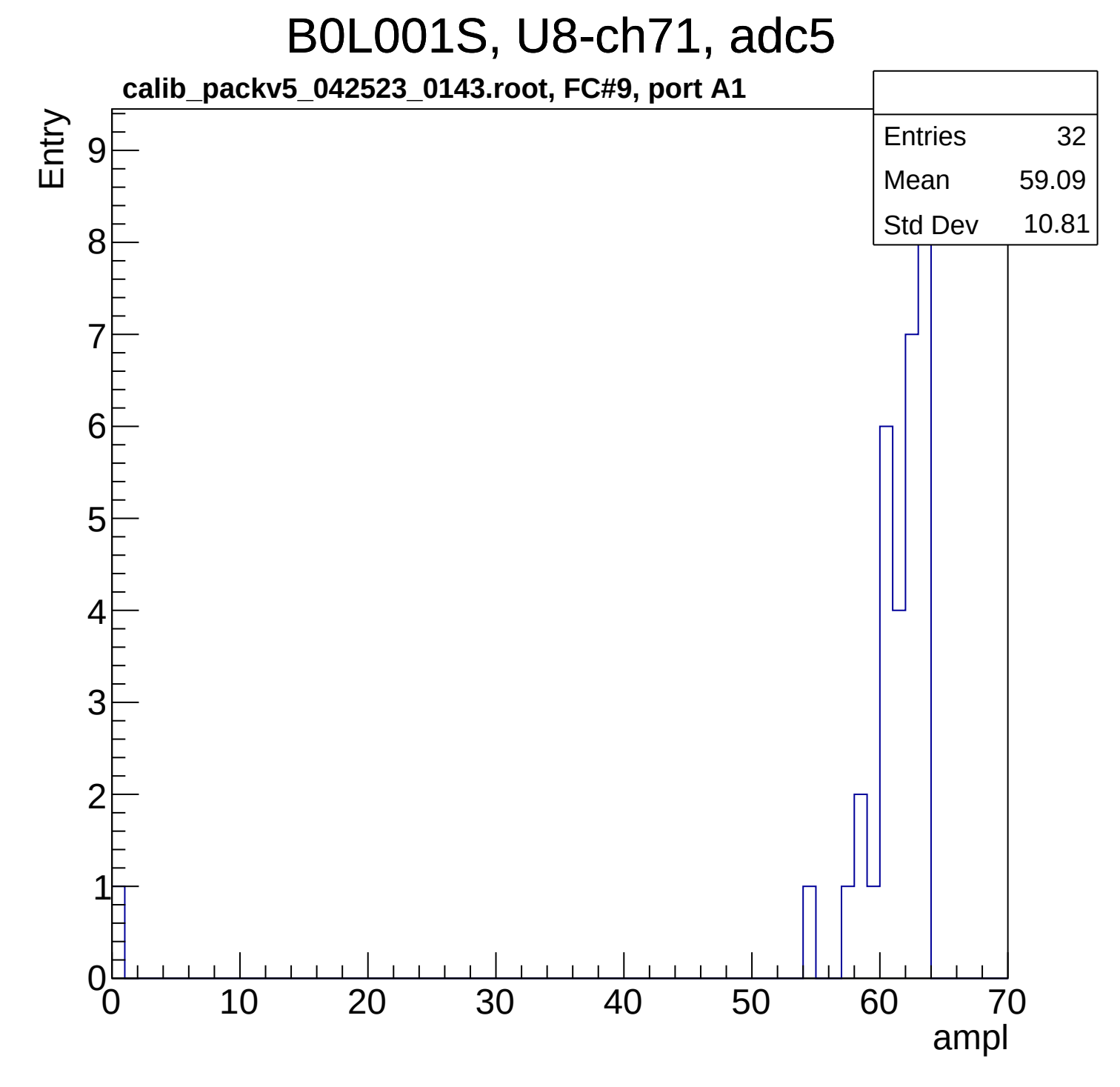
40
50
60
70


B0L001S, U8-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.15
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.09
Std Dev 10.81
Entry
ampl
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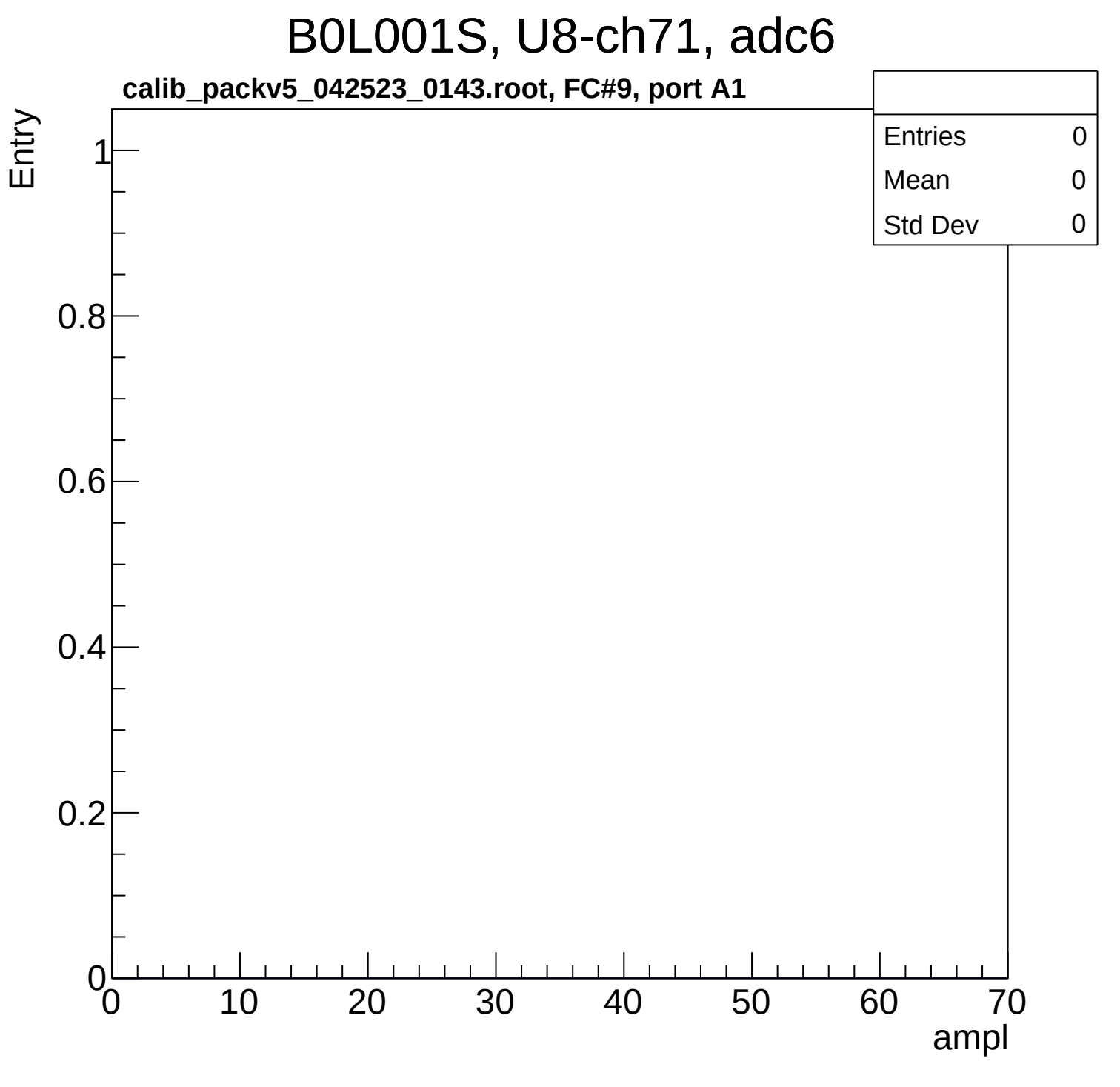
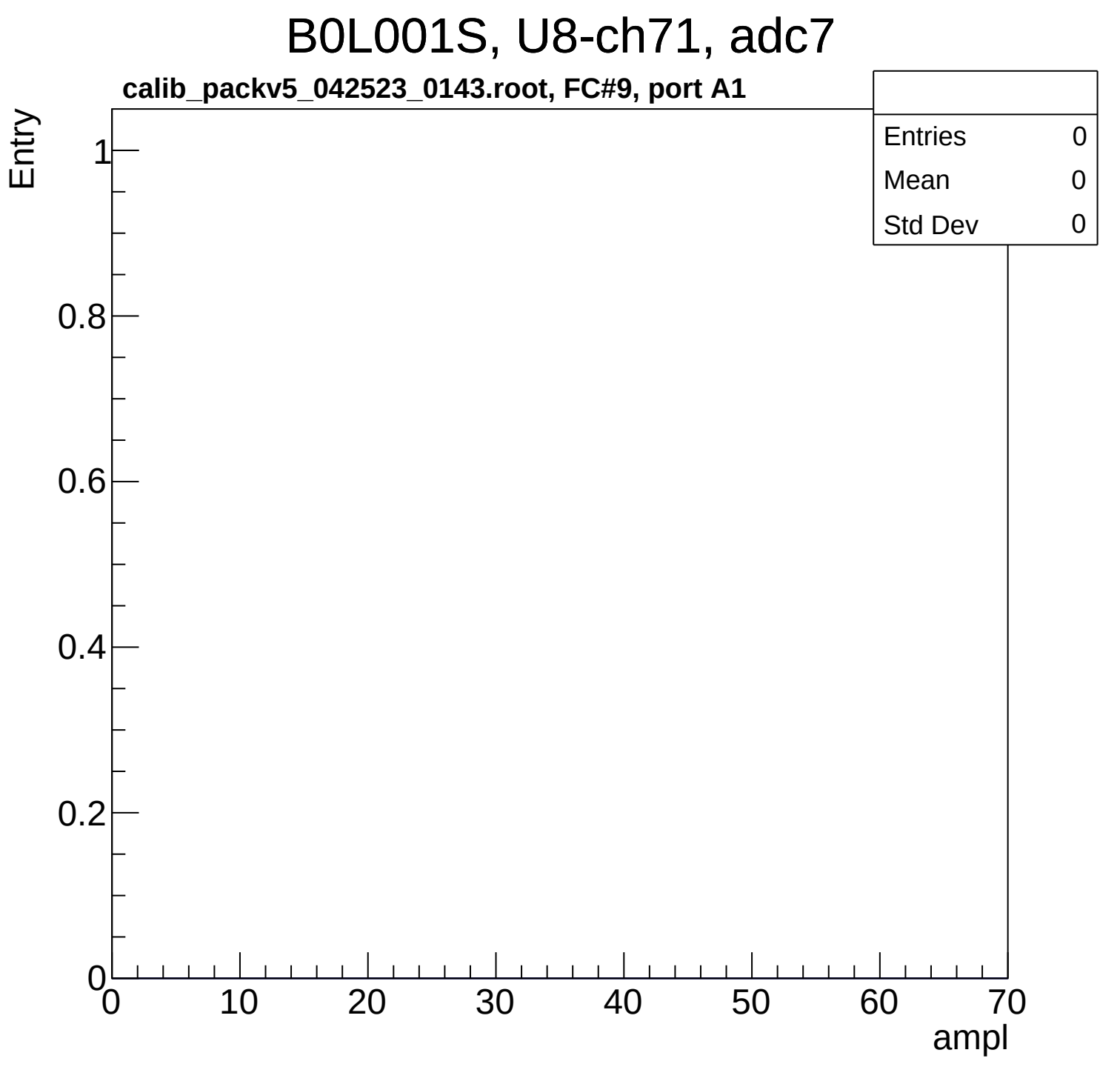
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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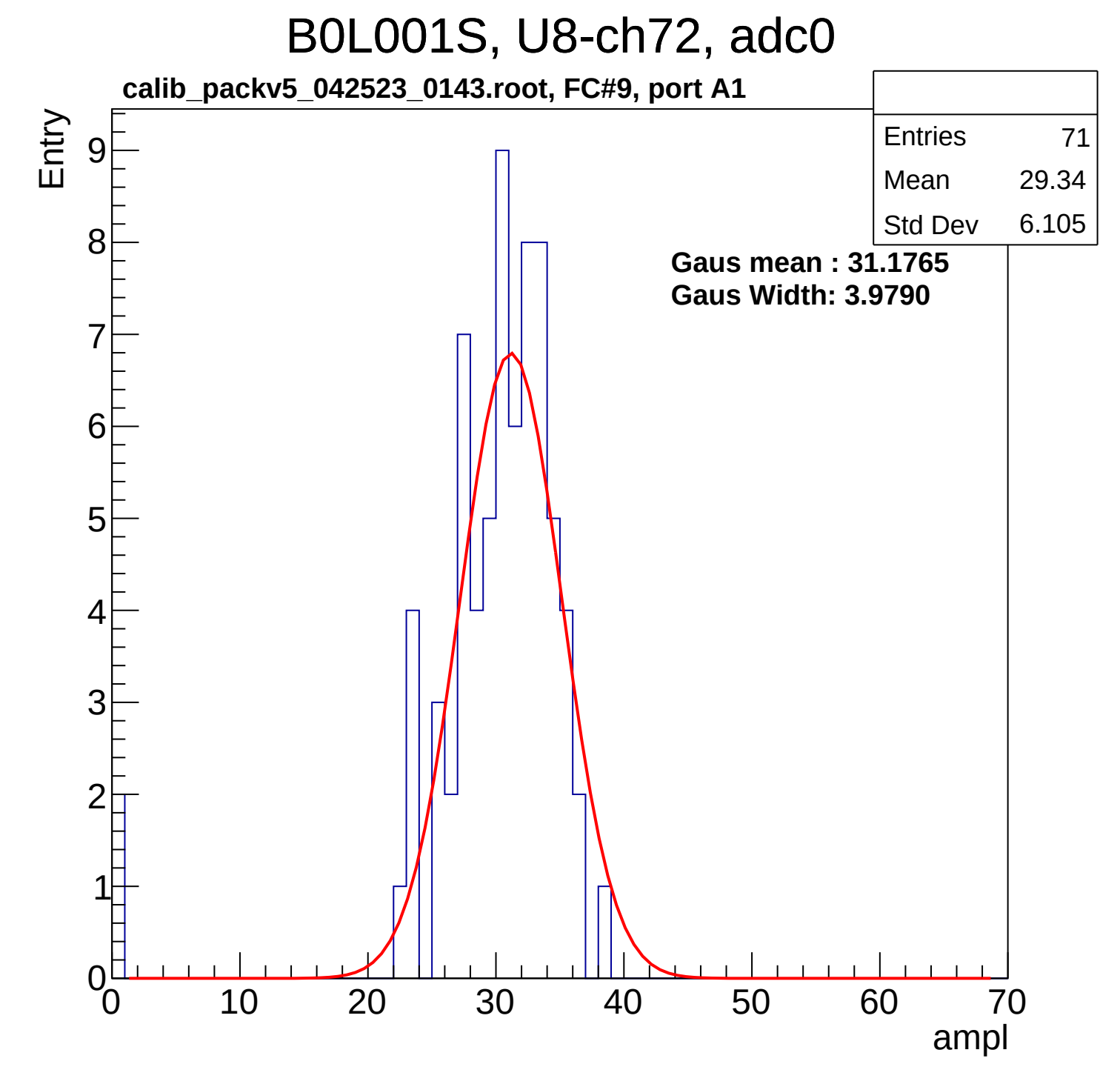
50
60
70
ampl


B0L001S, U8-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.34
Std Dev 6.105
Gaus mean : 31.1765
Gaus Width: 3.9790
Entry
ampl
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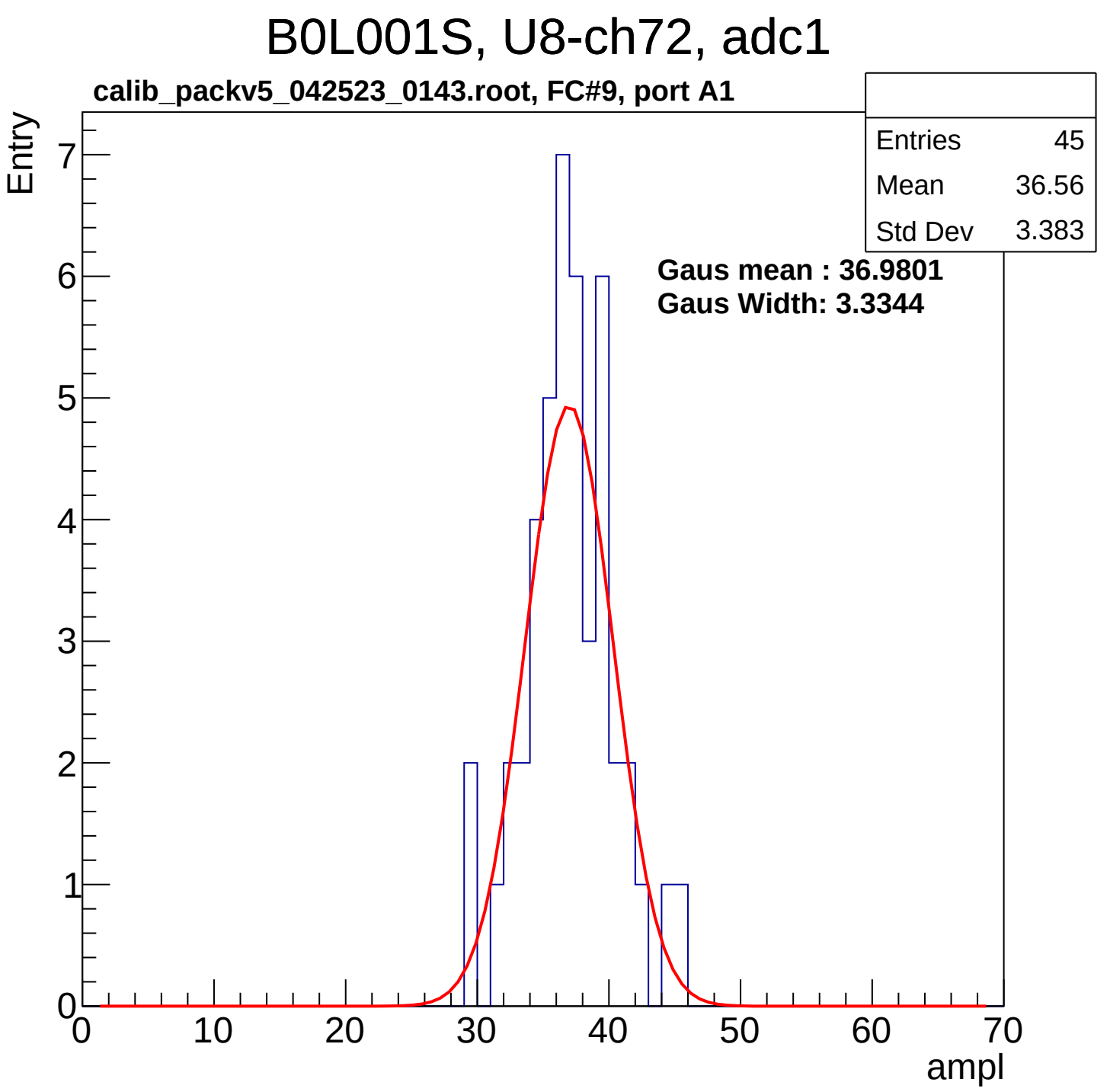
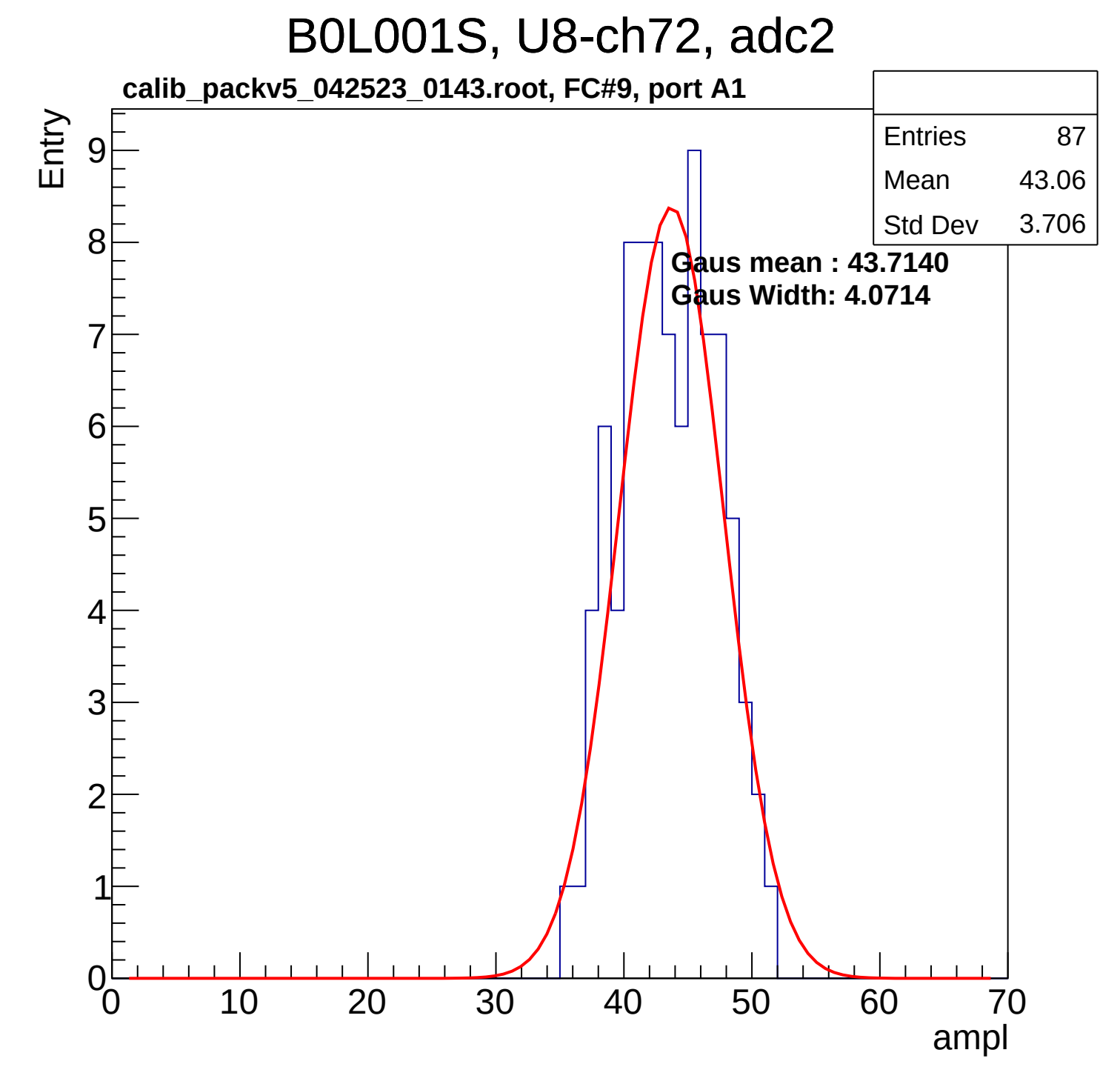
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 36.56
Std Dev 3.383
Gaus mean : 36.9801
Gaus Width: 3.3344
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 43.06
Std Dev 3.706
Gaus mean : 43.7140
Gaus Width: 4.0714
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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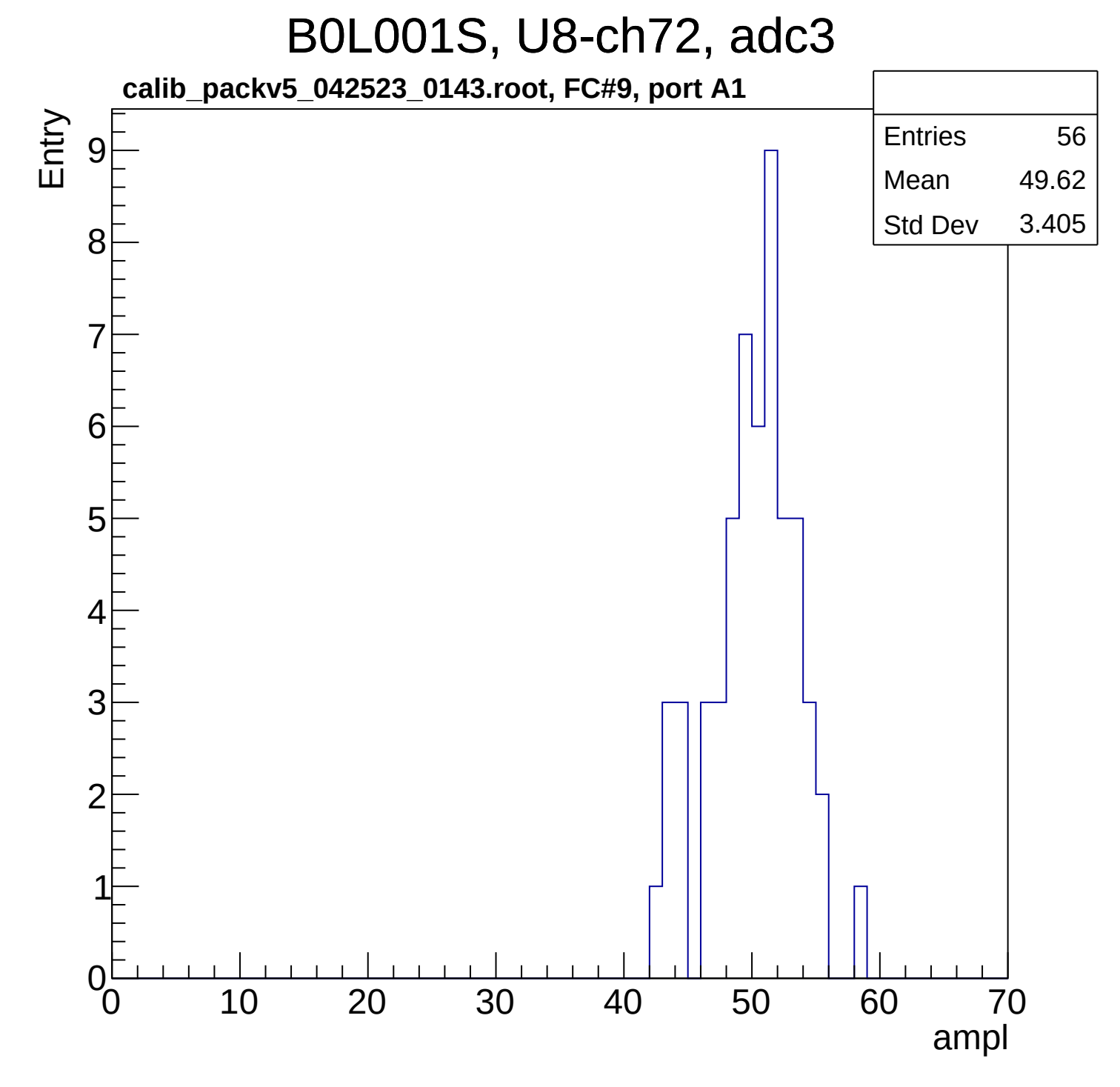
B0L001S, U8-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.62
Std Dev 3.405
Entry
ampl
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3
4
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6
7
8
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0
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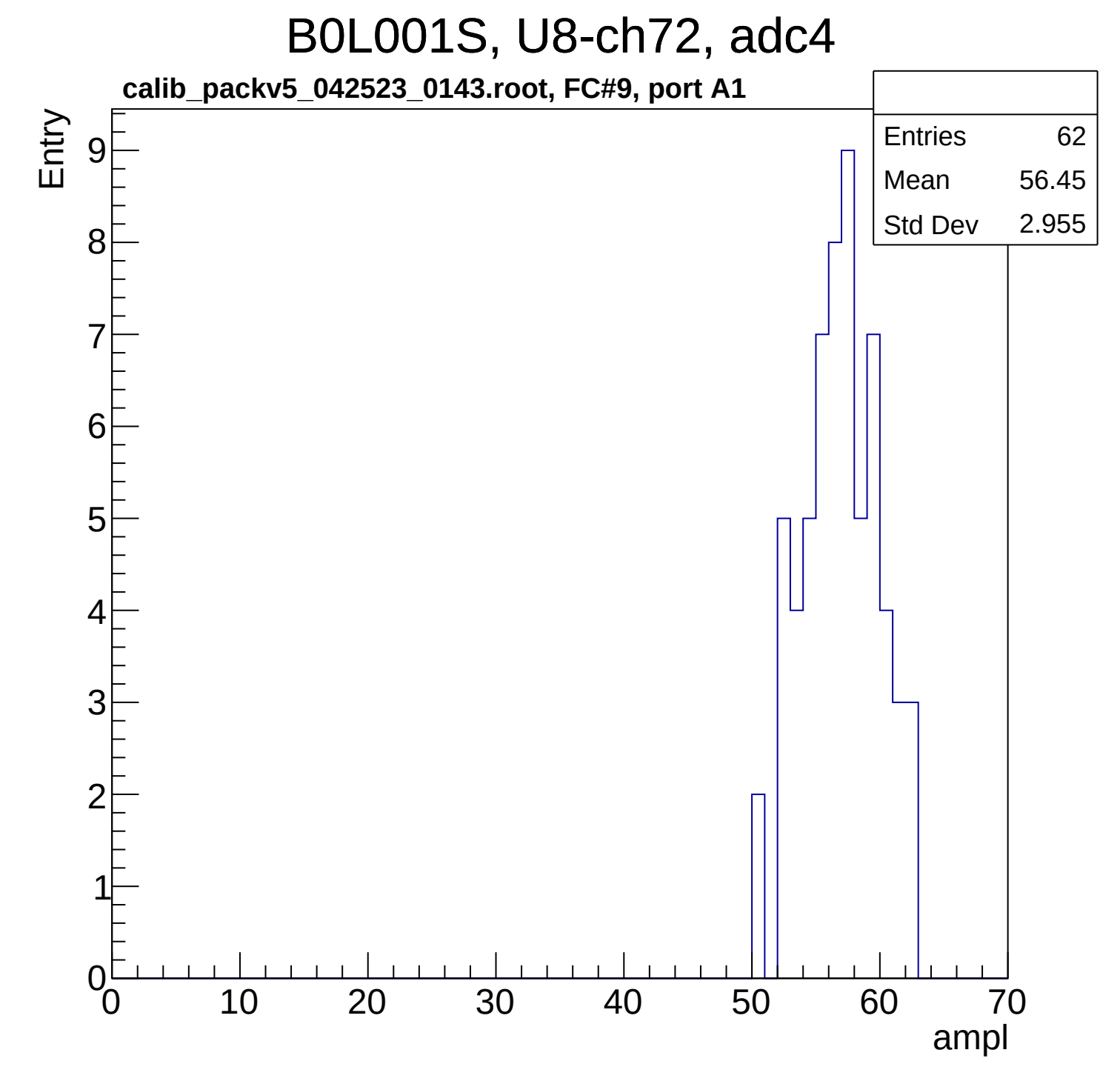
B0L001S, U8-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.45
Std Dev 2.955
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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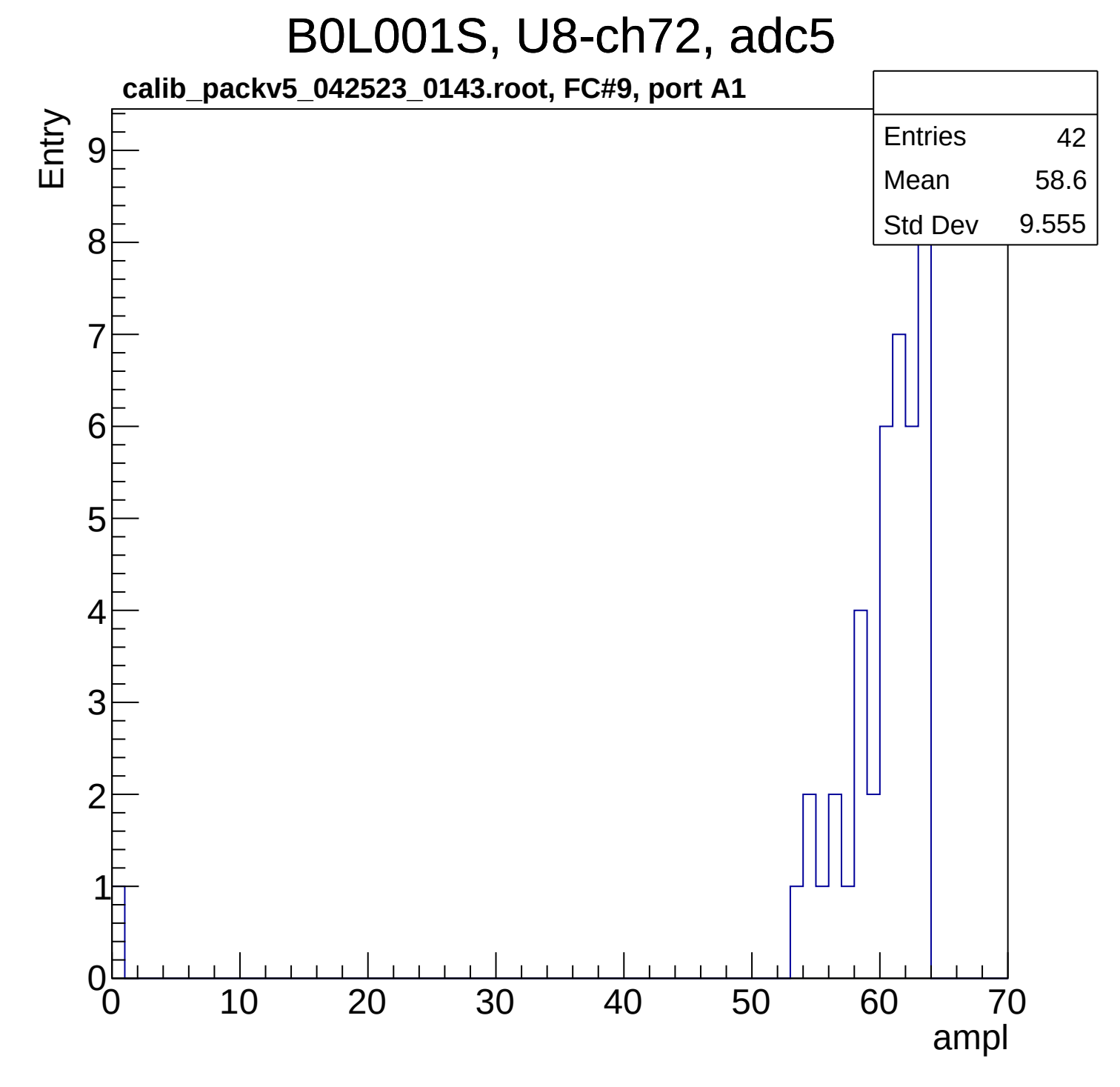
B0L001S, U8-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.6
Std Dev 9.555
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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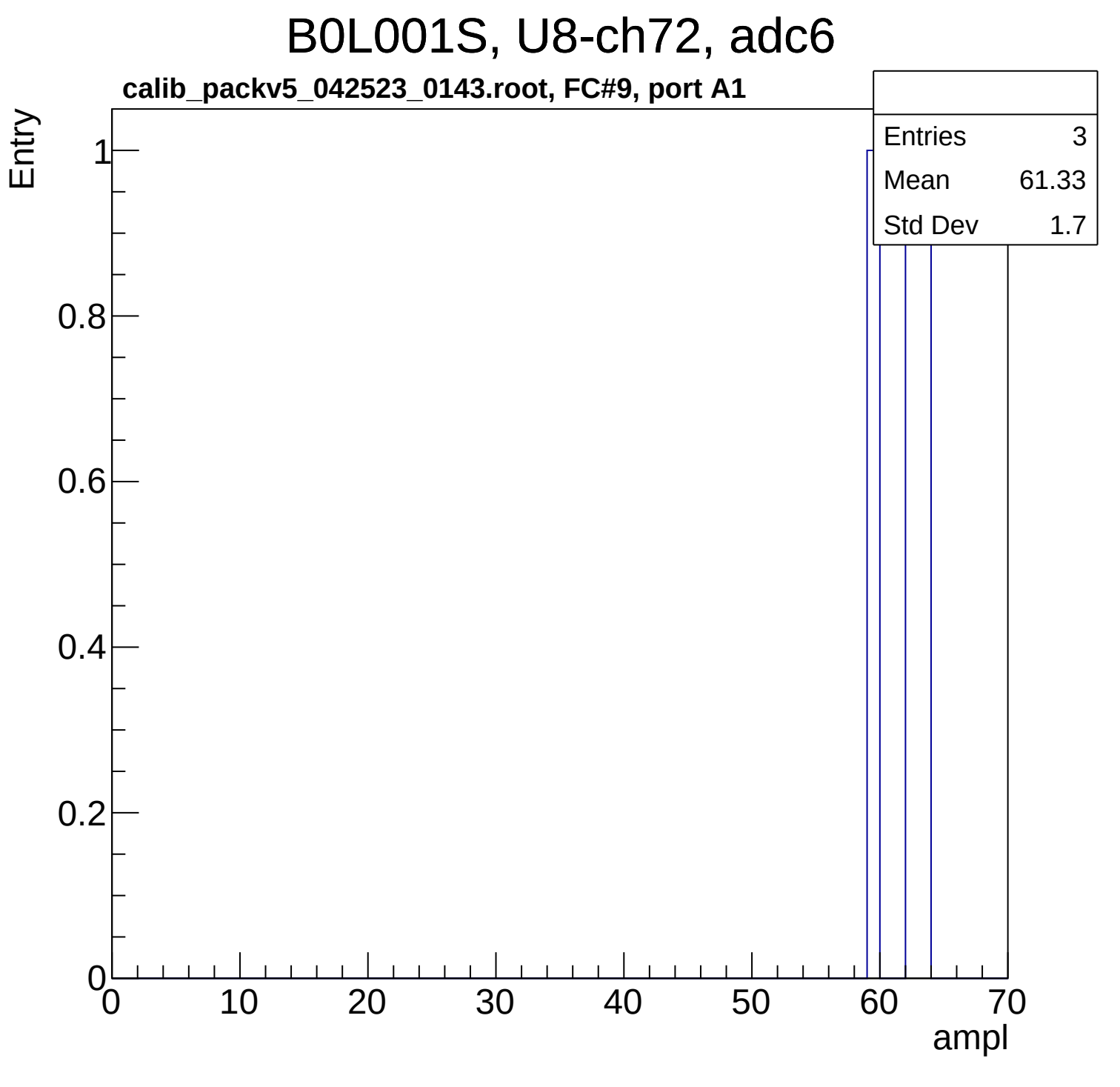
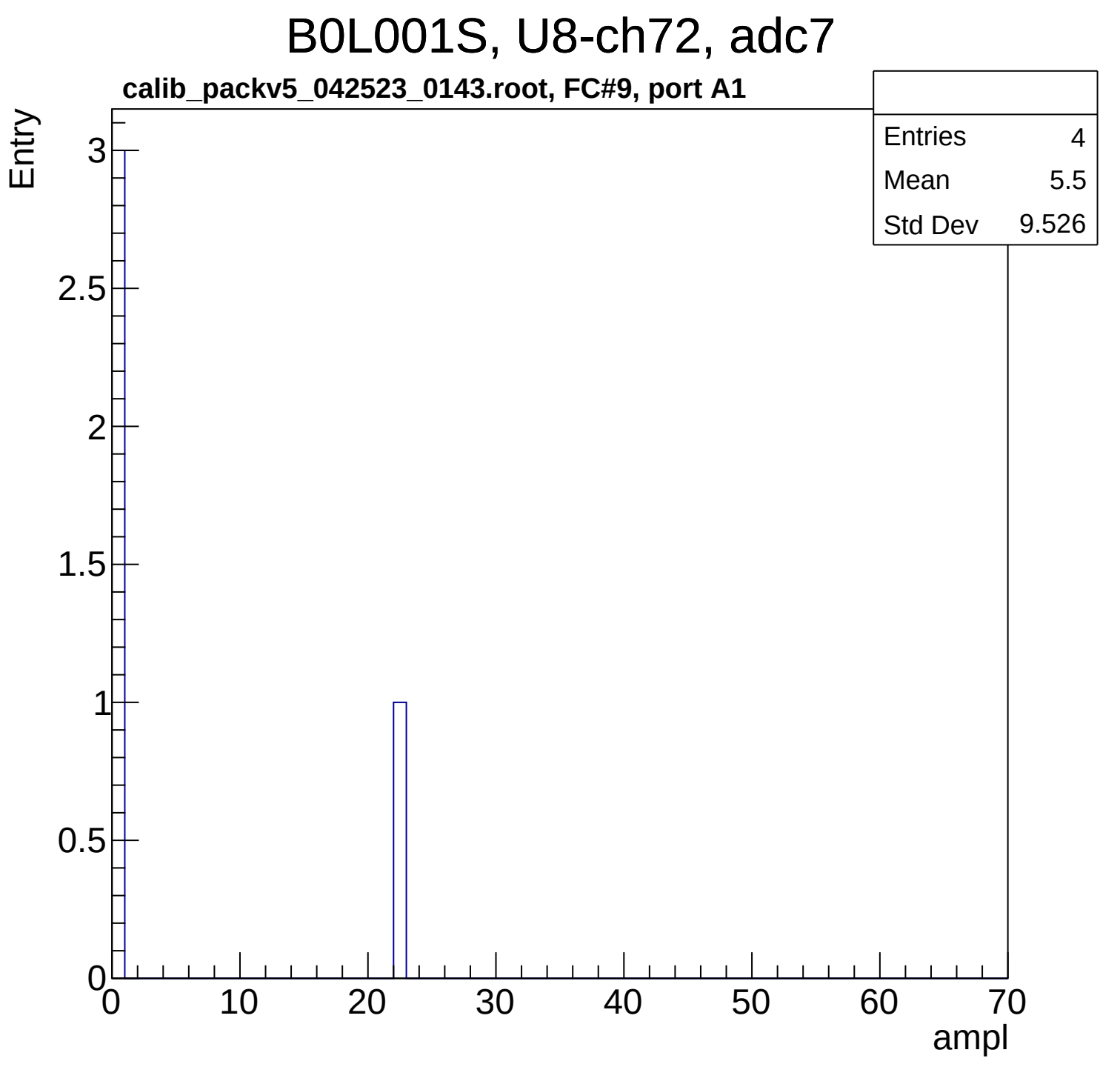

B0L001S, U8-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 1.7
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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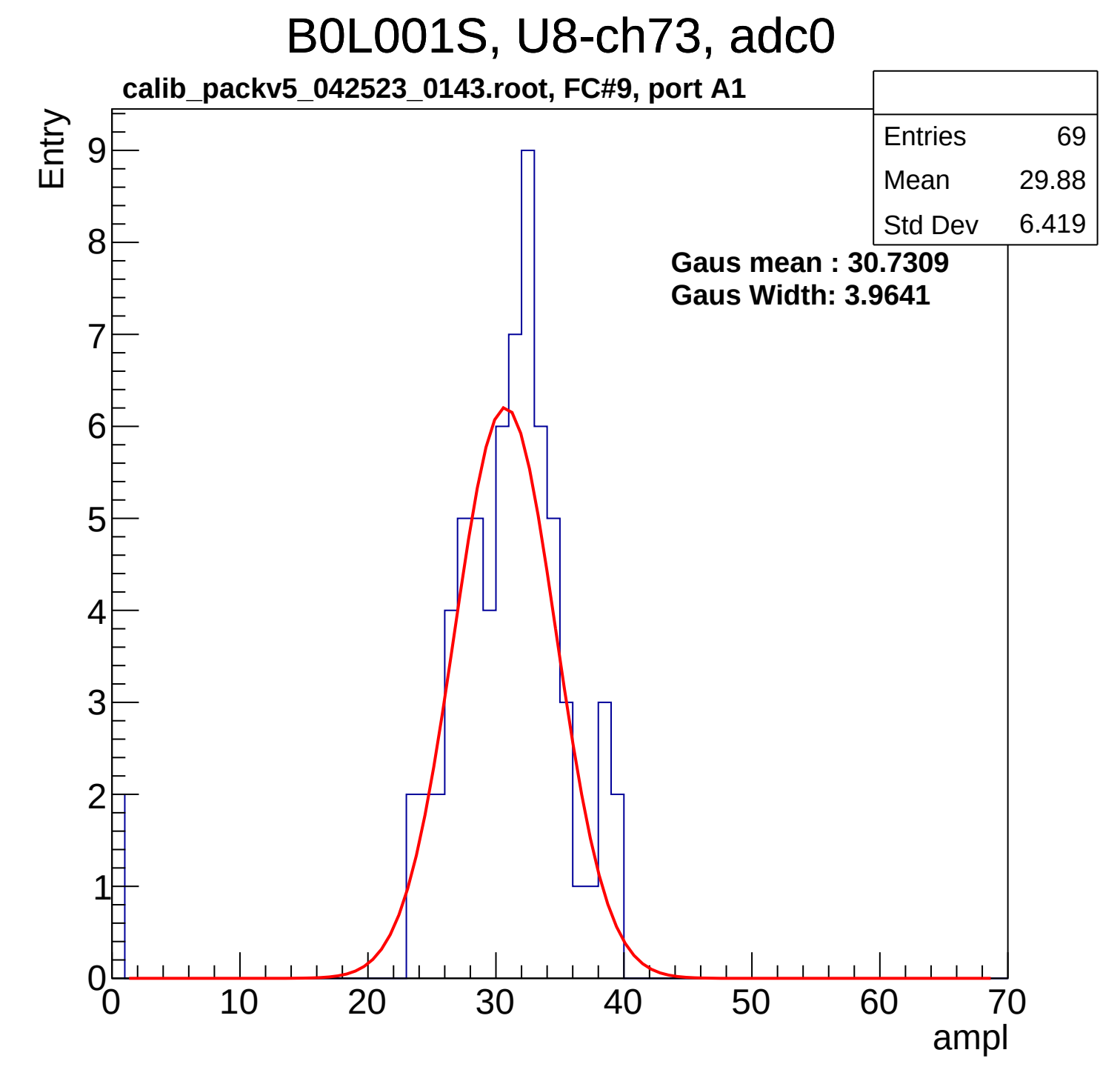
B0L001S, U8-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.88
Std Dev 6.419
Gaus mean : 30.7309
Gaus Width: 3.9641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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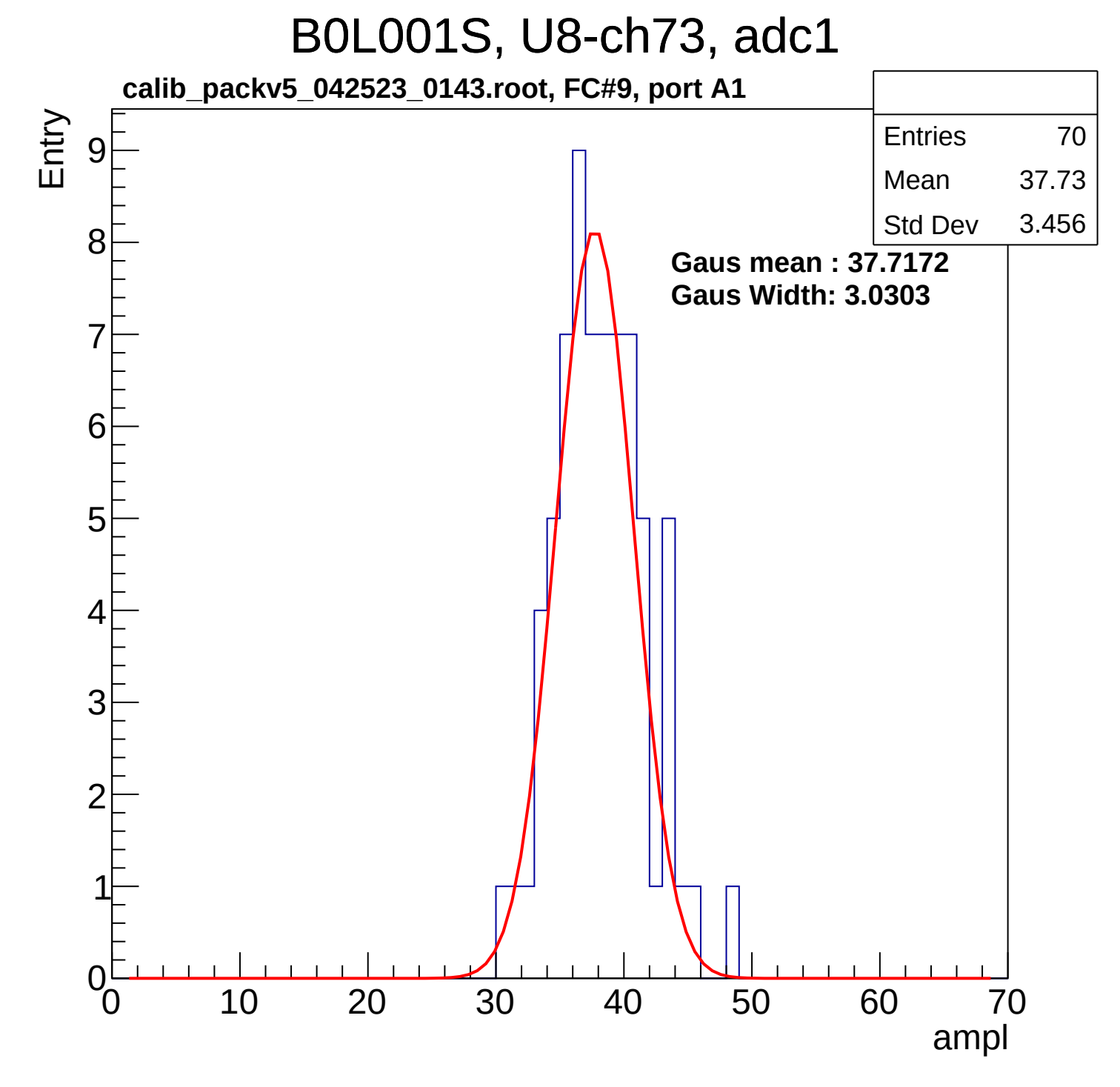
B0L001S, U8-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.73
Std Dev 3.456
Gaus mean : 37.7172
Gaus Width: 3.0303
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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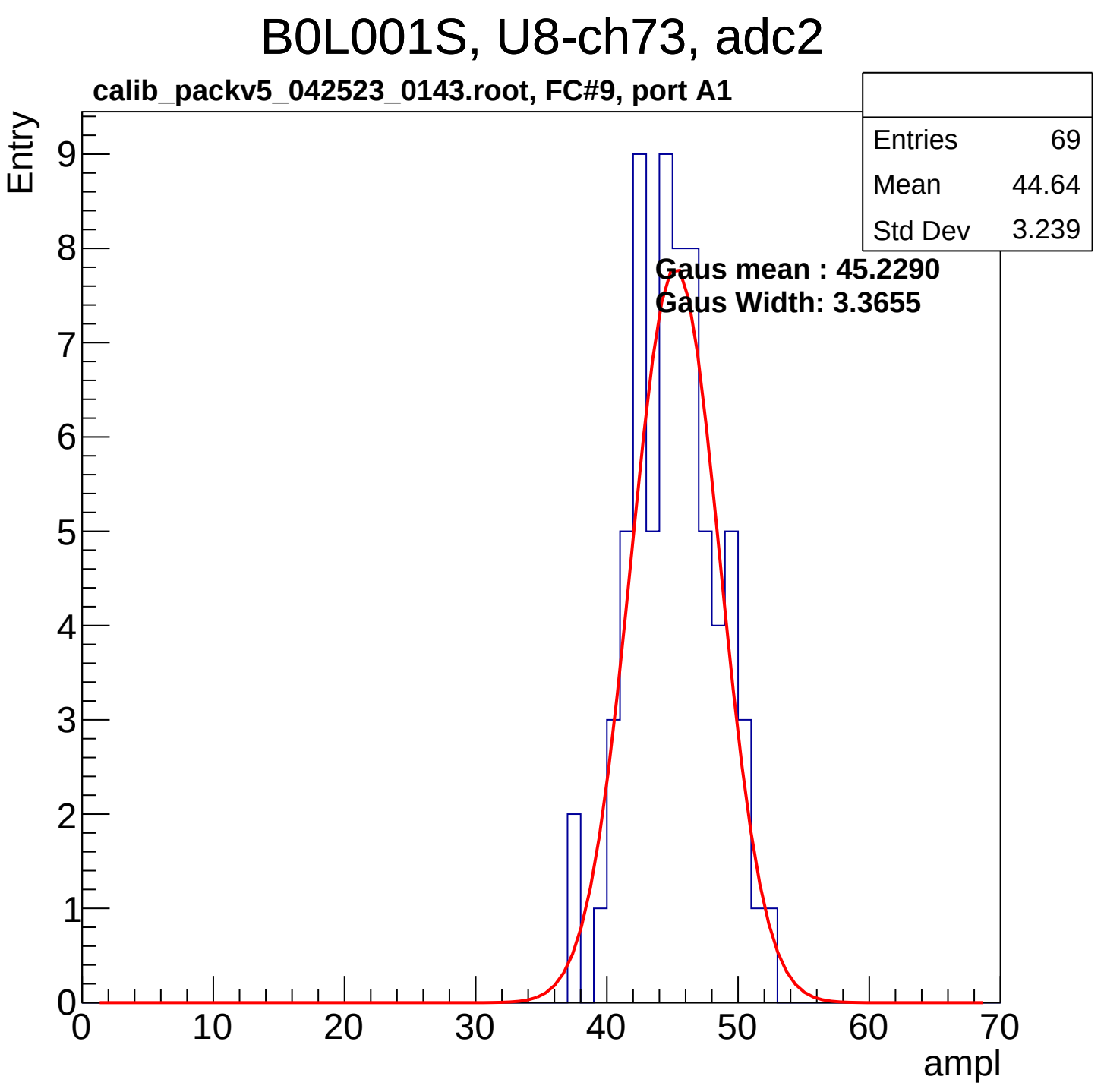
B0L001S, U8-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.64
Std Dev 3.239
Gaus mean : 45.2290
Gaus Width: 3.3655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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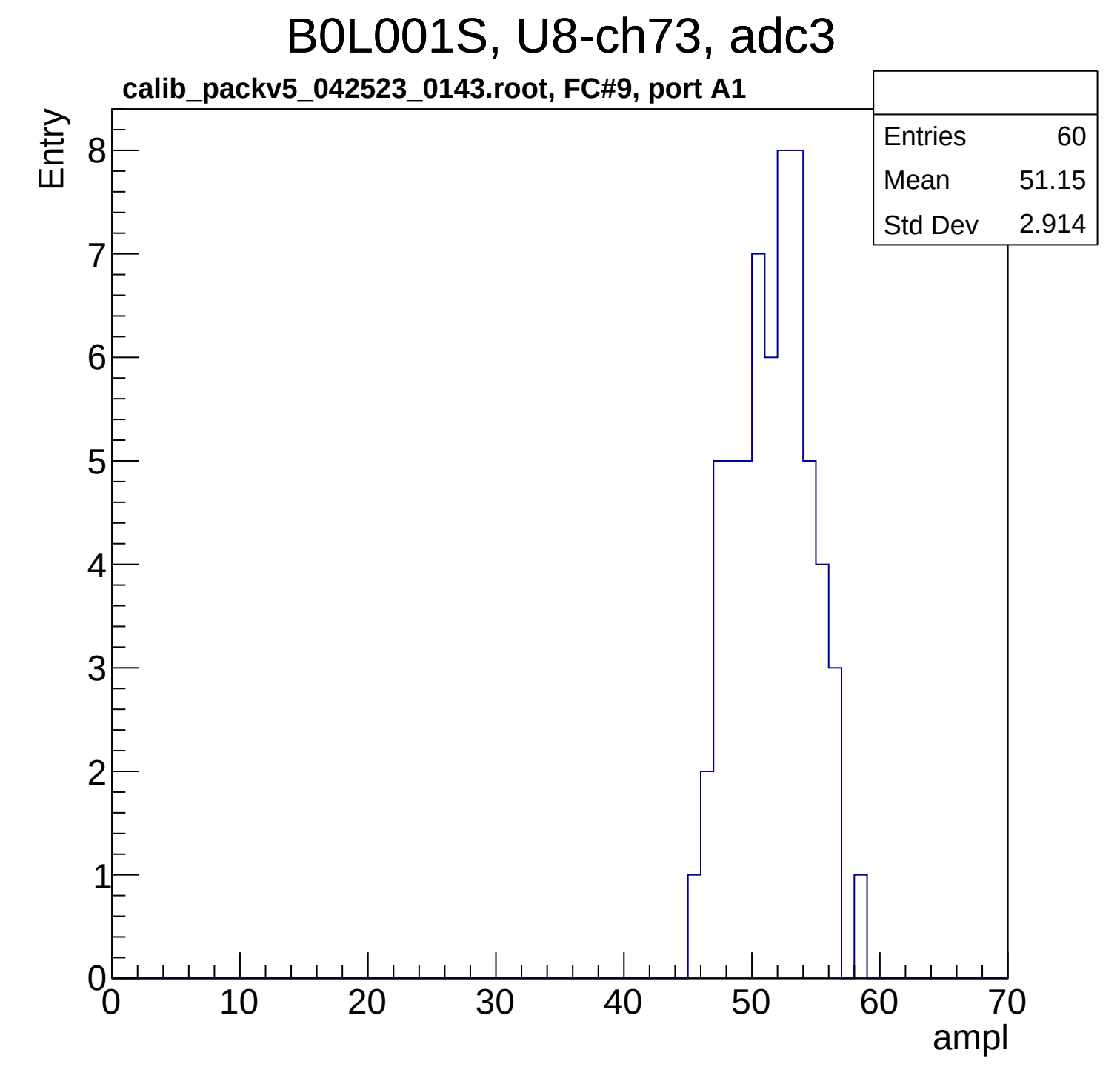
40
50
60
70


B0L001S, U8-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.15
Std Dev 2.914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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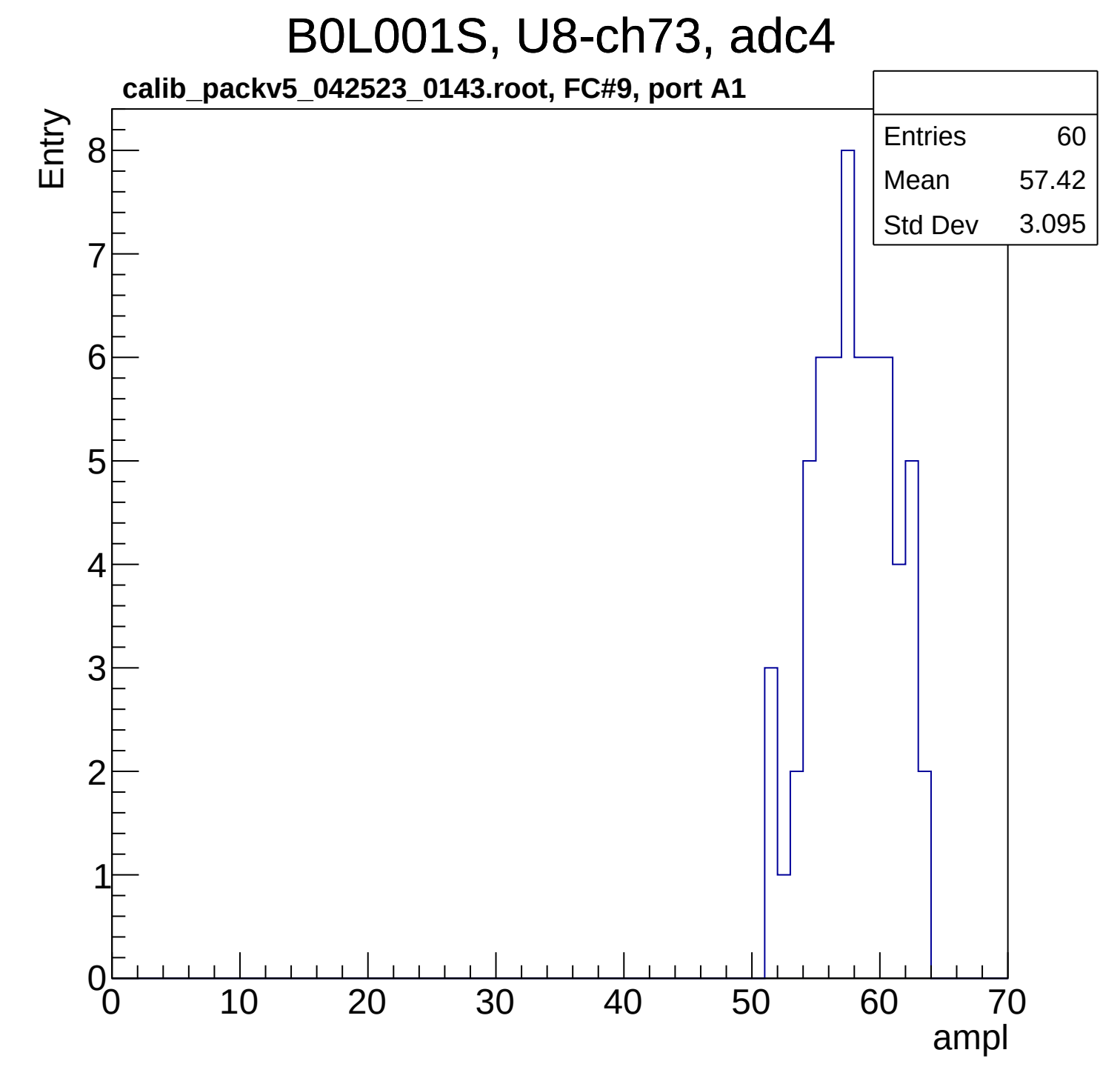
B0L001S, U8-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.42
Std Dev 3.095
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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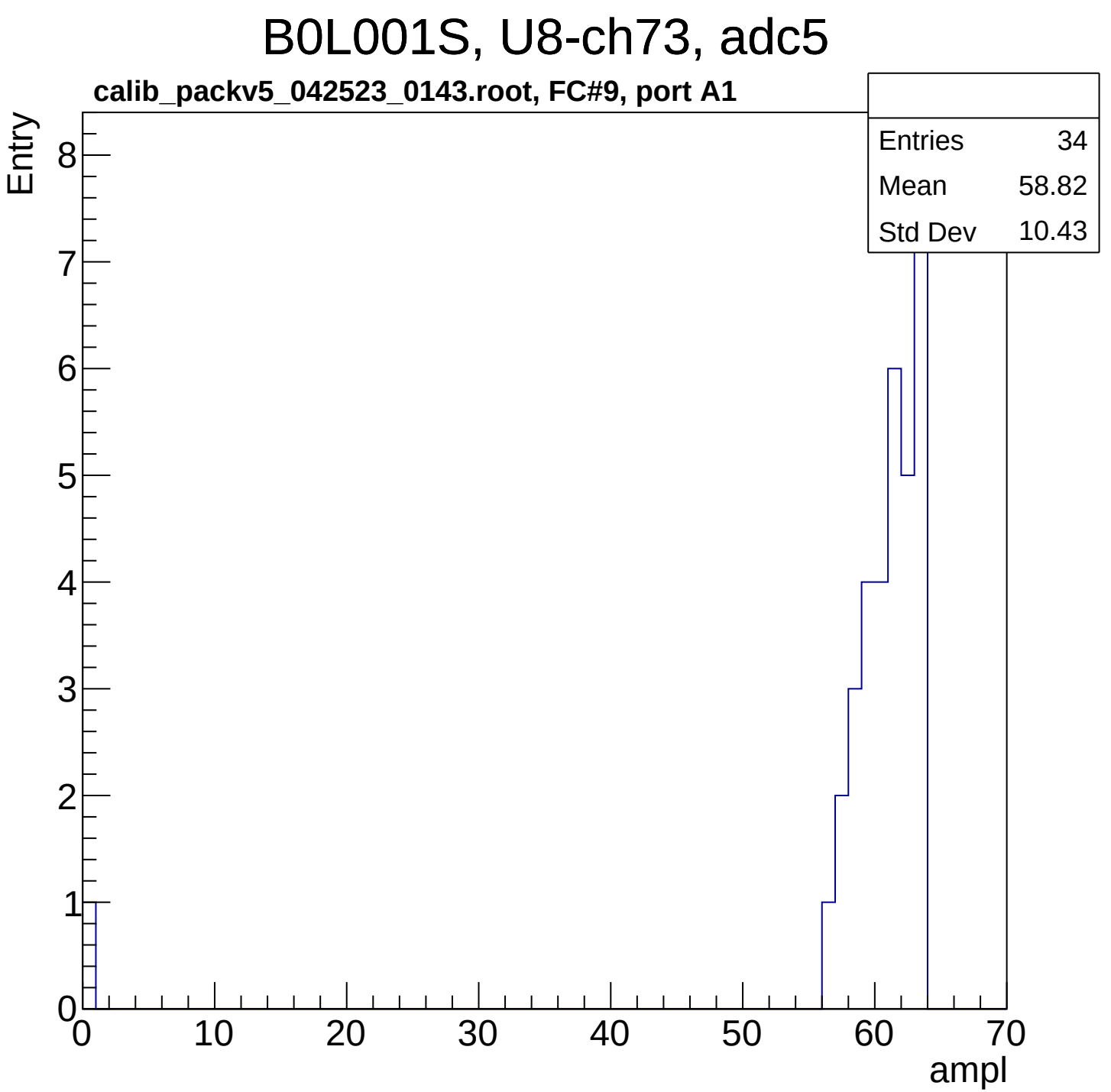
B0L001S, U8-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.82
Std Dev 10.43
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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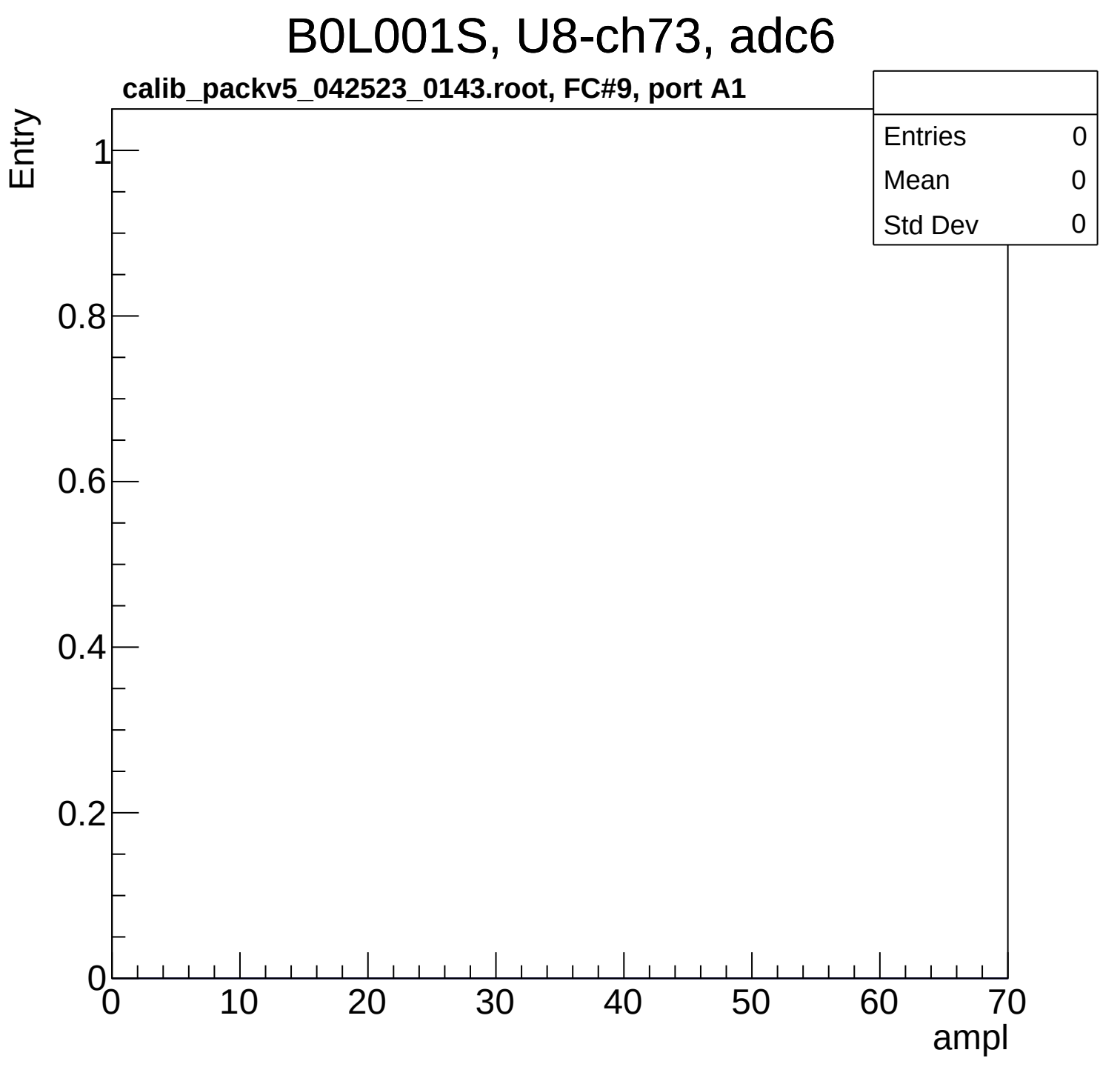
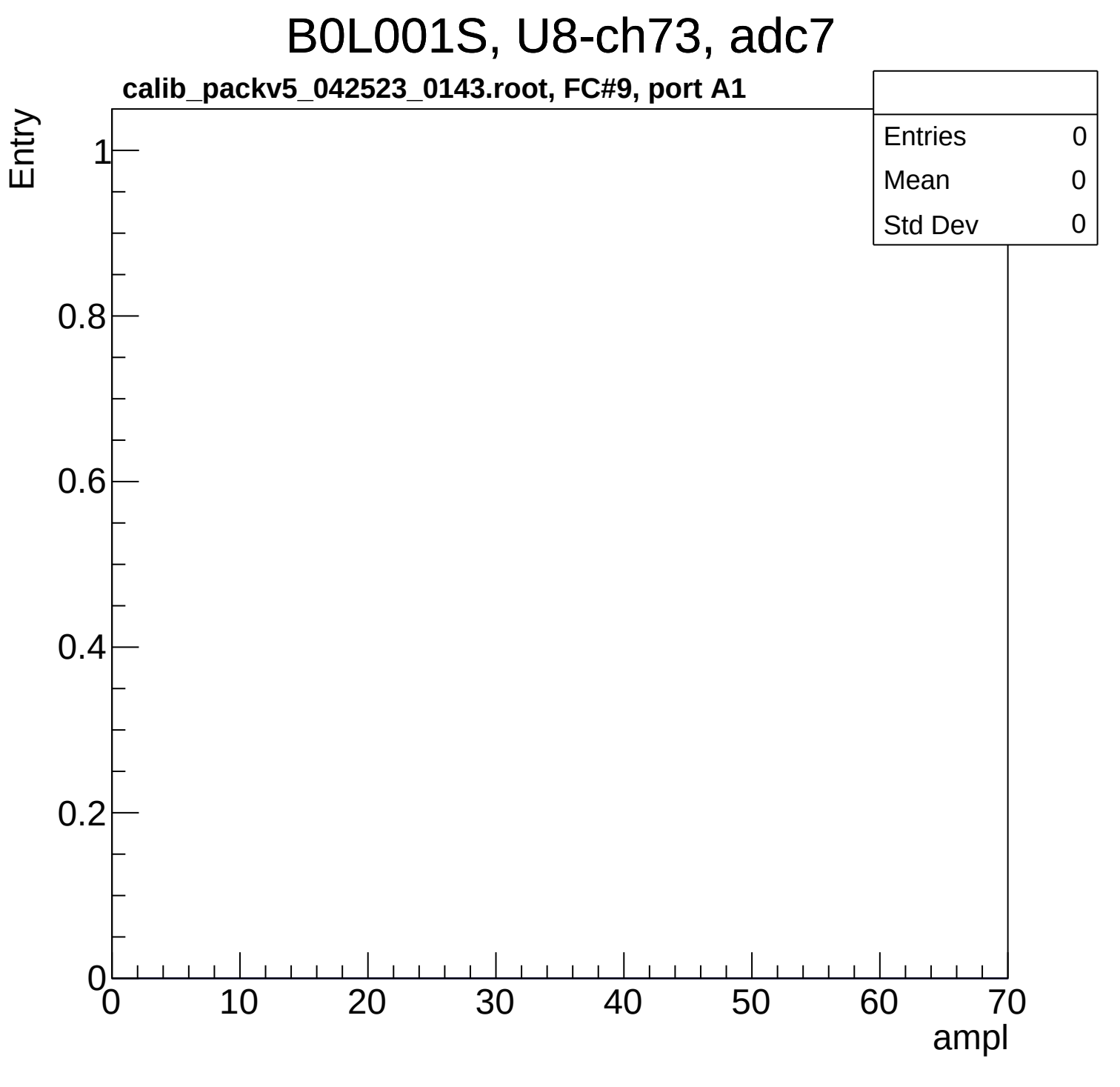
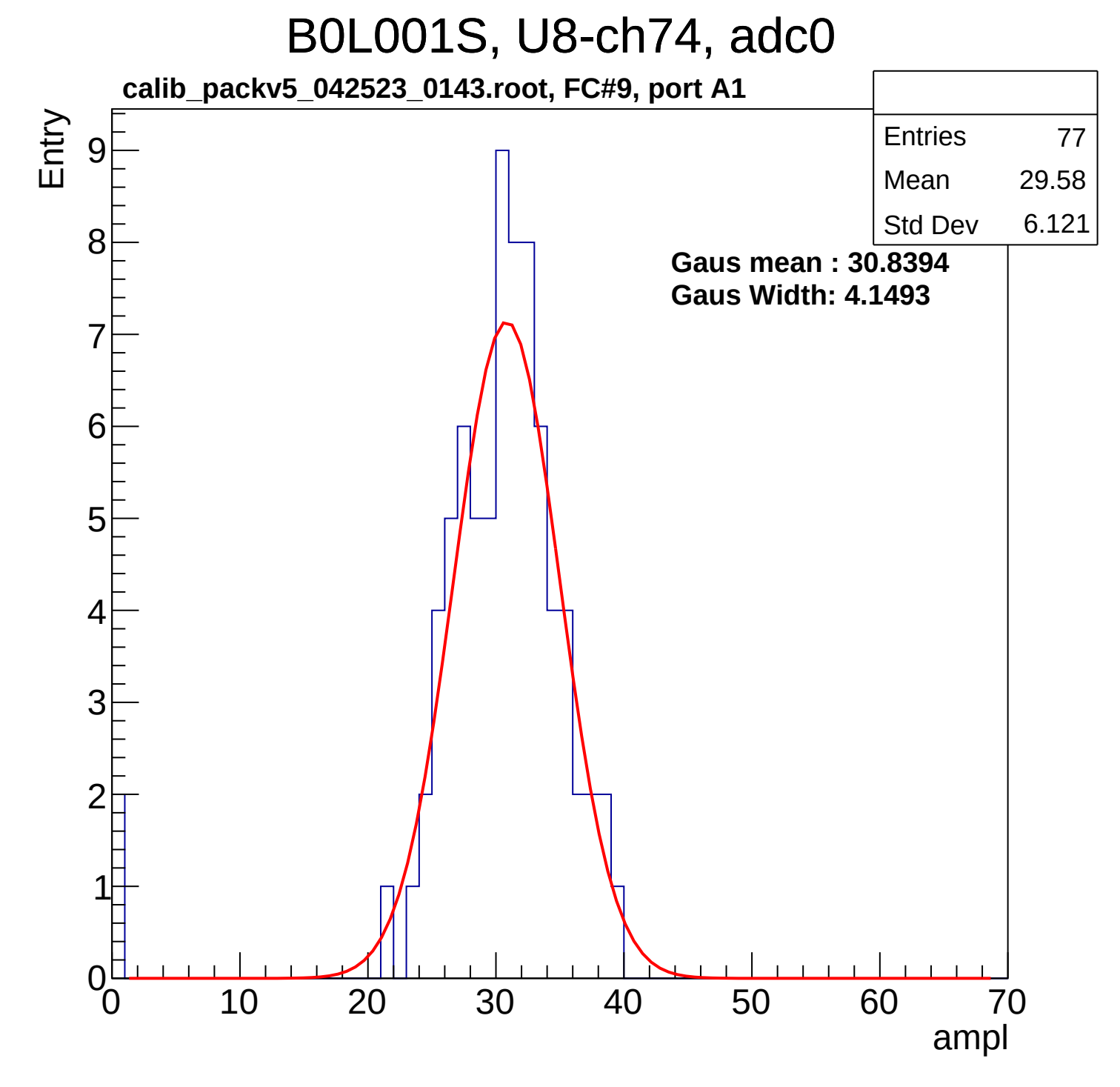
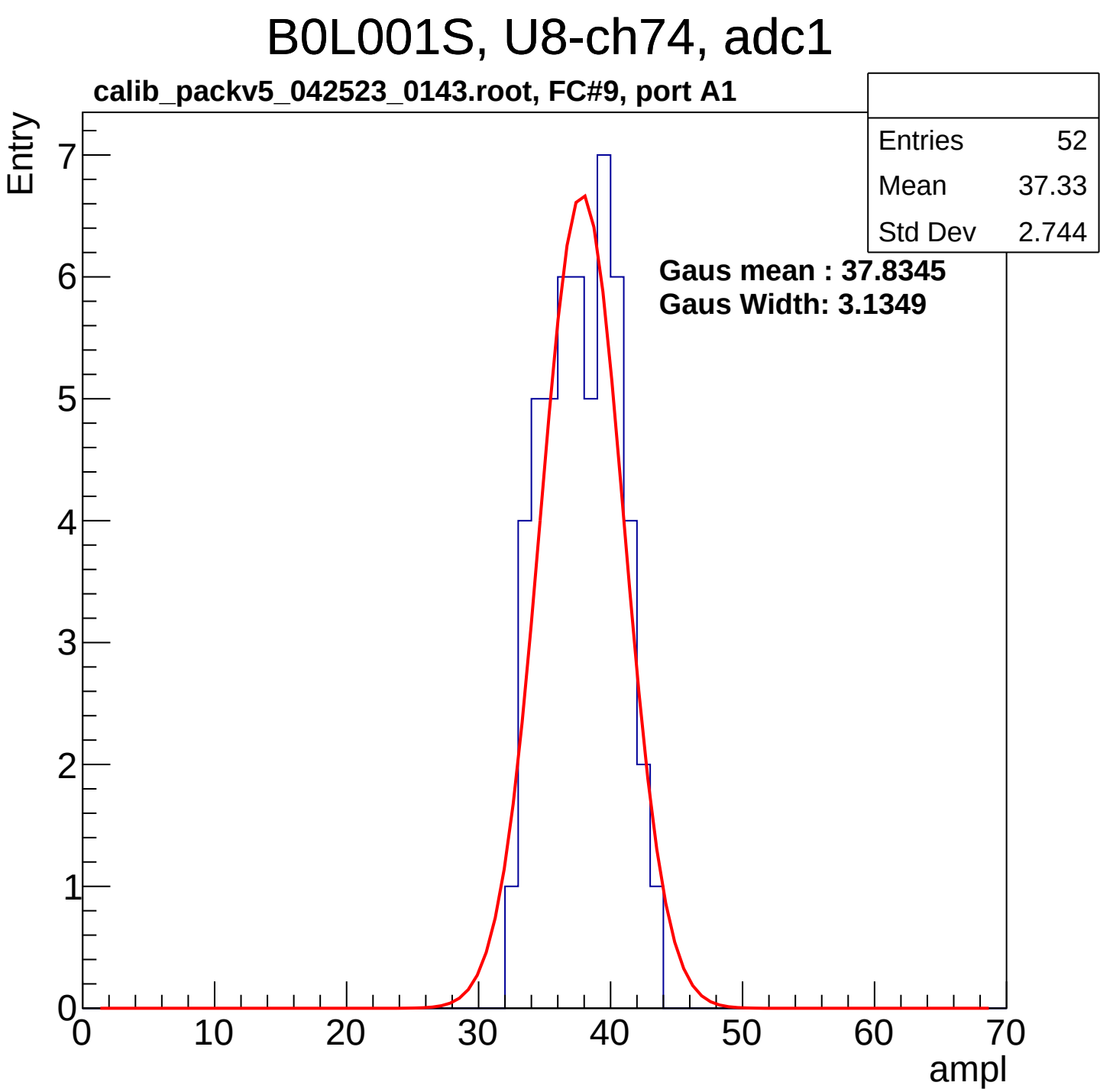

B0L001S, U8-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.58
Std Dev 6.121
Gaus mean : 30.8394
Gaus Width: 4.1493
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 37.33
Std Dev 2.744
Gaus mean : 37.8345
Gaus Width: 3.1349
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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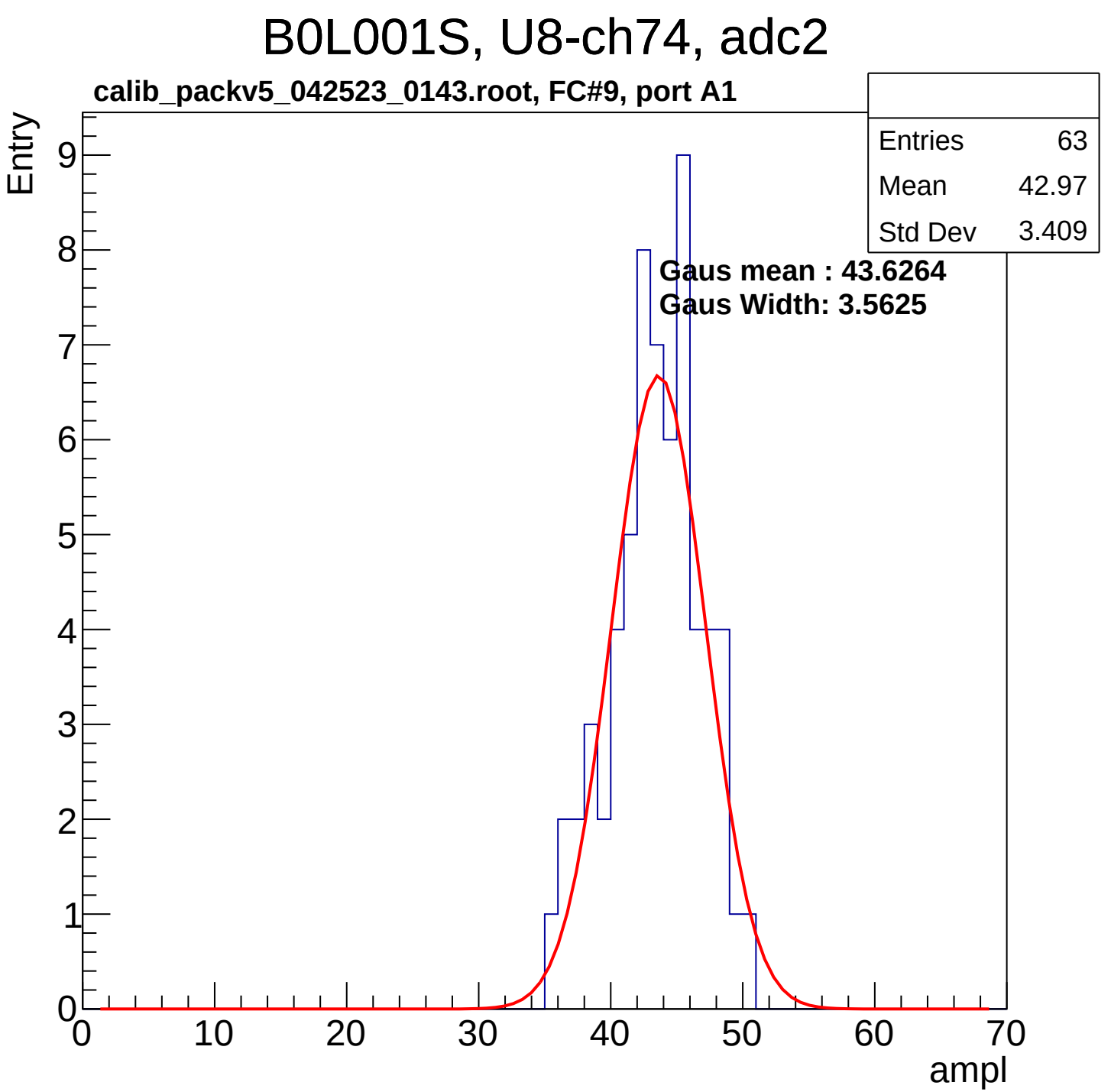
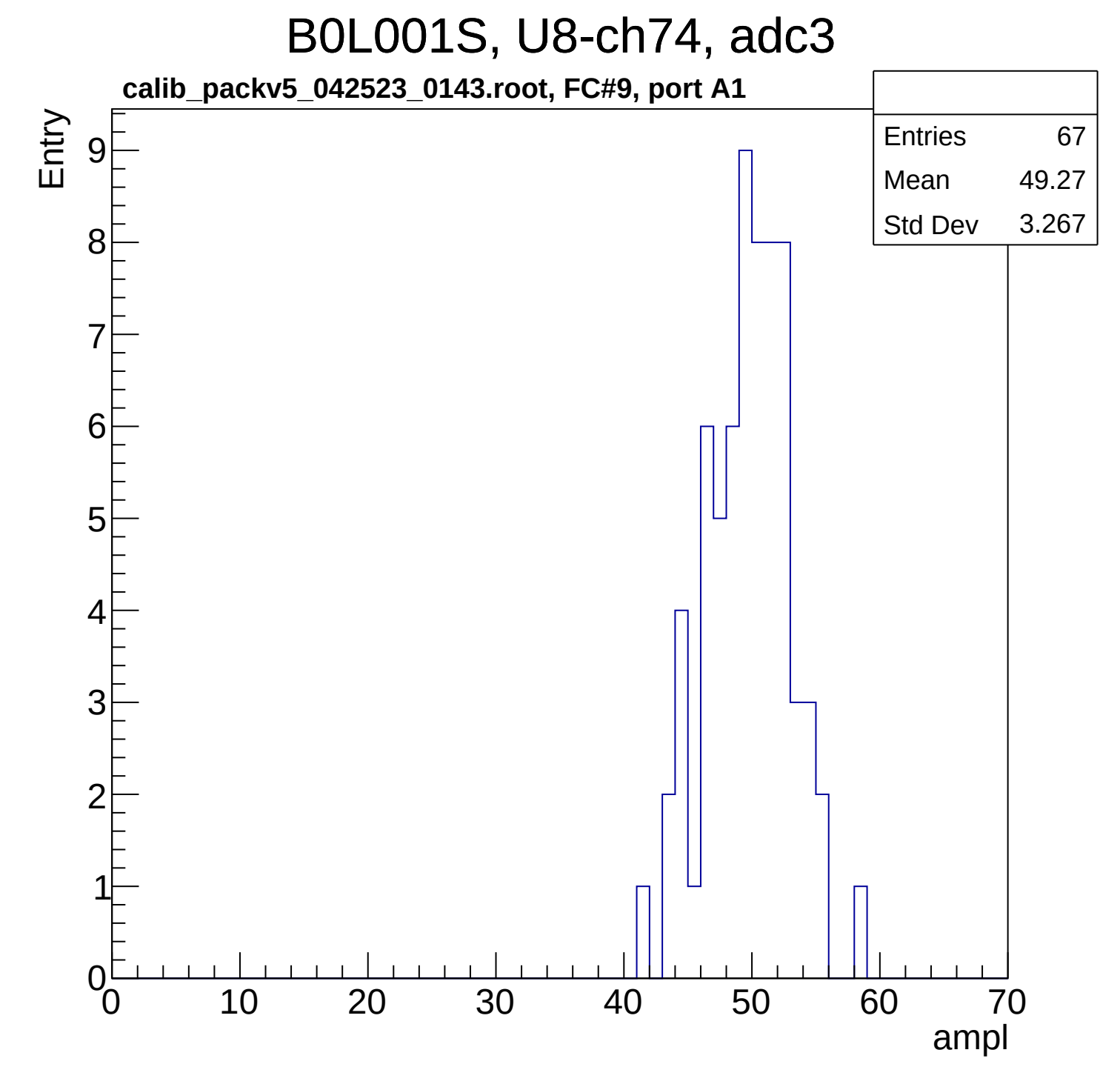
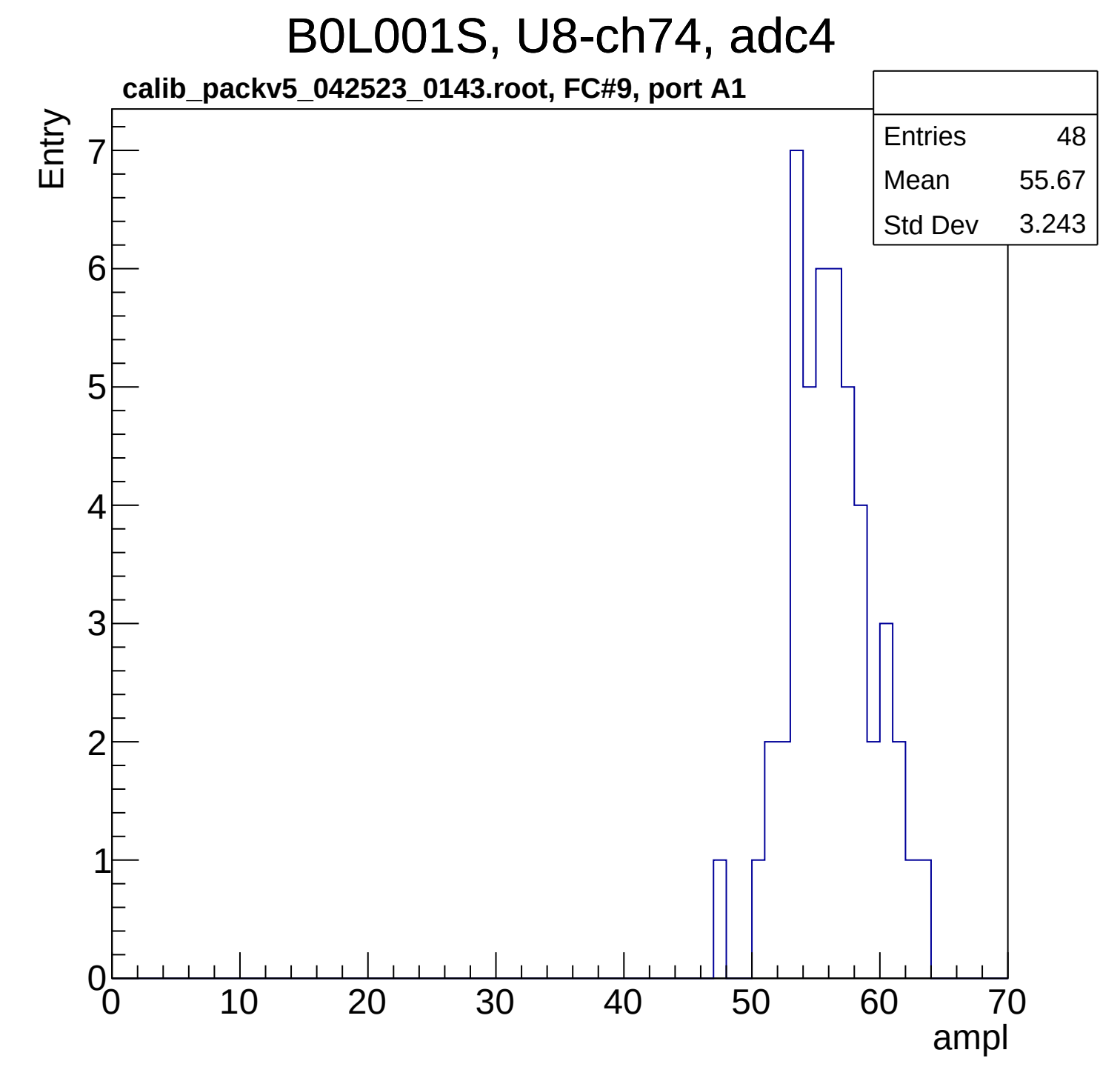

B0L001S, U8-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.97
Std Dev 3.409
Gaus mean : 43.6264
Gaus Width: 3.5625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.27
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.67
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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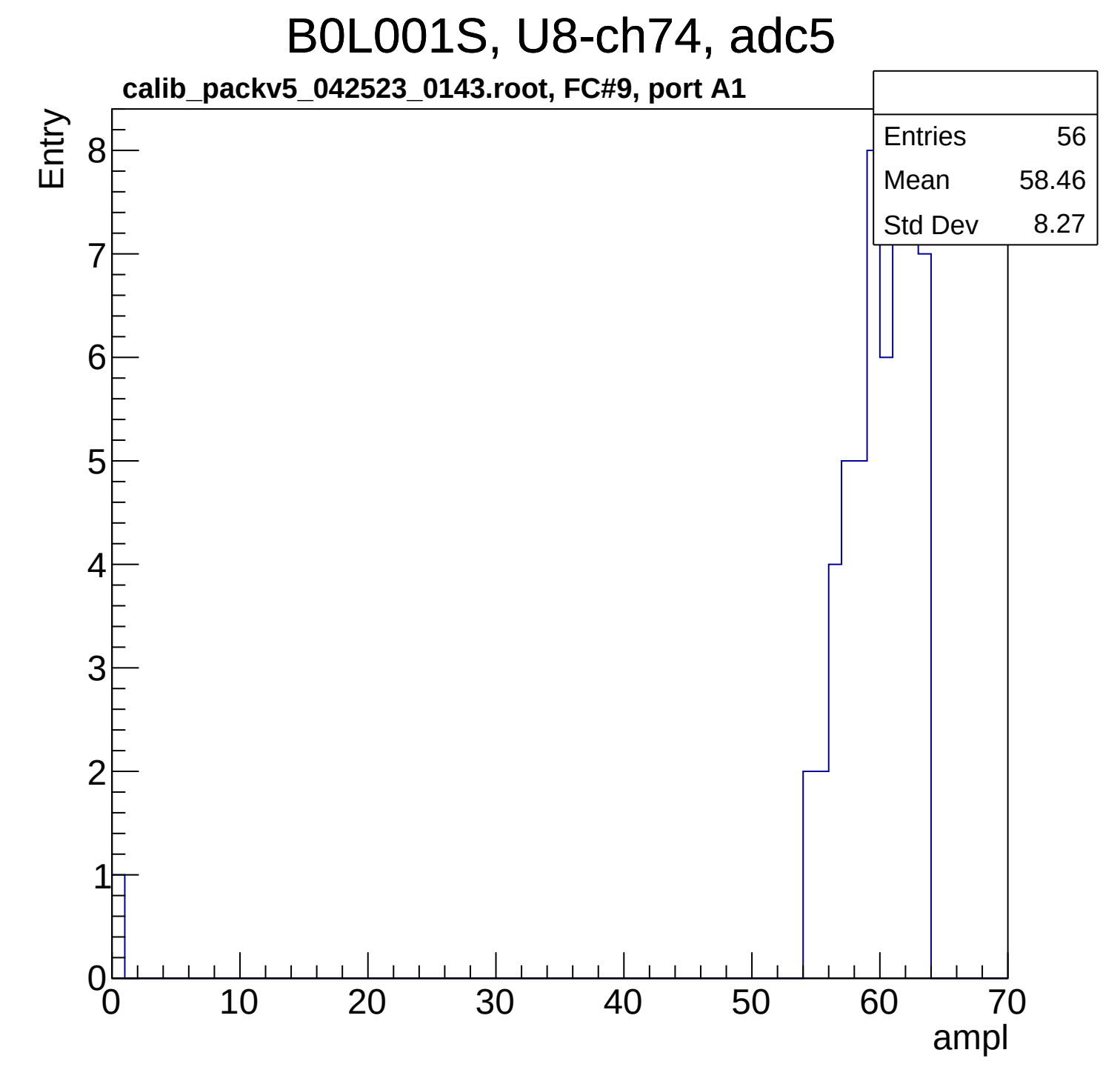
B0L001S, U8-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.46
Std Dev 8.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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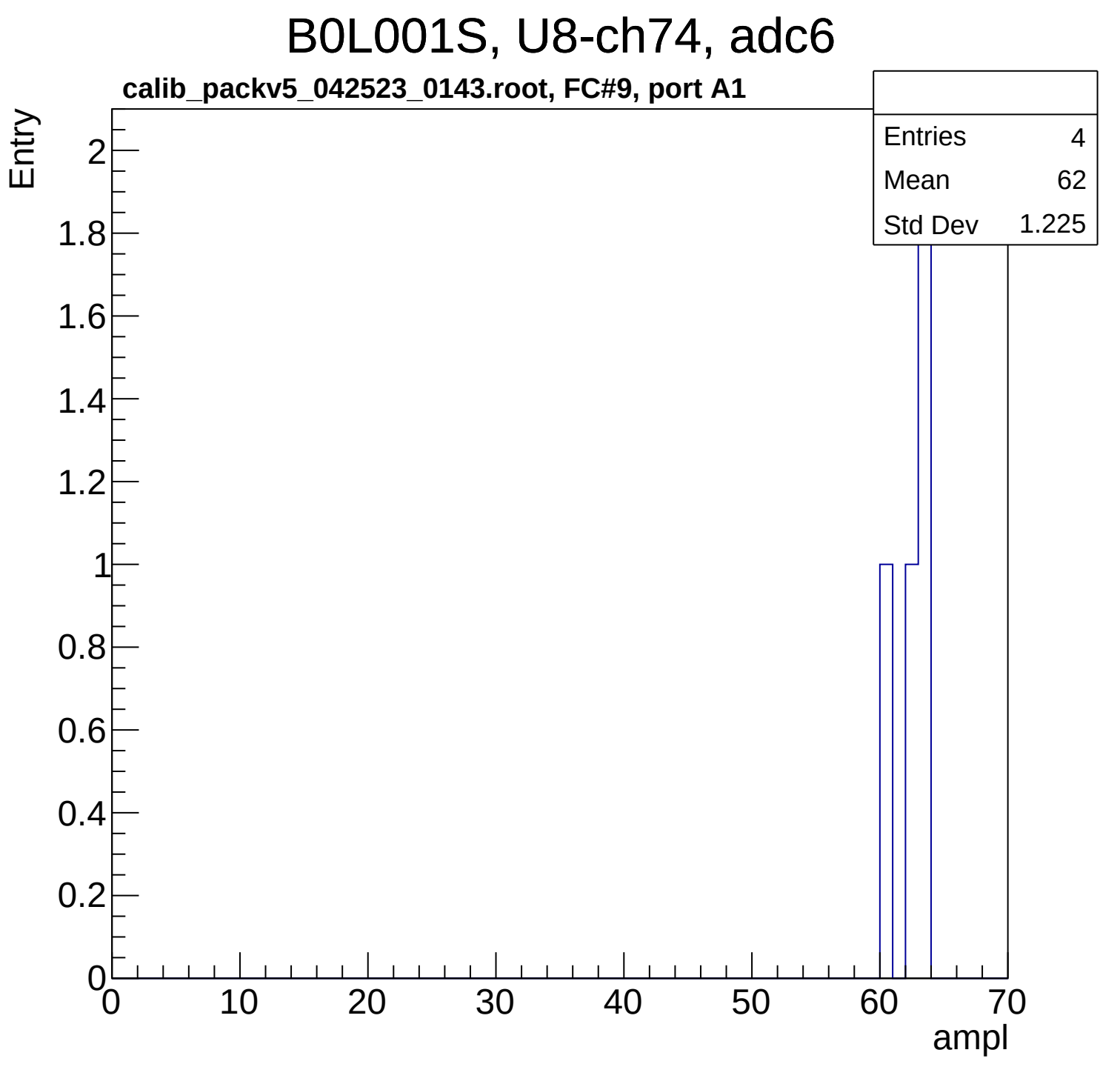
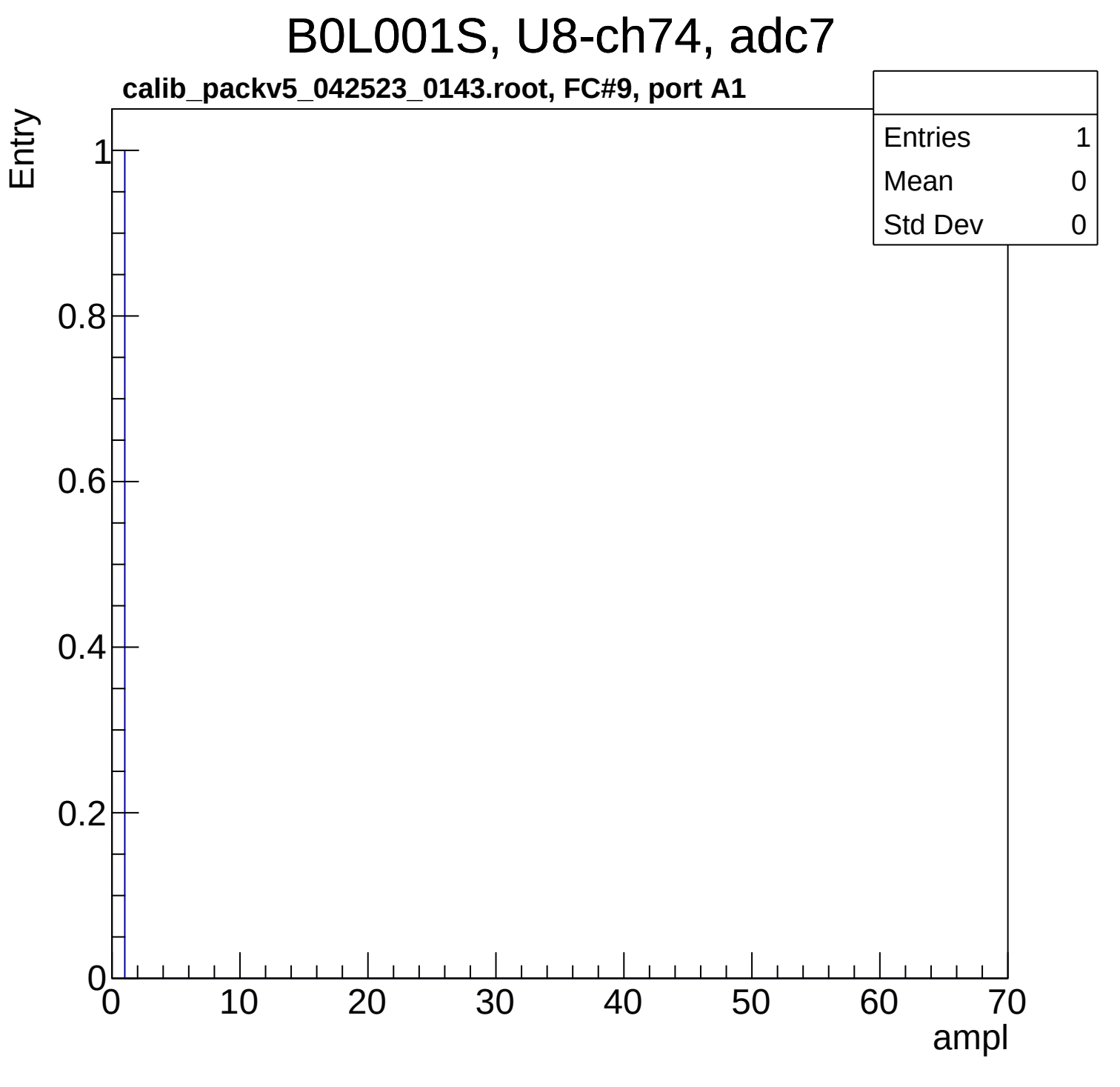
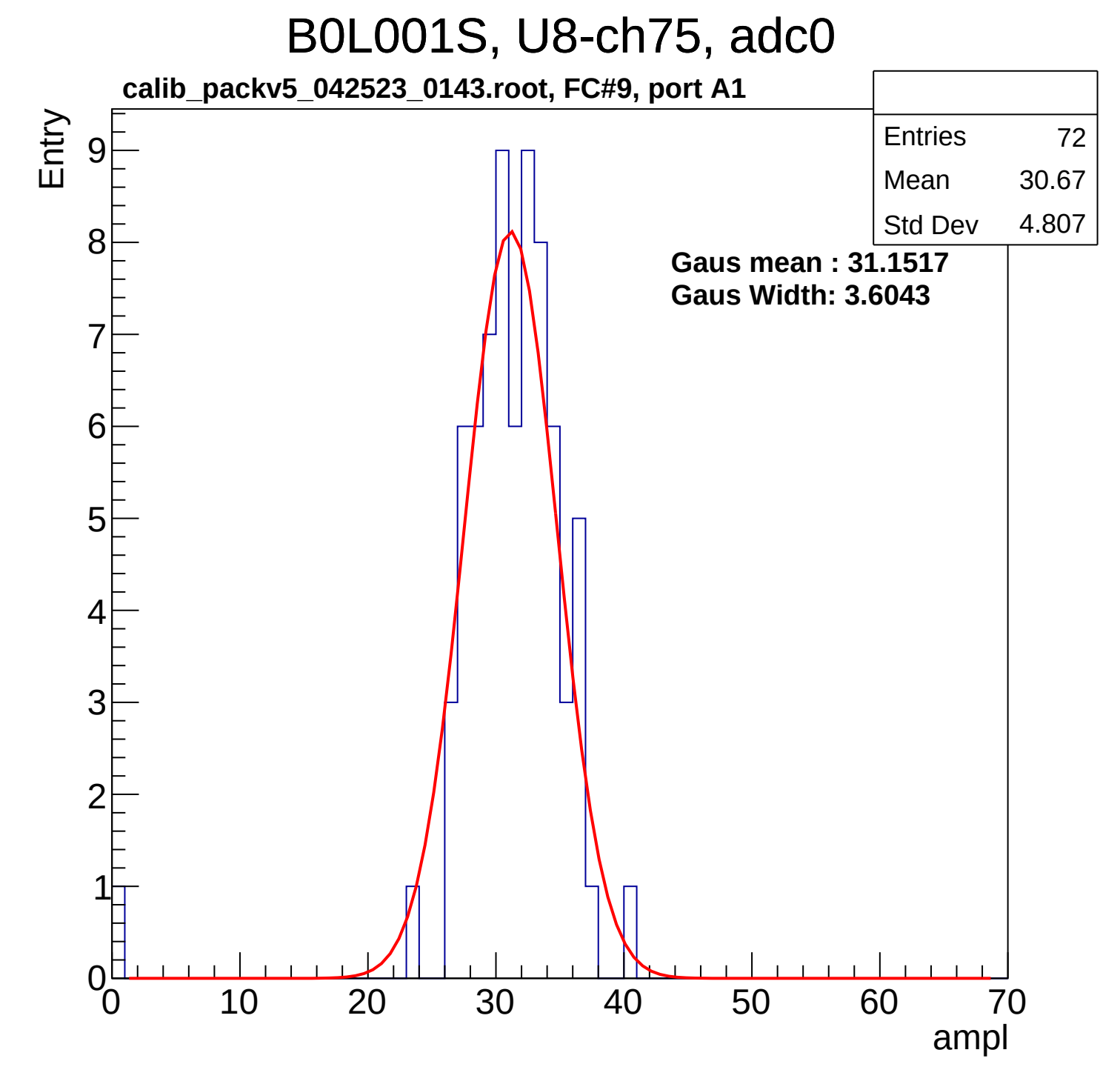
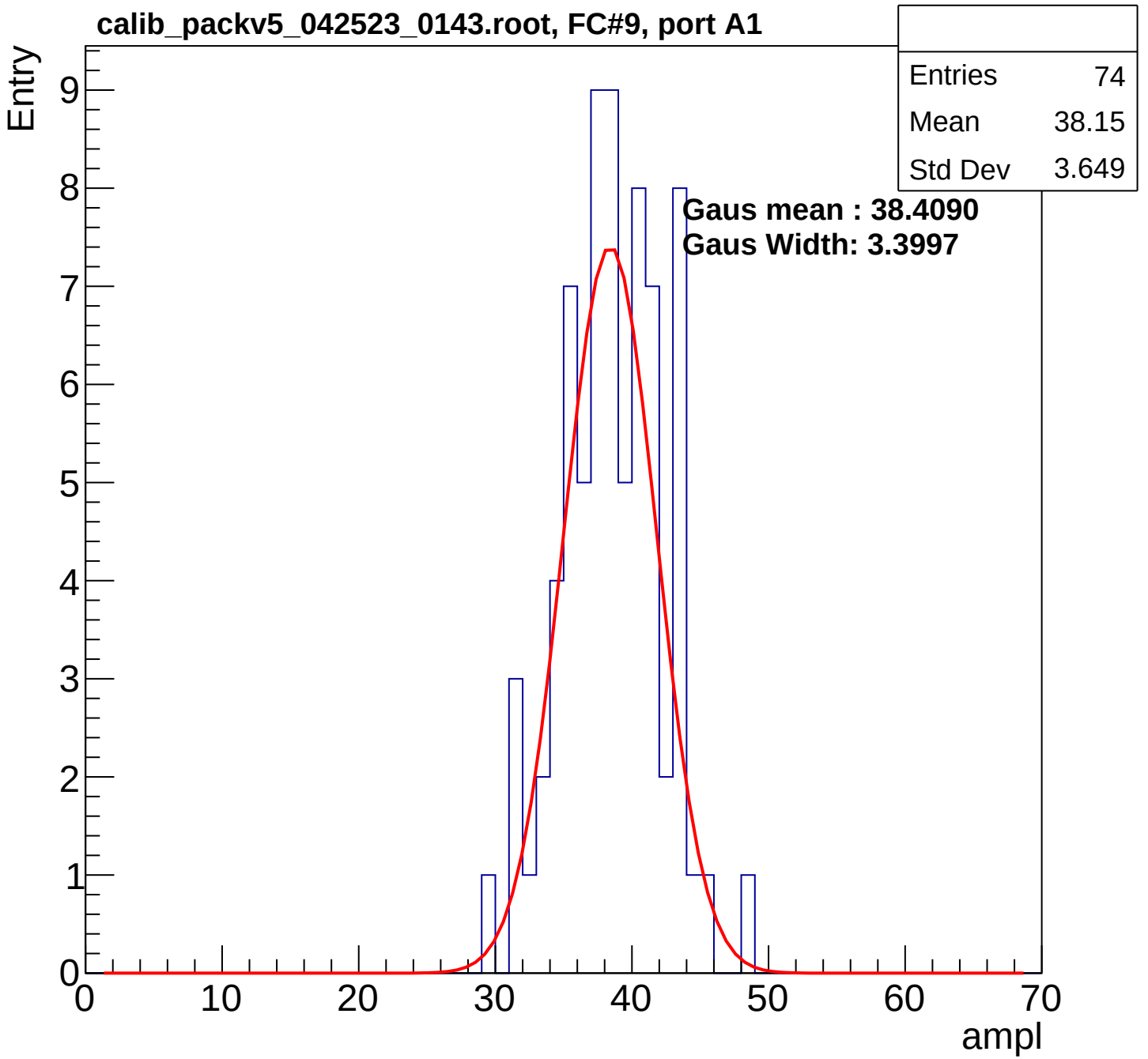
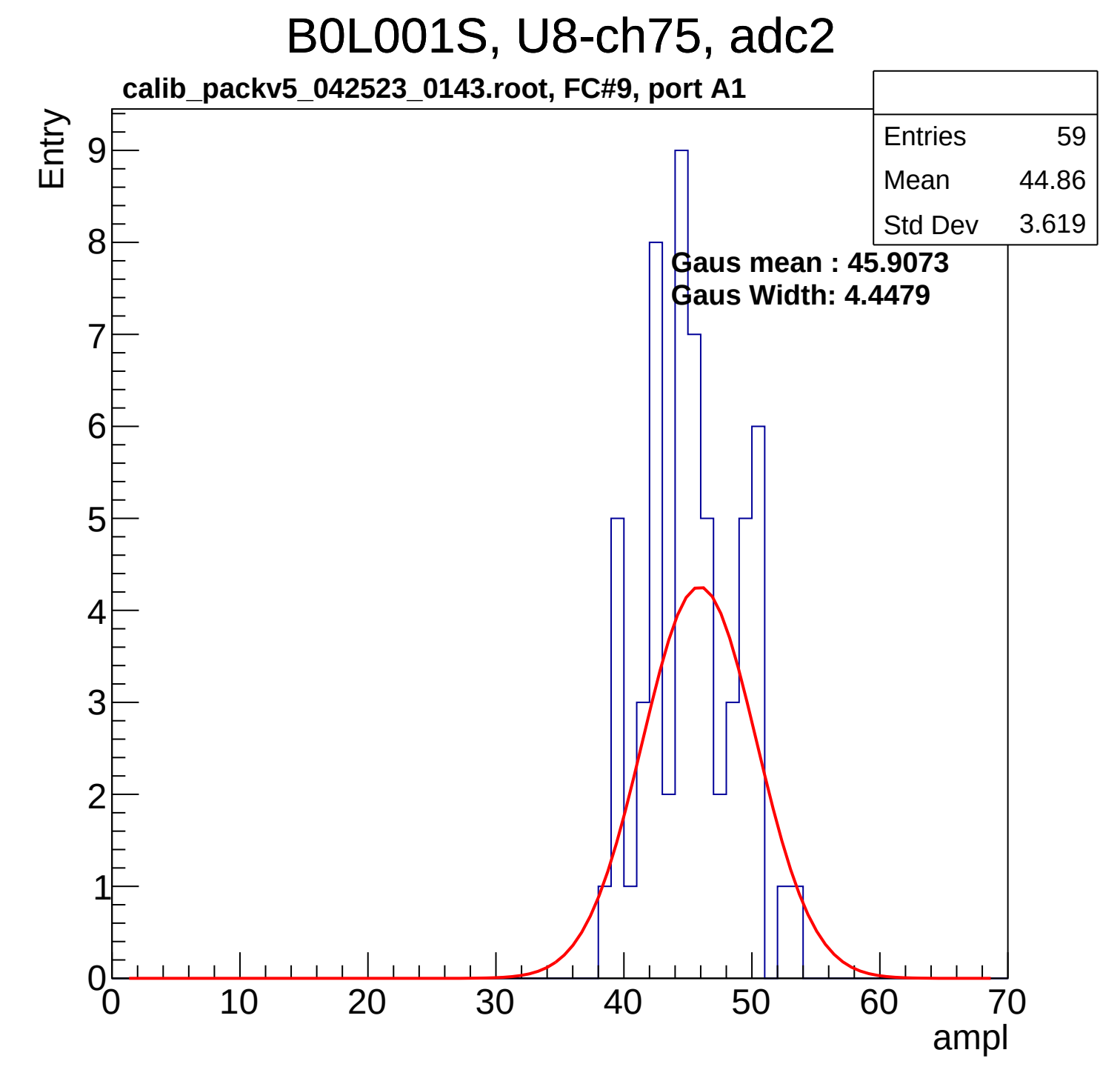

B0L001S, U8-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.67
Std Dev 4.807
Gaus mean : 31.1517
Gaus Width: 3.6043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 38.15
Std Dev 3.649
Gaus mean : 38.4090
Gaus Width: 3.3997
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U8-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.86
Std Dev 3.619
Gaus mean : 45.9073
Gaus Width: 4.4479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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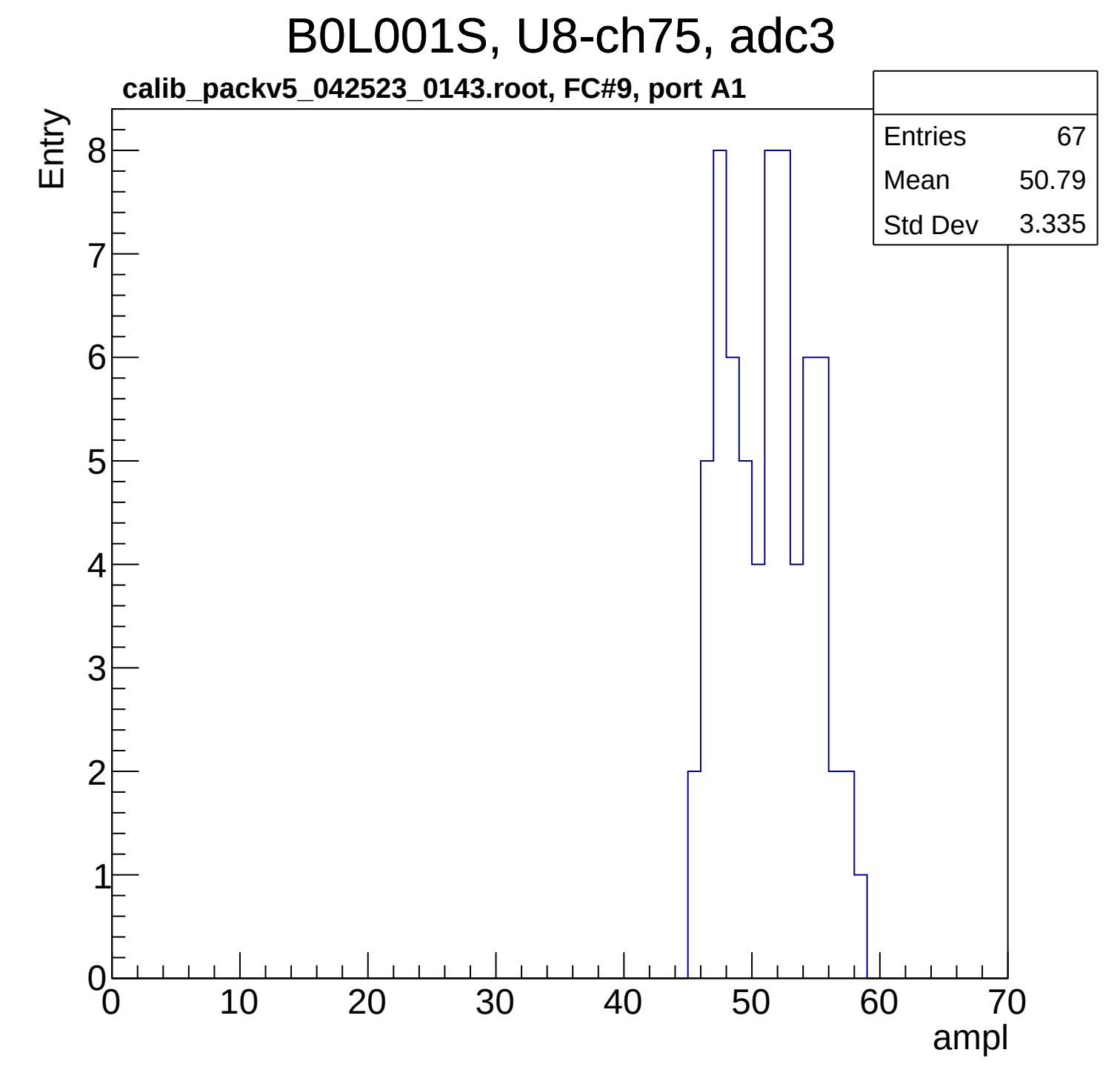
B0L001S, U8-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.79
Std Dev 3.335
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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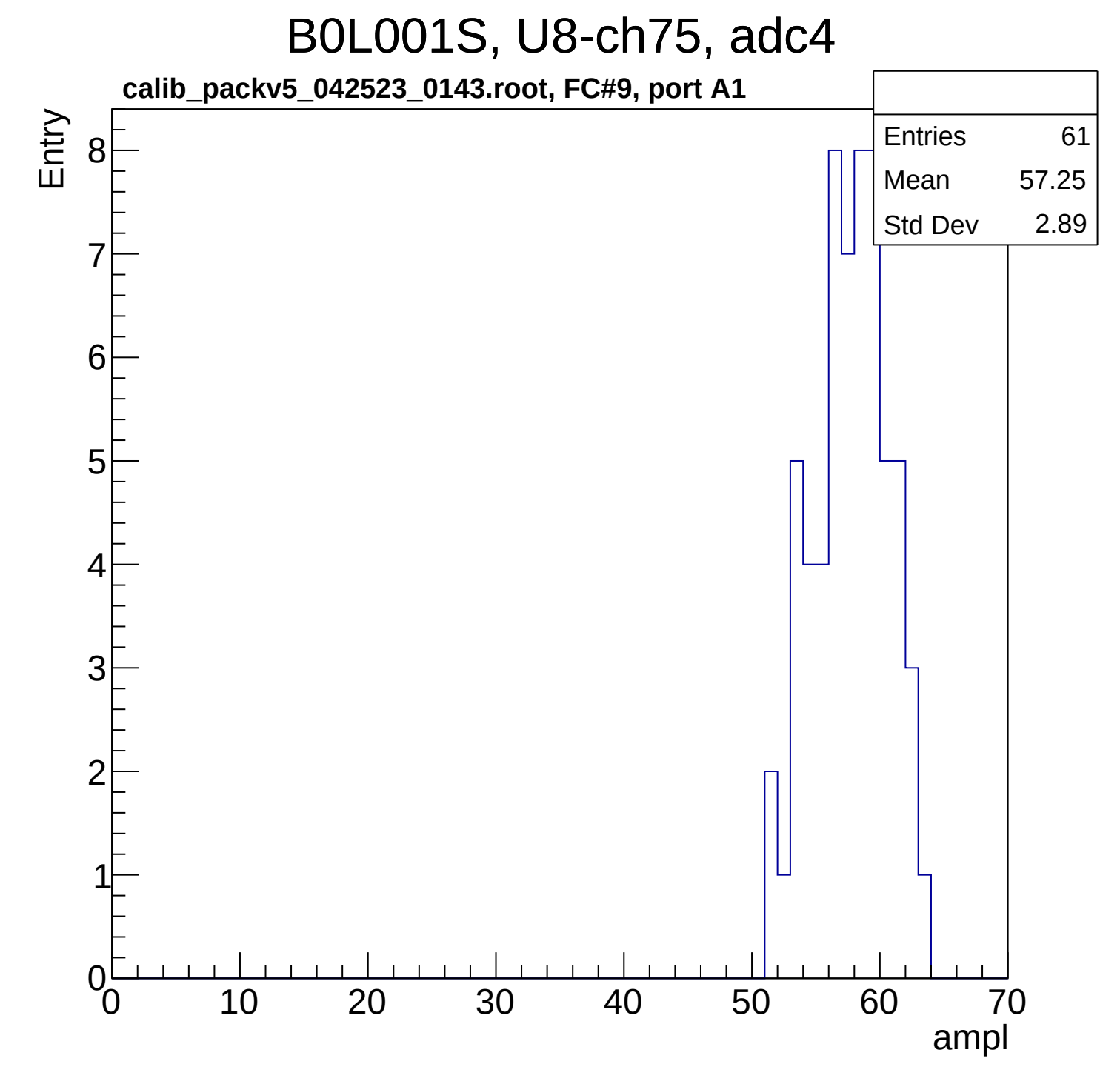
40
50
60
70


B0L001S, U8-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.25
Std Dev 2.89
Entry
ampl
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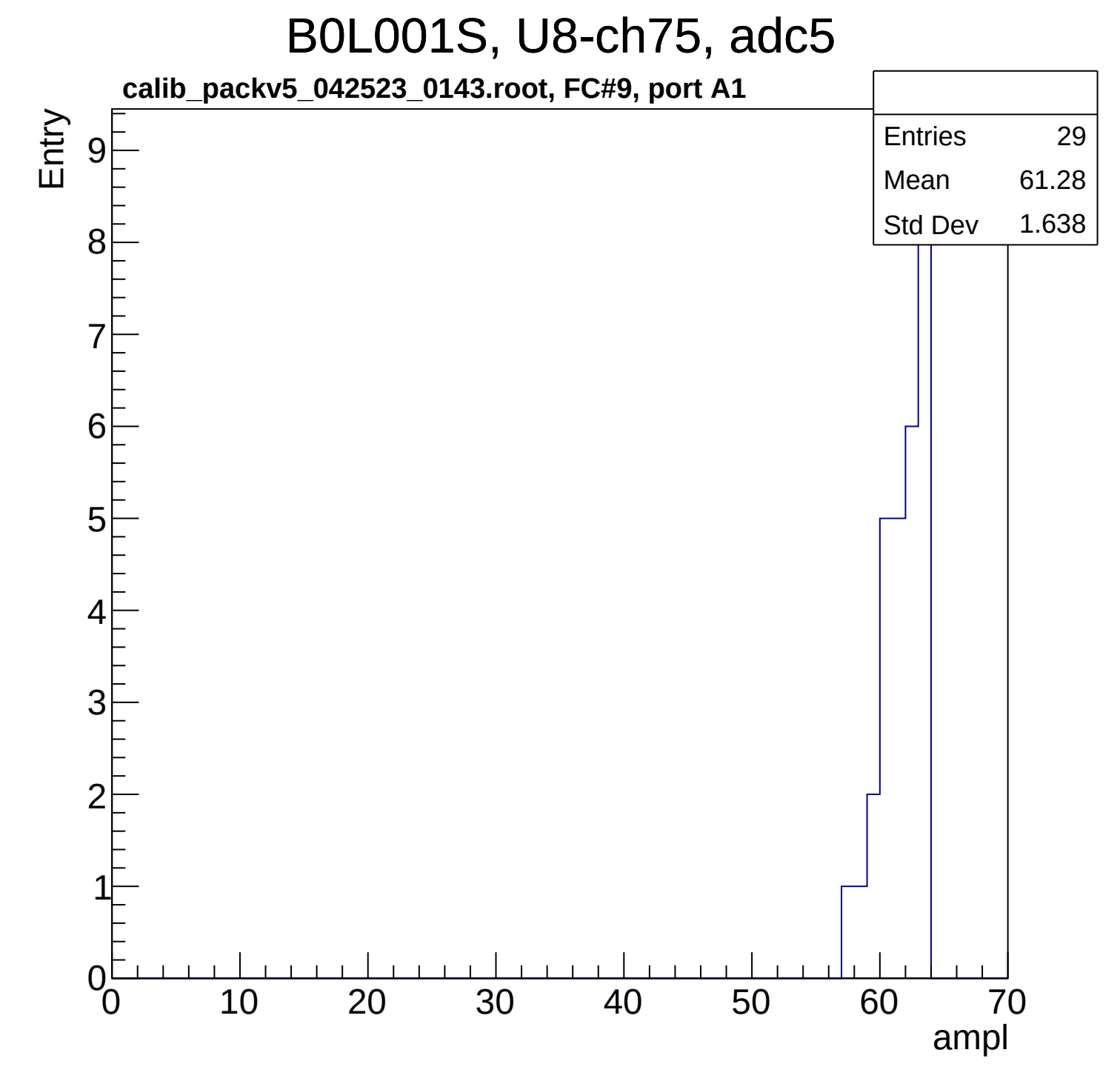
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 61.28
Std Dev 1.638
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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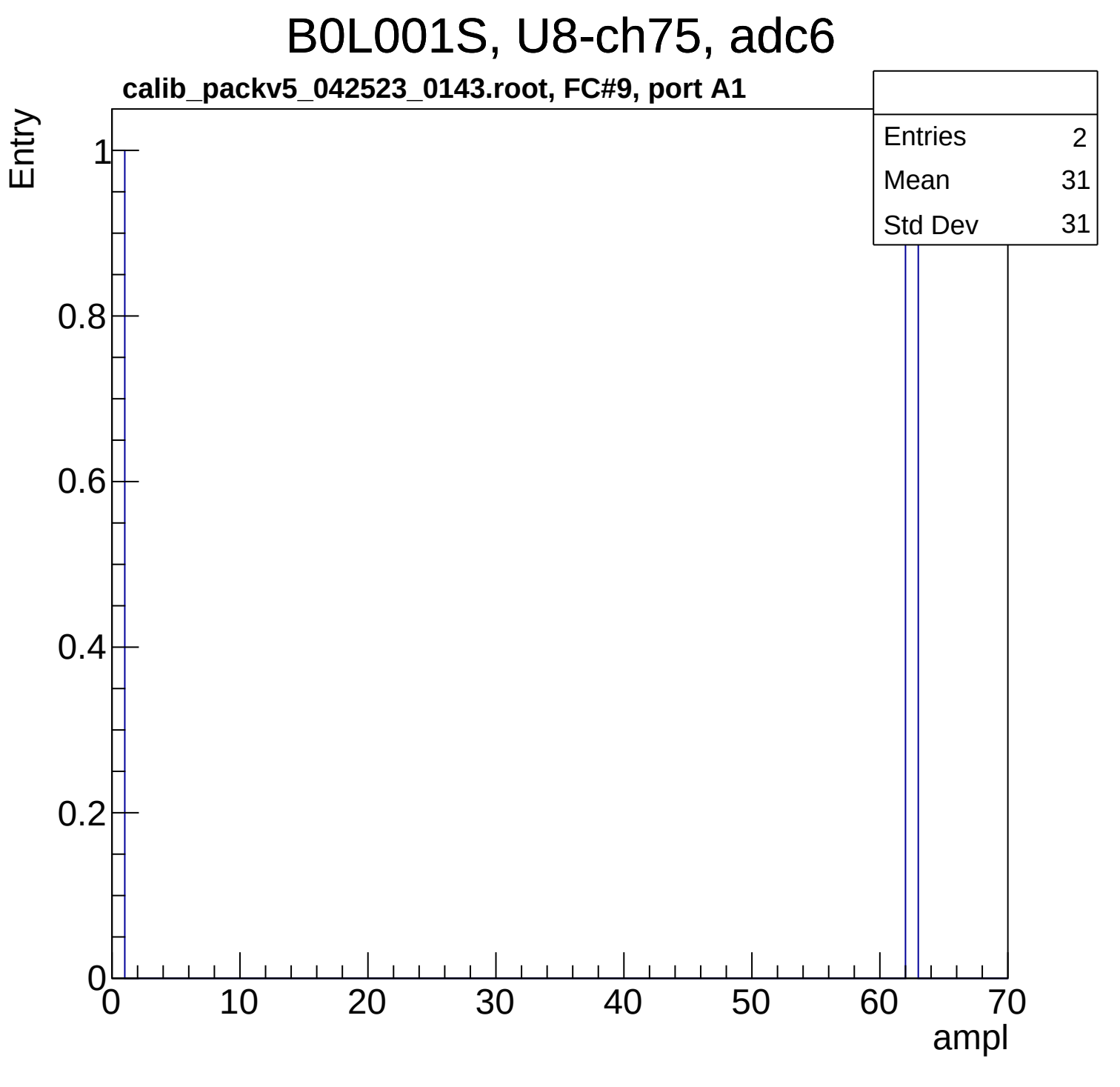
40
50
60
70


B0L001S, U8-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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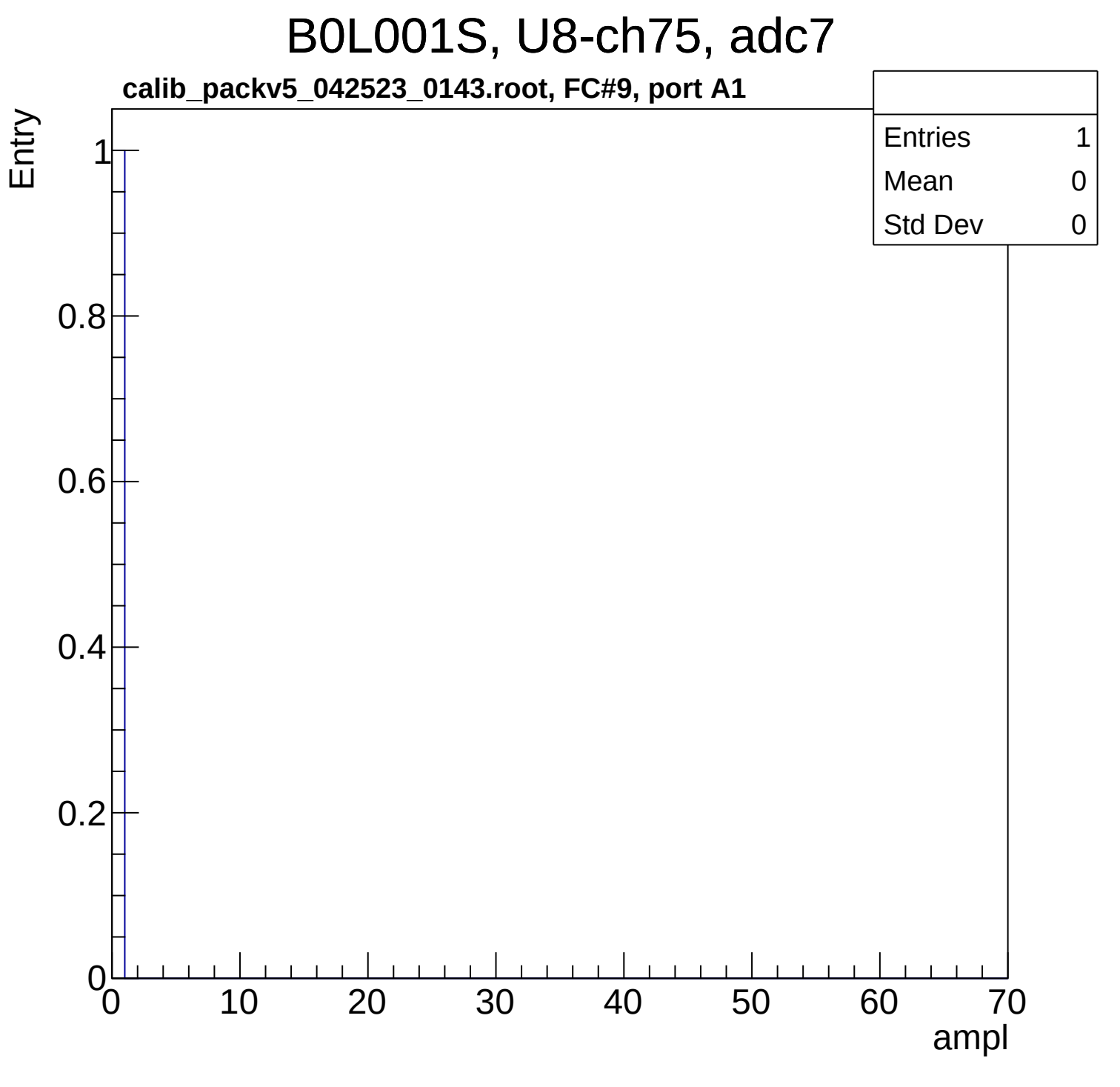
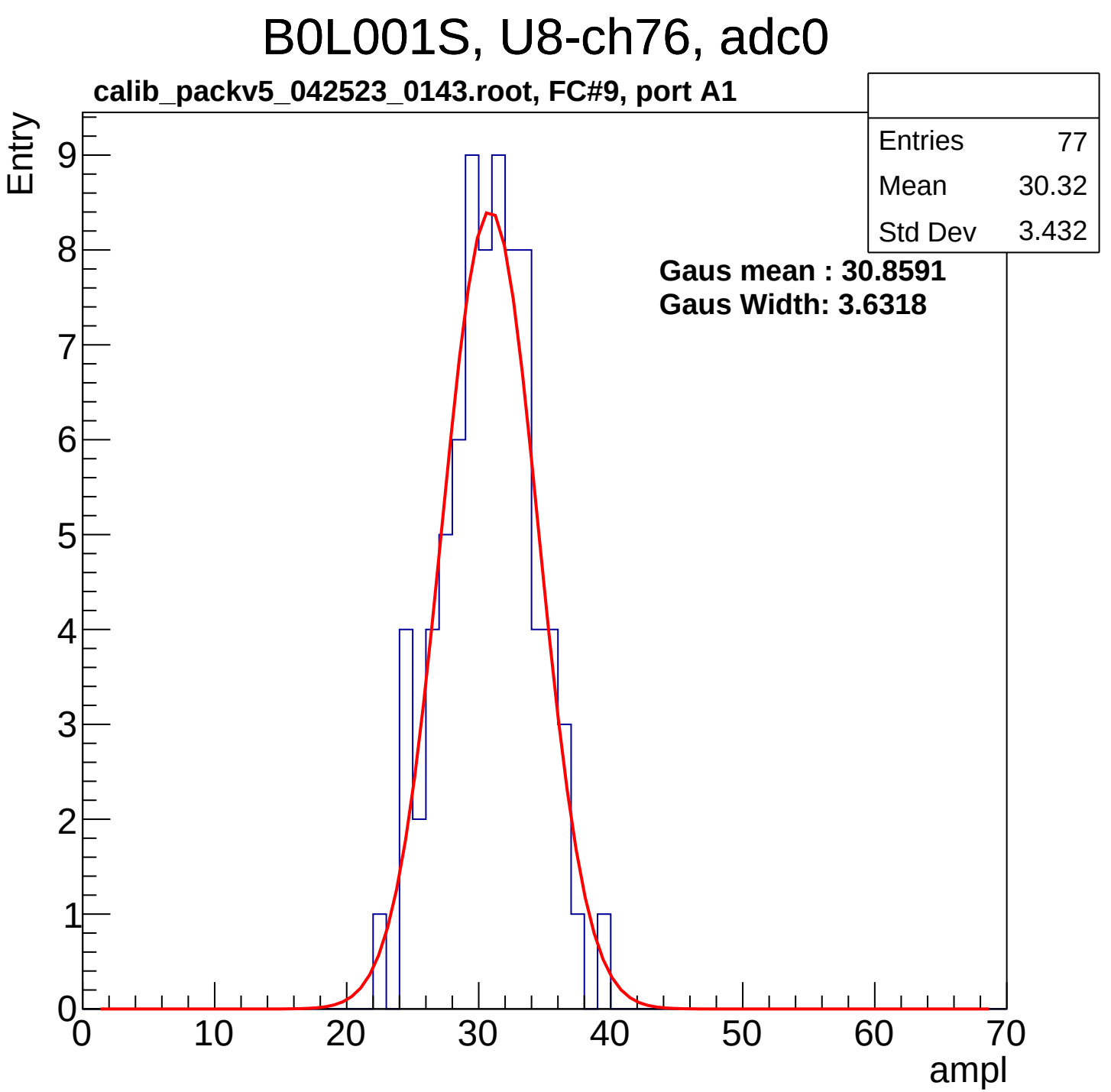
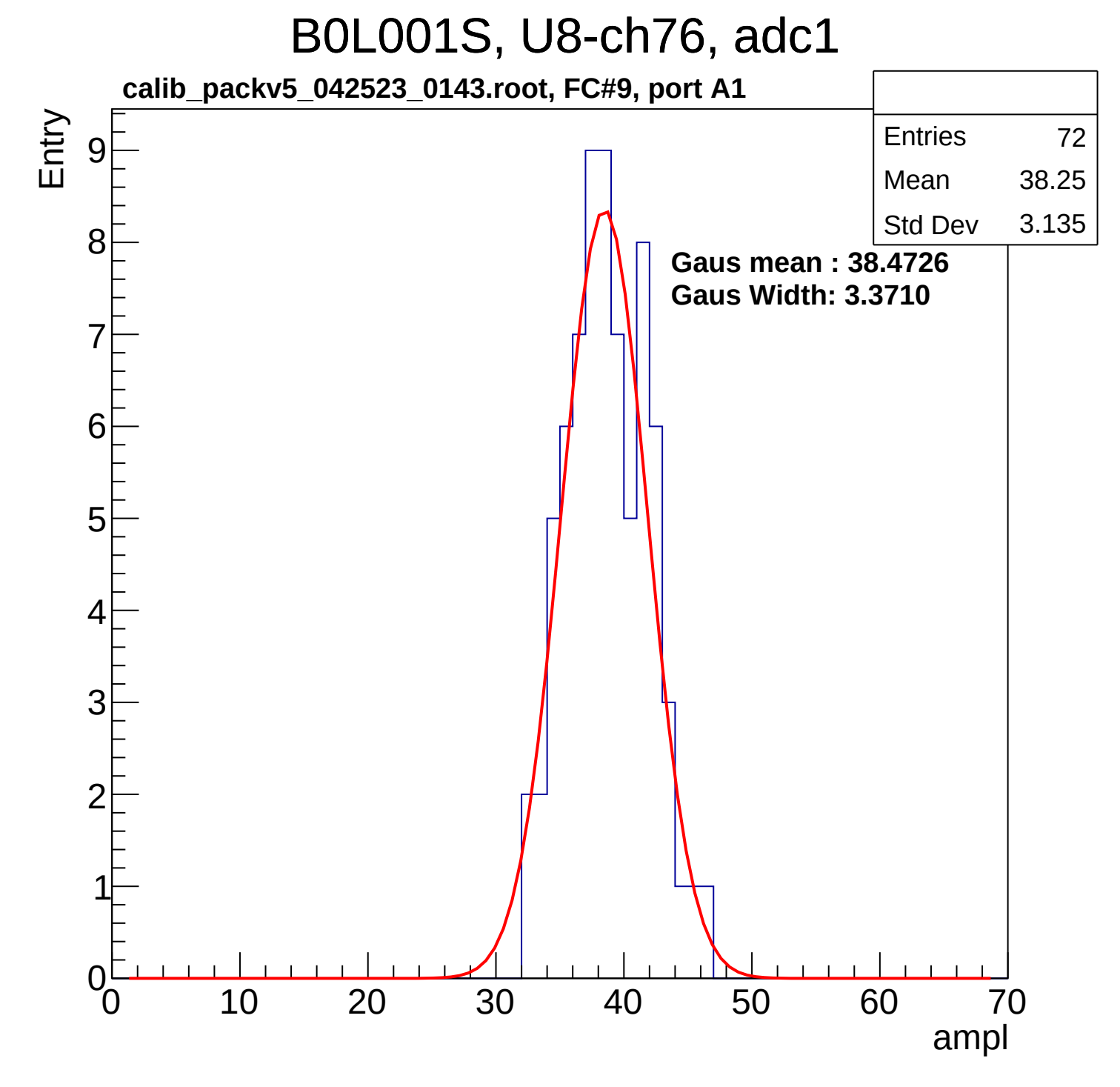
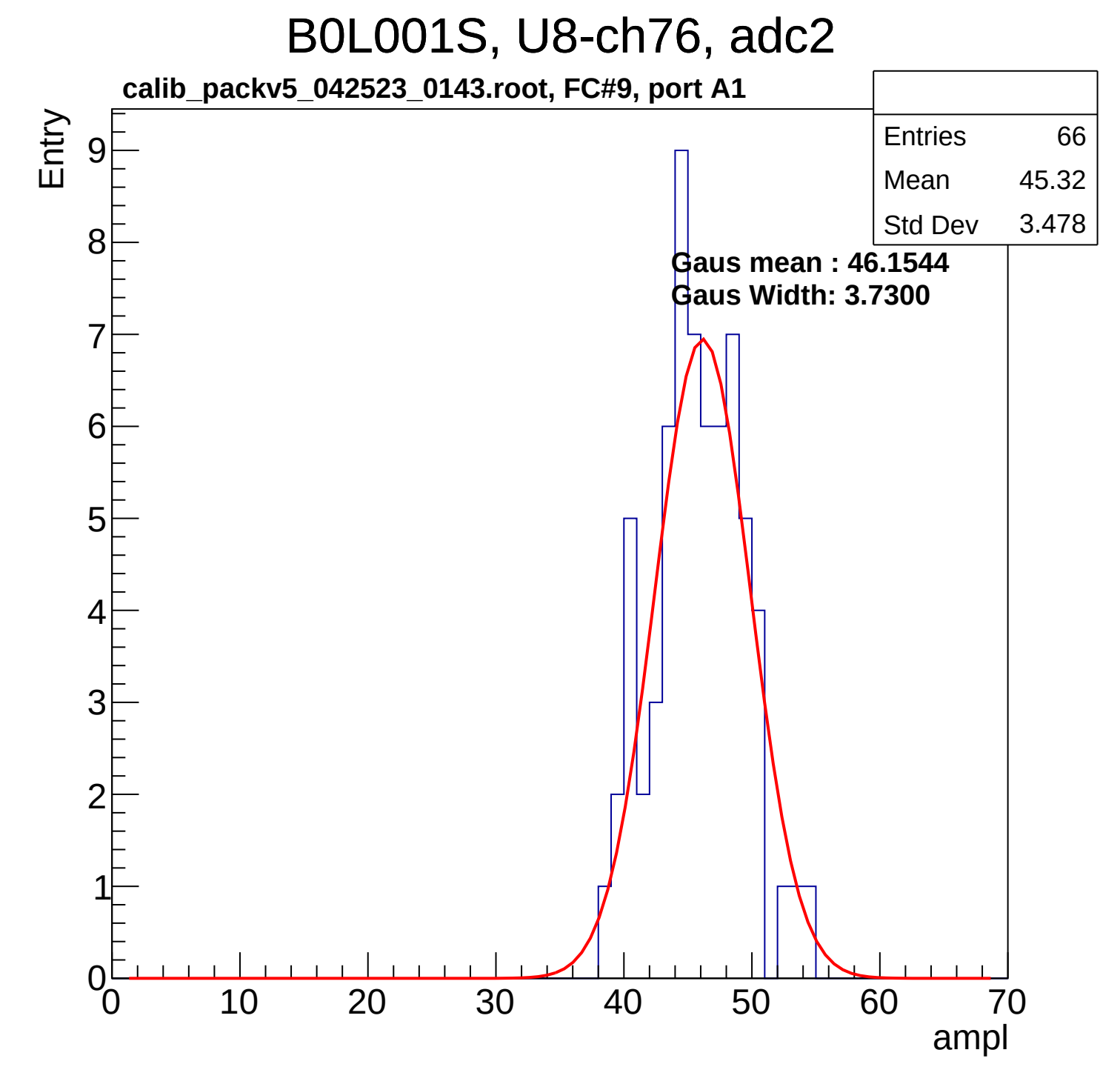
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.32
Std Dev 3.432
Gaus mean : 30.8591
Gaus Width: 3.6318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.25
Std Dev 3.135
Gaus mean : 38.4726
Gaus Width: 3.3710
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 45.32
Std Dev 3.478
Gaus mean : 46.1544
Gaus Width: 3.7300
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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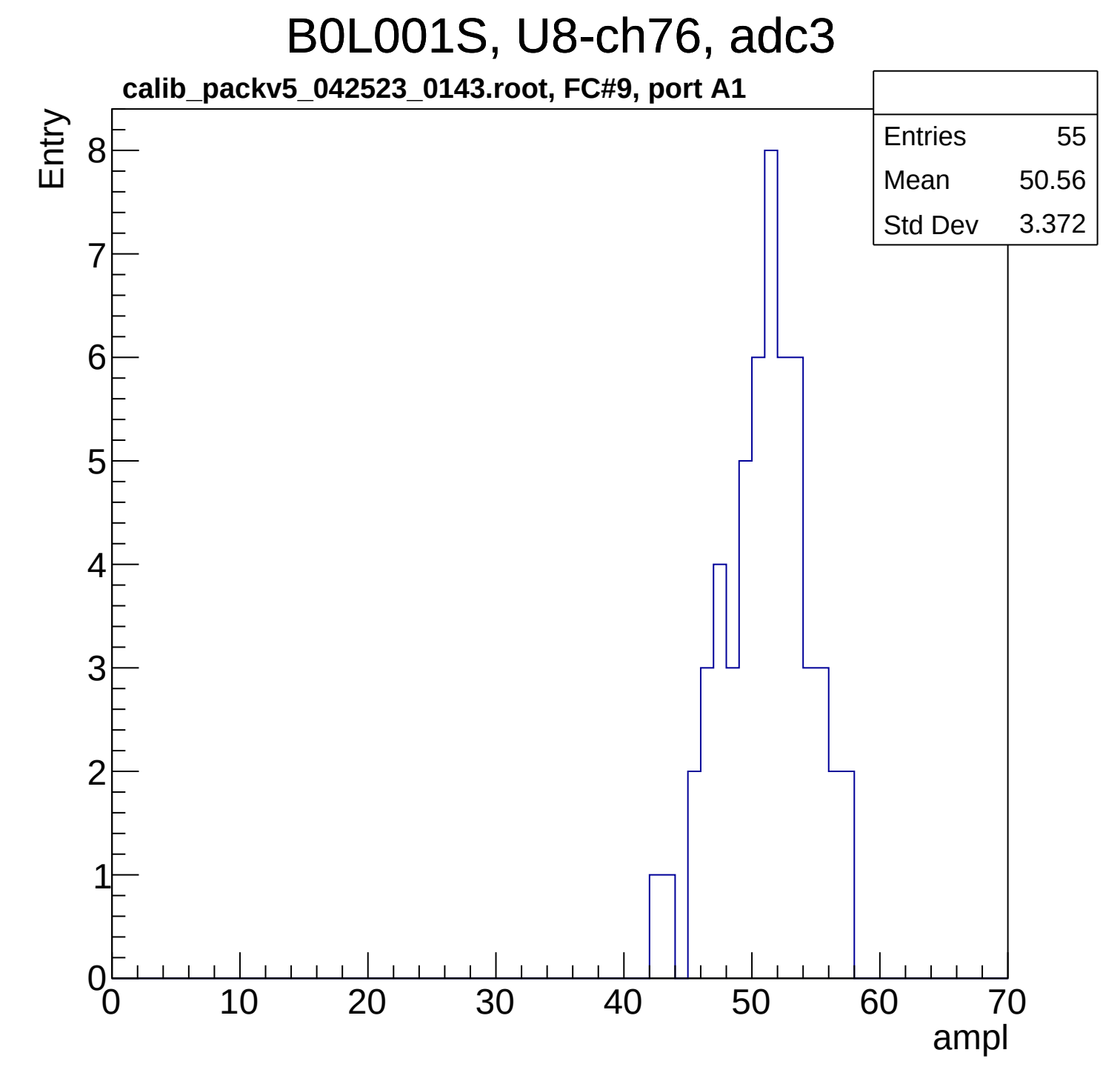
B0L001S, U8-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.56
Std Dev 3.372
Entry
ampl
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3
4
5
6
7
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10
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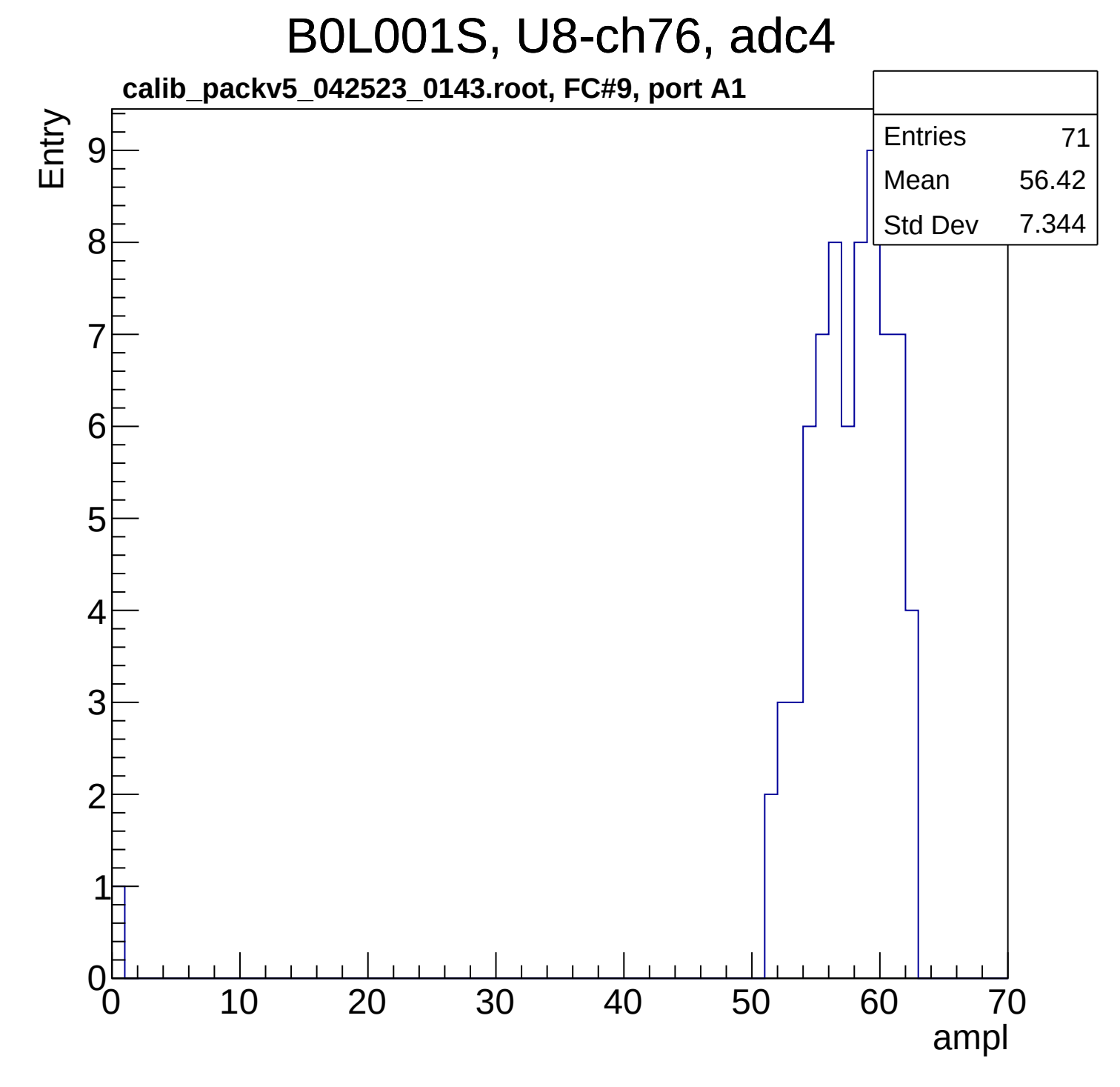
40
50
60
70


B0L001S, U8-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 56.42
Std Dev 7.344
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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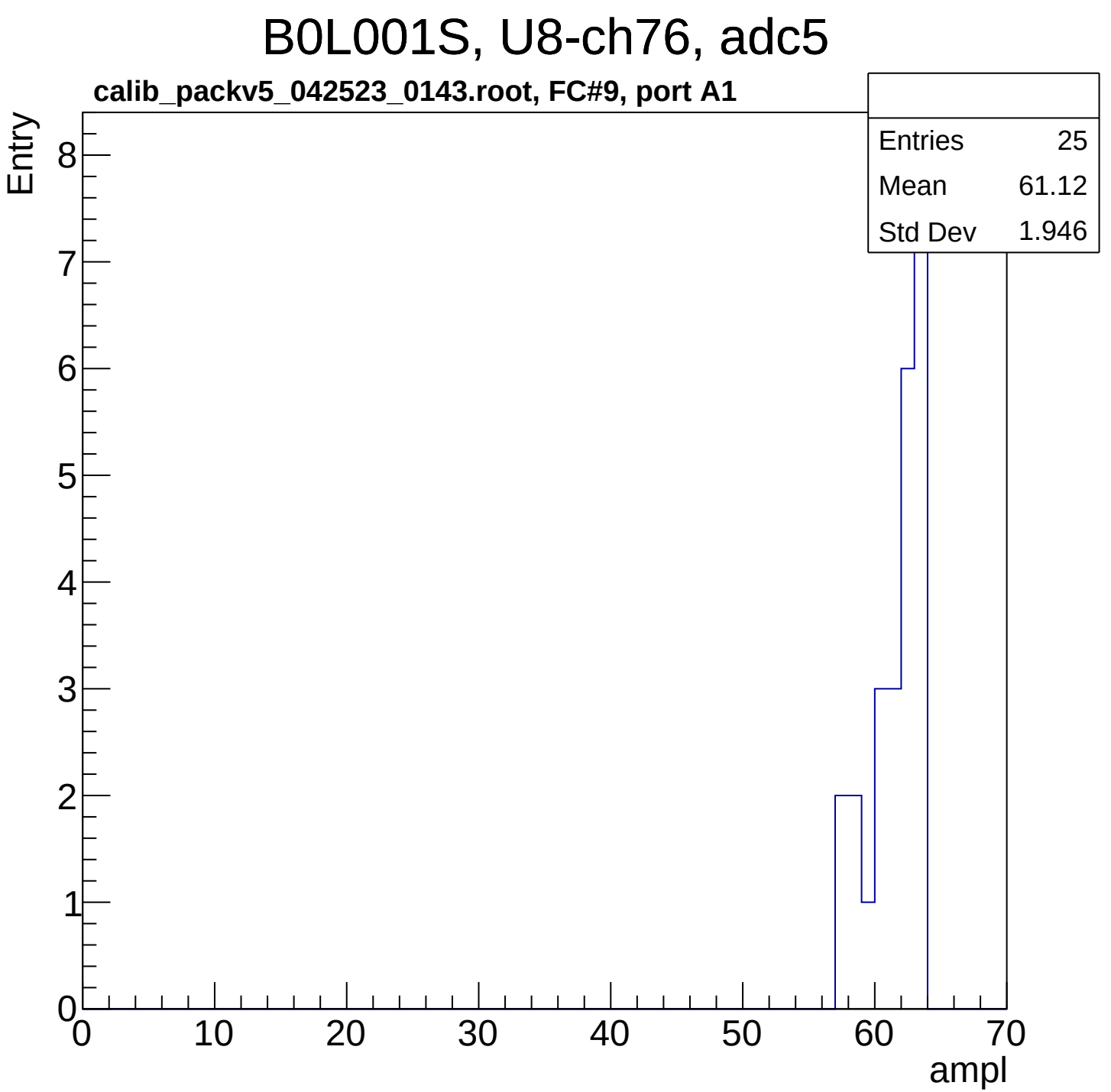
40
50
60
70


B0L001S, U8-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 61.12
Std Dev 1.946
Entry
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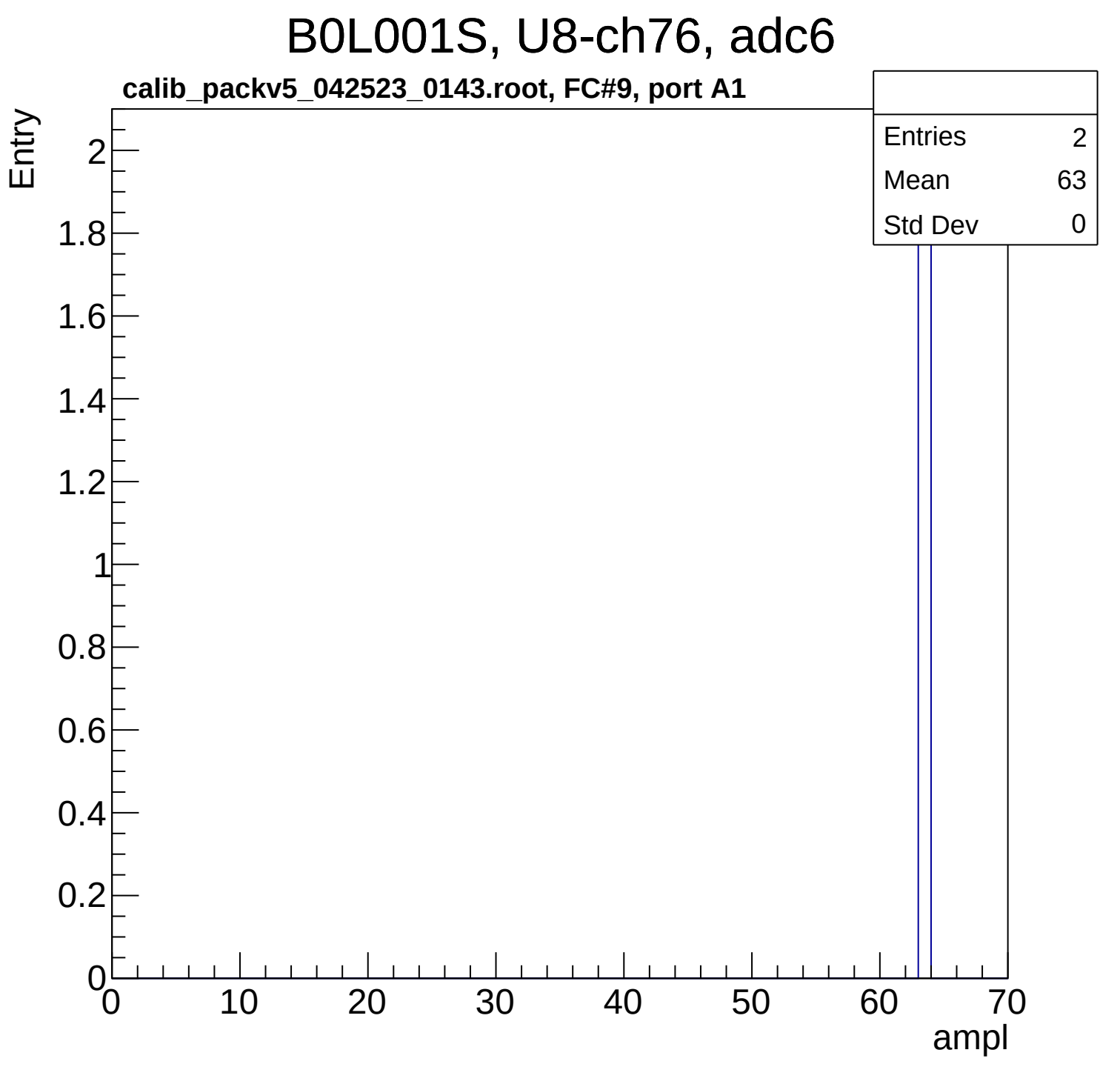
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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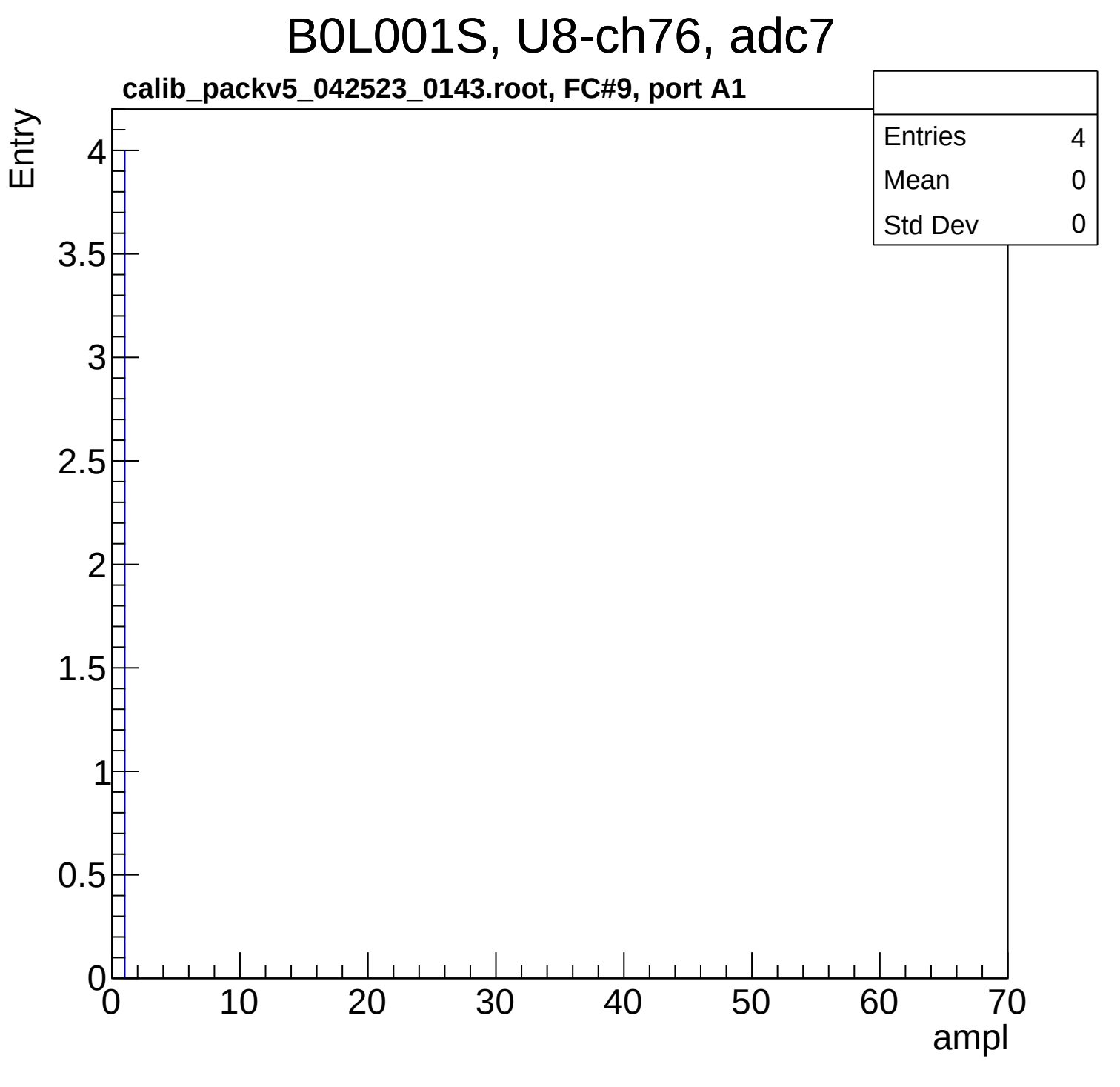
B0L001S, U8-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
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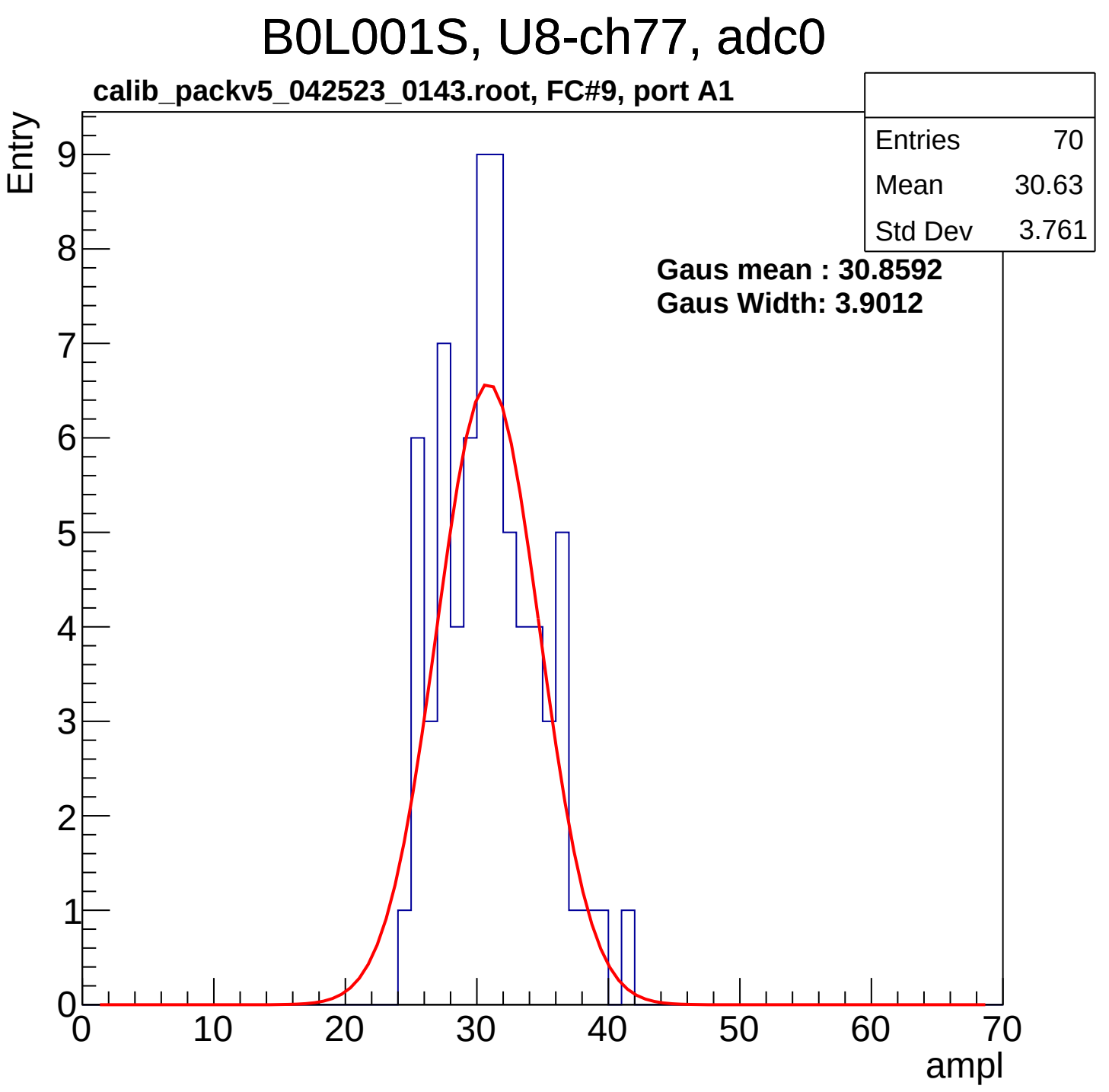
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 30.63
Std Dev 3.761
Gaus mean : 30.8592
Gaus Width: 3.9012
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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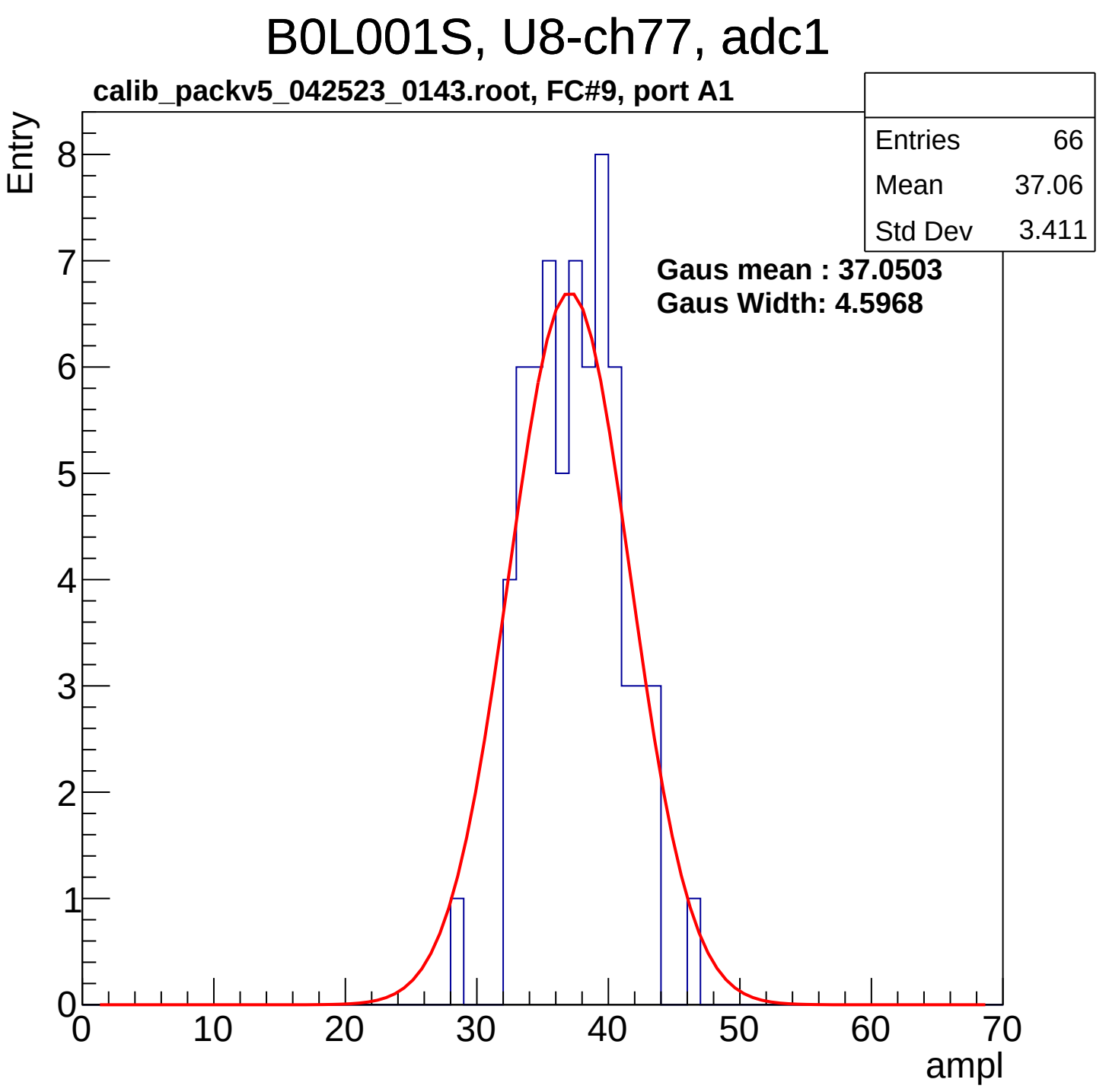
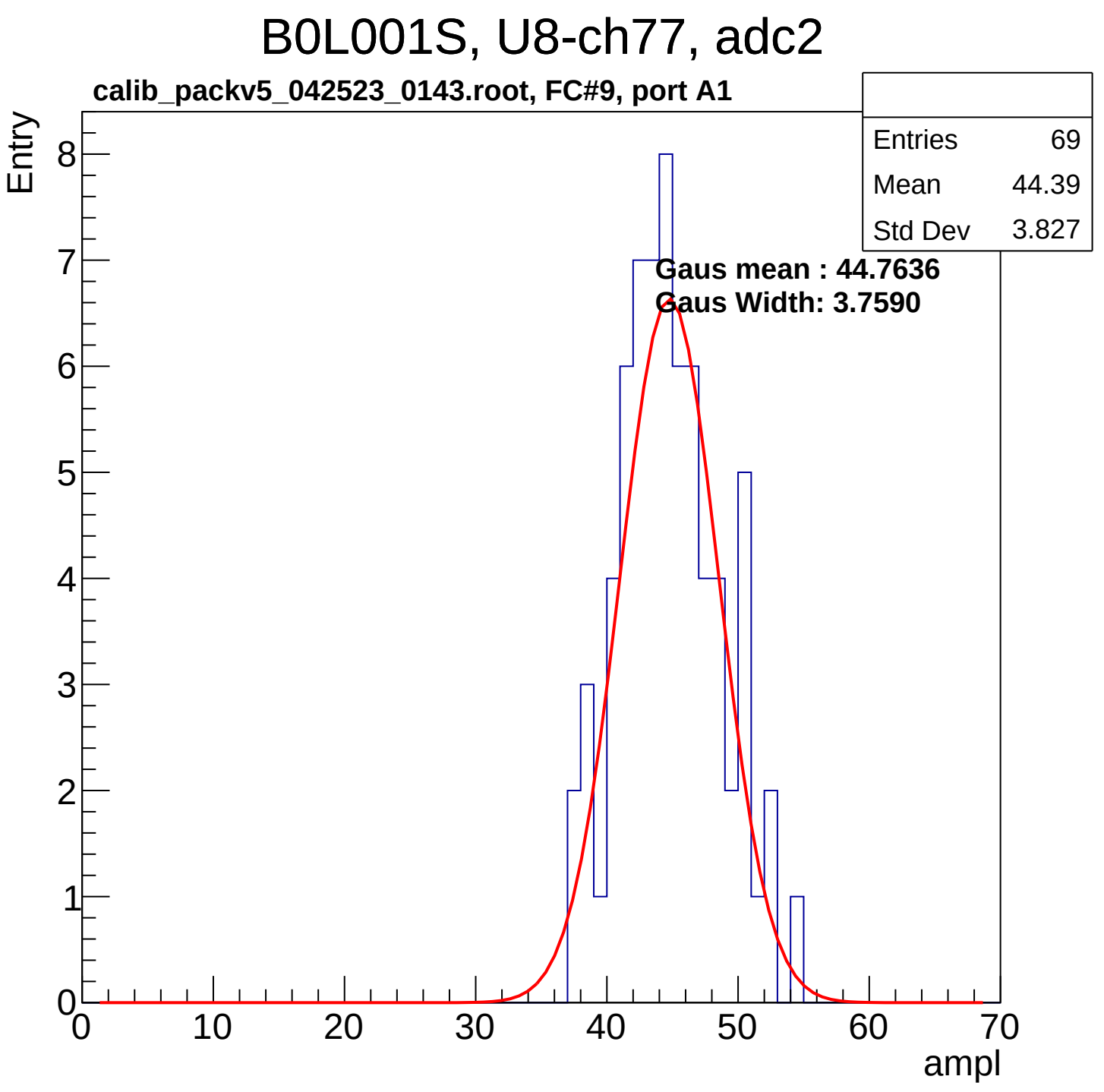

B0L001S, U8-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.06
Std Dev 3.411
Gaus mean : 37.0503
Gaus Width: 4.5968
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.39
Std Dev 3.827
Gaus mean : 44.7636
Gaus Width: 3.7590
Entry
ampl
0
1
2
3
4
5
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7
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0
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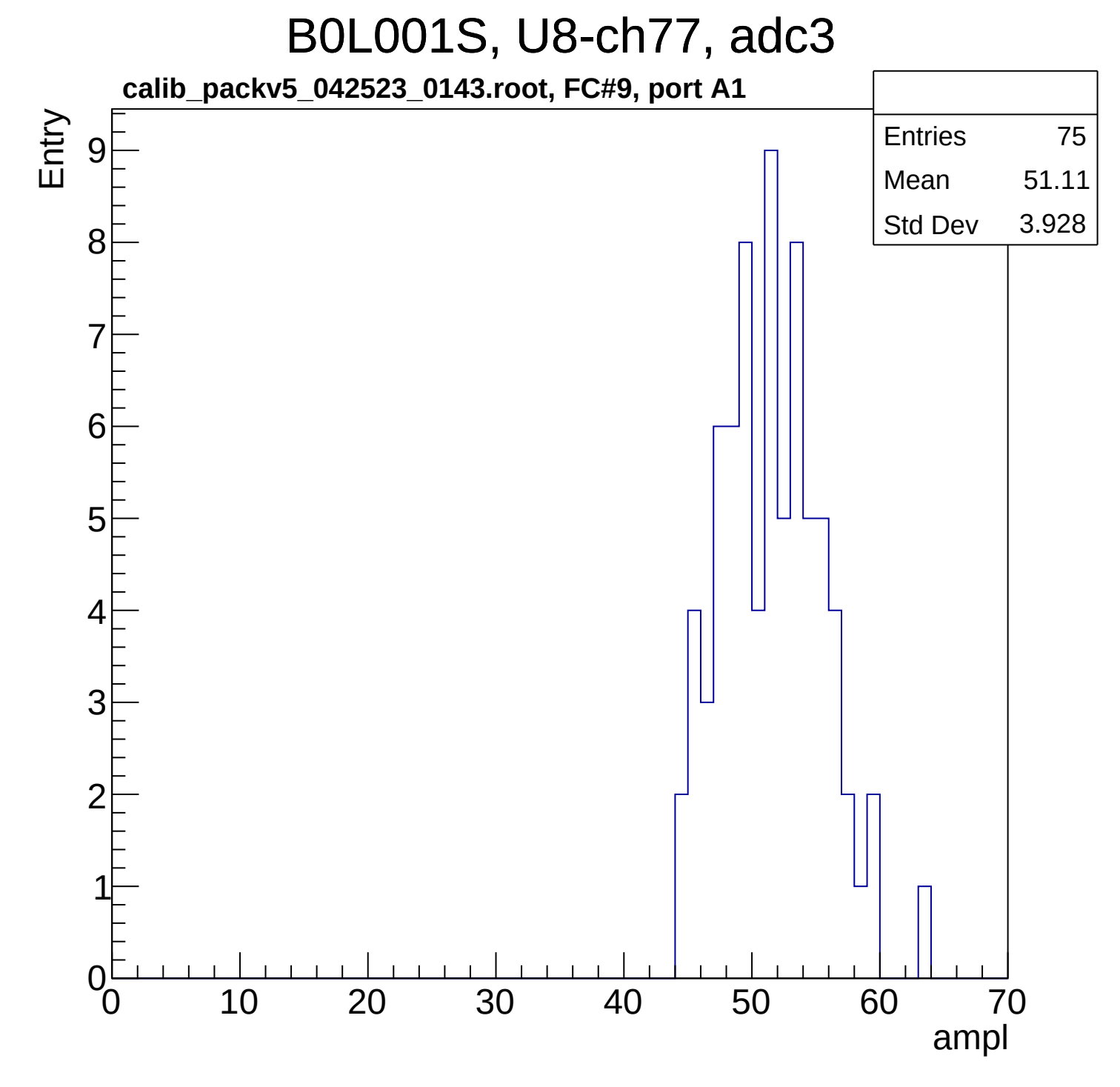
B0L001S, U8-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.11
Std Dev 3.928
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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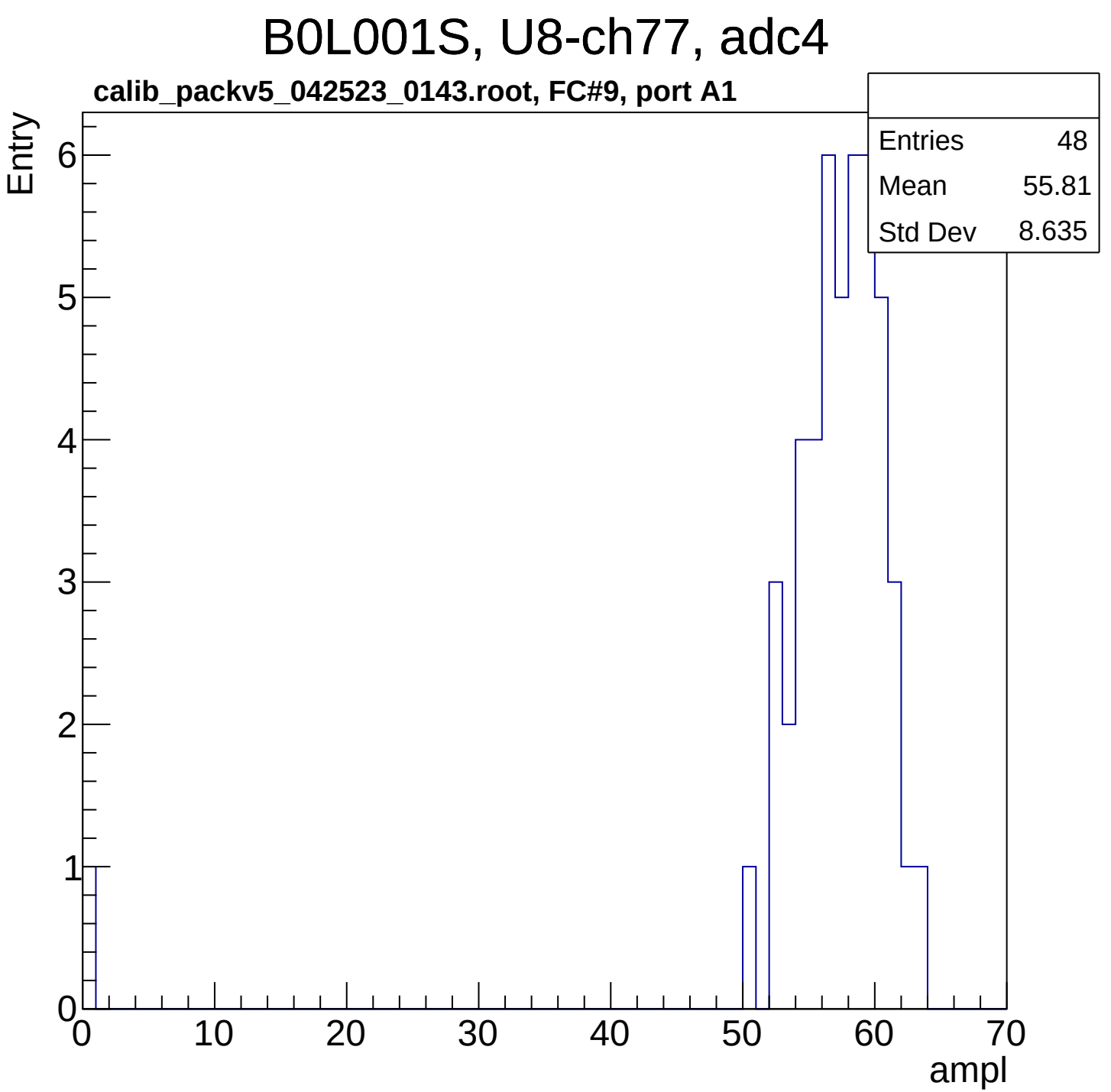
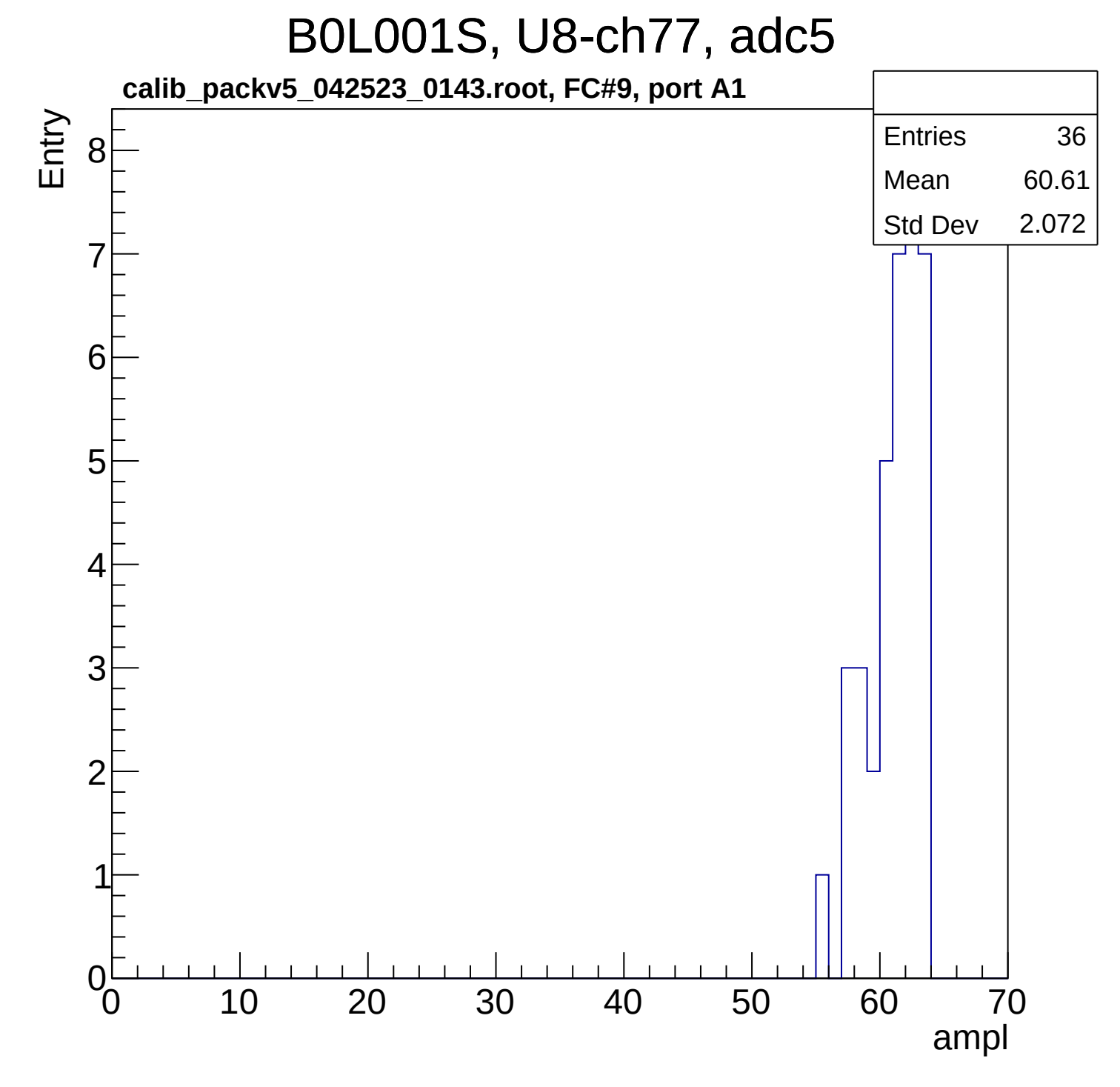
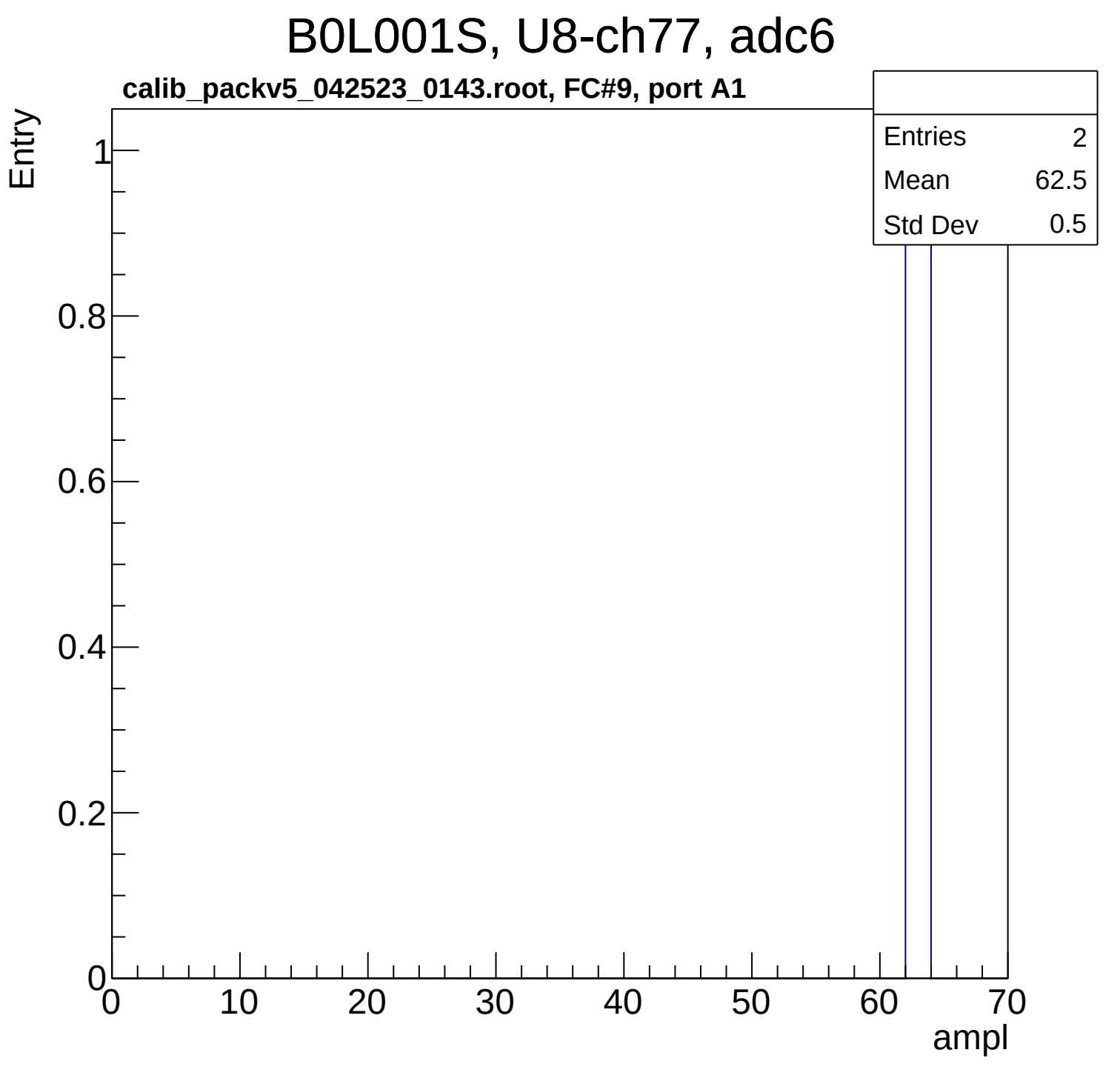
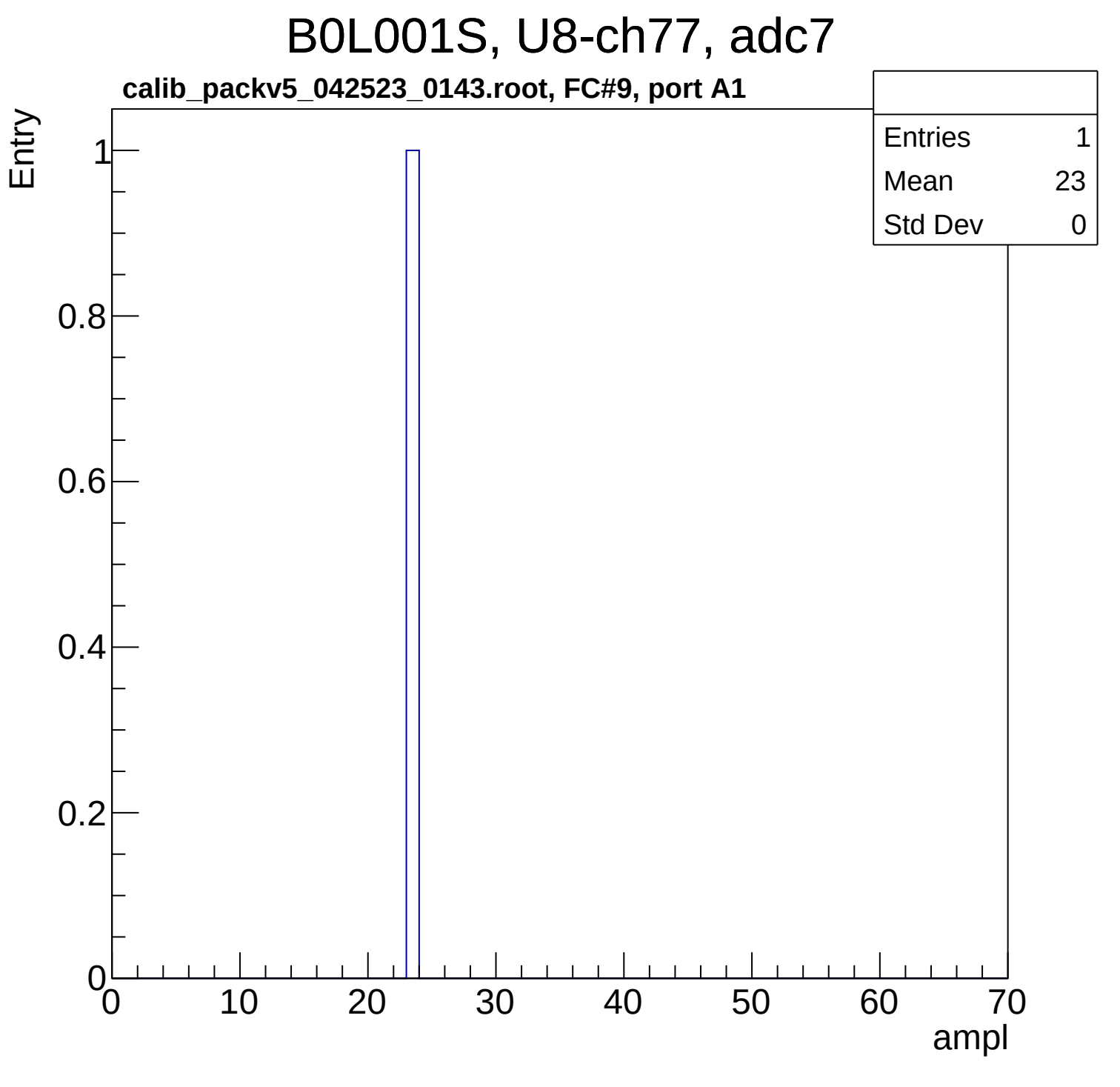
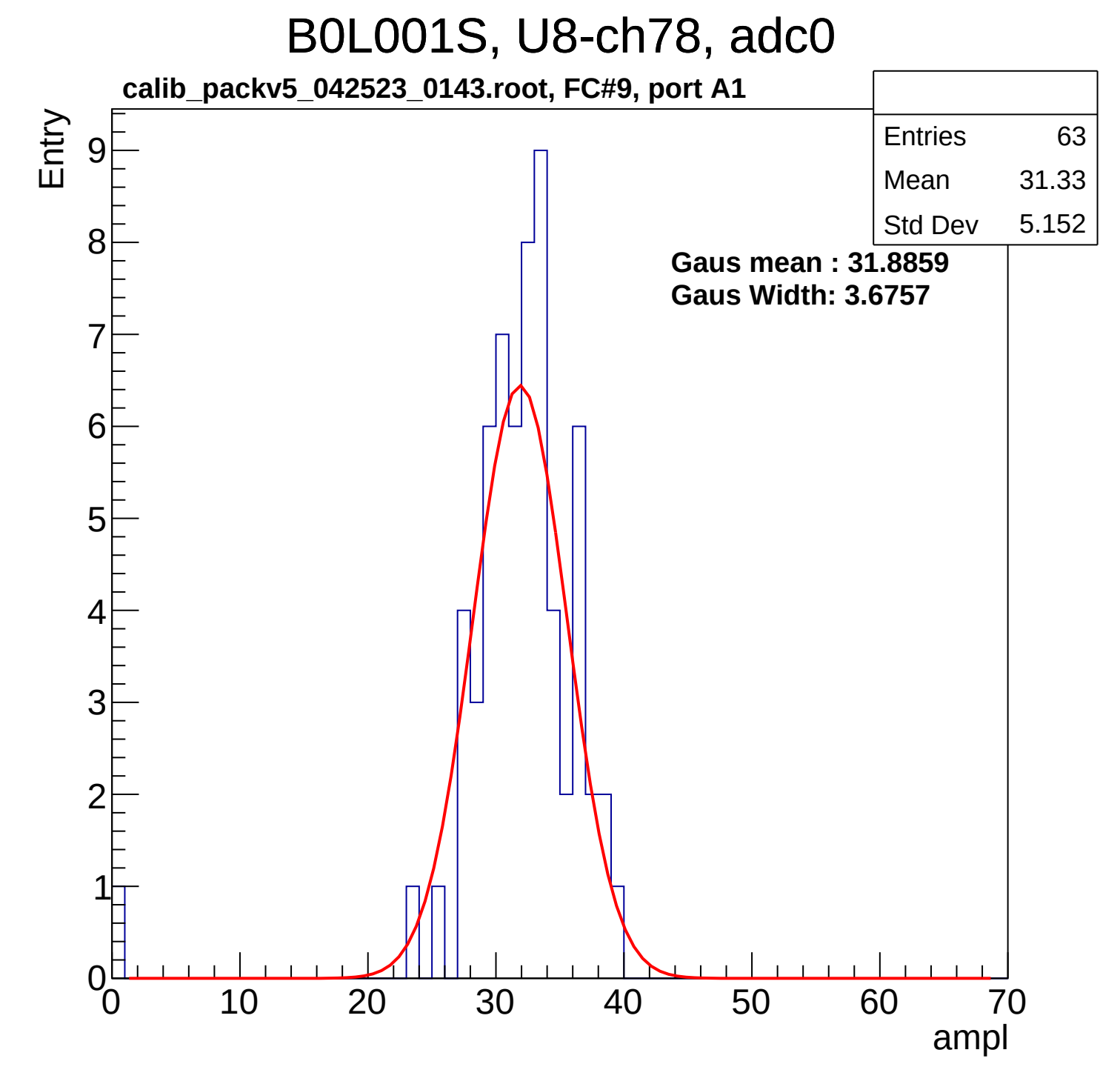
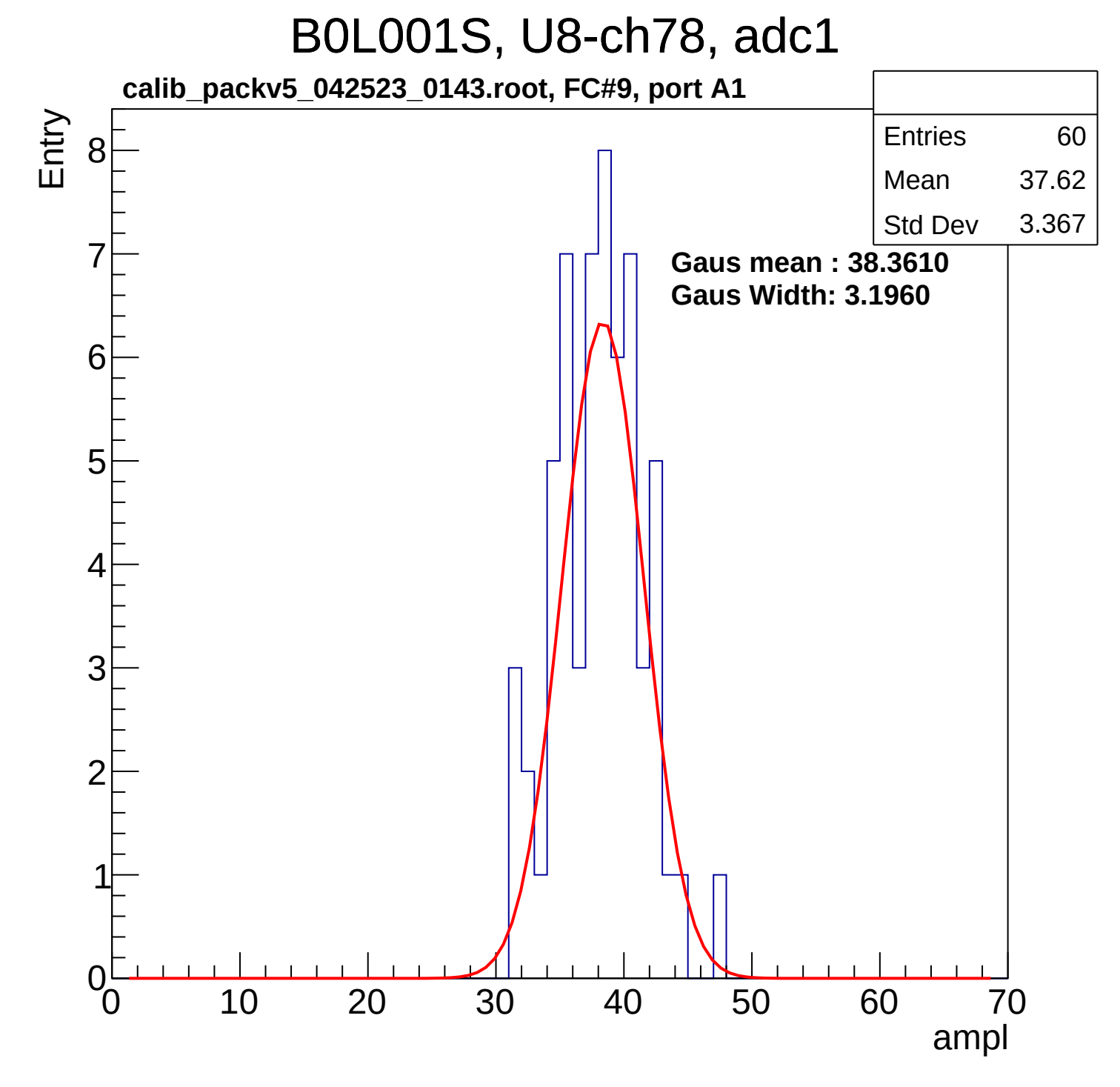

B0L001S, U8-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.81
Std Dev 8.635
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.61
Std Dev 2.072
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.33
Std Dev 5.152
Gaus mean : 31.8859
Gaus Width: 3.6757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.62
Std Dev 3.367
Gaus mean : 38.3610
Gaus Width: 3.1960
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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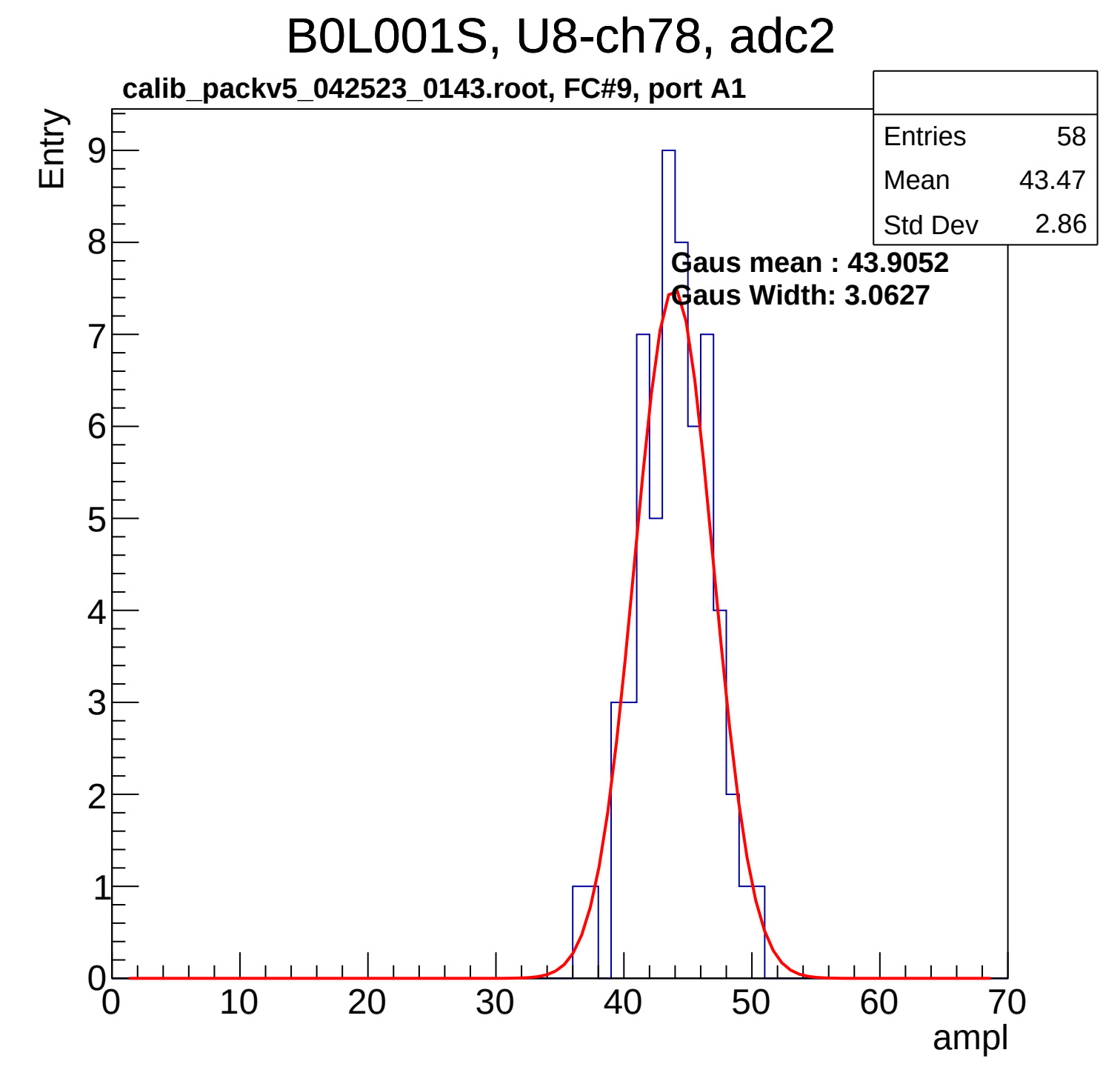
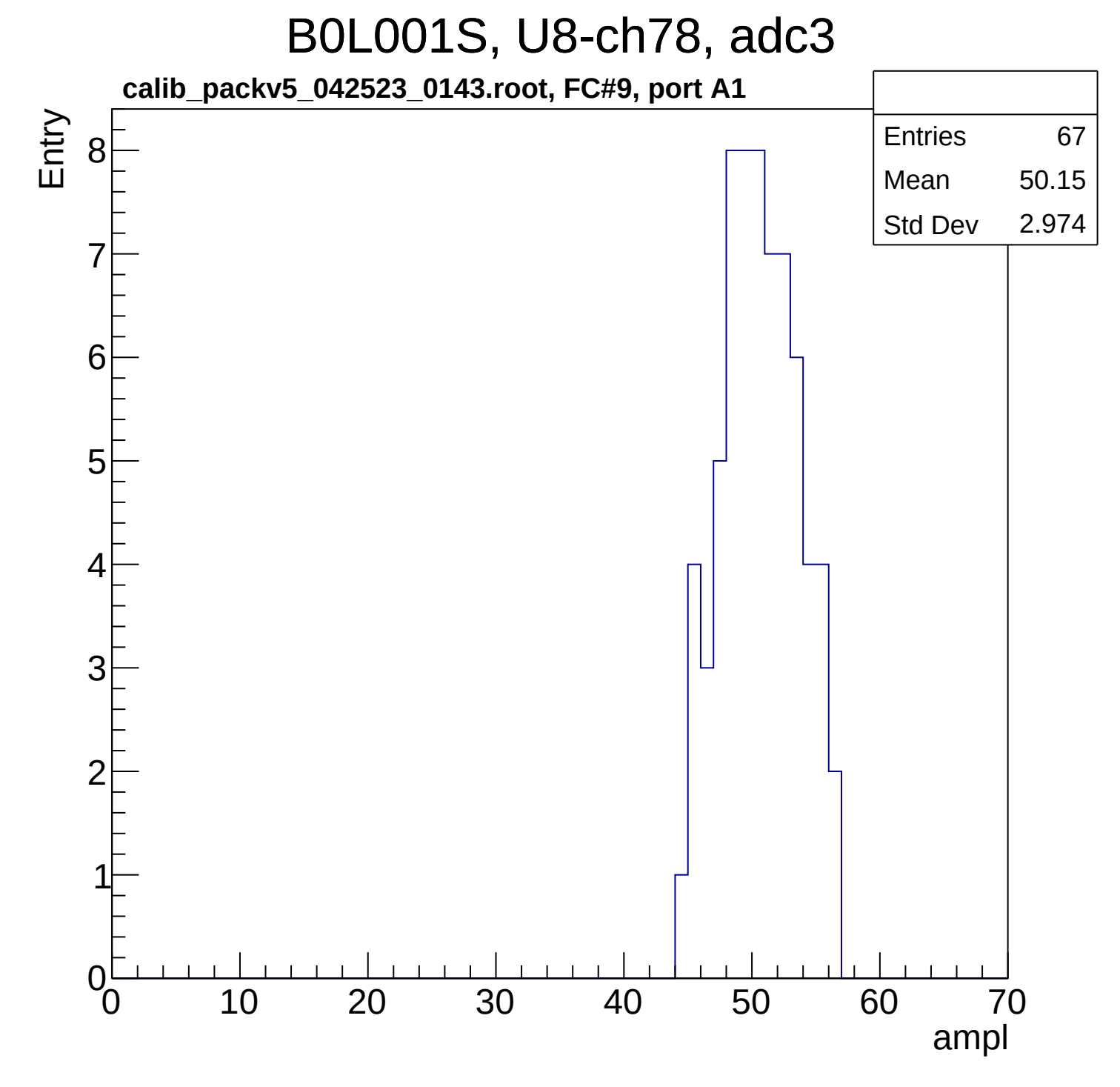
40
50
60
70


B0L001S, U8-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.47
Std Dev 2.86
Gaus mean : 43.9052
Gaus Width: 3.0627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.15
Std Dev 2.974
Entry
ampl
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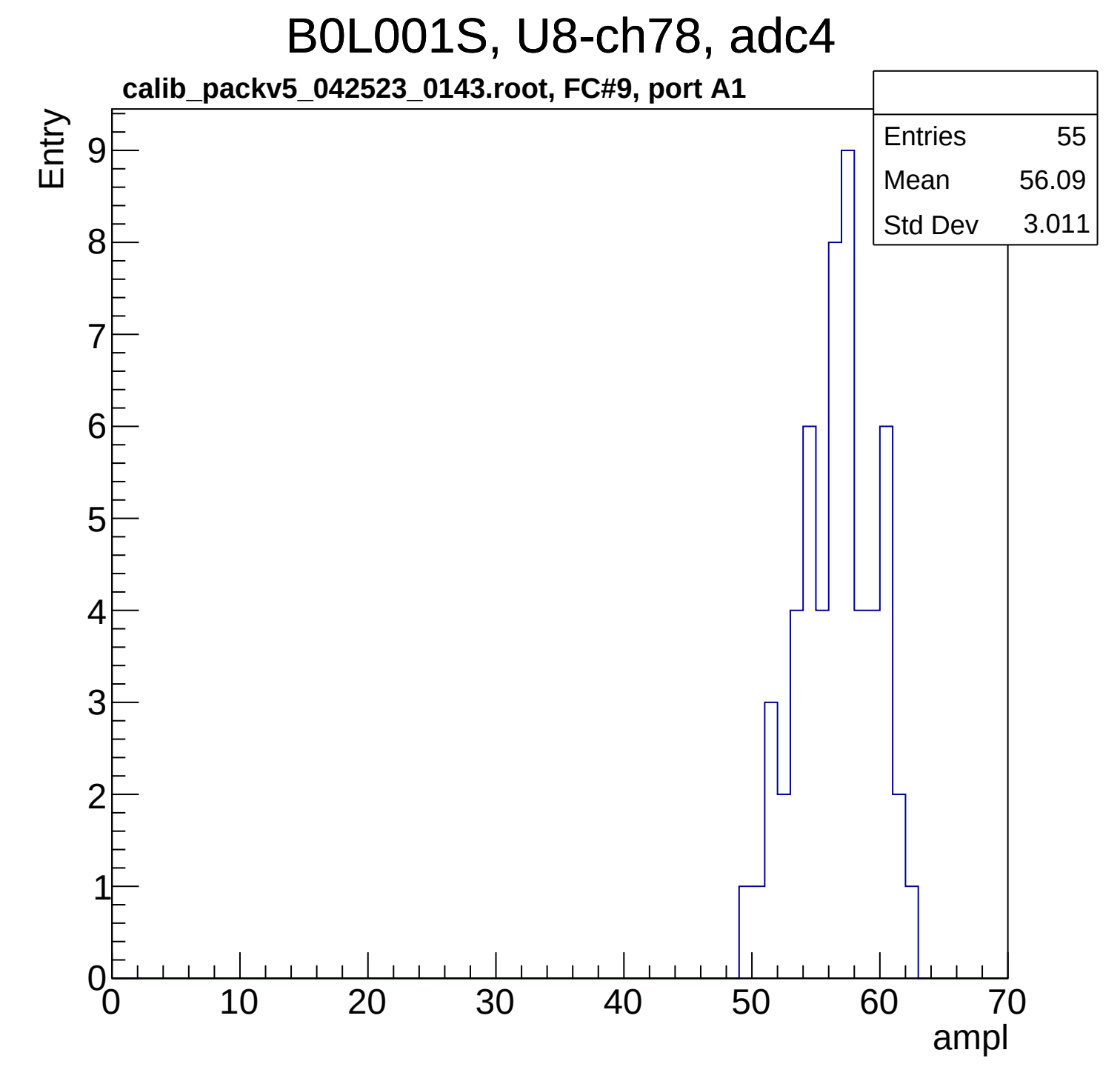
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.09
Std Dev 3.011
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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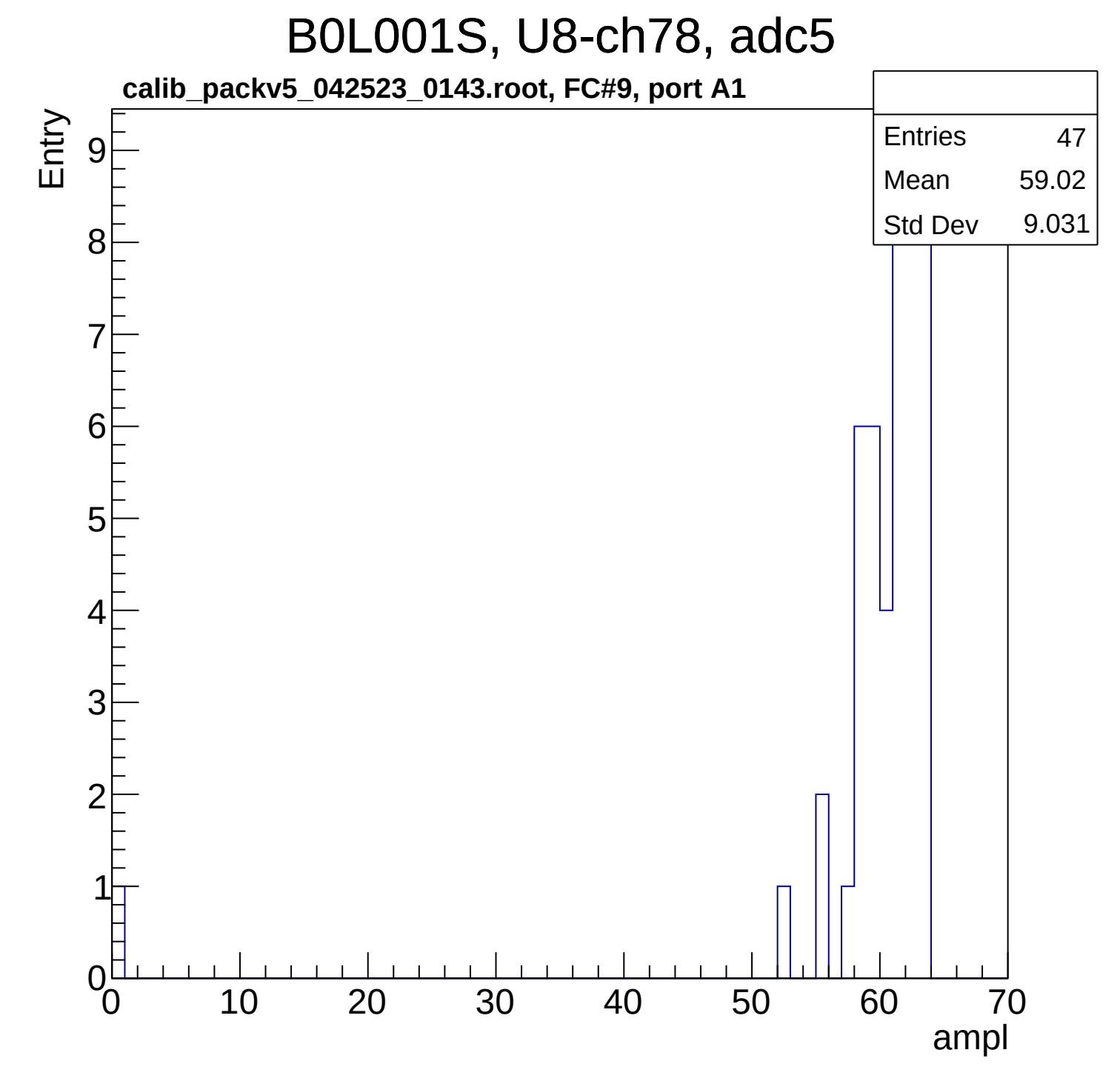
B0L001S, U8-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.02
Std Dev 9.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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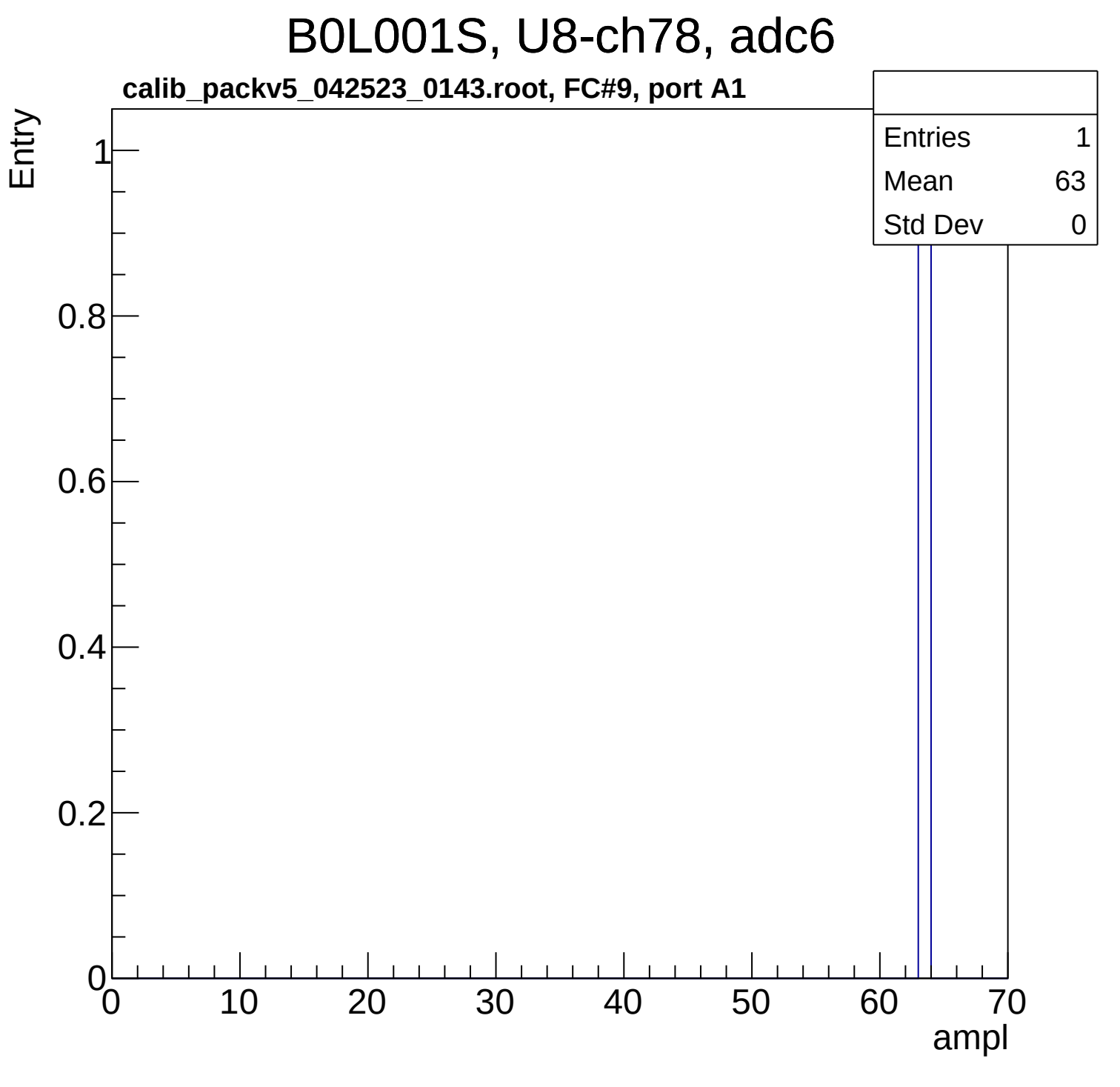
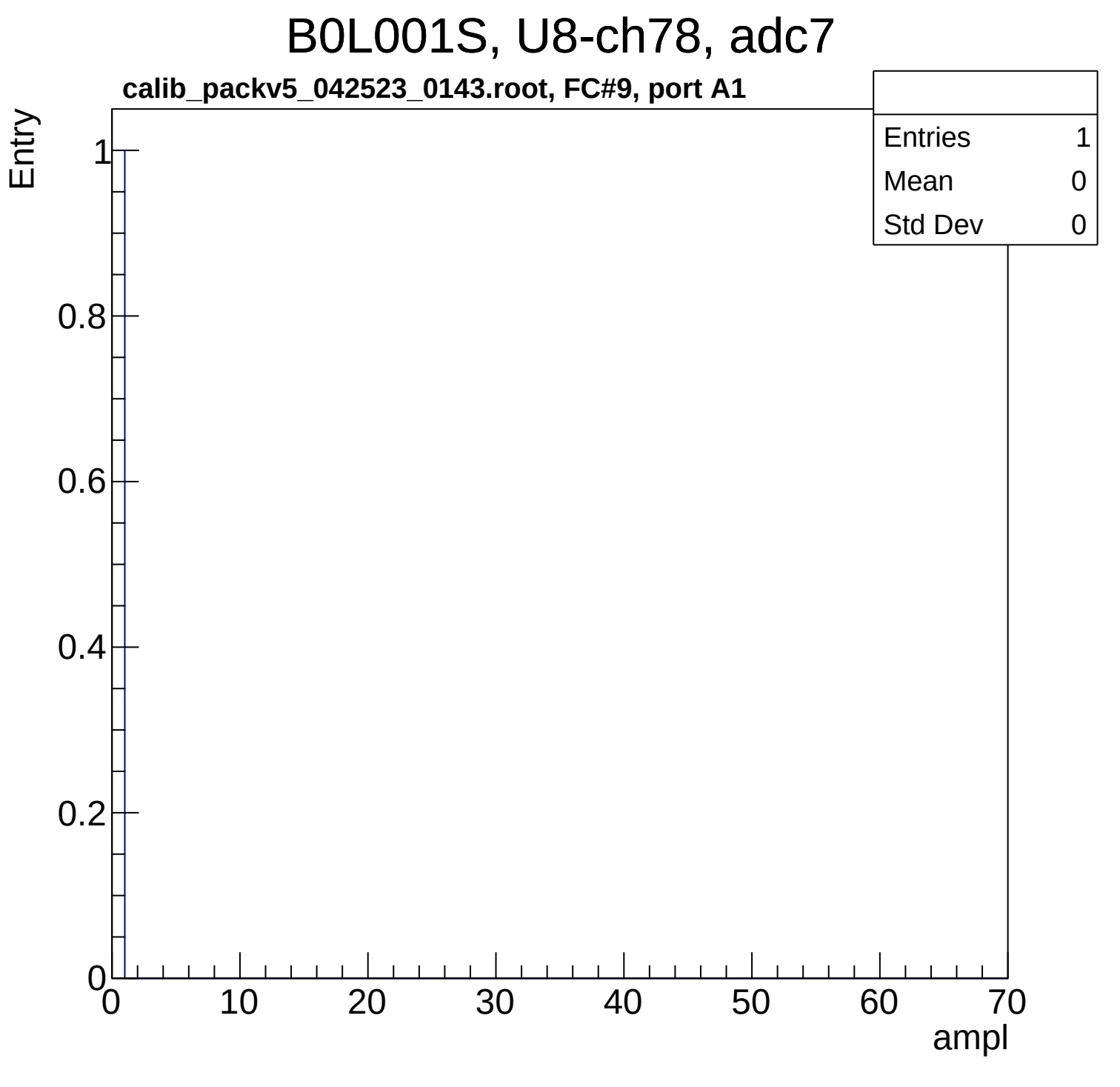
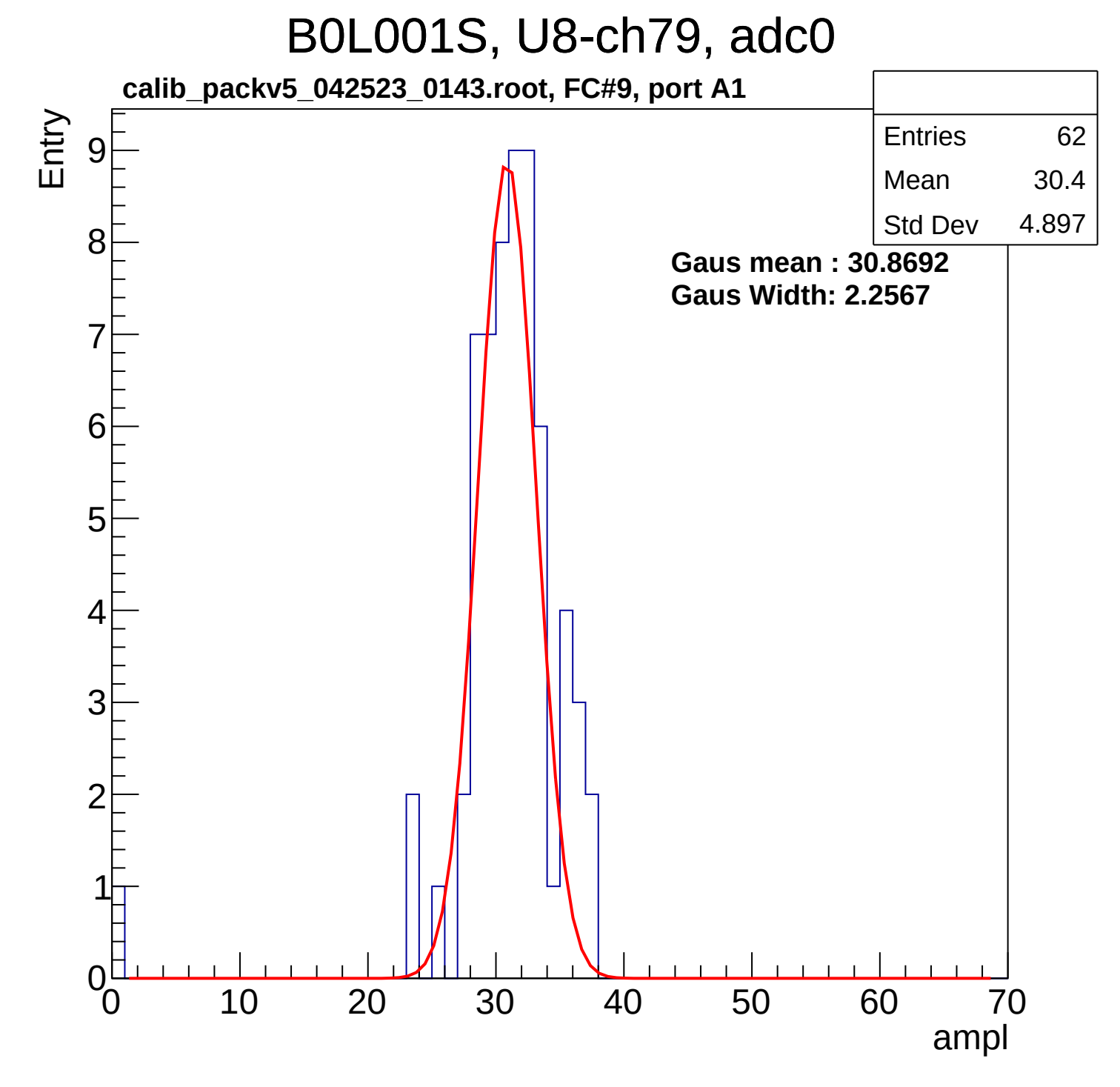
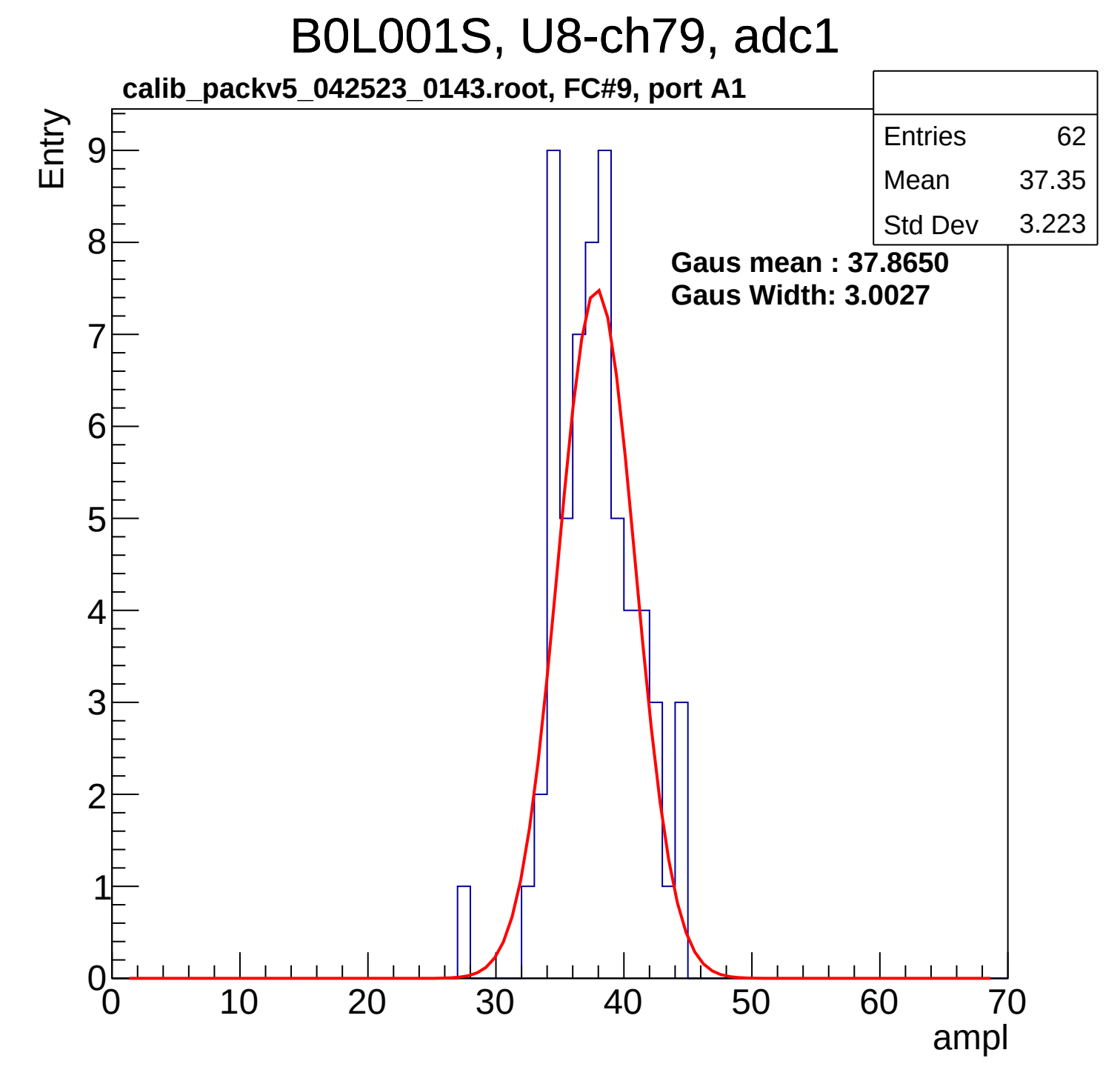

B0L001S, U8-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.4
Std Dev 4.897
Gaus mean : 30.8692
Gaus Width: 2.2567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.35
Std Dev 3.223
Gaus mean : 37.8650
Gaus Width: 3.0027
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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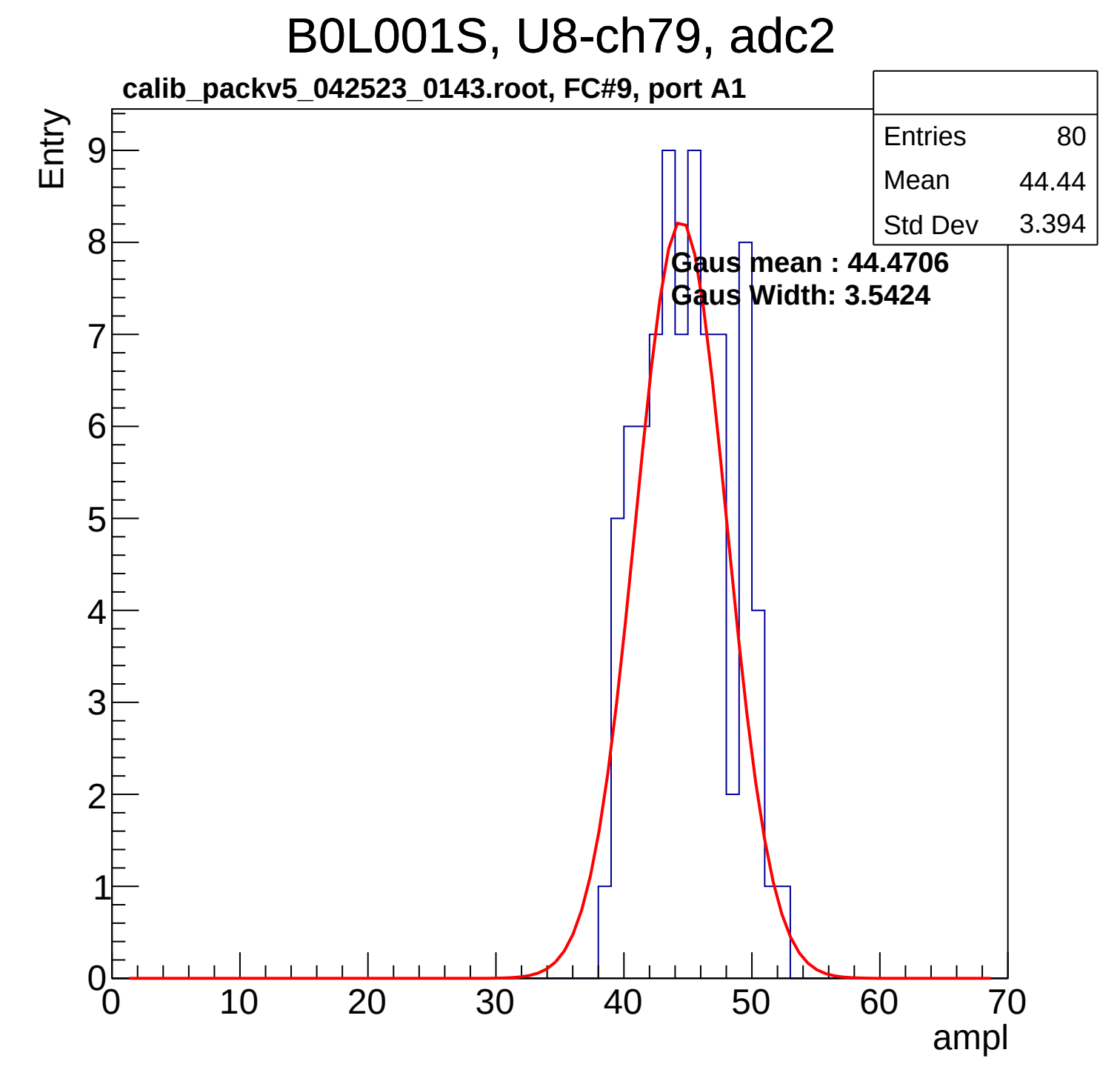
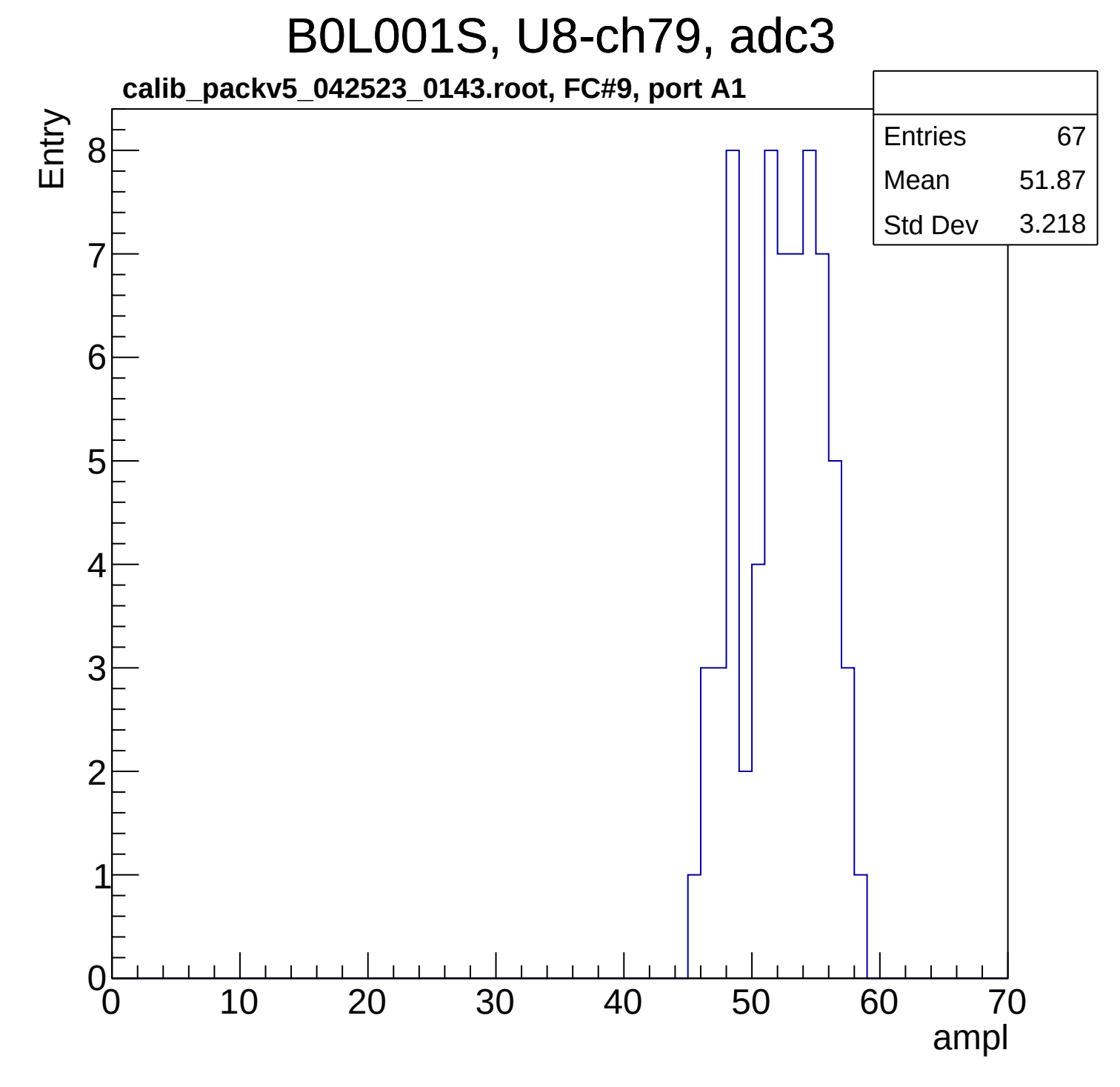

B0L001S, U8-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 44.44
Std Dev 3.394
Gaus mean : 44.4706
Gaus Width: 3.5424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.87
Std Dev 3.218
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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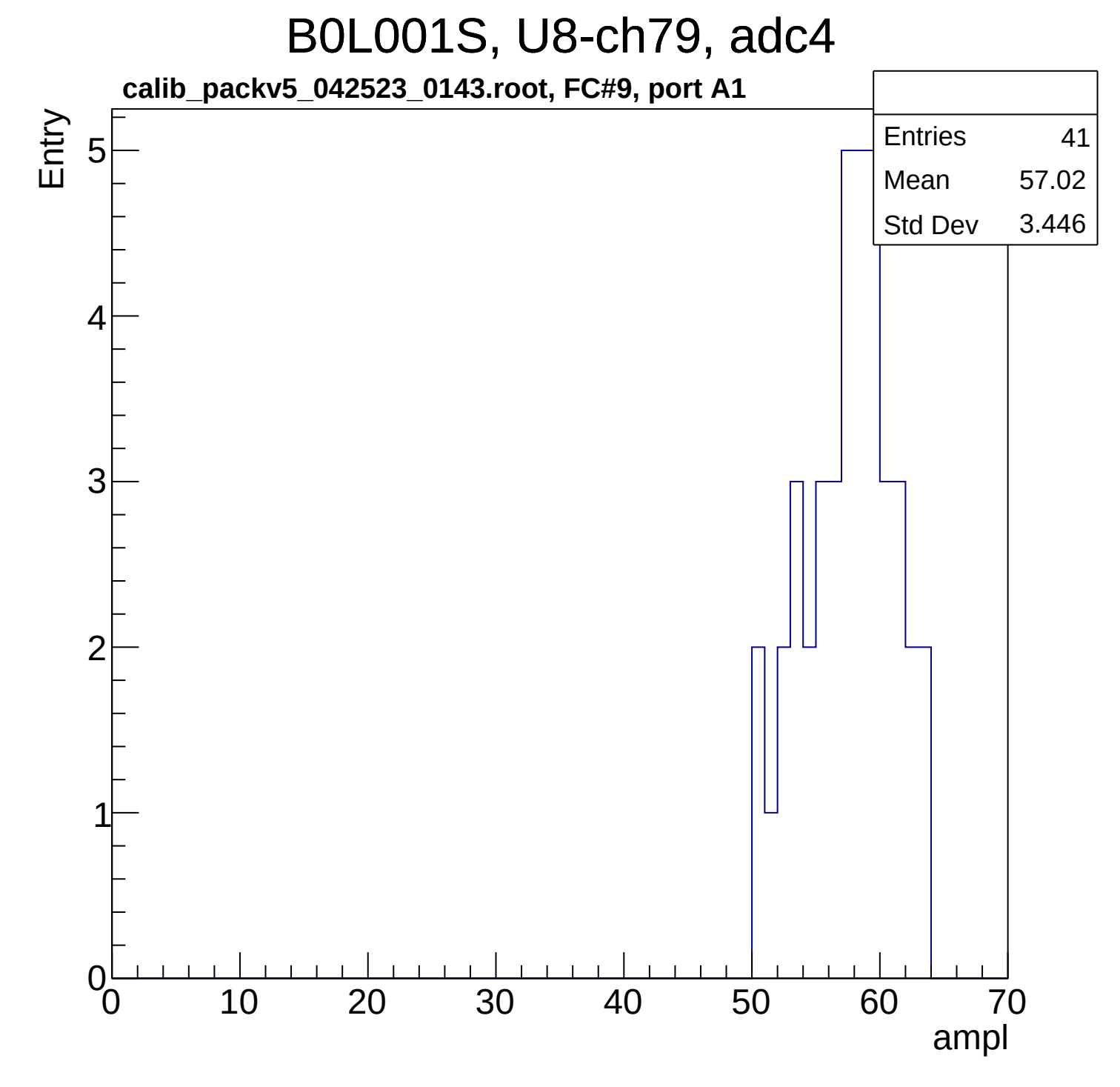
60
70


B0L001S, U8-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 57.02
Std Dev 3.446
Entry
ampl
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2
3
4
5
0
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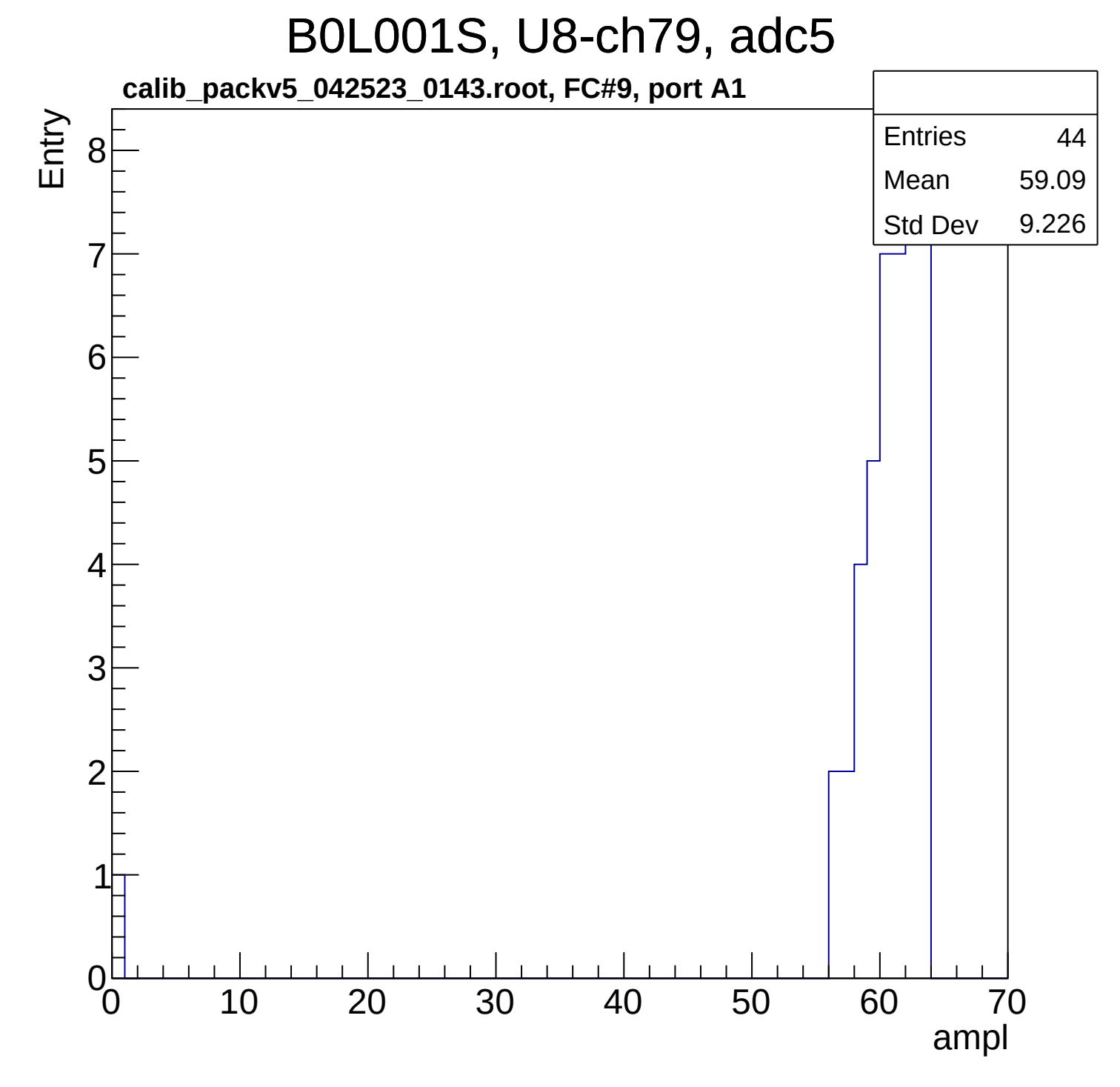
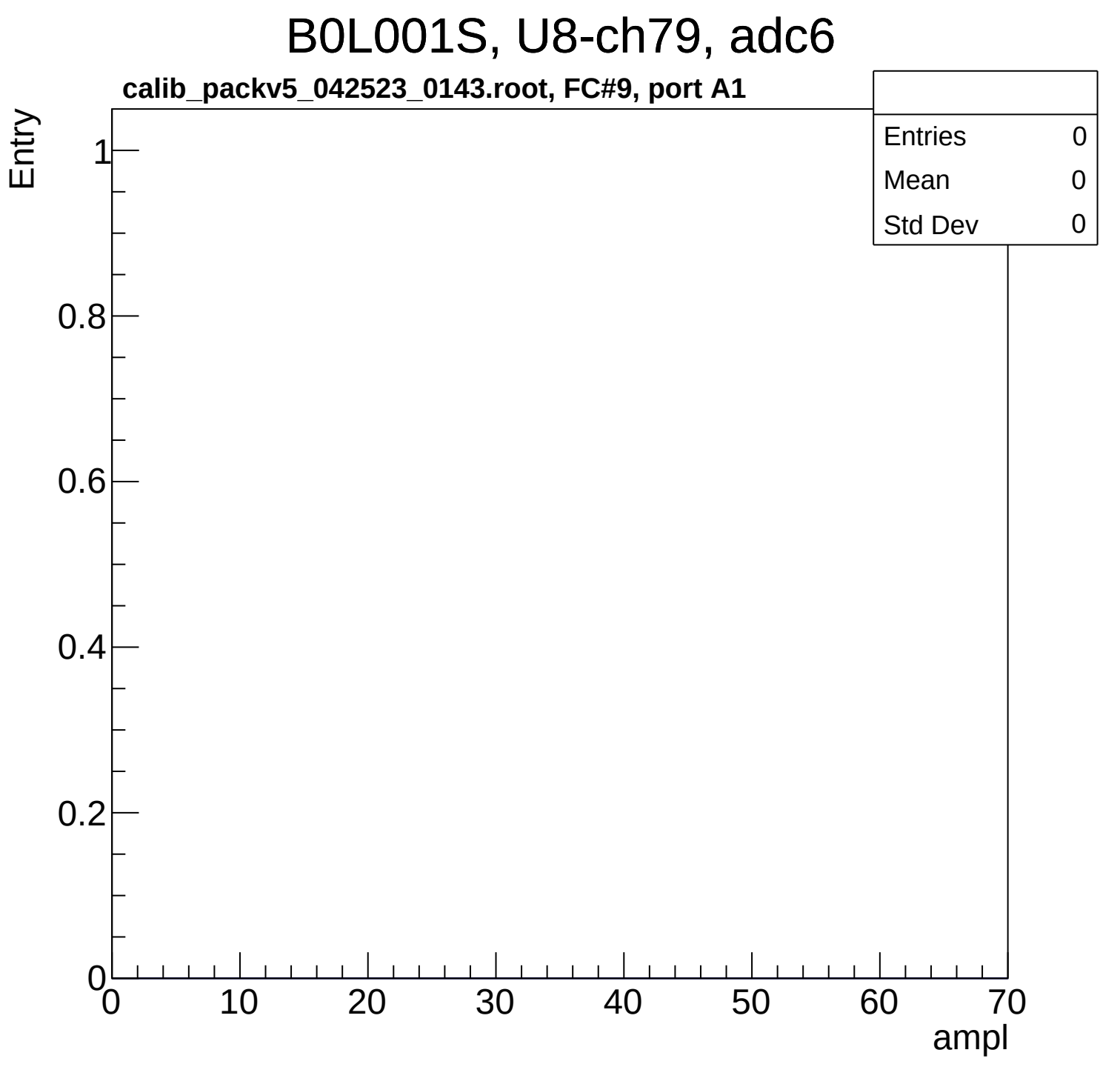
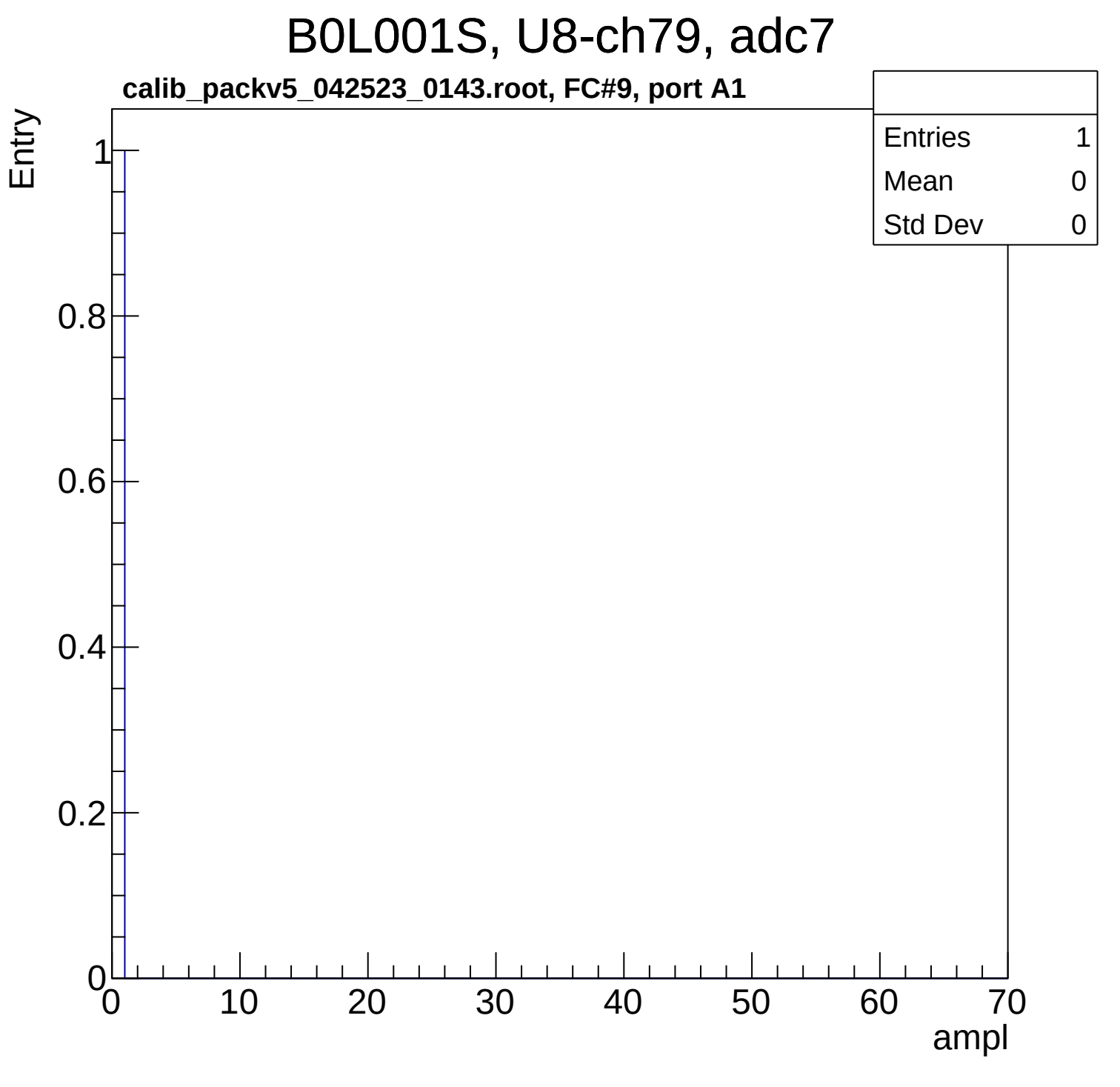
40
50
60
70


B0L001S, U8-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.09
Std Dev 9.226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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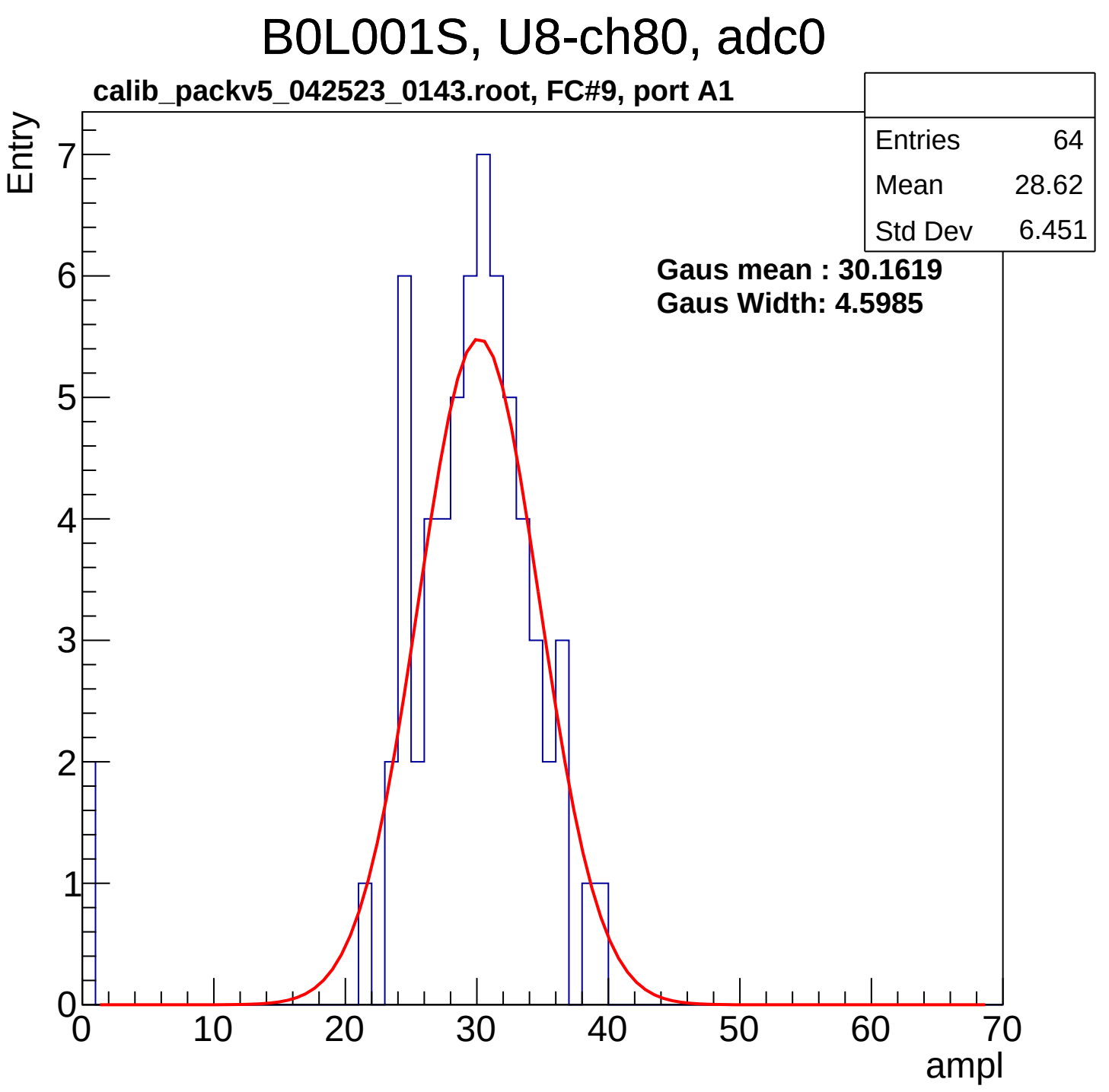
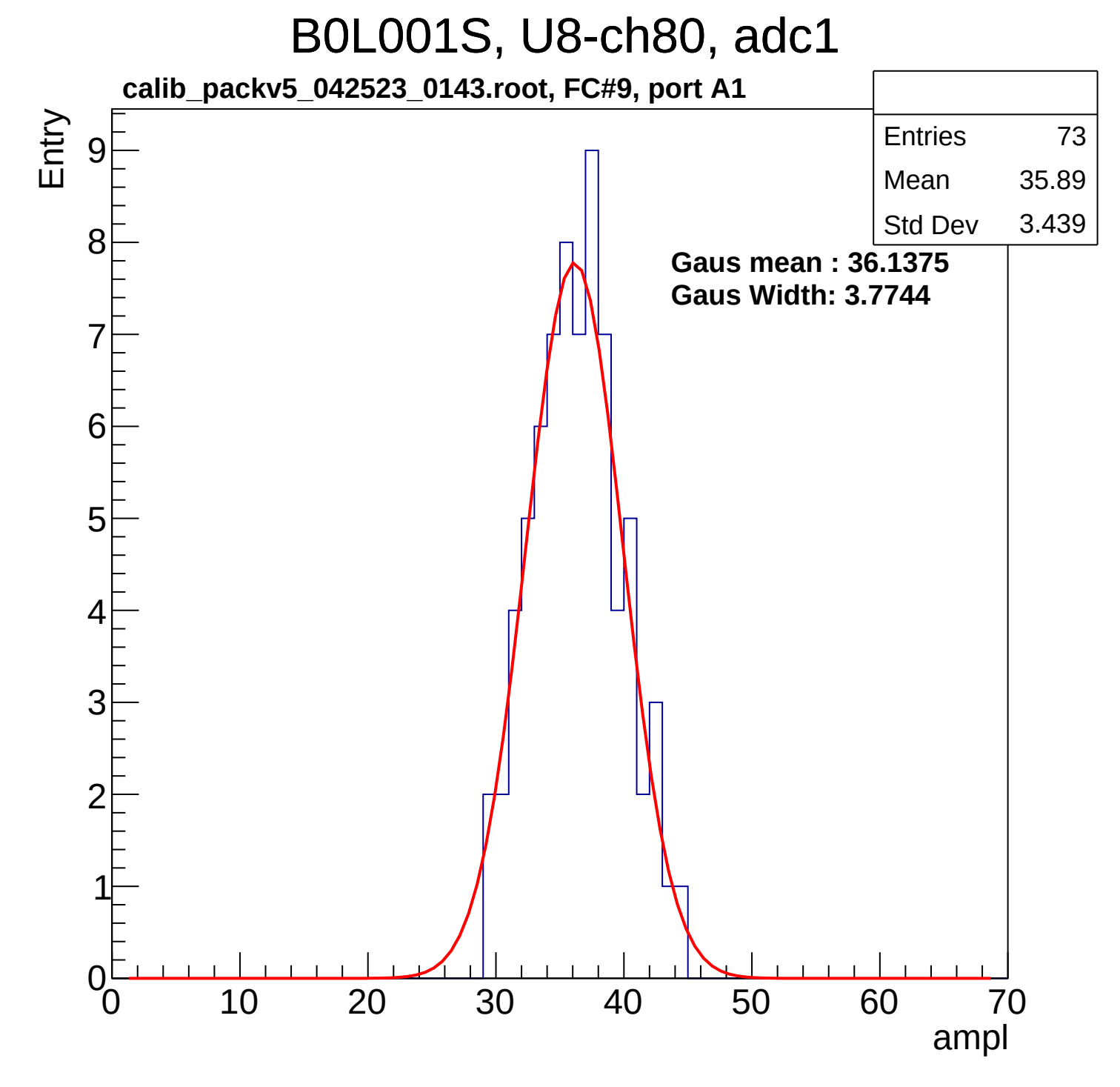
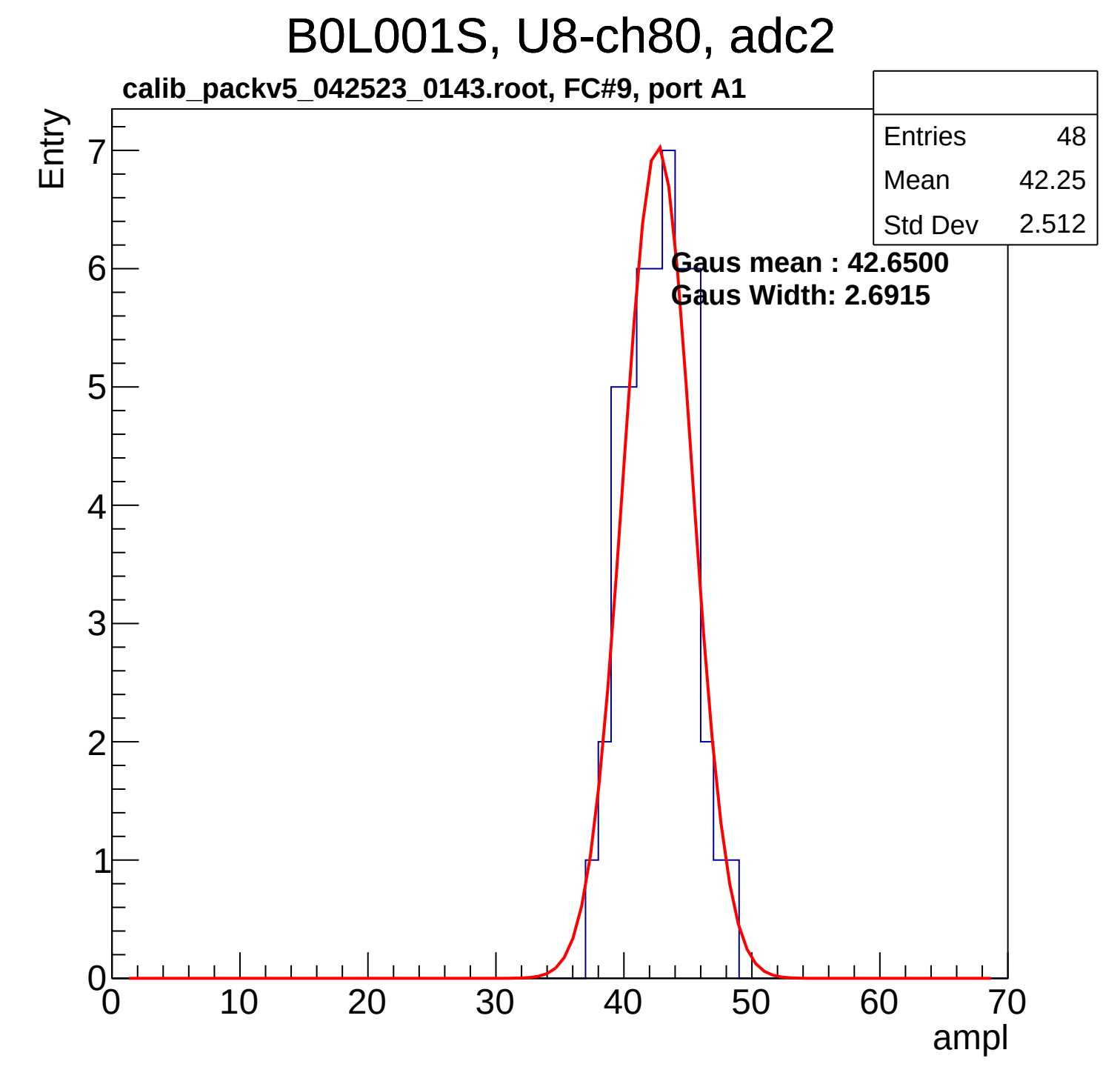
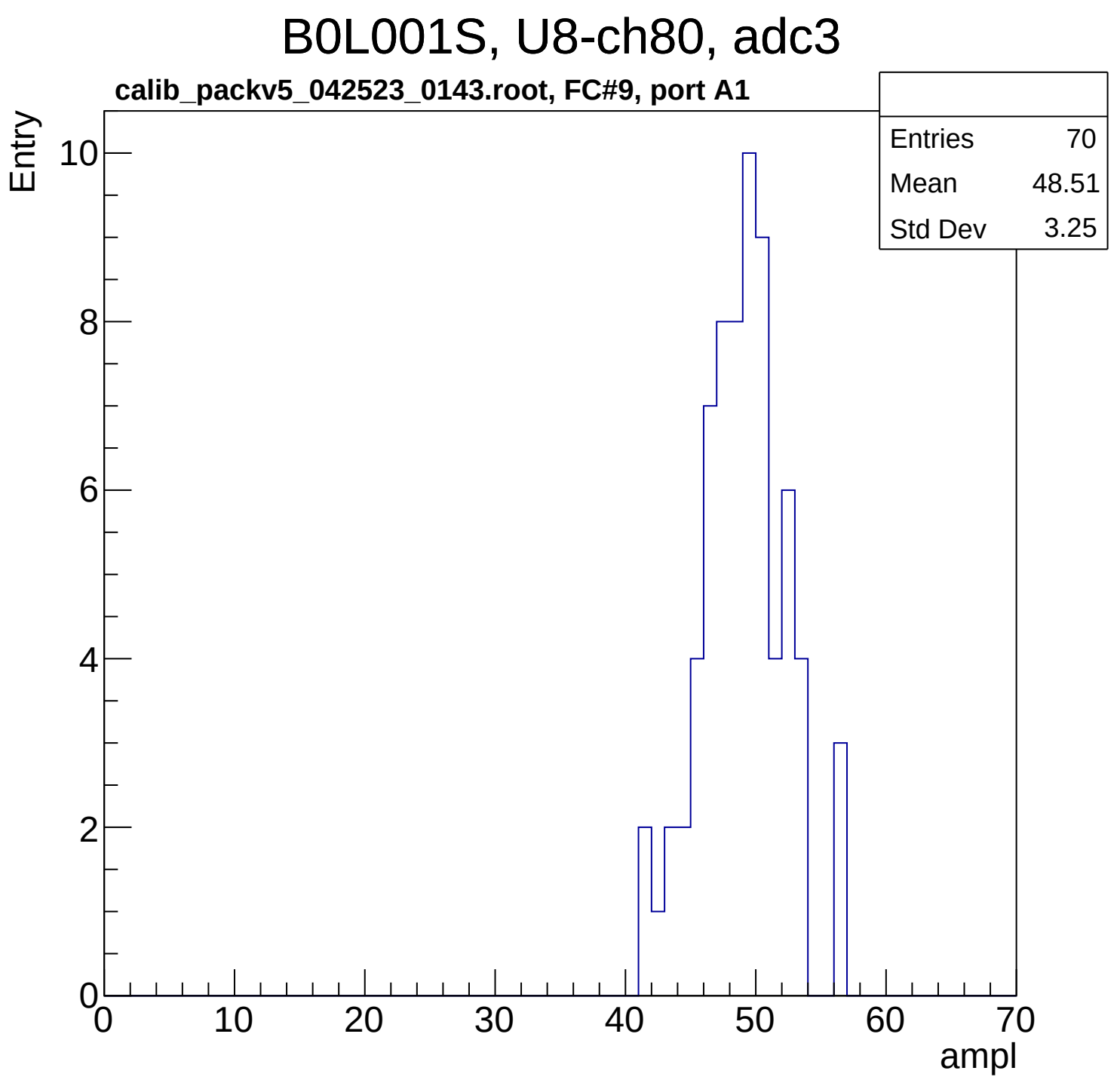
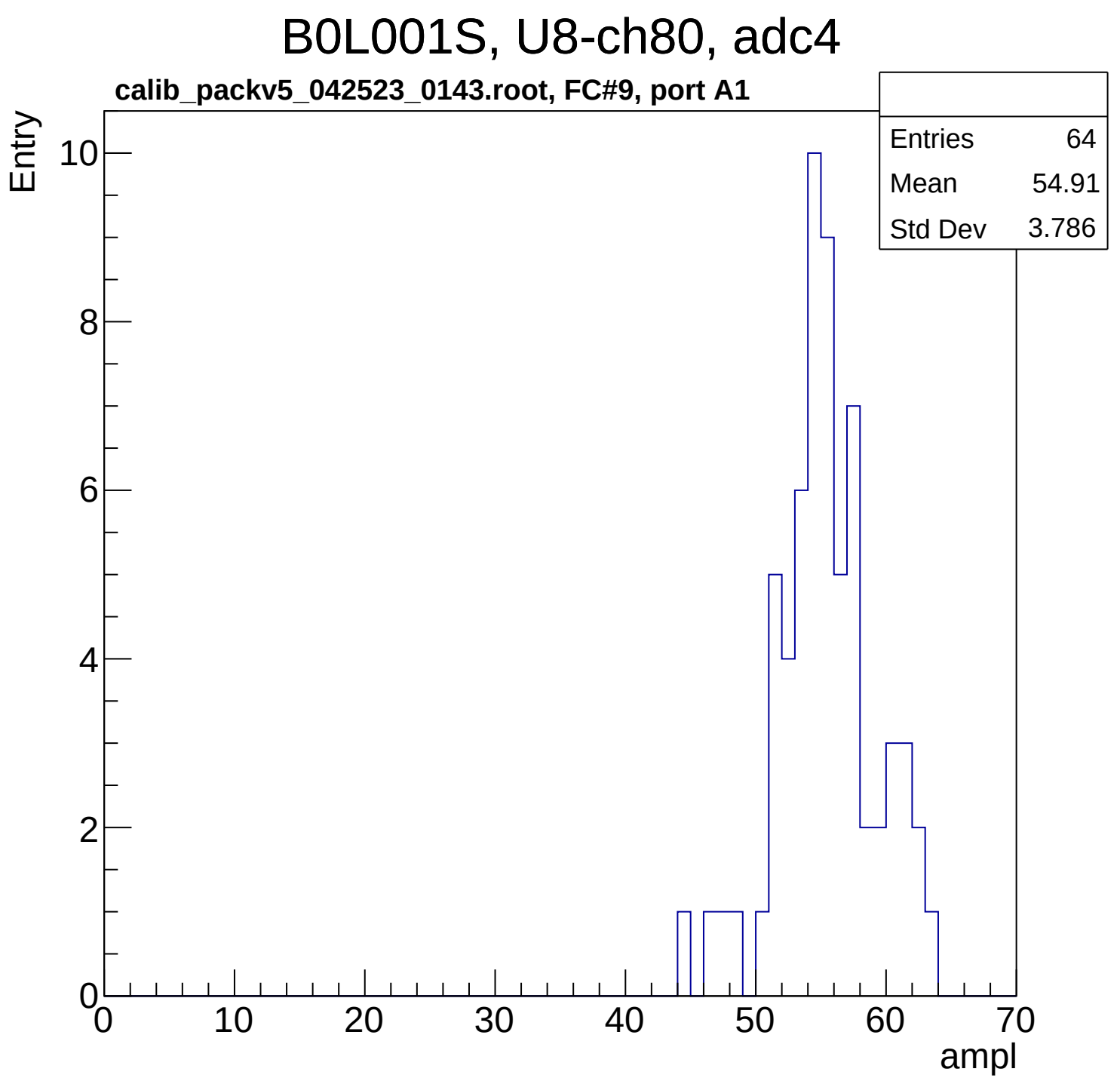
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

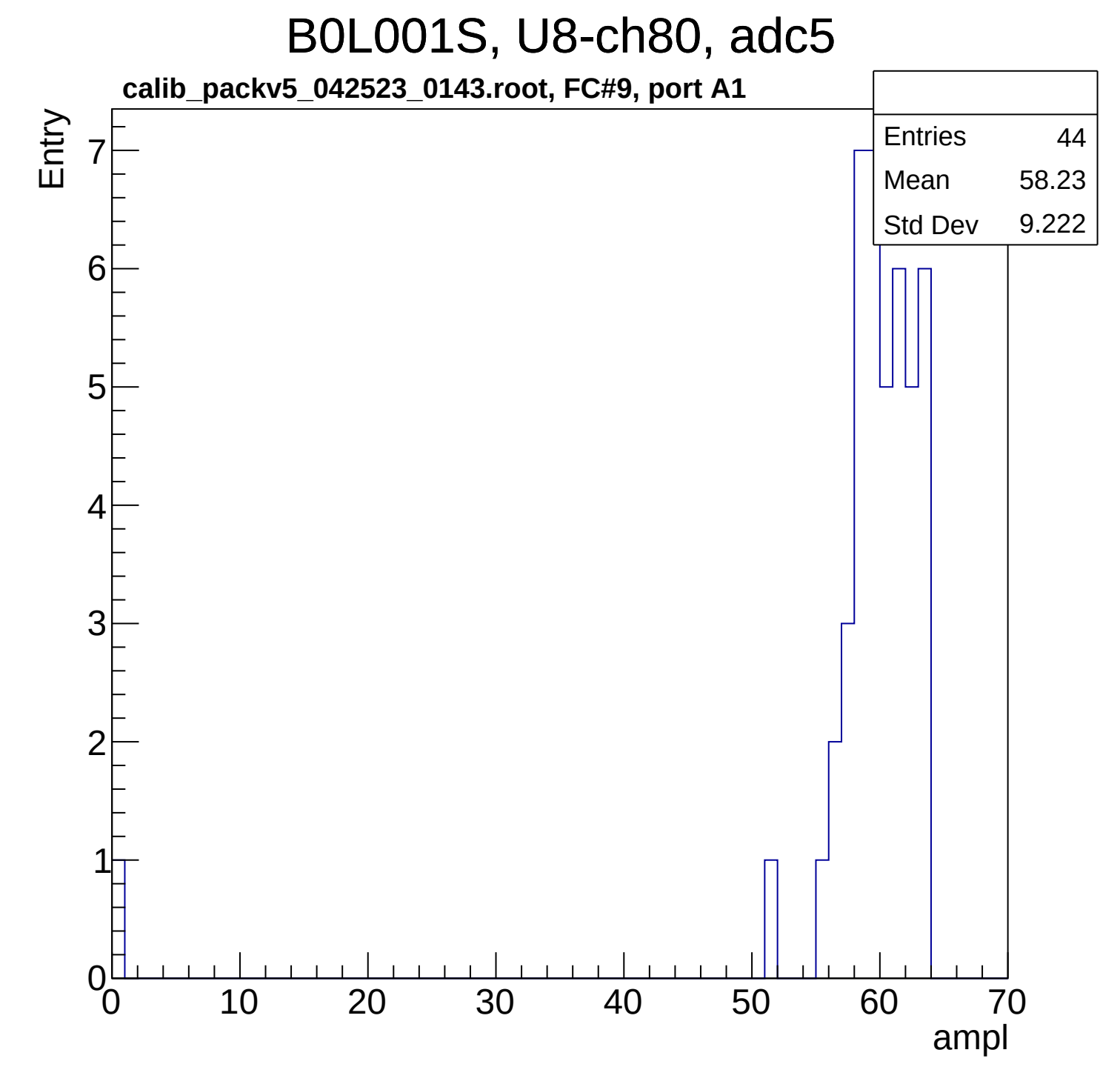

B0L001S, U8-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 28.62
Std Dev 6.451
Gaus mean : 30.1619
Gaus Width: 4.5985
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

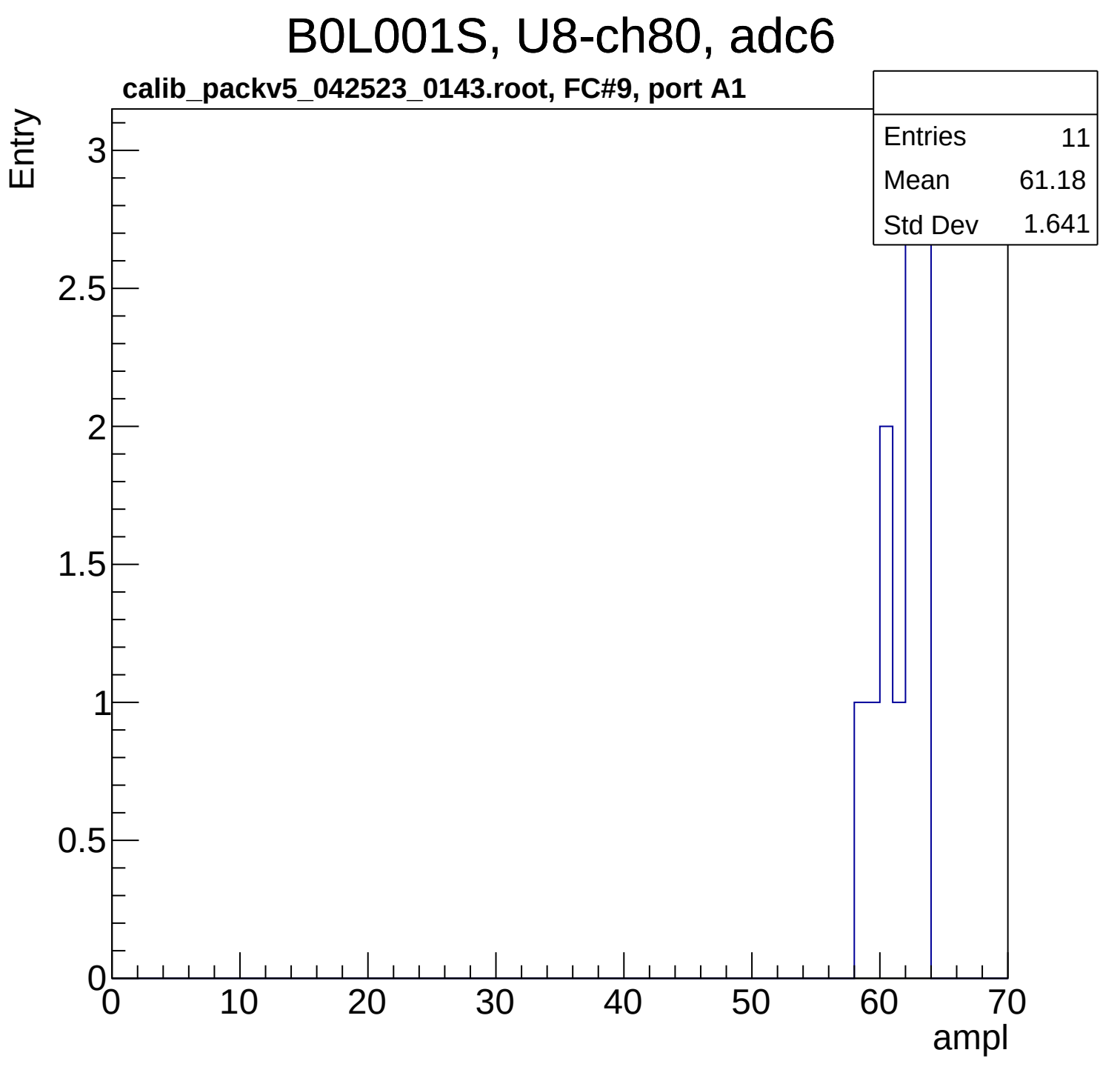

B0L001S, U8-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 35.89
Std Dev 3.439
Gaus mean : 36.1375
Gaus Width: 3.7744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 42.25
Std Dev 2.512
Gaus mean : 42.6500
Gaus Width: 2.6915
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 48.51
Std Dev 3.25
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 54.91
Std Dev 3.786
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.23
Std Dev 9.222
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.18
Std Dev 1.641
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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40
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60
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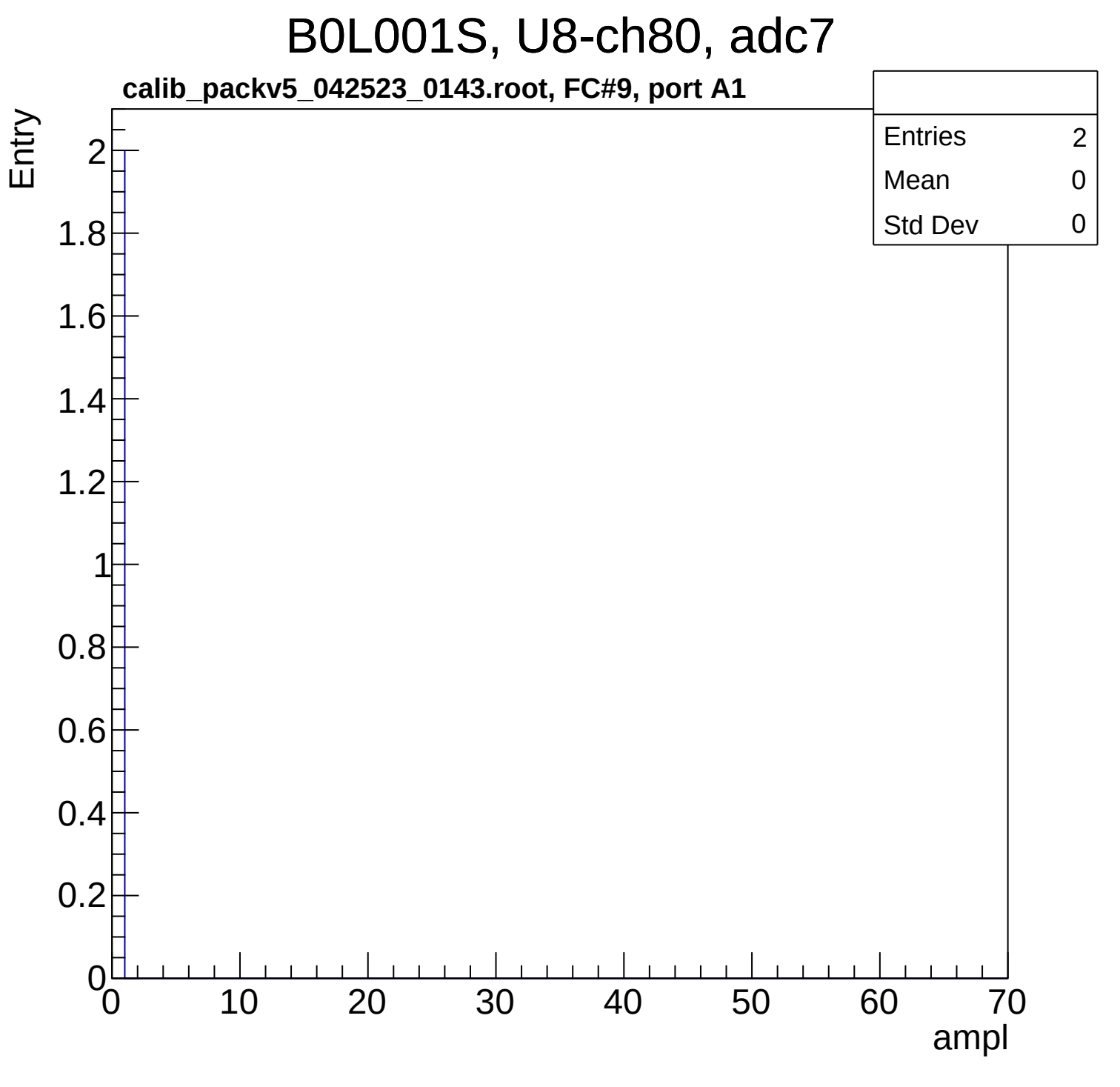
B0L001S, U8-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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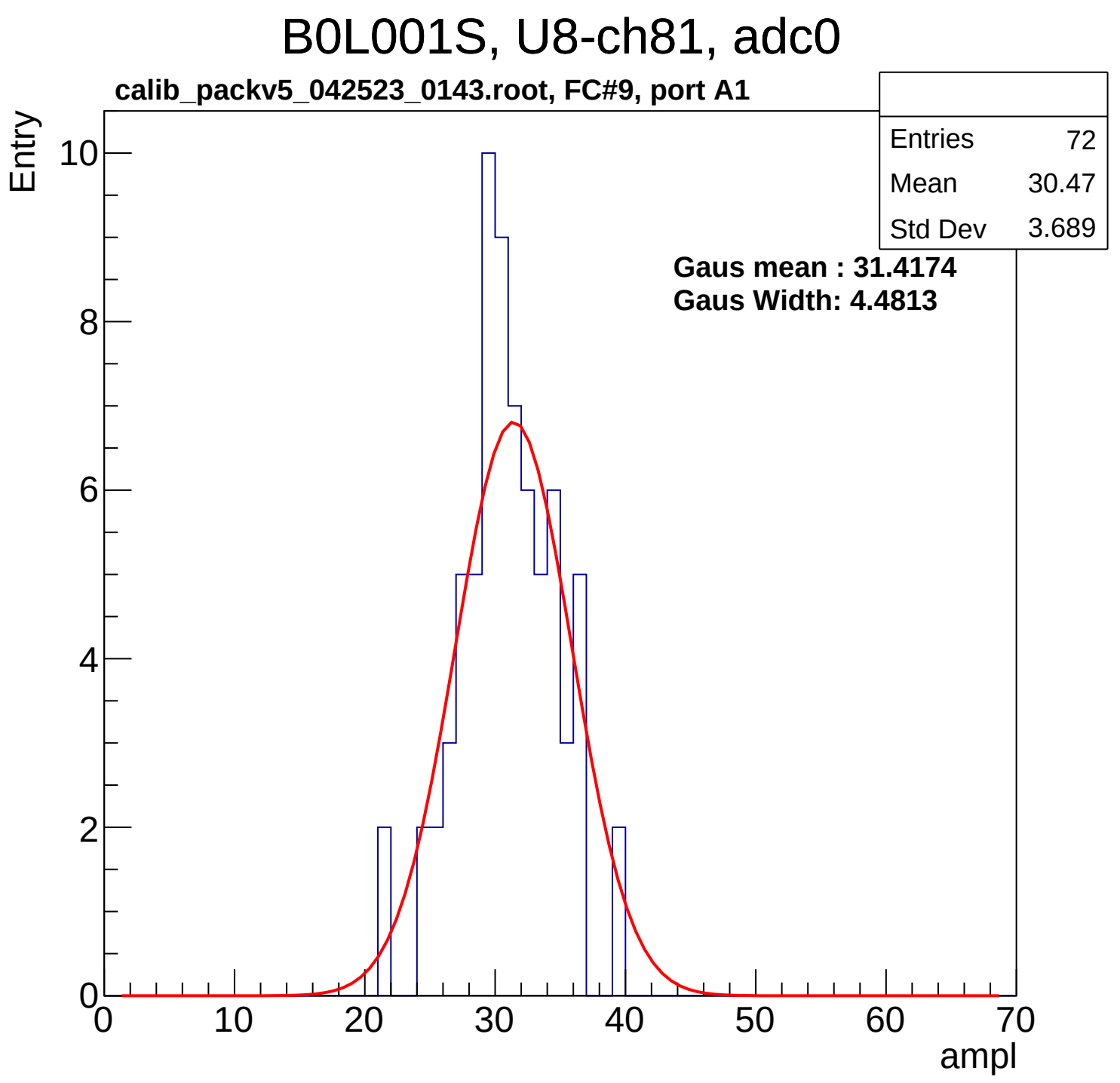
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.47
Std Dev 3.689
Gaus mean : 31.4174
Gaus Width: 4.4813
Entry
ampl
0
2
4
6
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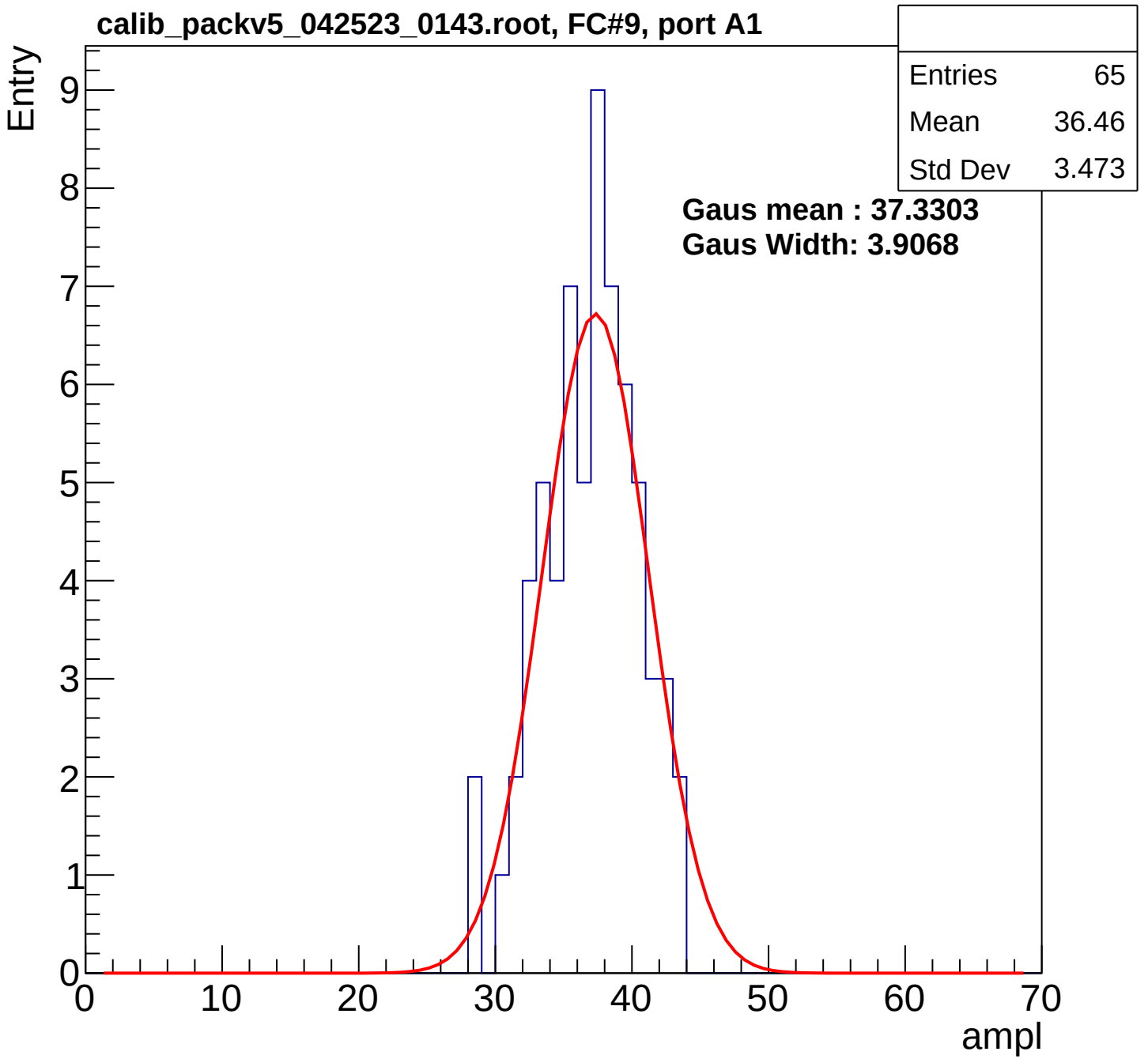
B0L001S, U8-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.46
Std Dev 3.473
Gaus mean : 37.3303
Gaus Width: 3.9068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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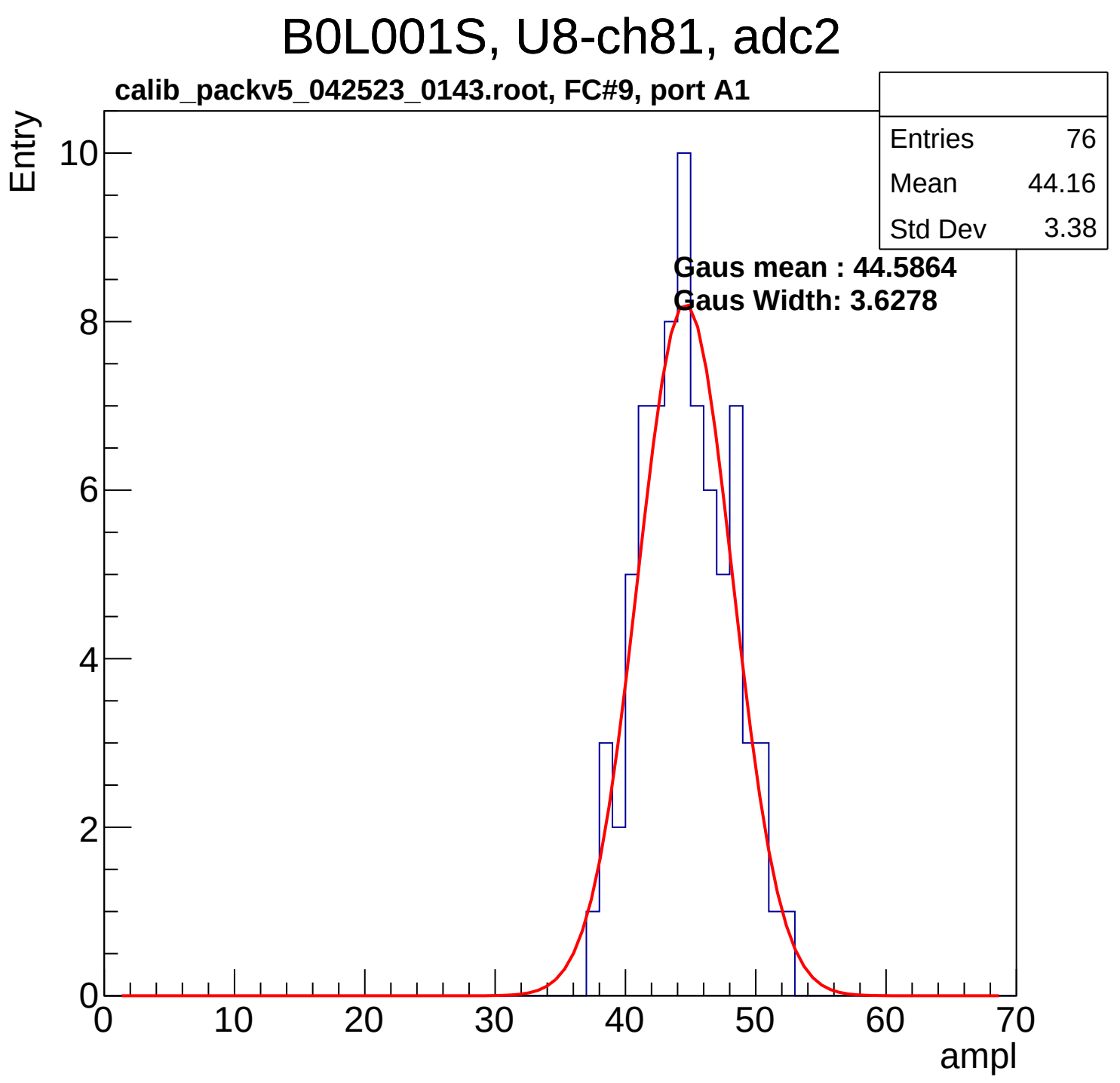
B0L001S, U8-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.16
Std Dev 3.38
Gaus mean : 44.5864
Gaus Width: 3.6278
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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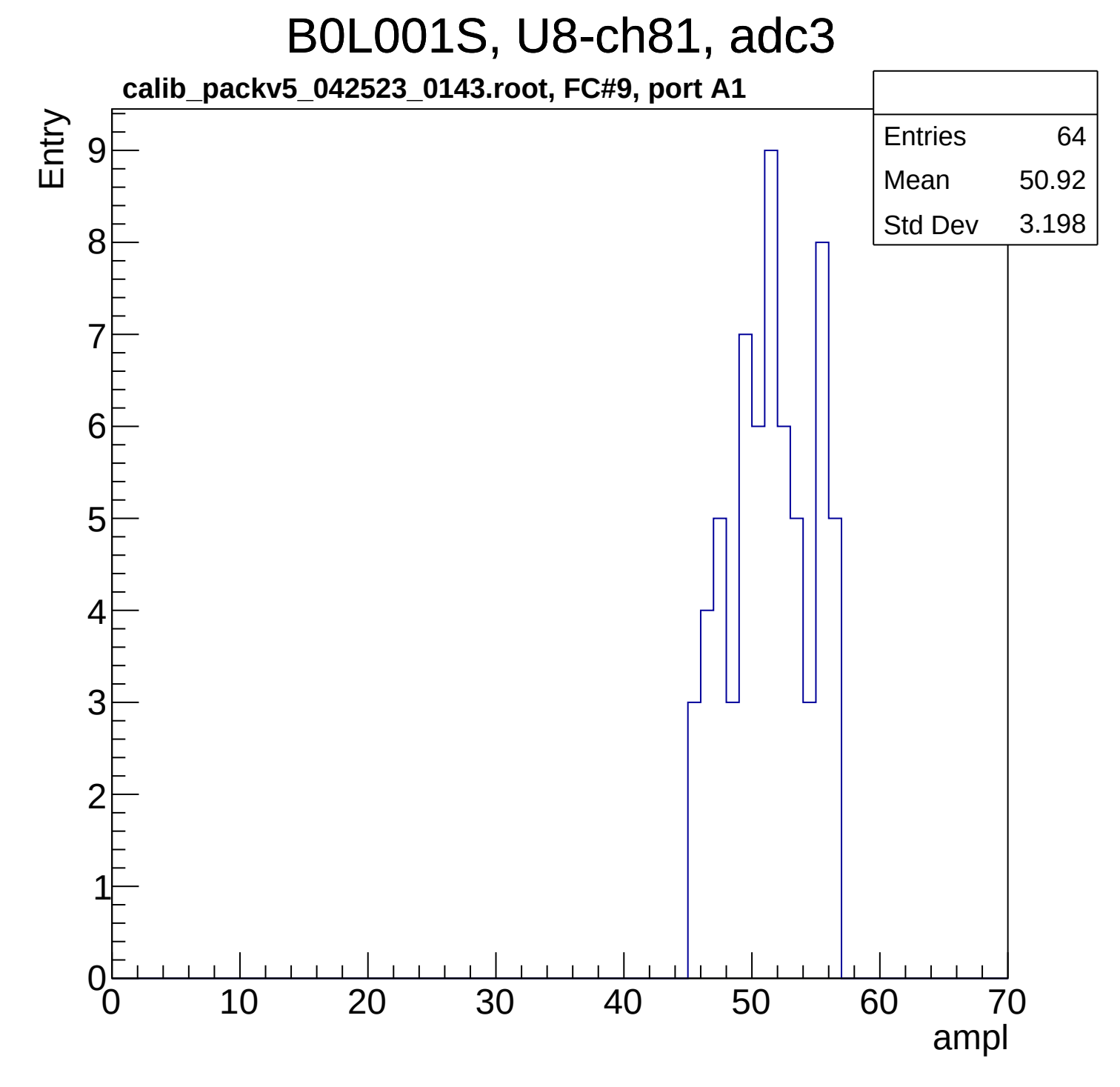
B0L001S, U8-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.92
Std Dev 3.198
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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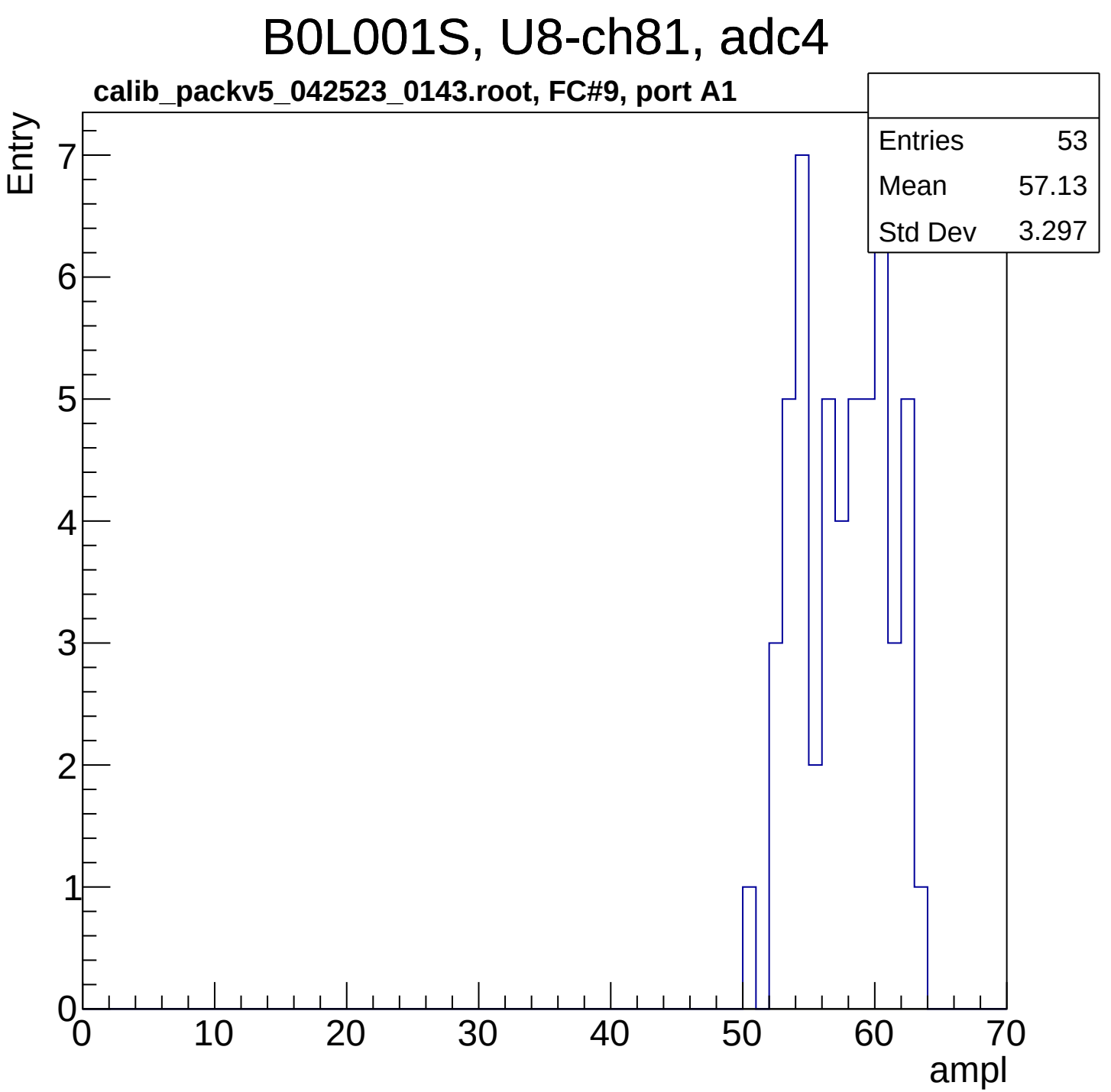
B0L001S, U8-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.13
Std Dev 3.297
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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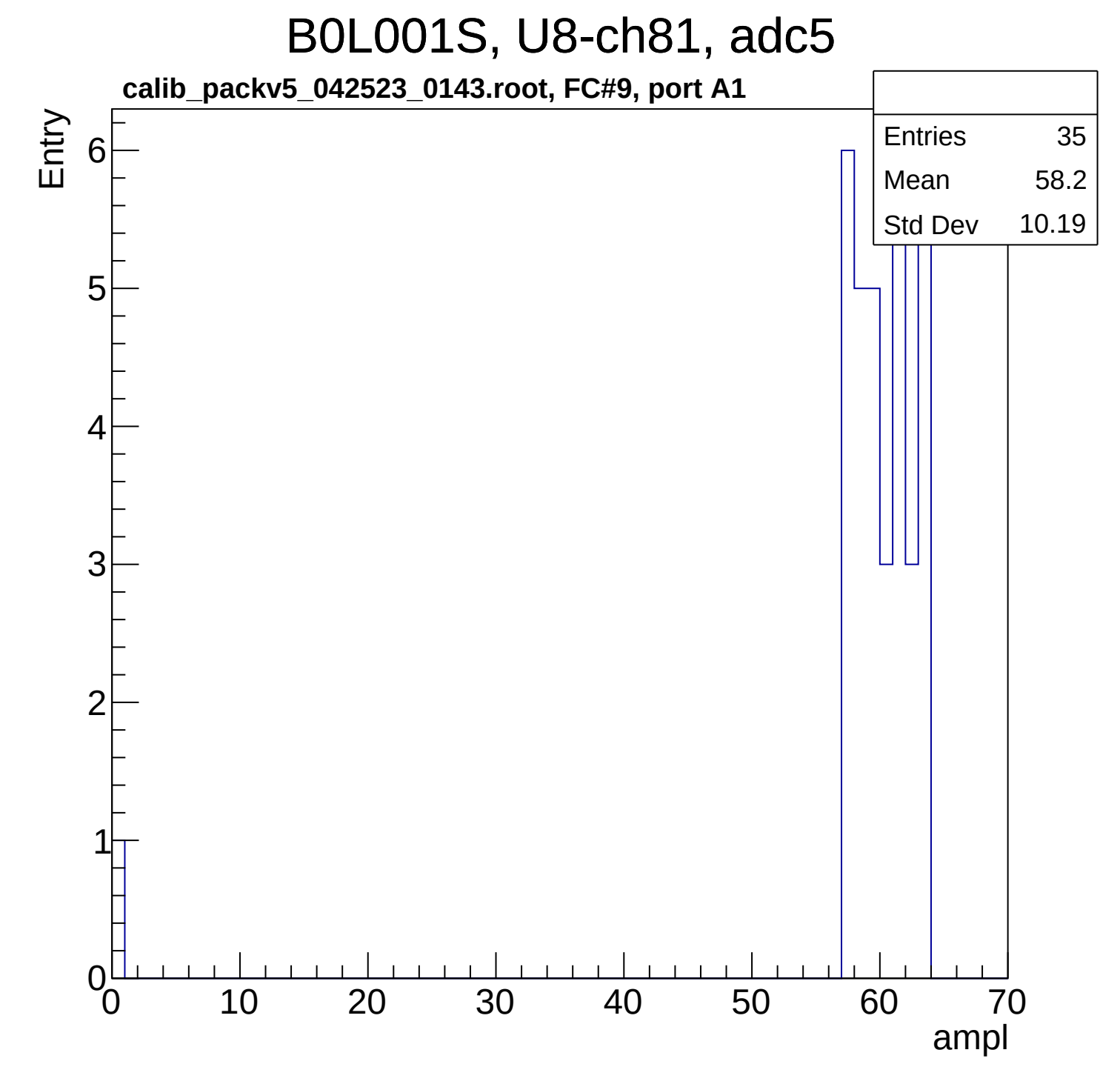
B0L001S, U8-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.2
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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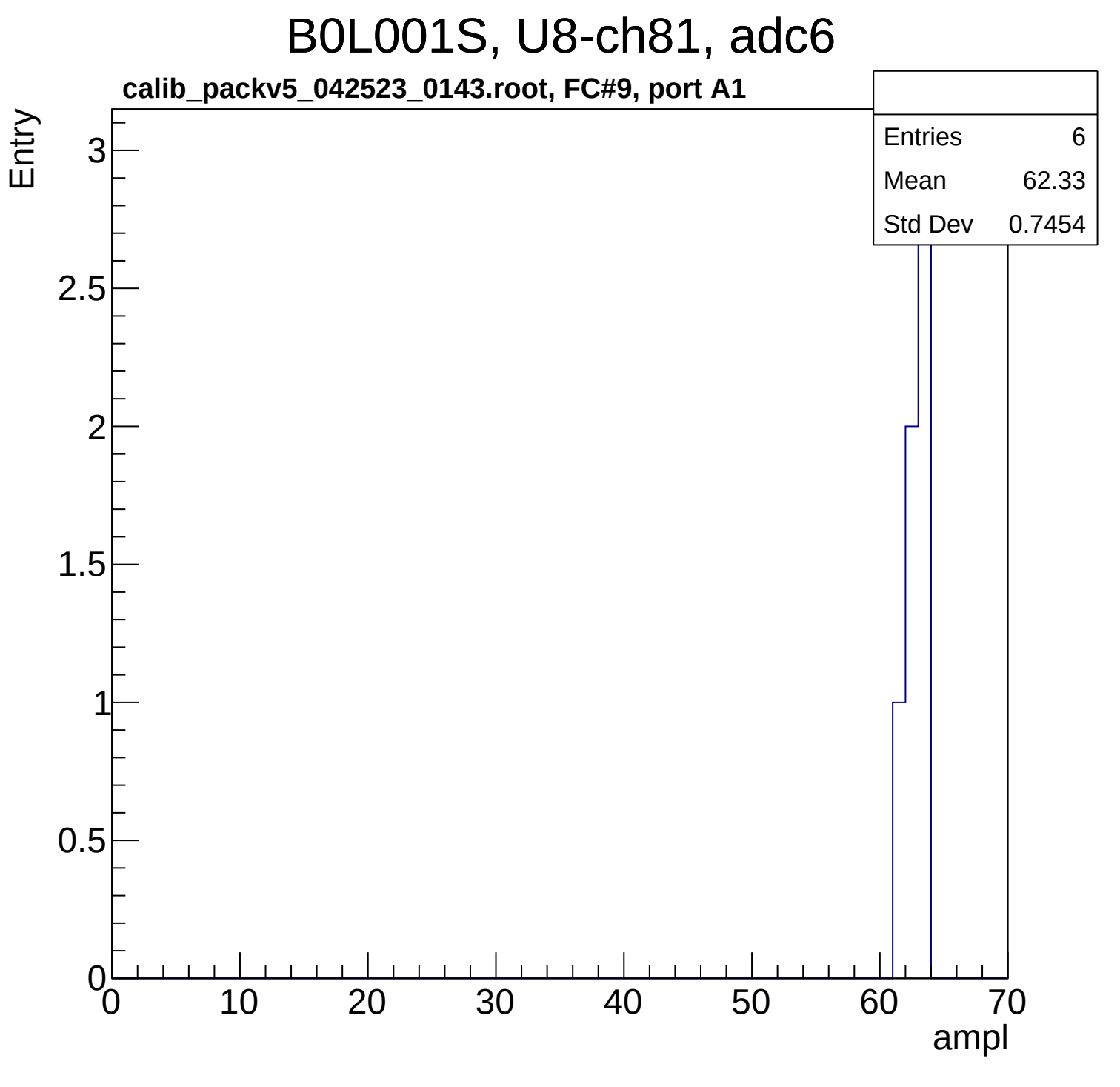
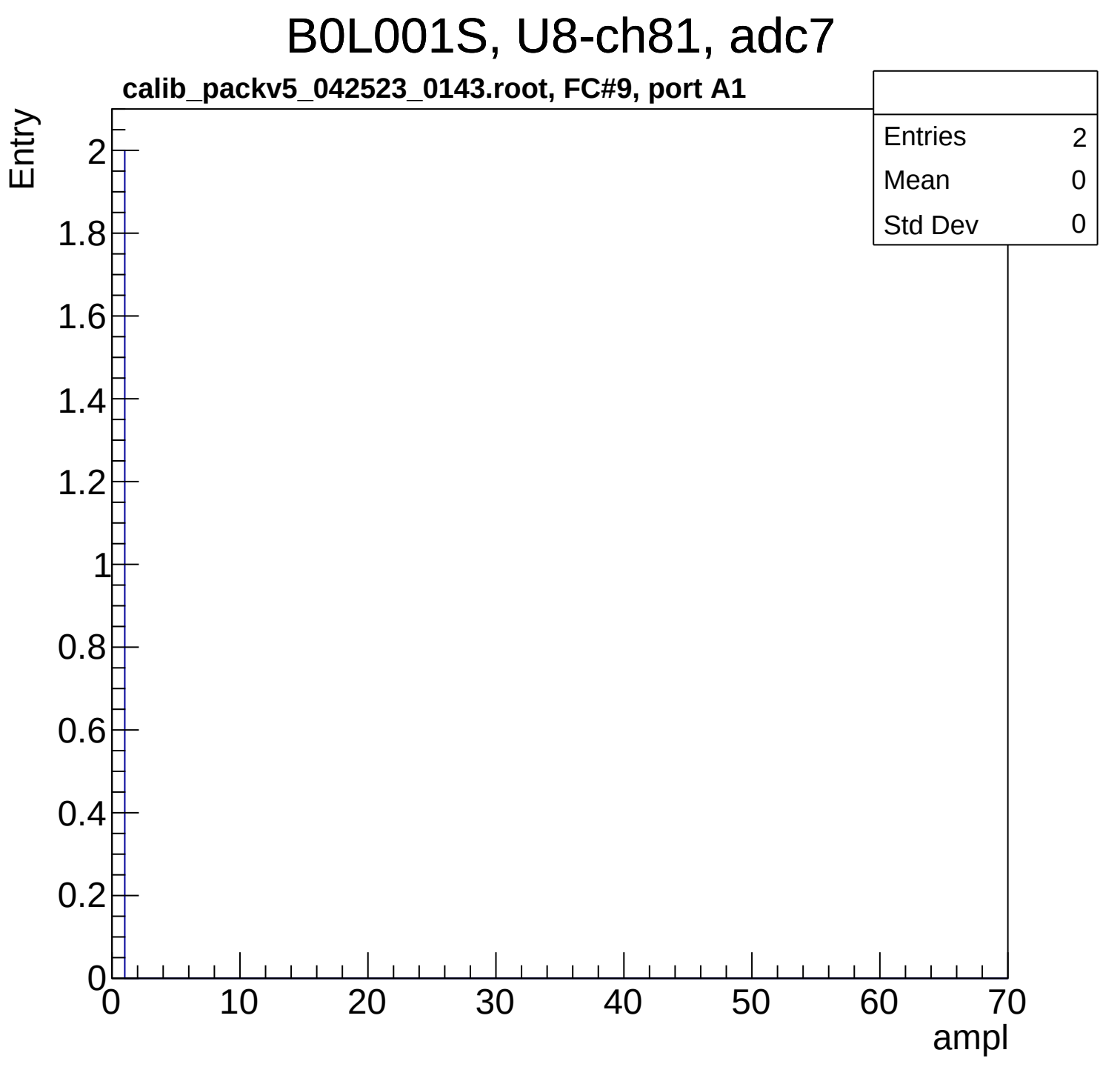
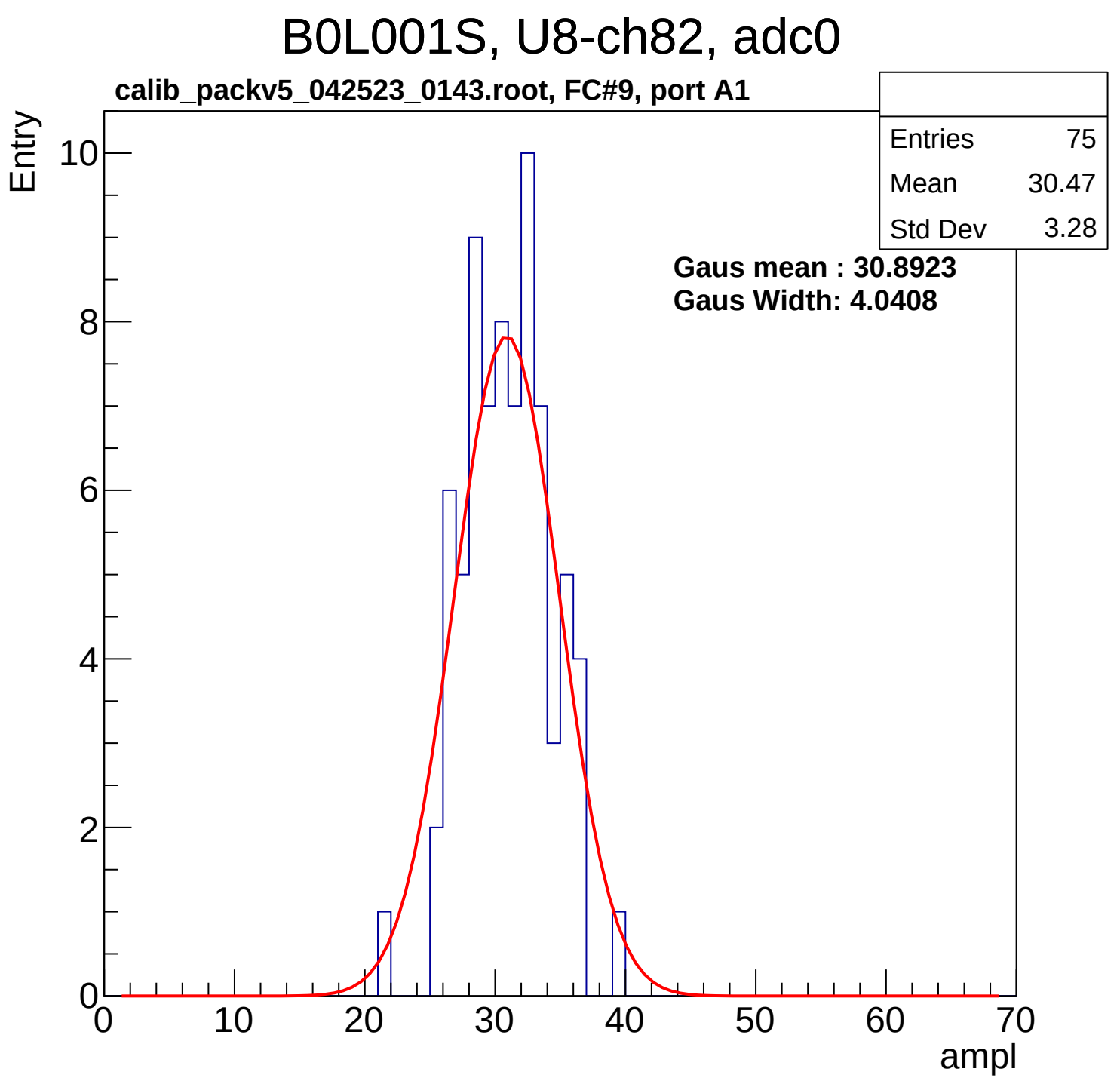
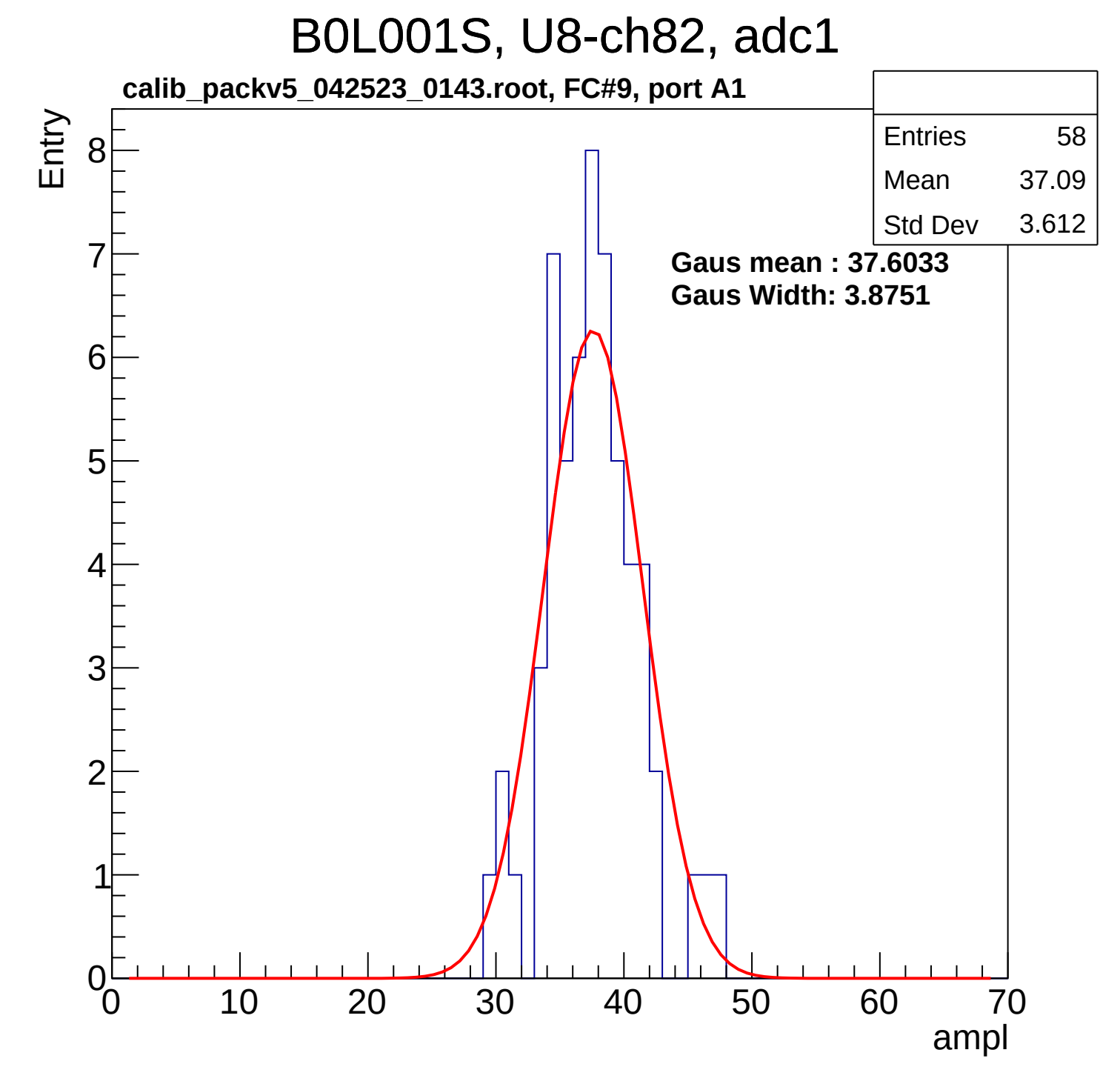
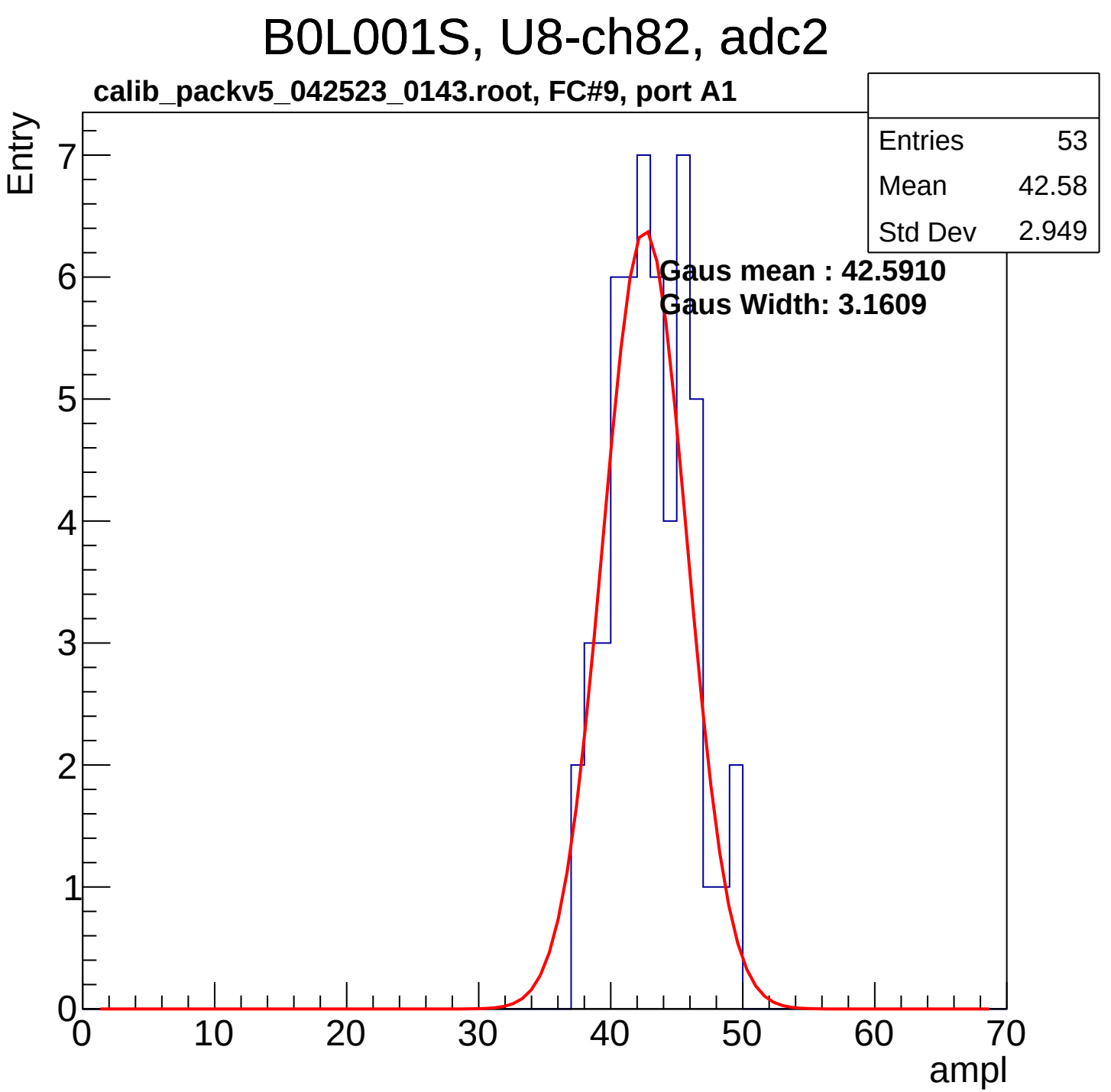
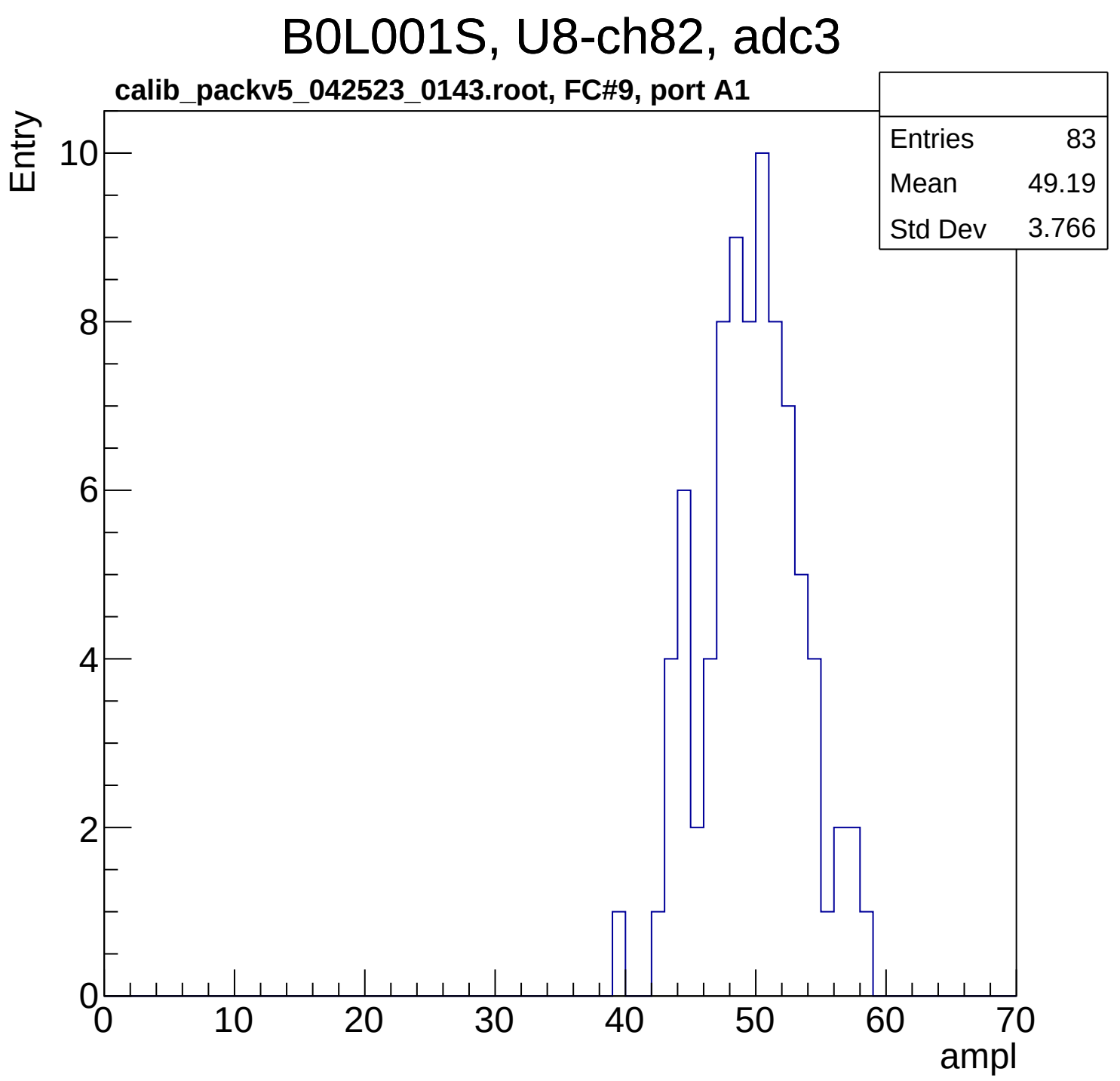

B0L001S, U8-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

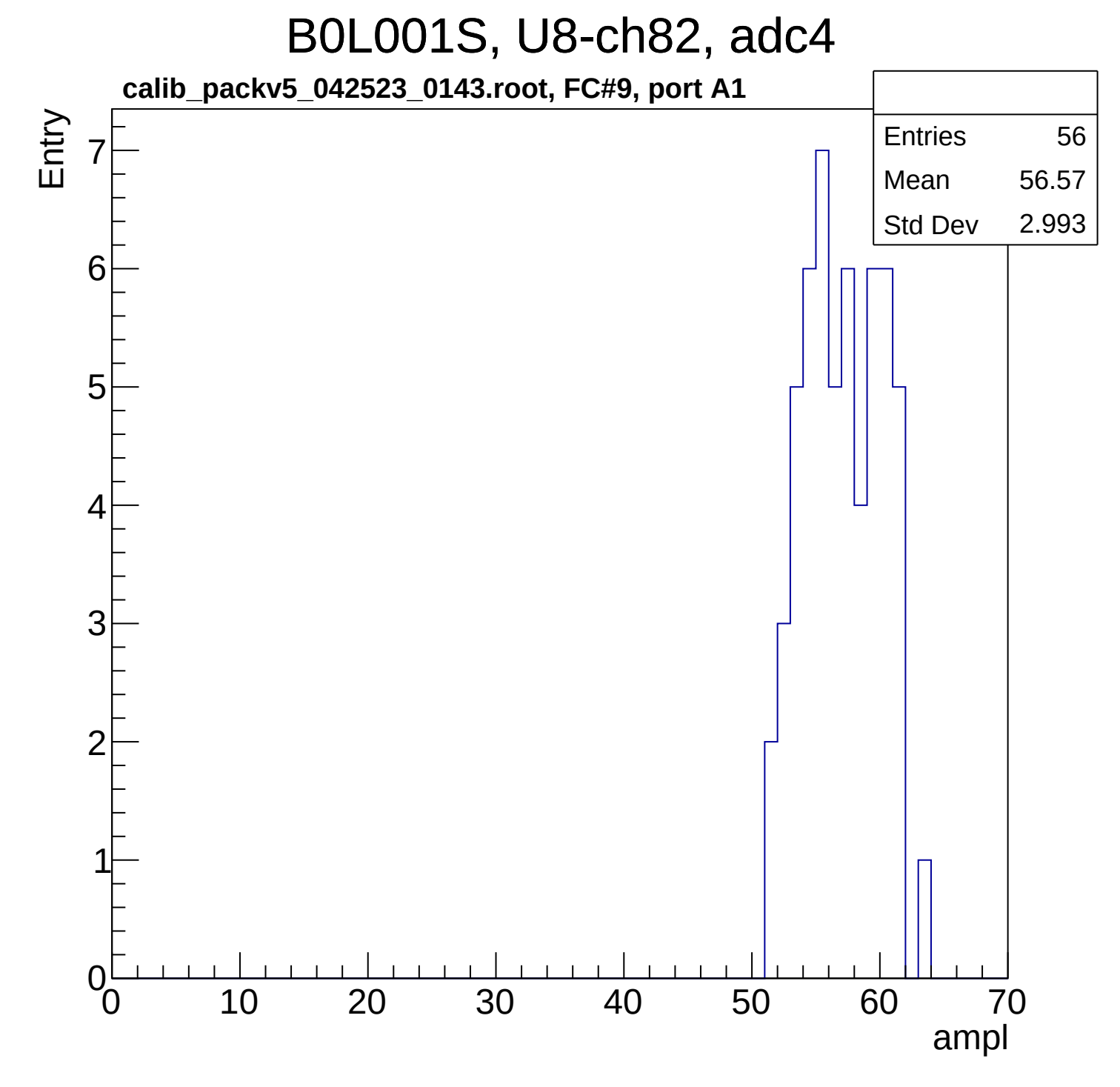

B0L001S, U8-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

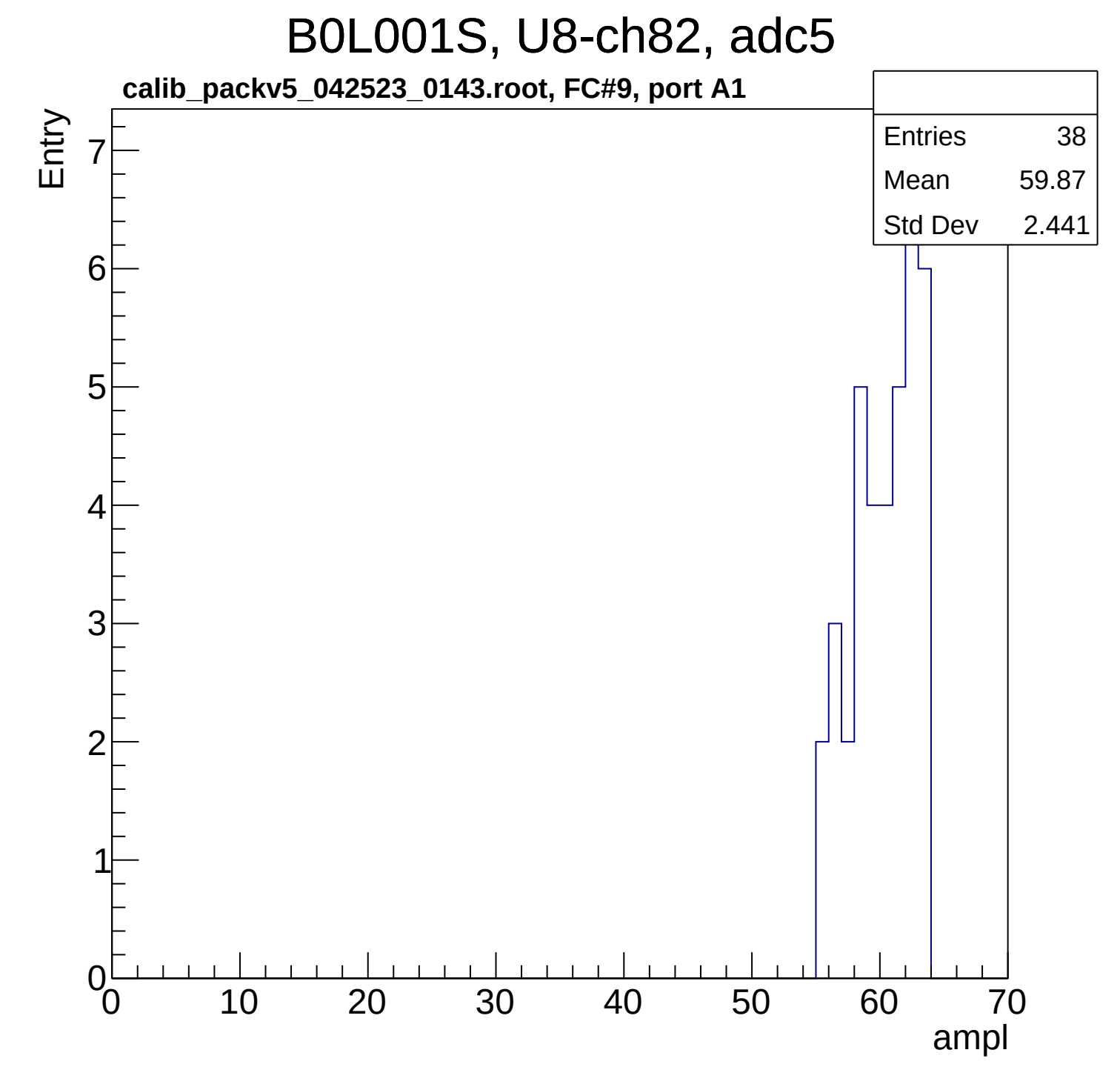

B0L001S, U8-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.47
Std Dev 3.28
Gaus mean : 30.8923
Gaus Width: 4.0408
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

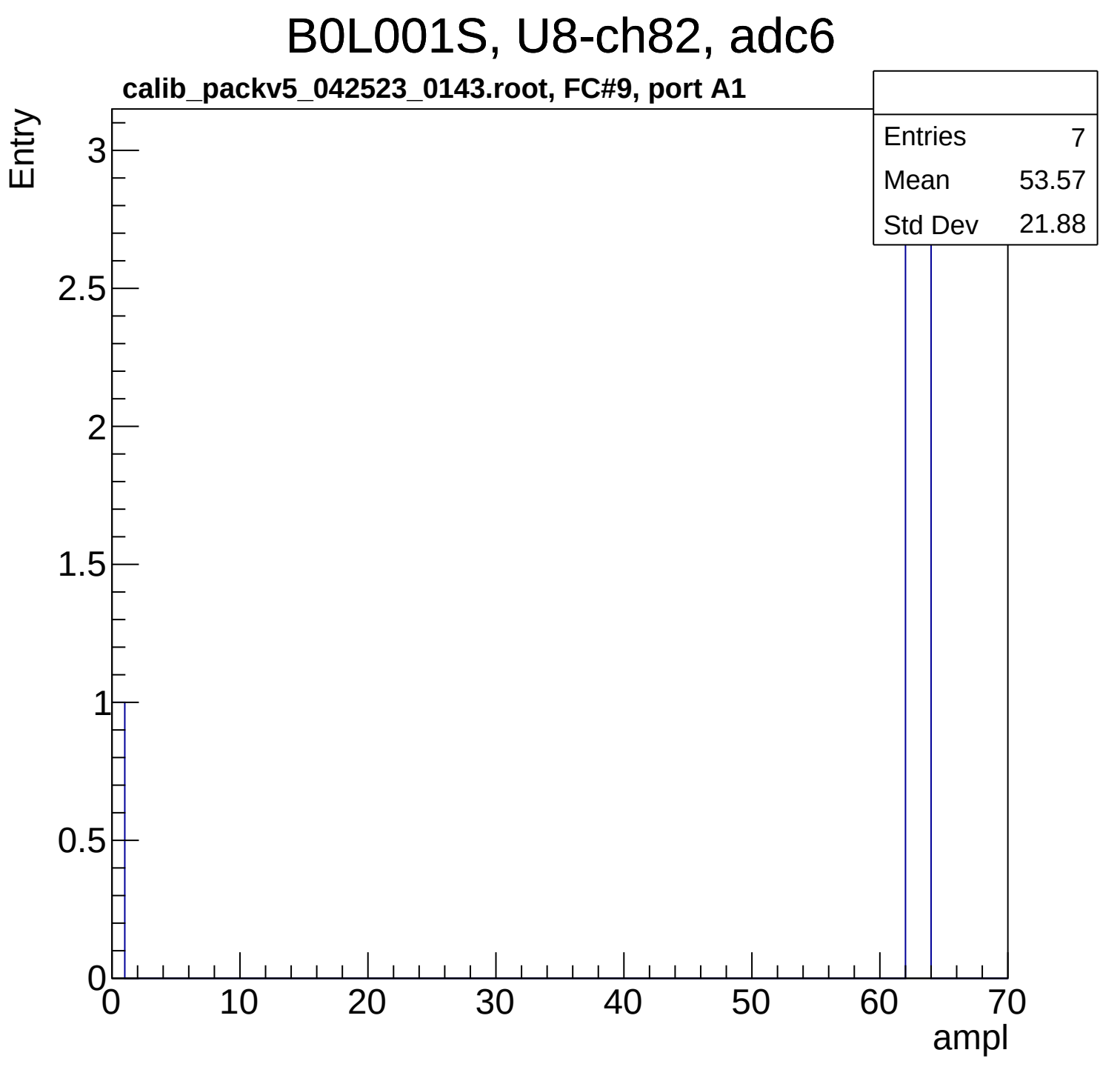

B0L001S, U8-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 37.09
Std Dev 3.612
Gaus mean : 37.6033
Gaus Width: 3.8751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

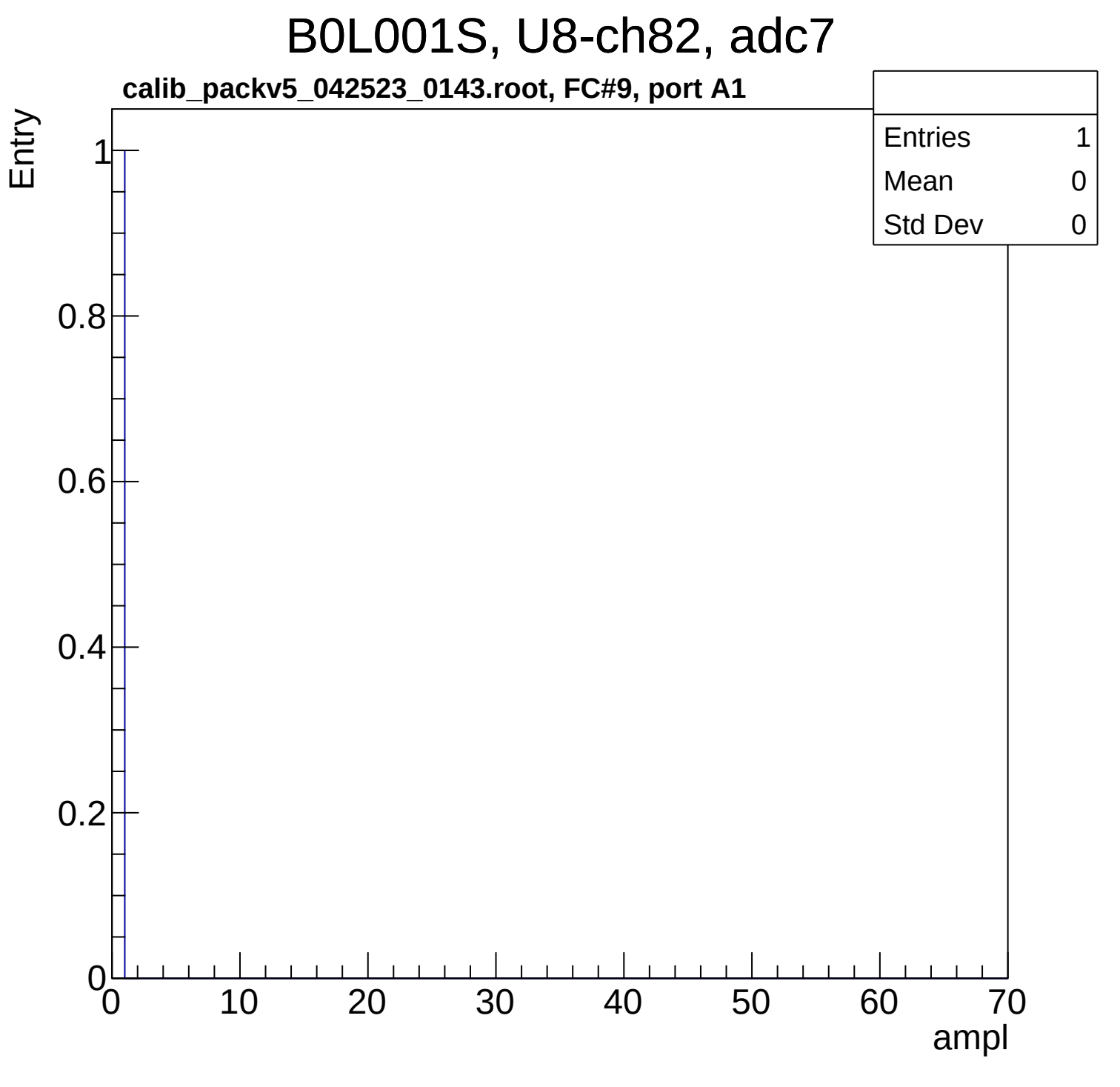

B0L001S, U8-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 42.58
Std Dev 2.949
Gaus mean : 42.5910
Gaus Width: 3.1609
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

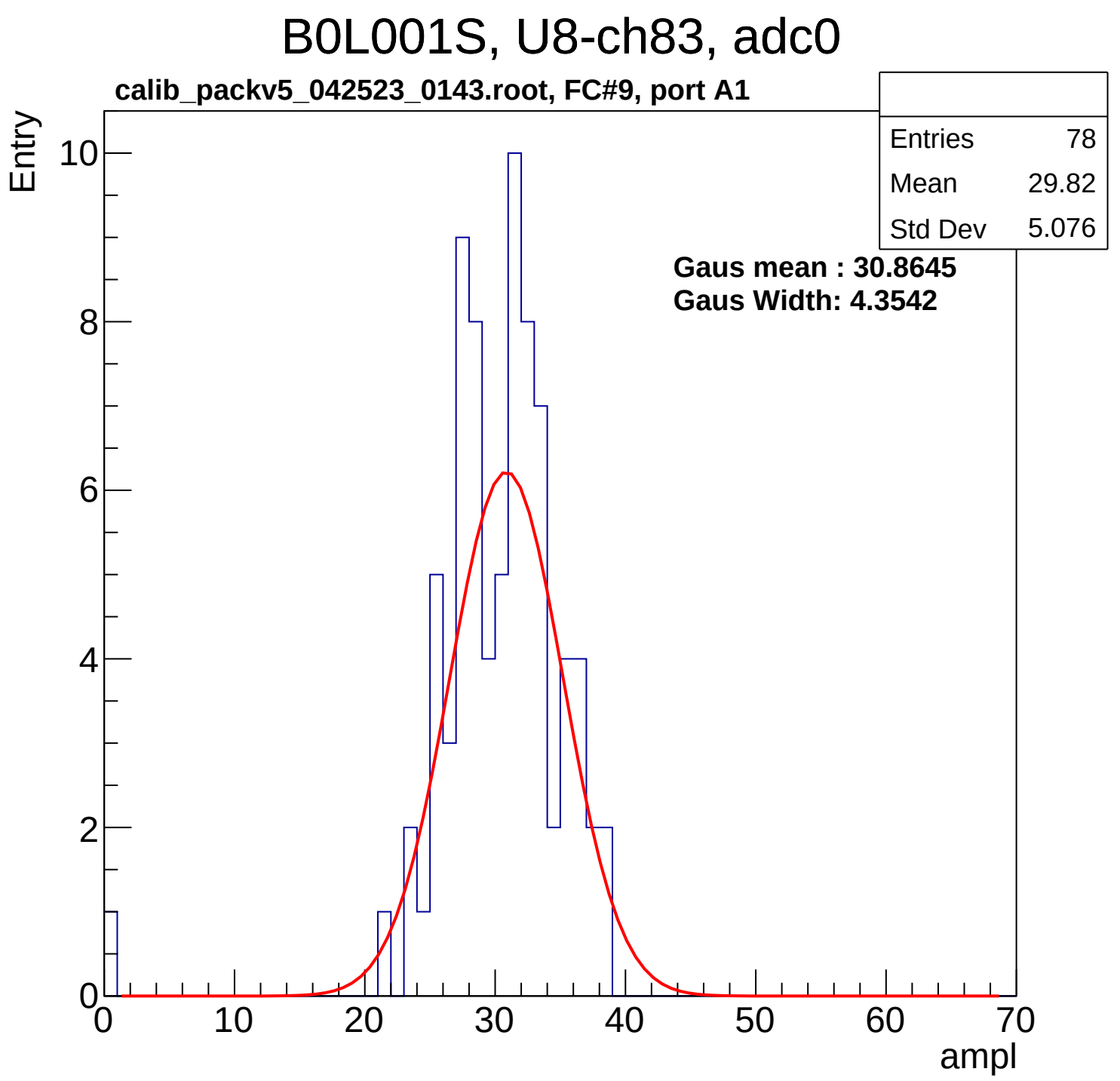

B0L001S, U8-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 49.19
Std Dev 3.766
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

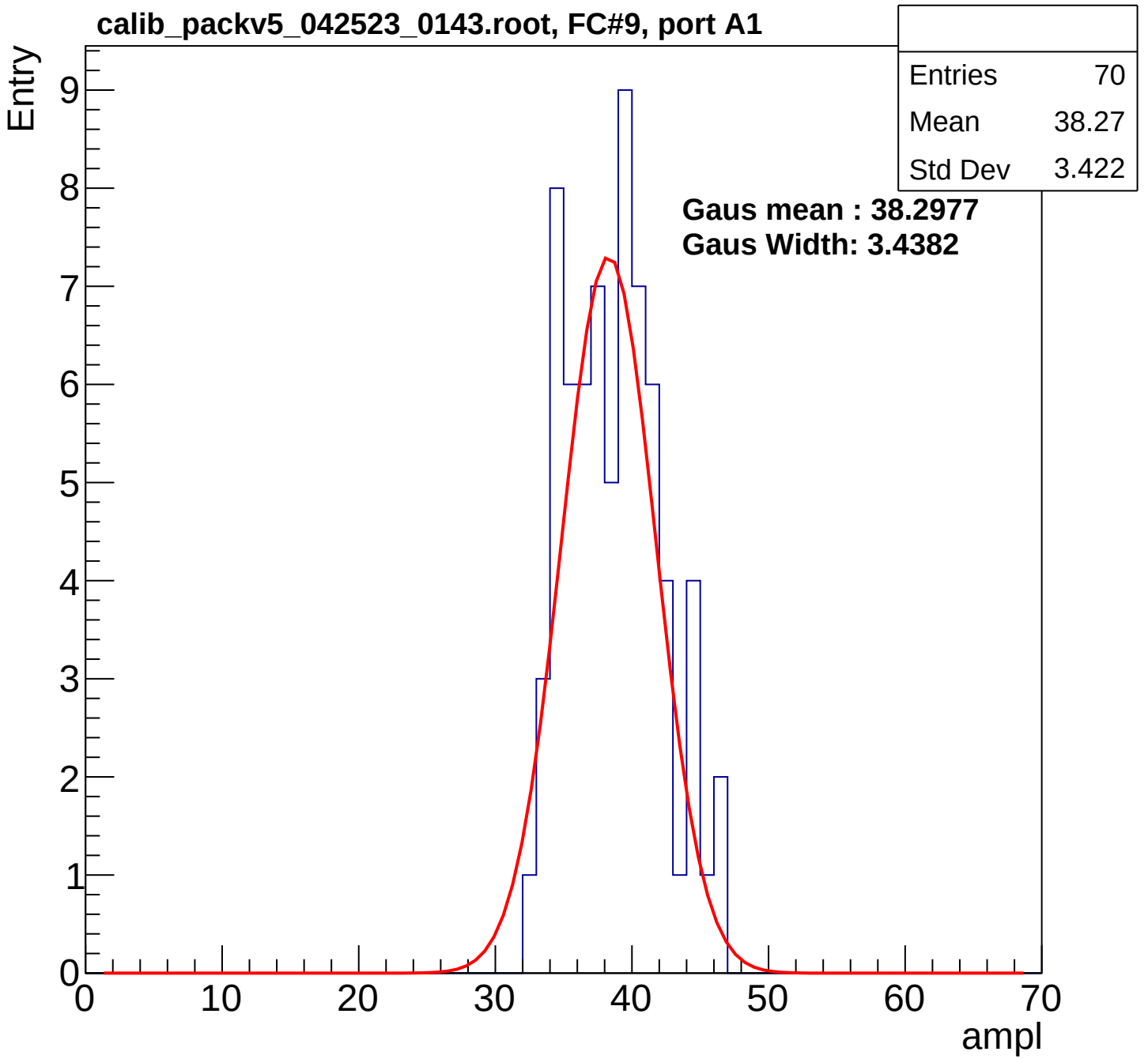

B0L001S, U8-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.57
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

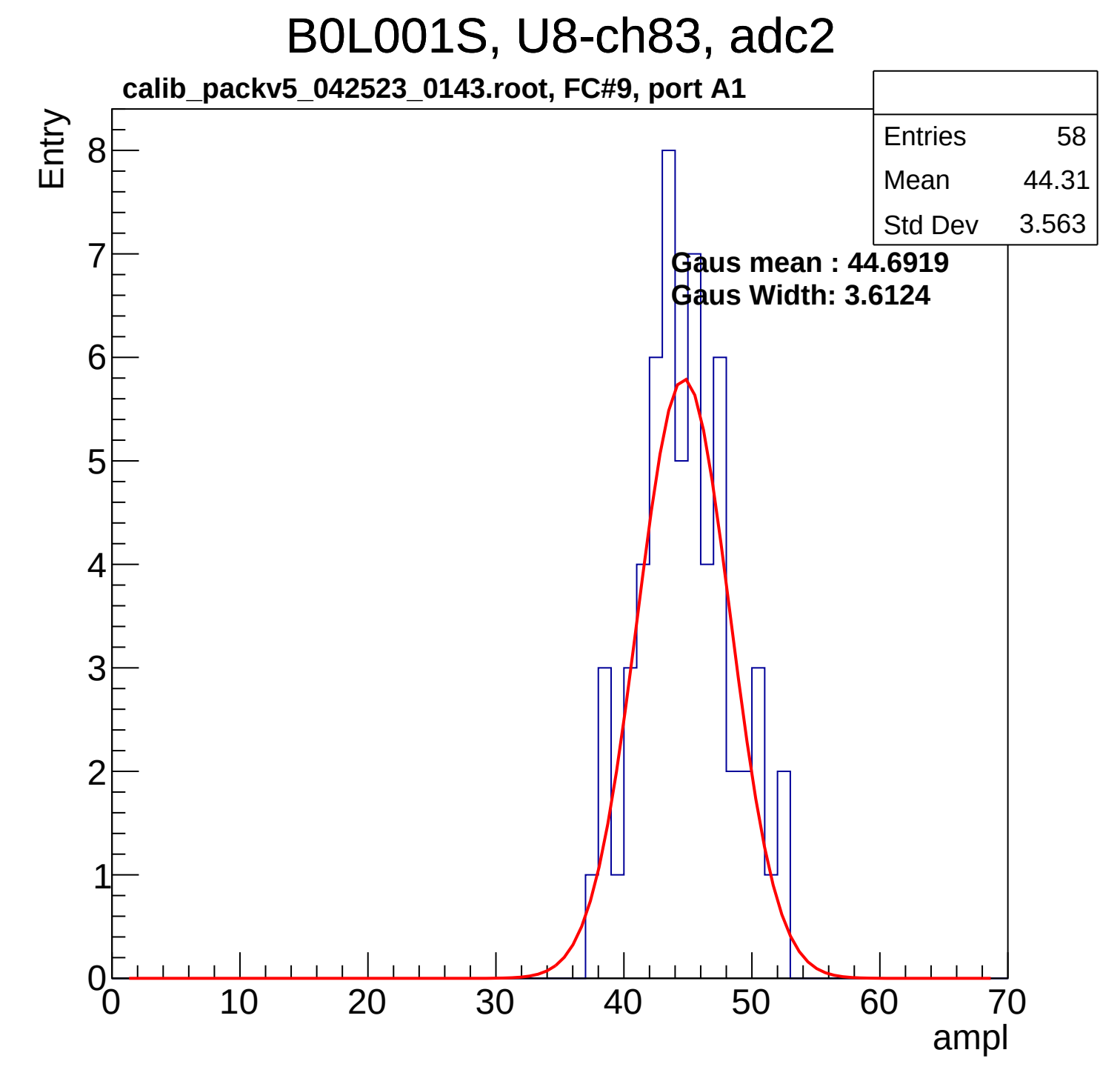

B0L001S, U8-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.87
Std Dev 2.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.82
Std Dev 5.076
Gaus mean : 30.8645
Gaus Width: 4.3542
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 38.27
Std Dev 3.422
Gaus mean : 38.2977
Gaus Width: 3.4382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.31
Std Dev 3.563
Gaus mean : 44.6919
Gaus Width: 3.6124
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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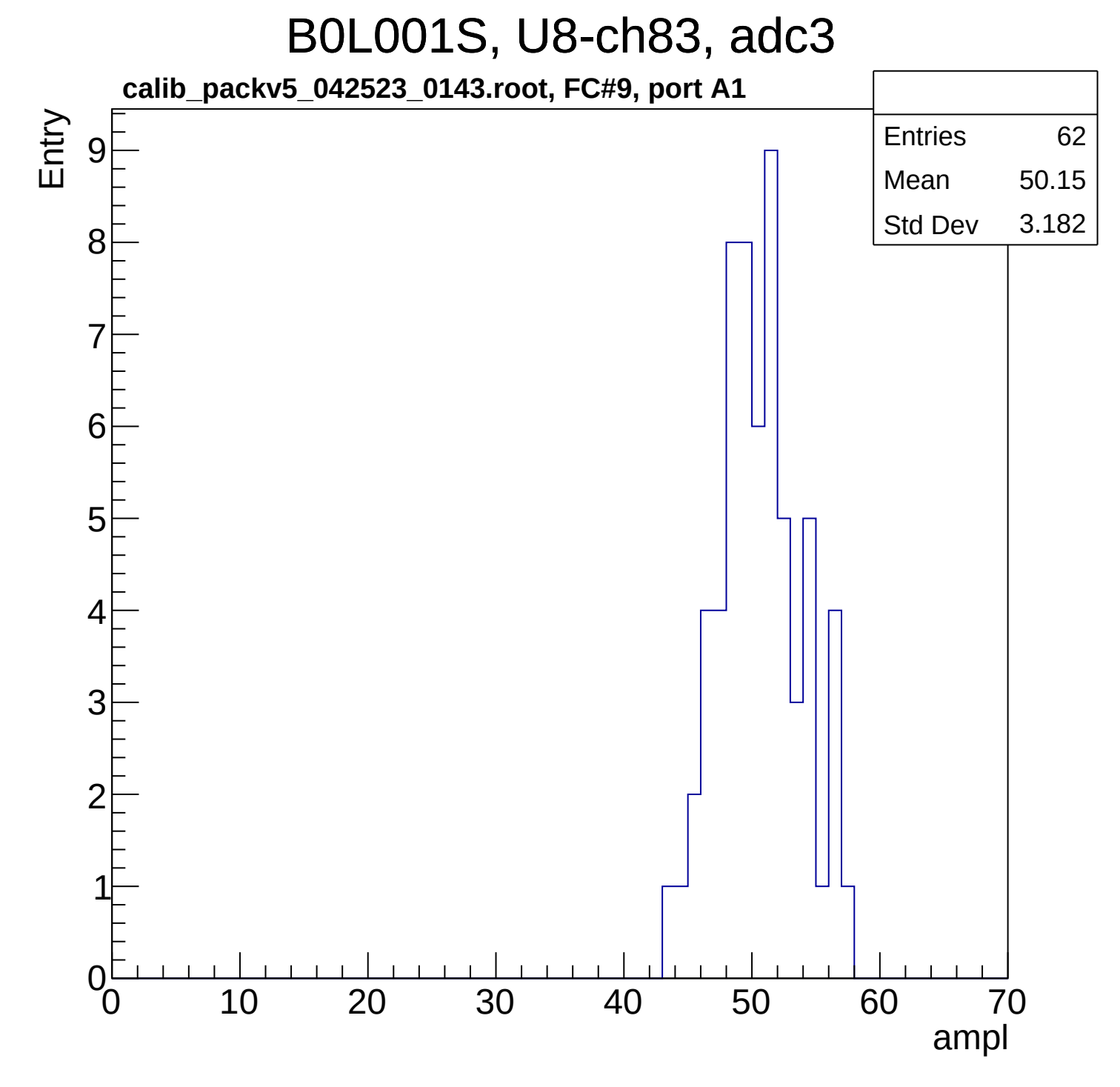
B0L001S, U8-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.15
Std Dev 3.182
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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60
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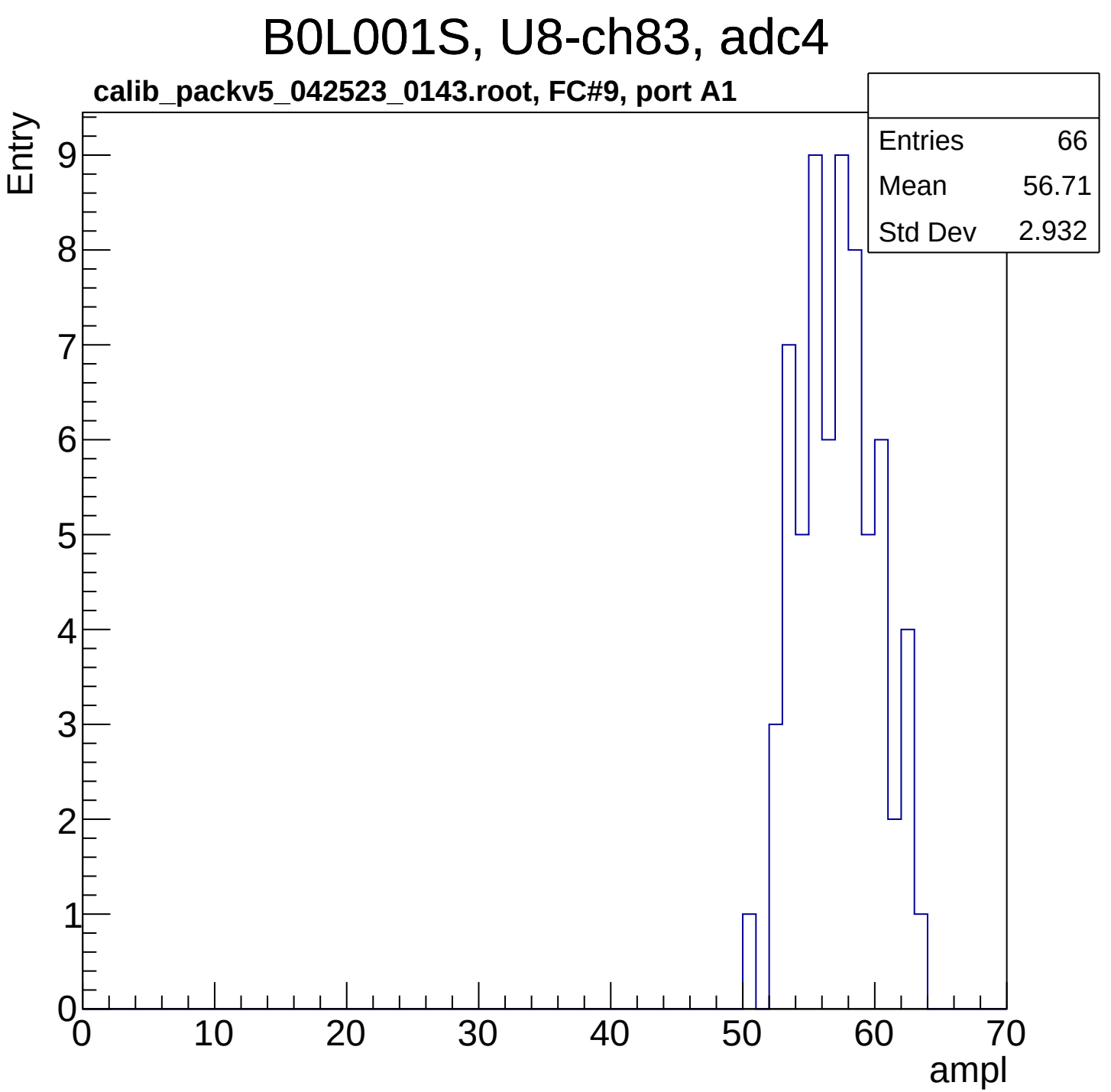
B0L001S, U8-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.71
Std Dev 2.932
Entry
ampl
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2
3
4
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6
7
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0
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60
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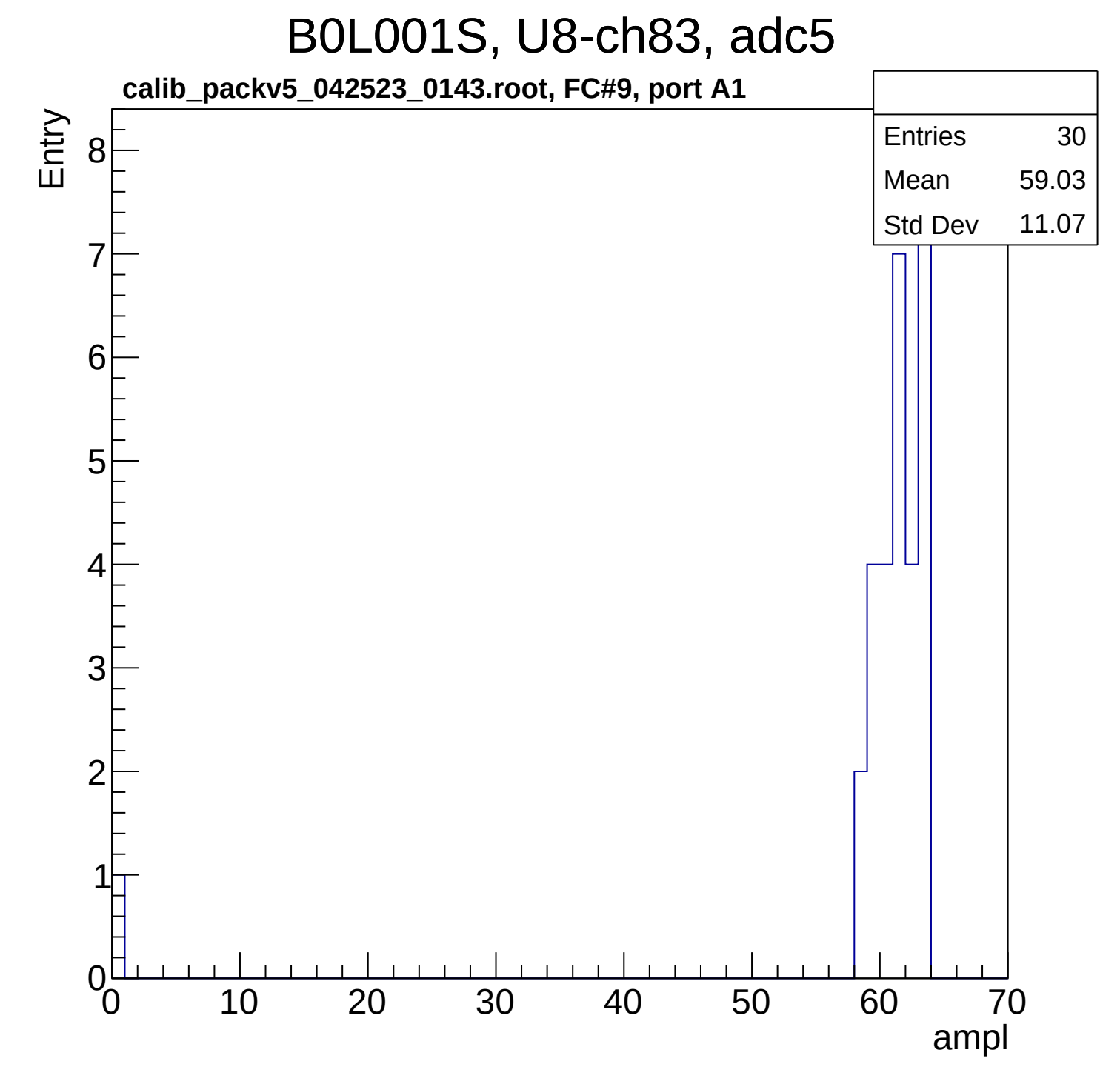
B0L001S, U8-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59.03
Std Dev 11.07
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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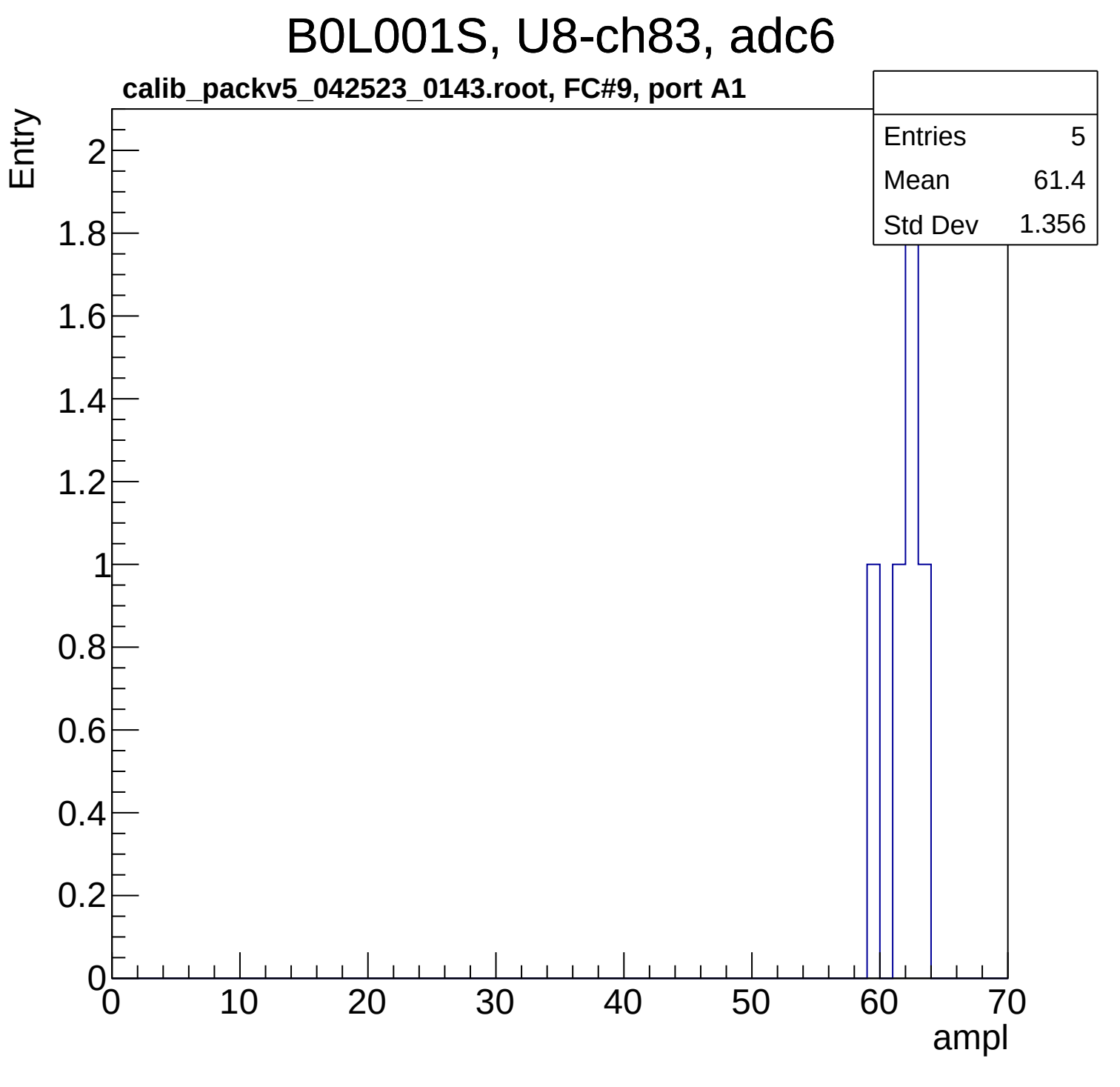
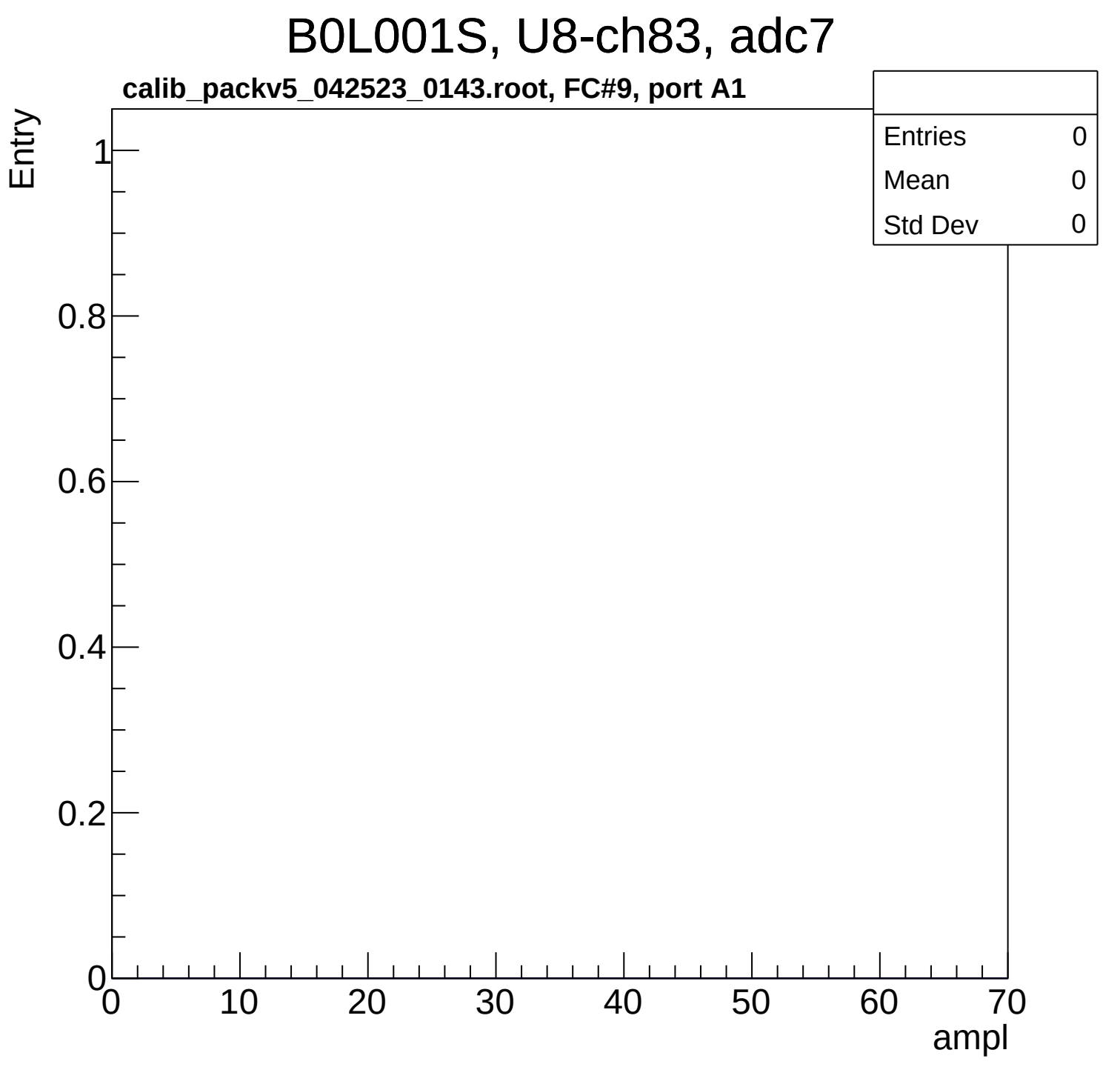
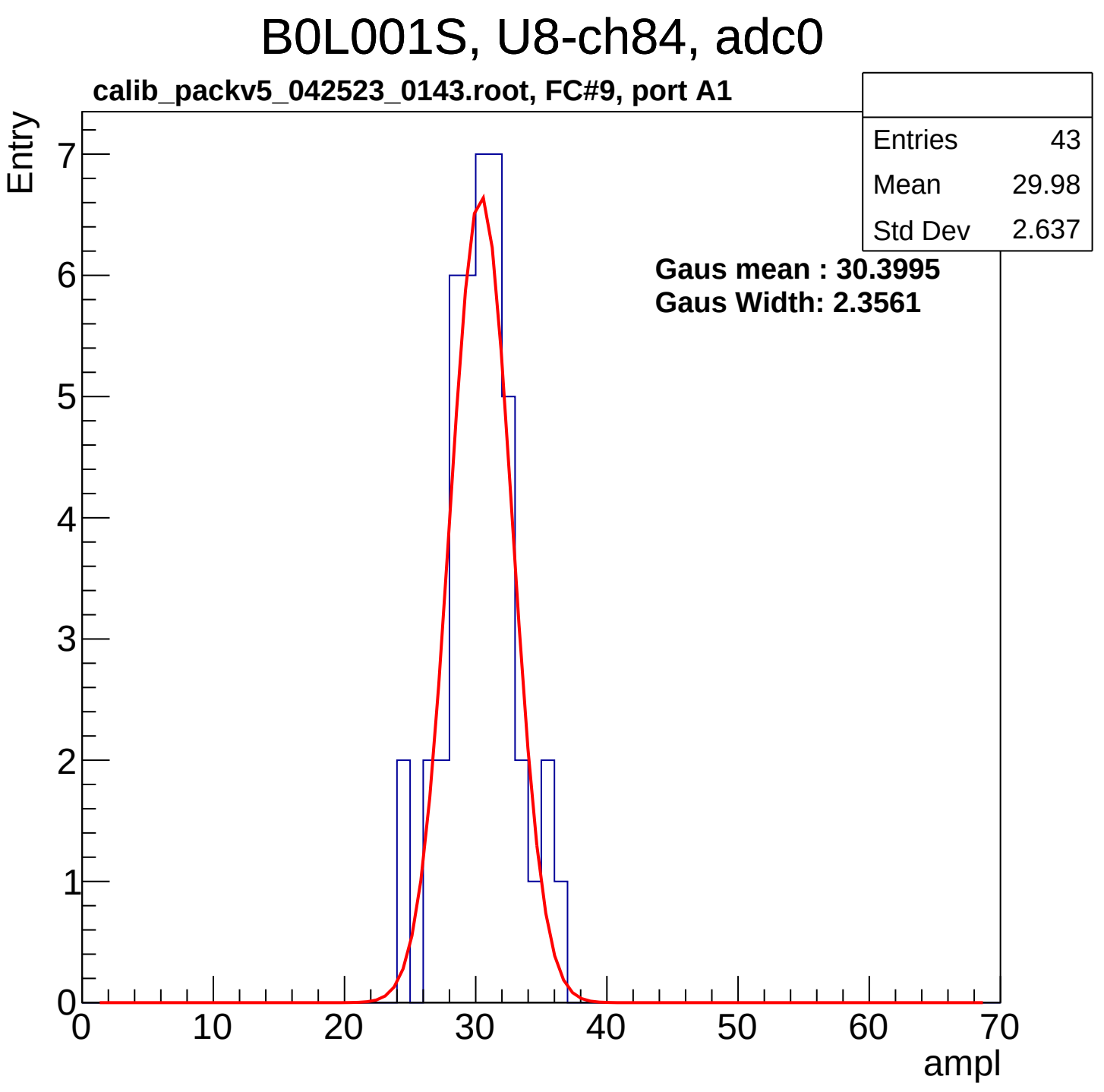
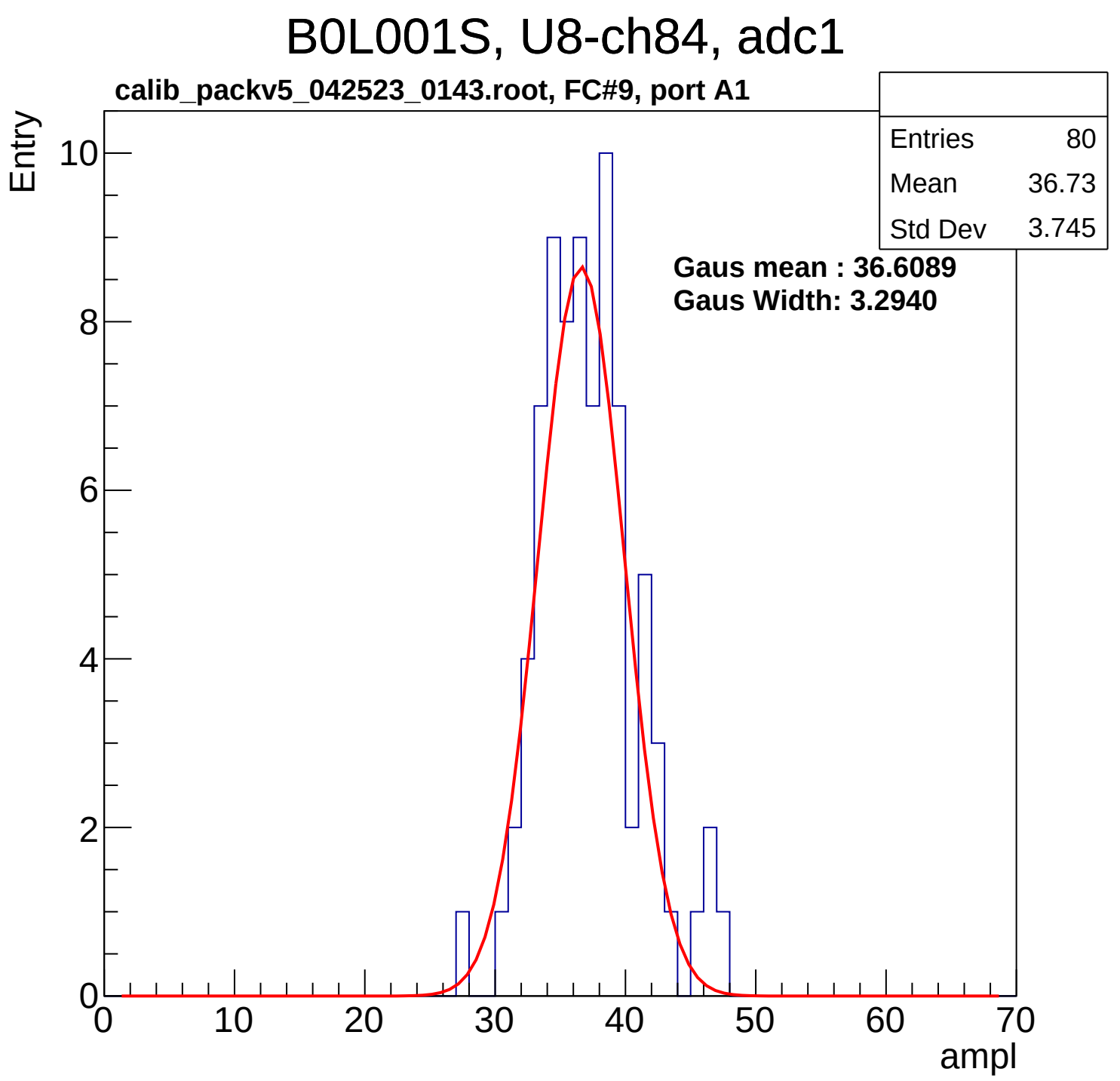
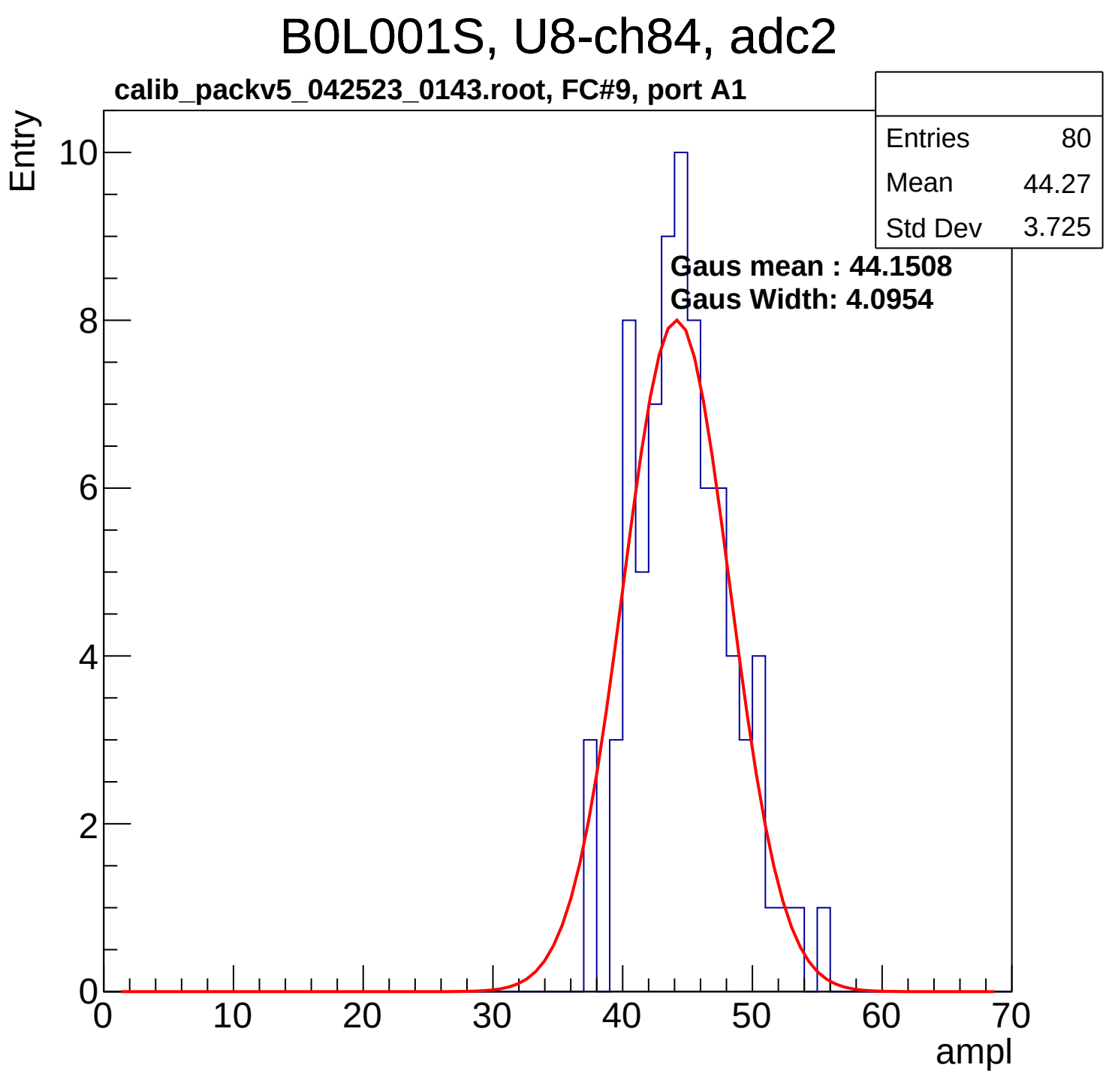
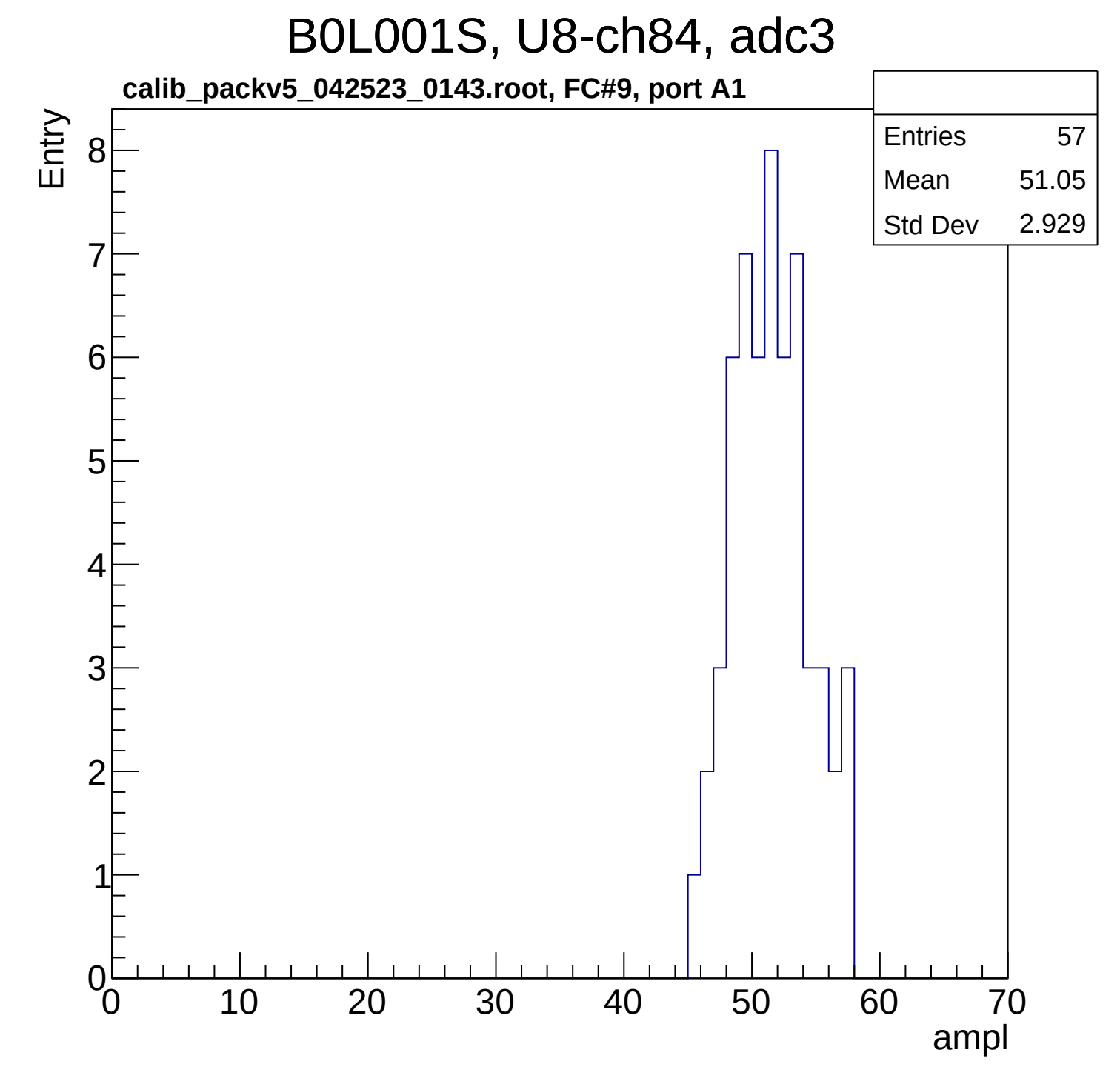

B0L001S, U8-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 29.98
Std Dev 2.637
Gaus mean : 30.3995
Gaus Width: 2.3561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 36.73
Std Dev 3.745
Gaus mean : 36.6089
Gaus Width: 3.2940
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 44.27
Std Dev 3.725
Gaus mean : 44.1508
Gaus Width: 4.0954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.05
Std Dev 2.929
Entry
ampl
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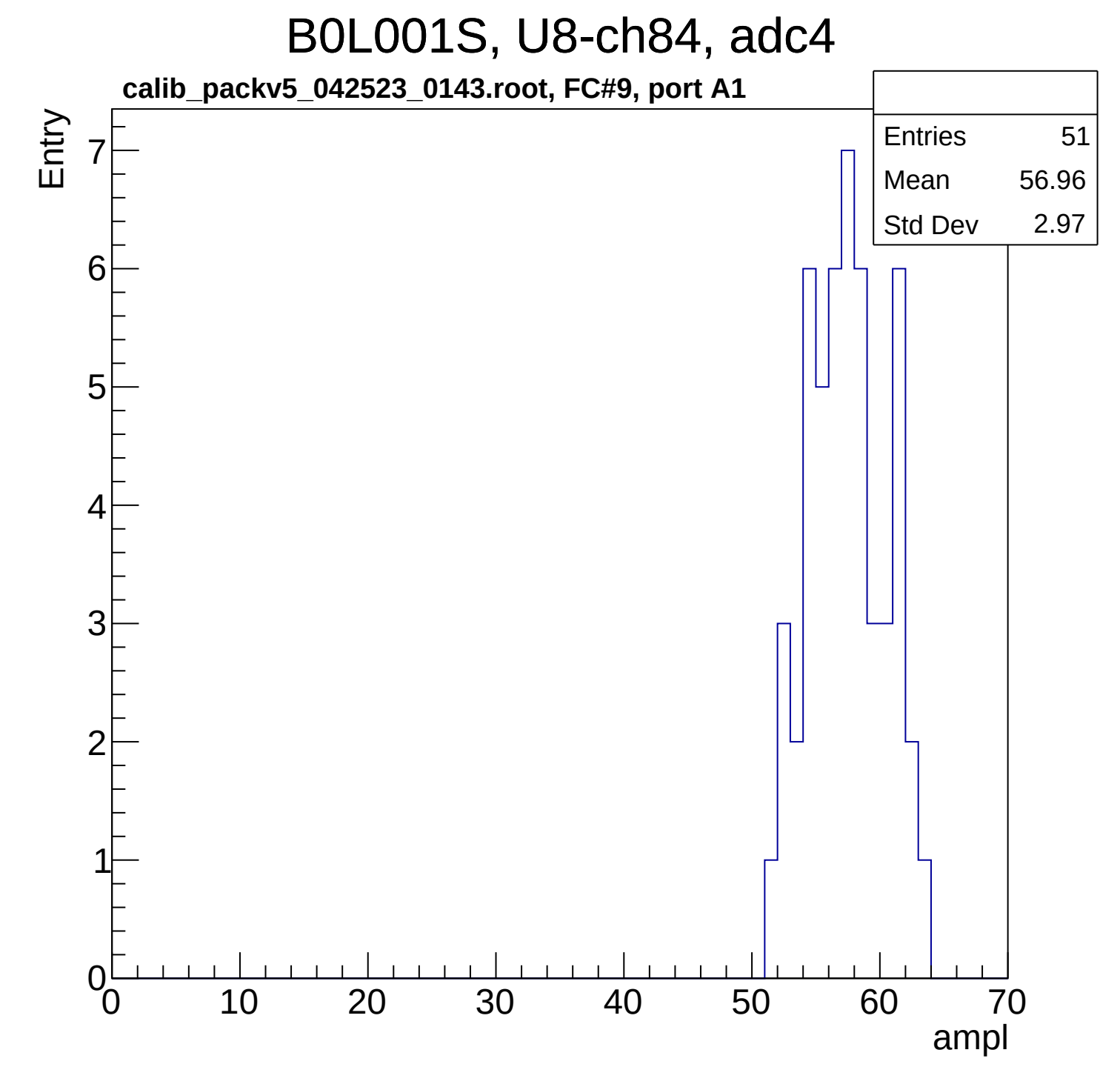
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.96
Std Dev 2.97
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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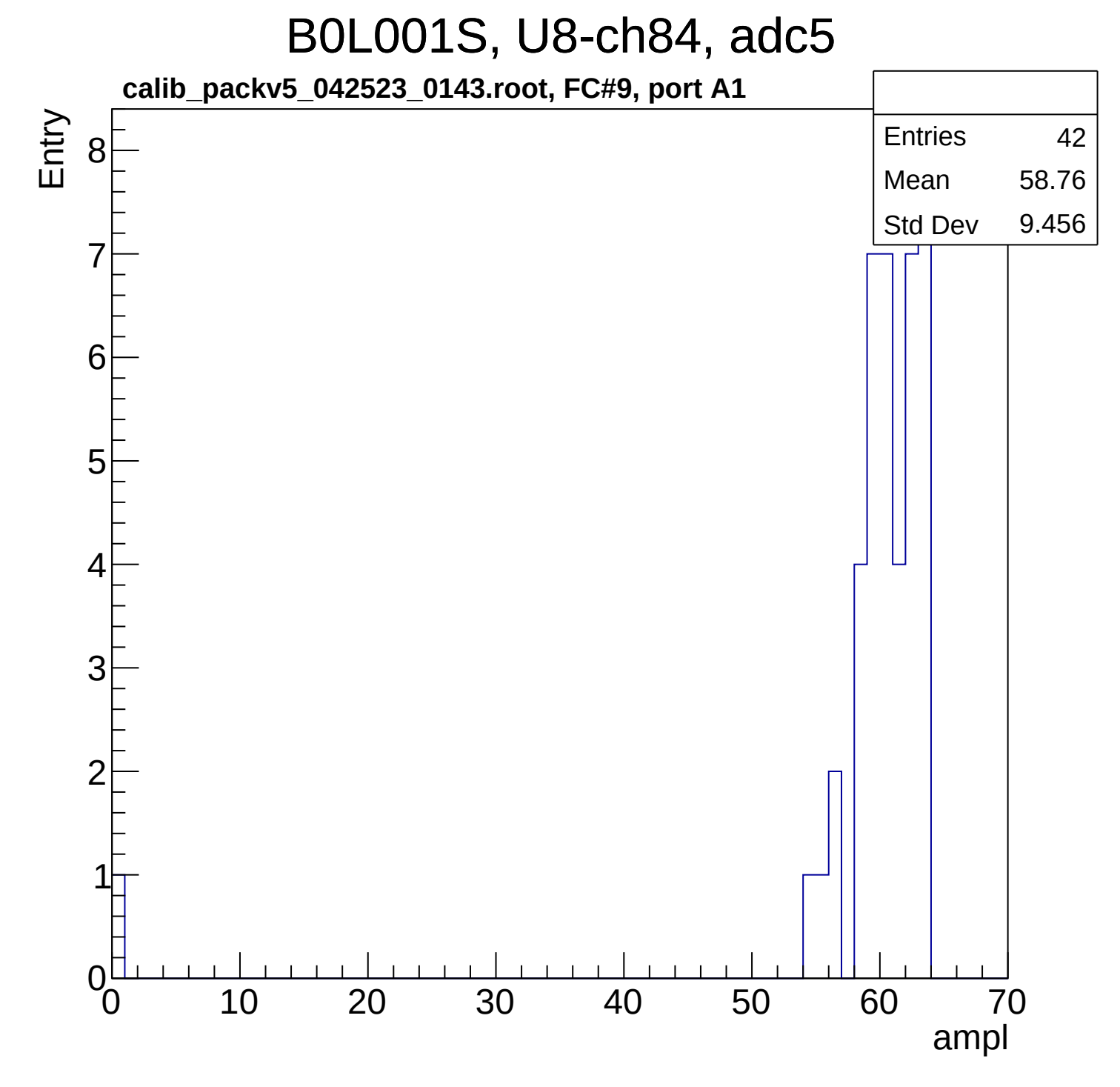
B0L001S, U8-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.76
Std Dev 9.456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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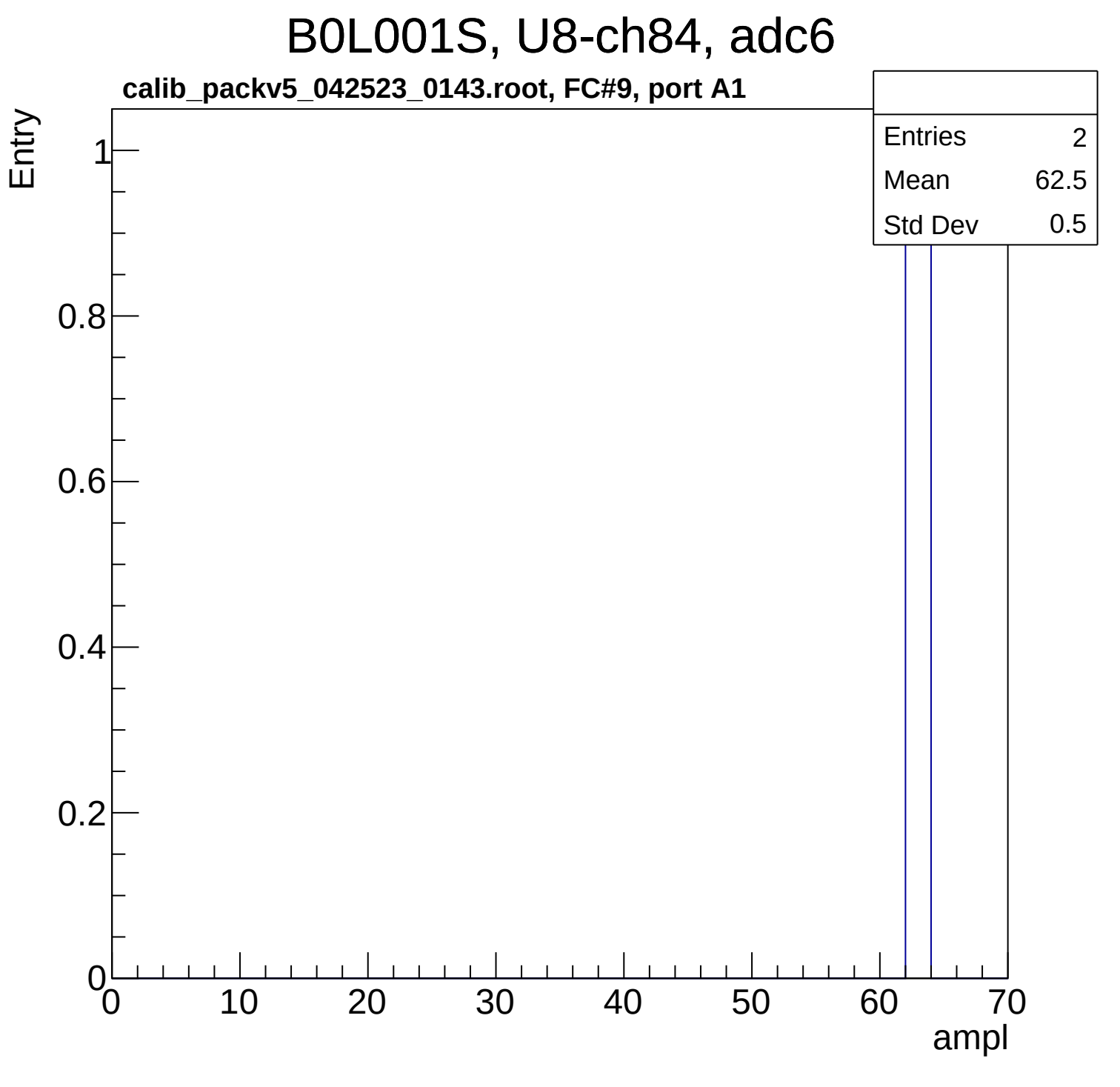
B0L001S, U8-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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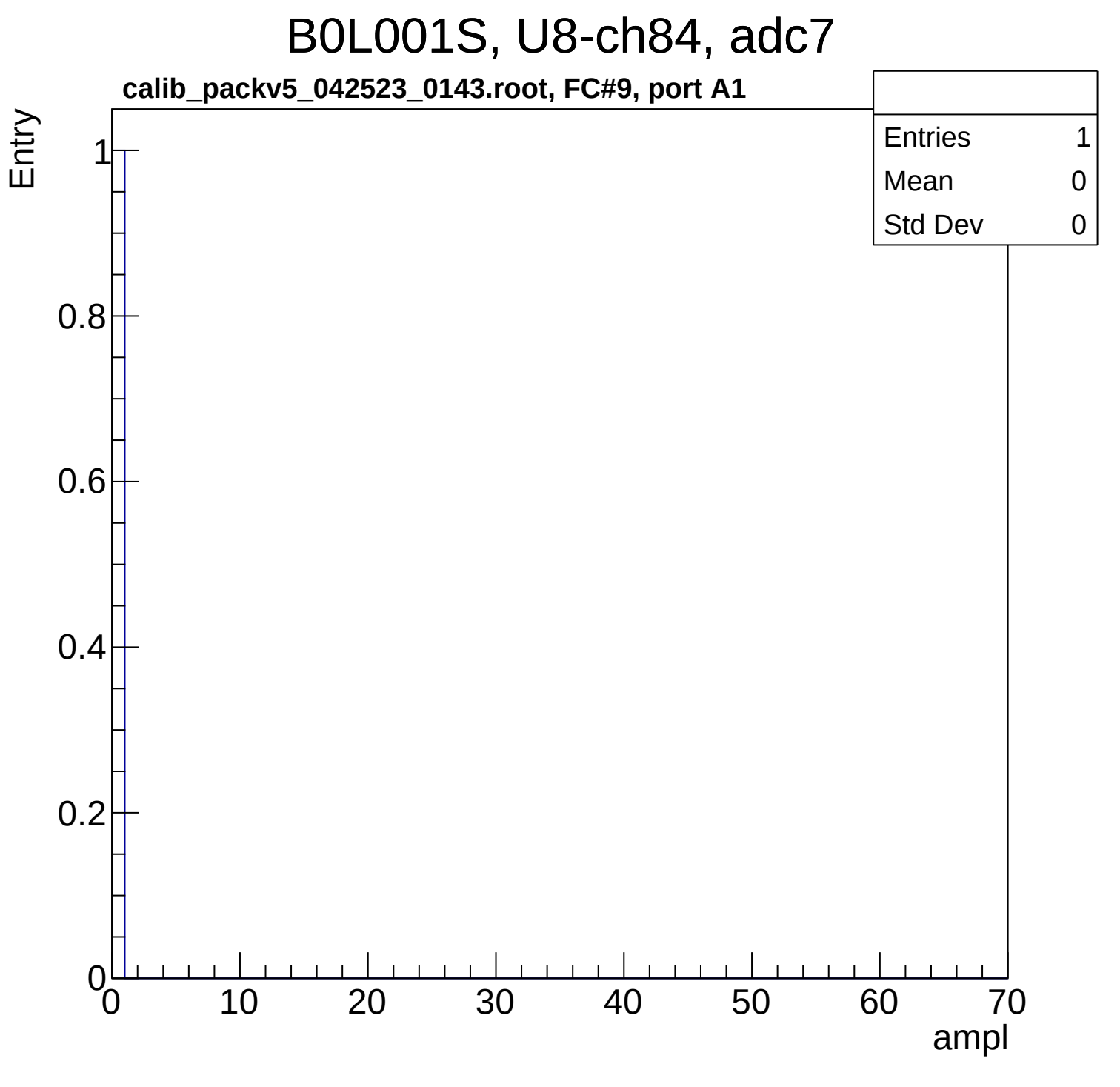
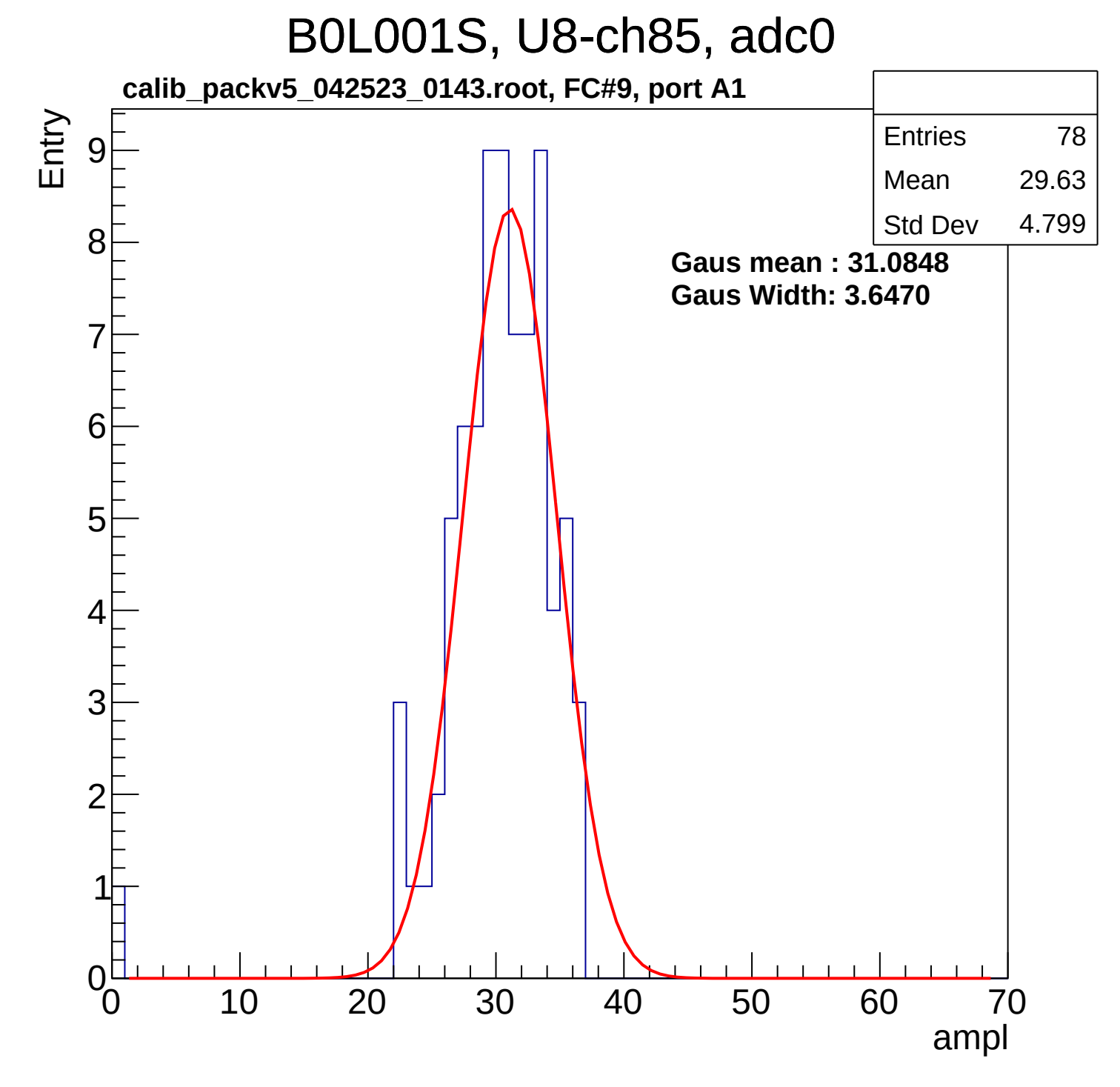
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.63
Std Dev 4.799
Gaus mean : 31.0848
Gaus Width: 3.6470
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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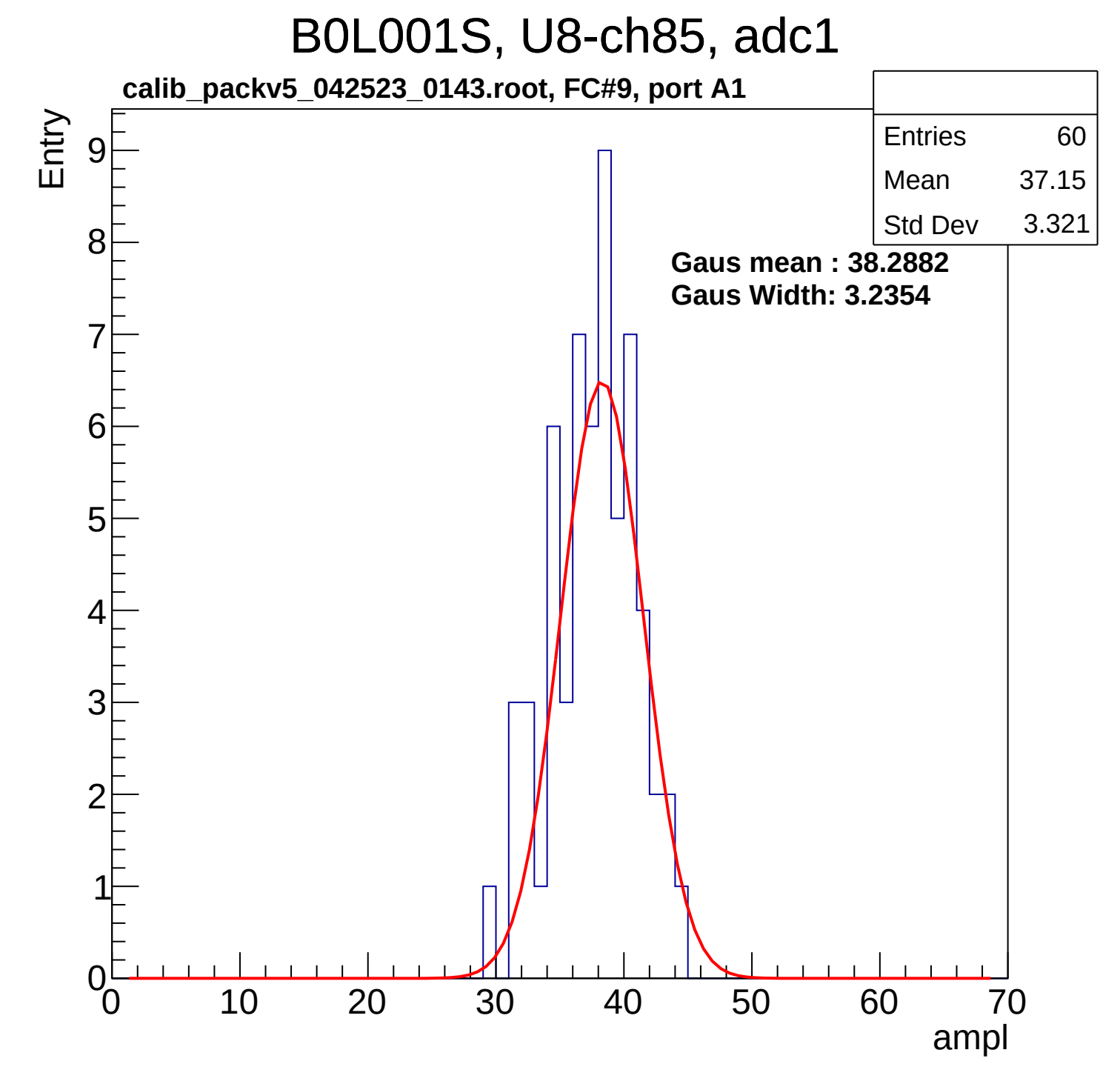
B0L001S, U8-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.15
Std Dev 3.321
Gaus mean : 38.2882
Gaus Width: 3.2354
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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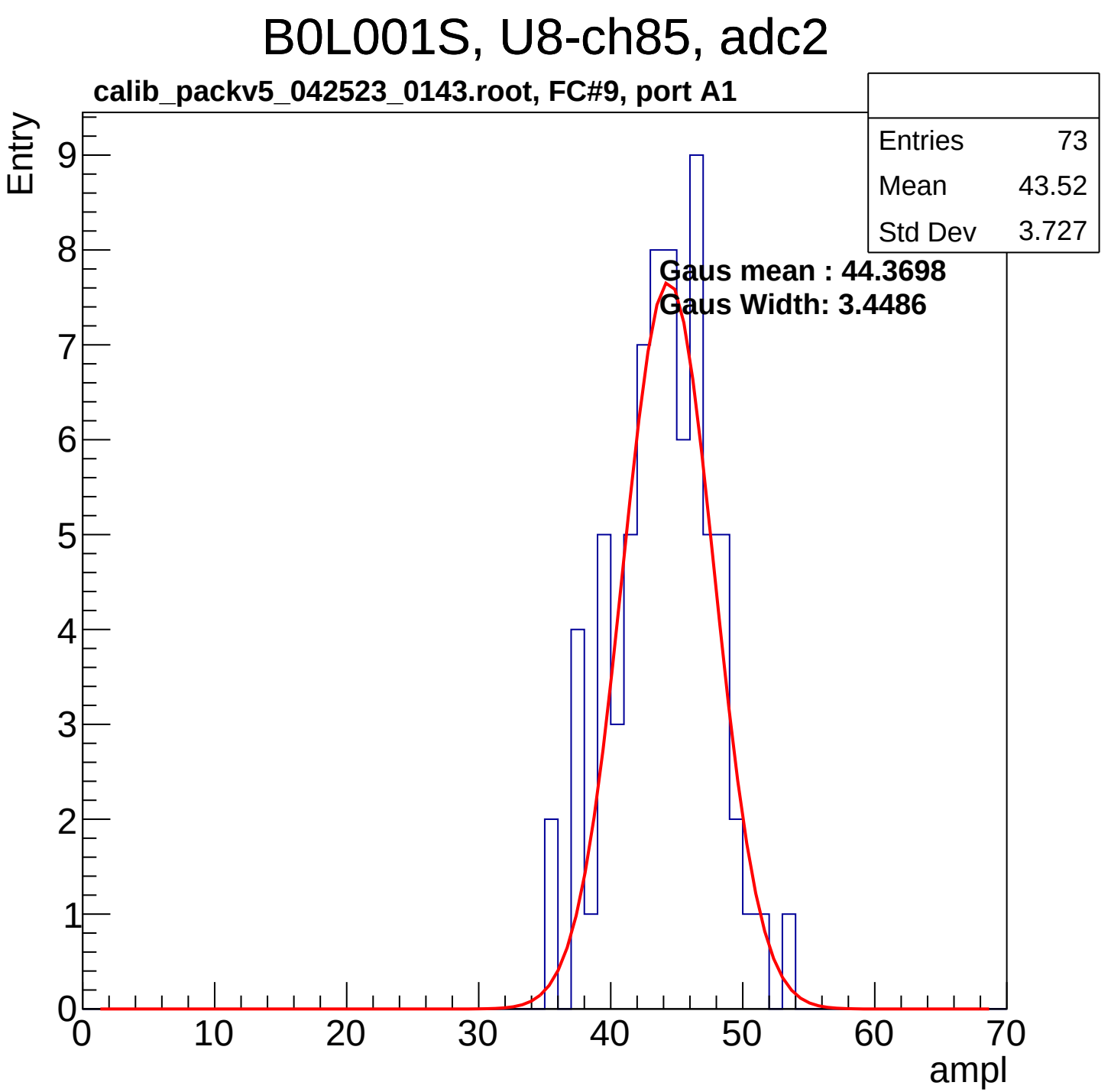
B0L001S, U8-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.52
Std Dev 3.727
Gaus mean : 44.3698
Gaus Width: 3.4486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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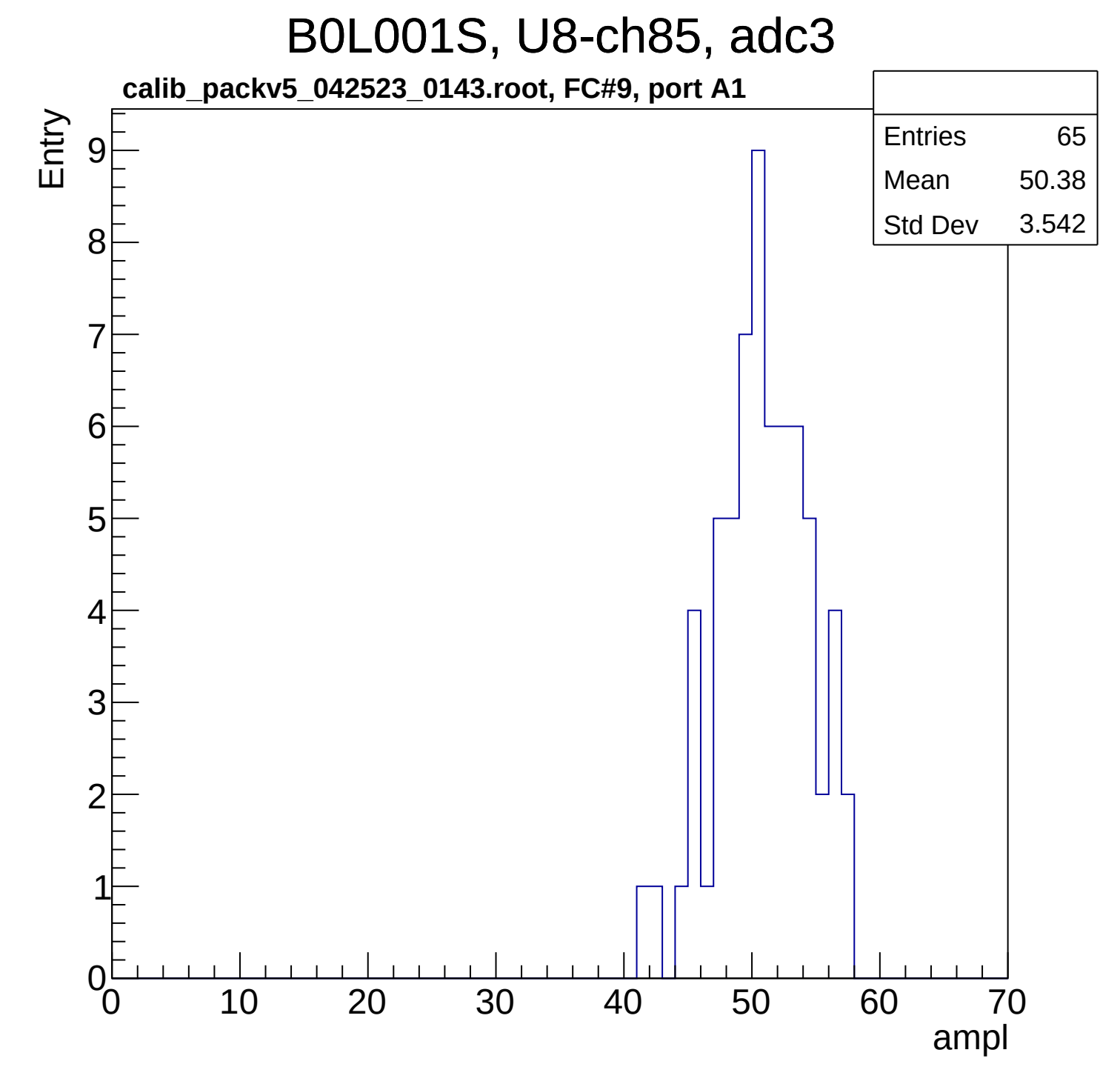
B0L001S, U8-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.38
Std Dev 3.542
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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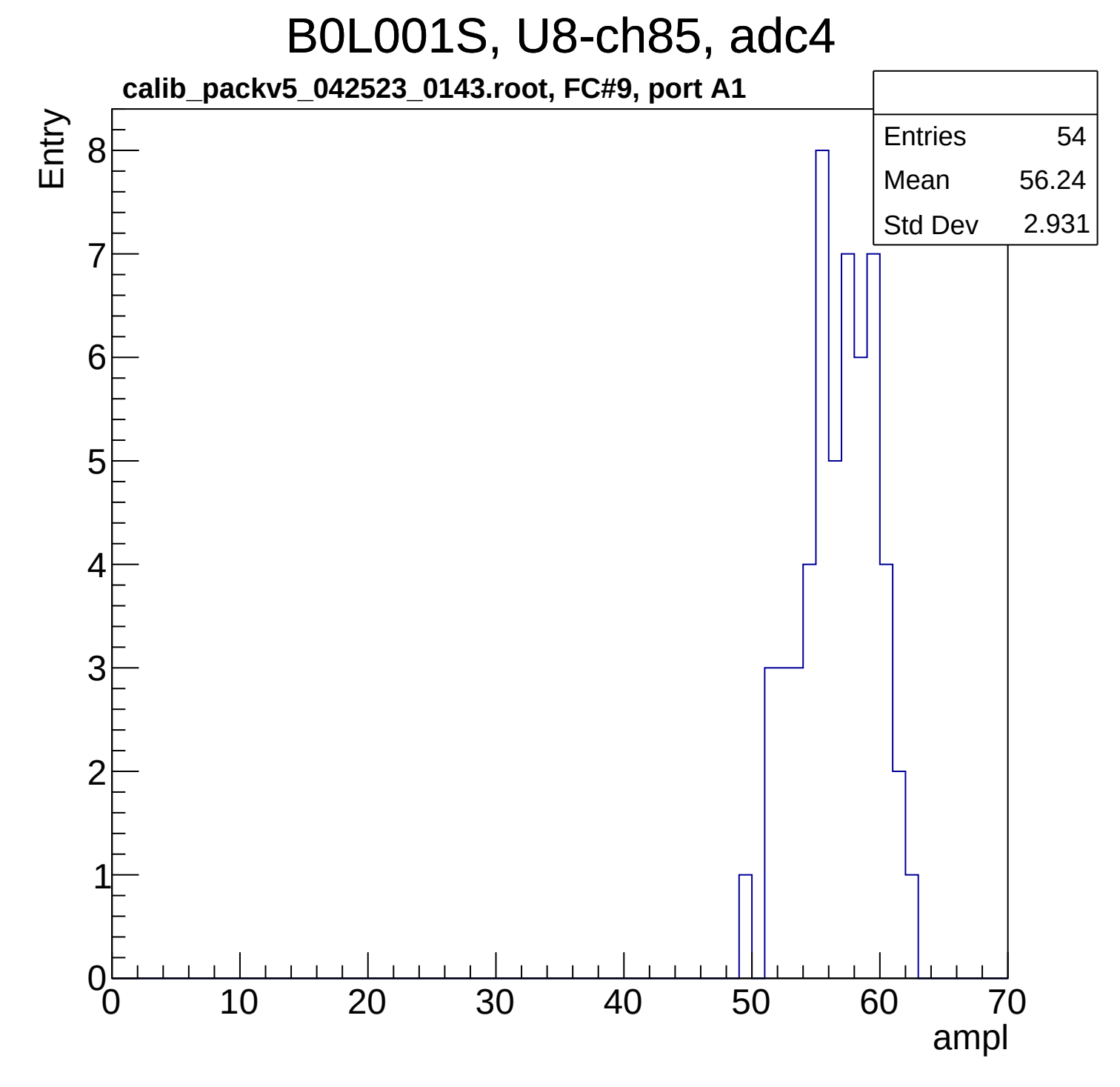
B0L001S, U8-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.24
Std Dev 2.931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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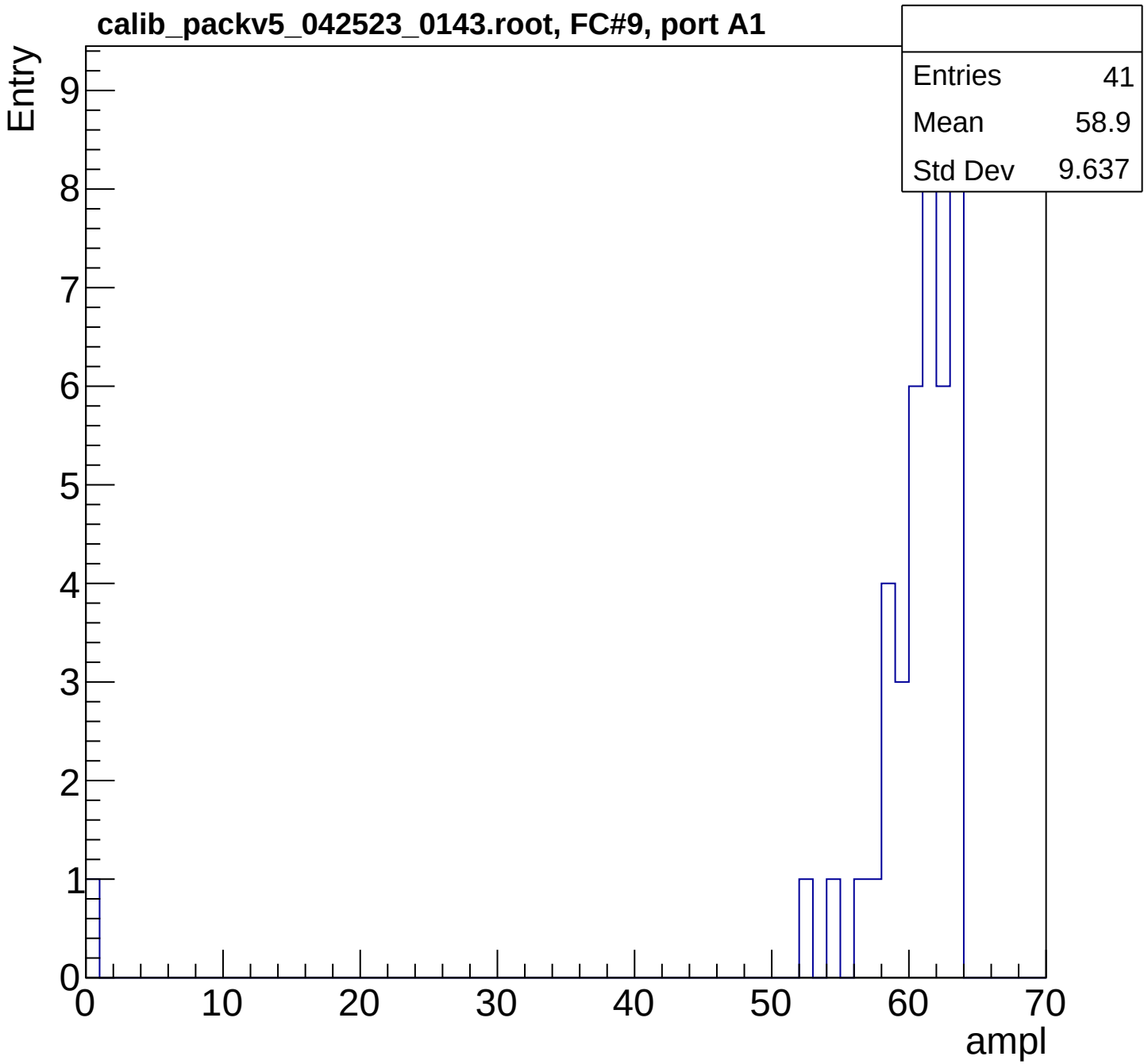
B0L001S, U8-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.9
Std Dev 9.637
Entry
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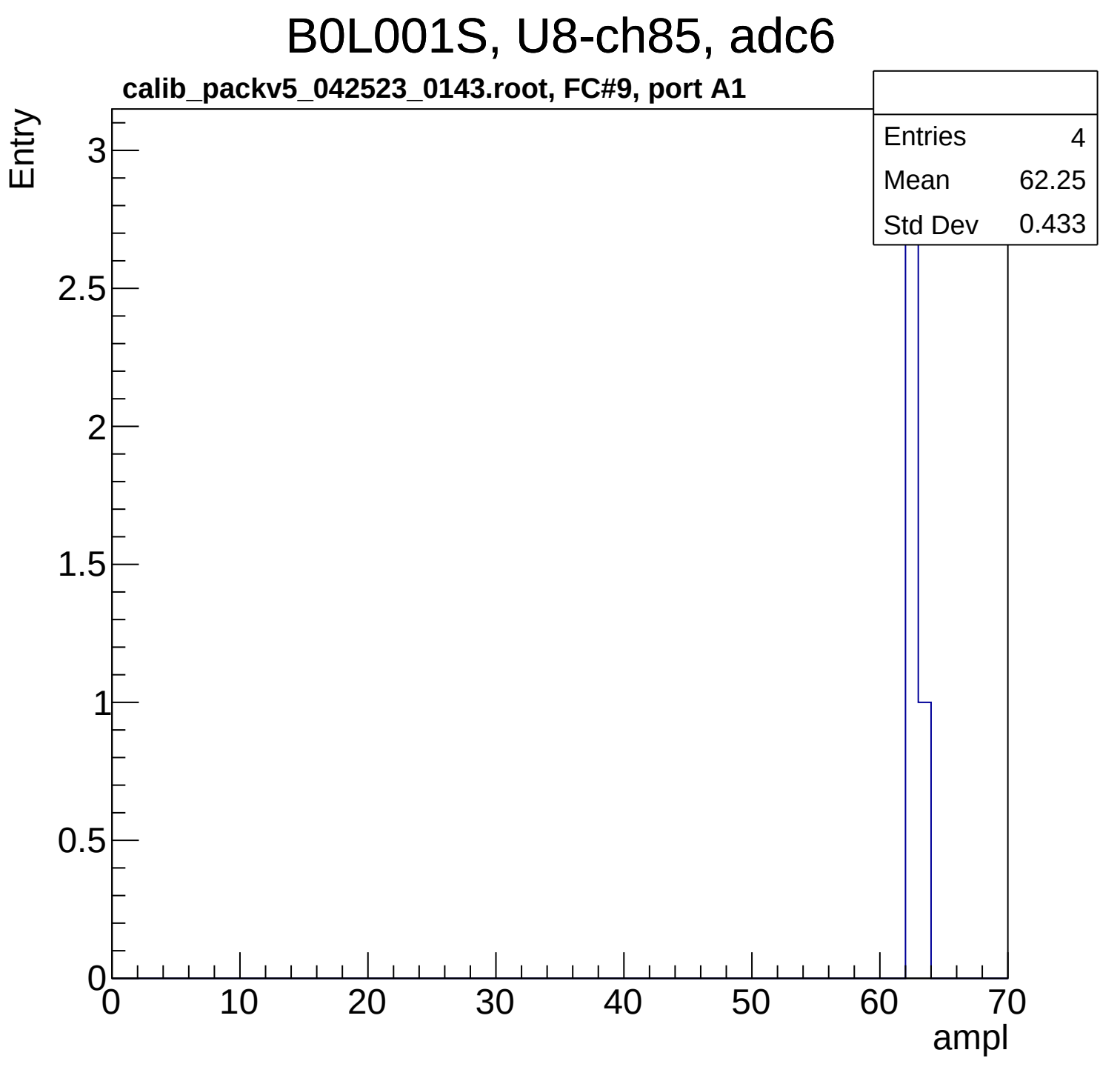
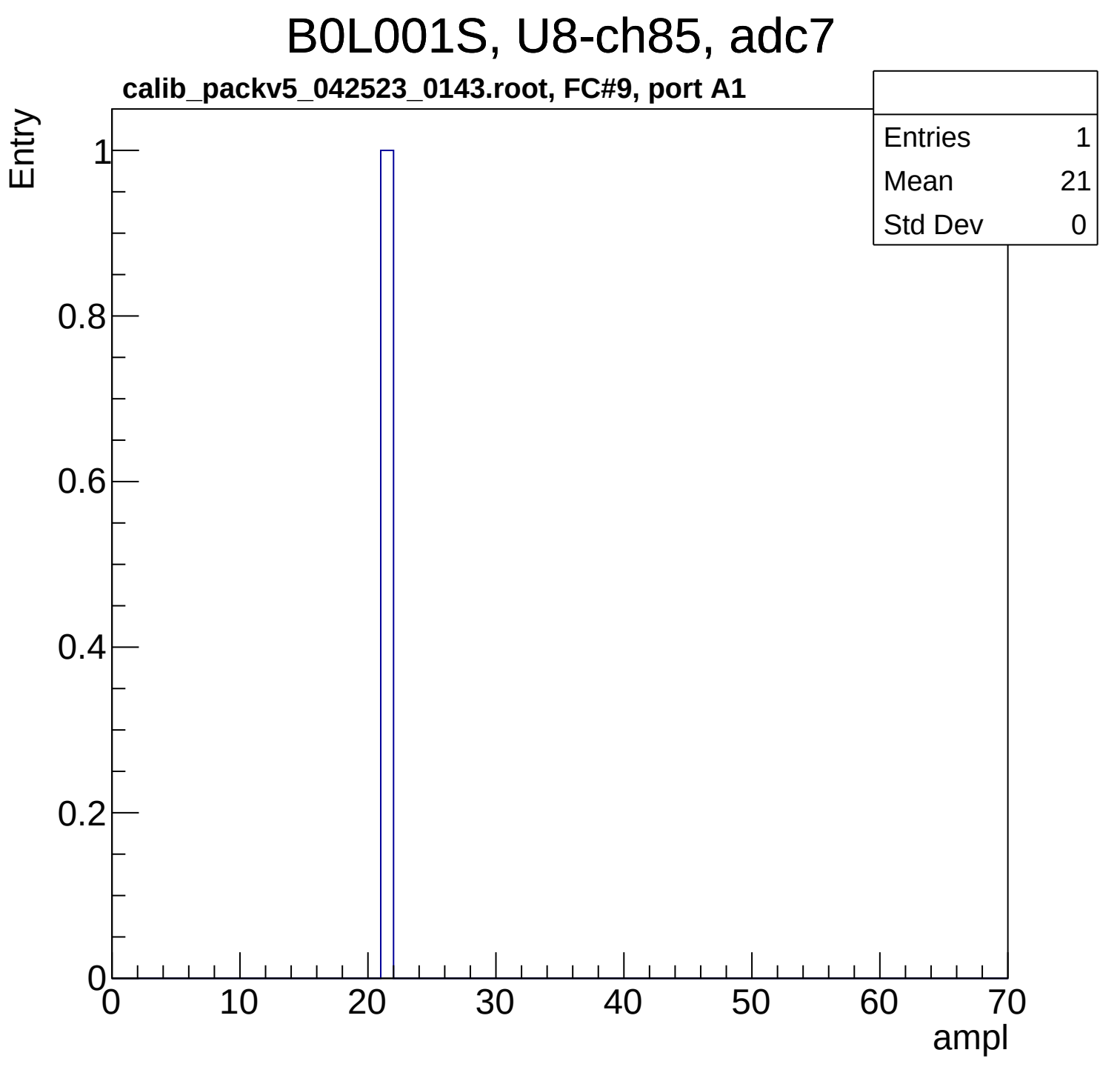
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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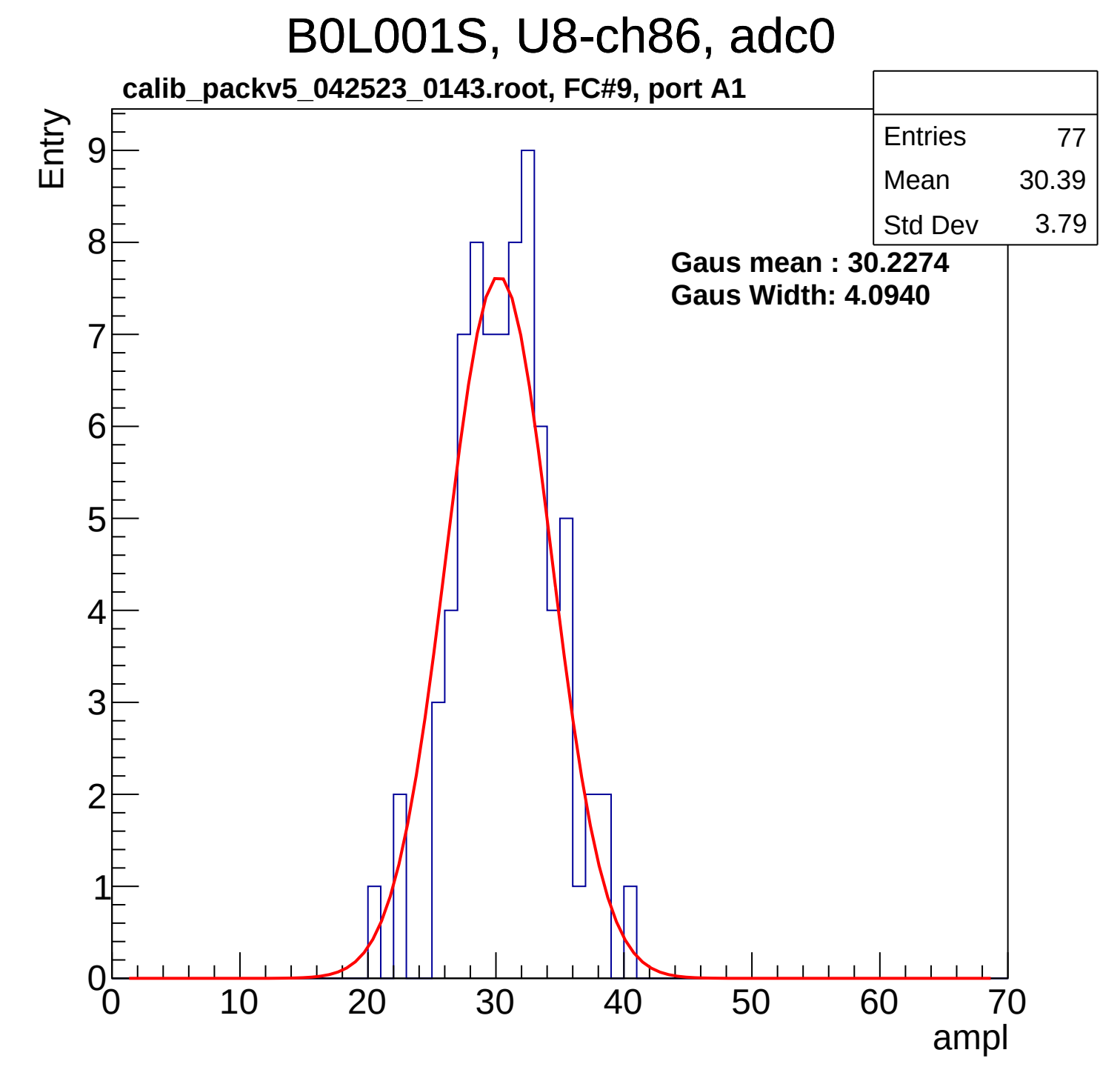
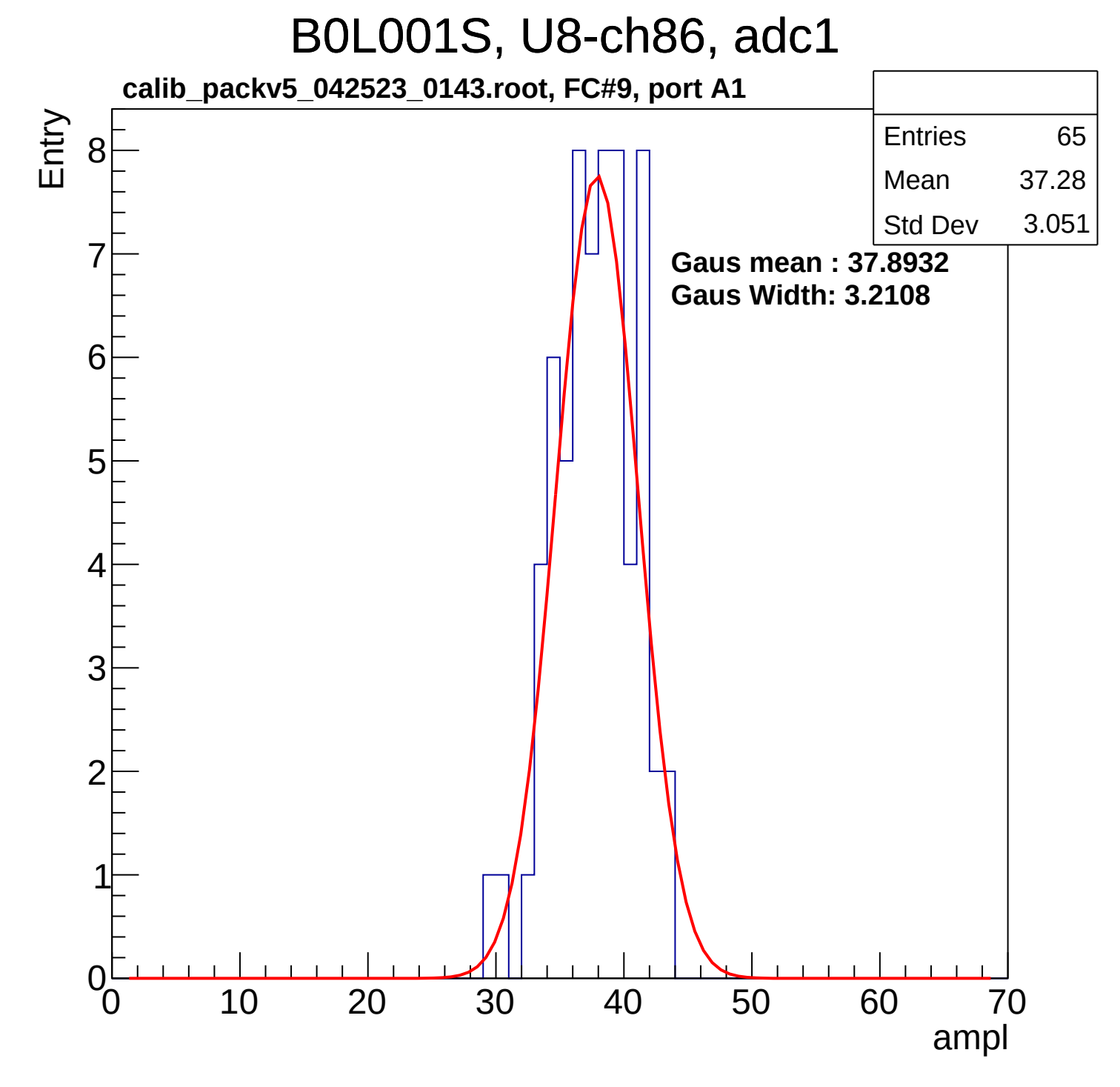
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.39
Std Dev 3.79
Gaus mean : 30.2274
Gaus Width: 4.0940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.28
Std Dev 3.051
Gaus mean : 37.8932
Gaus Width: 3.2108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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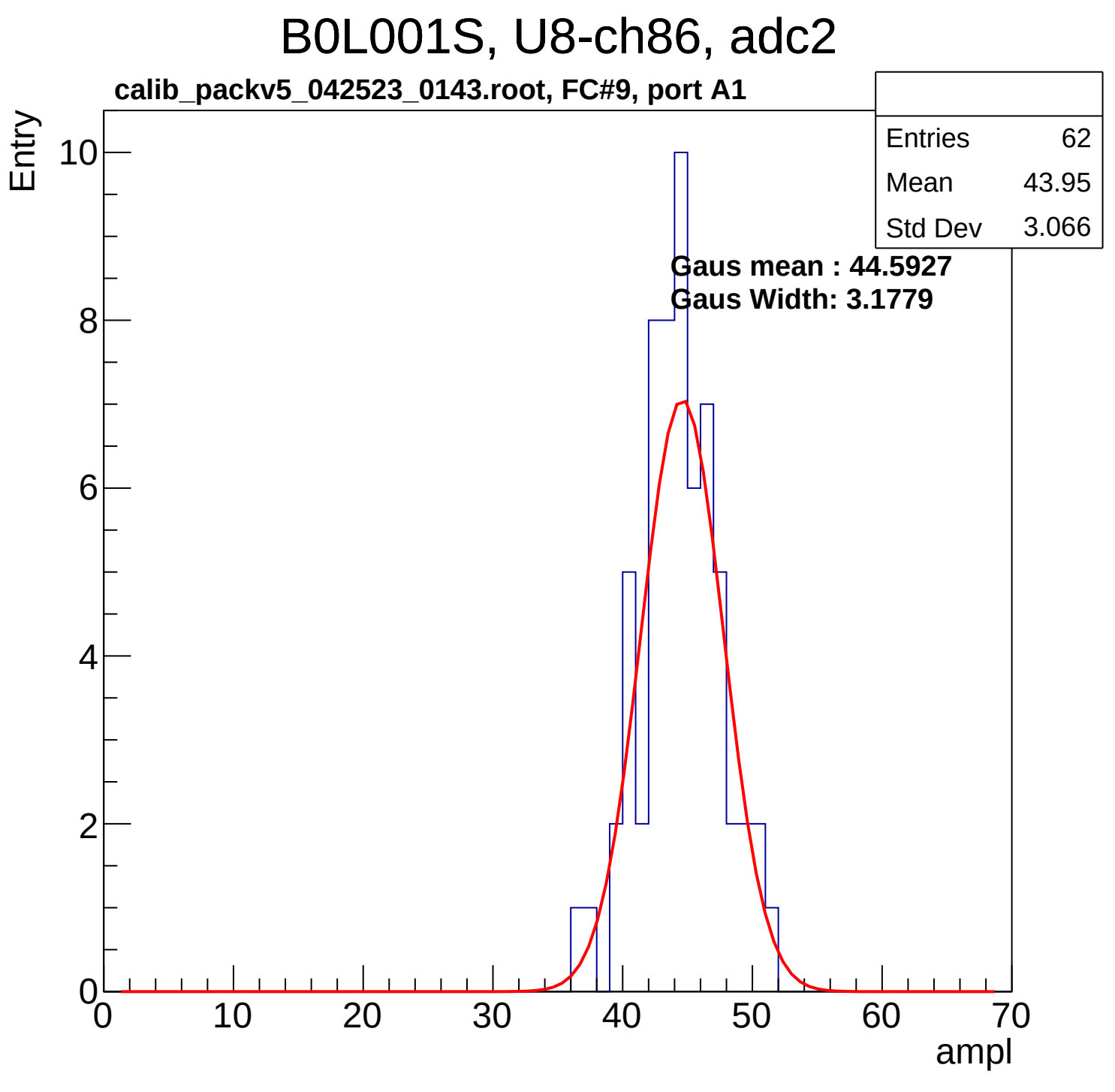
B0L001S, U8-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.95
Std Dev 3.066
Gaus mean : 44.5927
Gaus Width: 3.1779
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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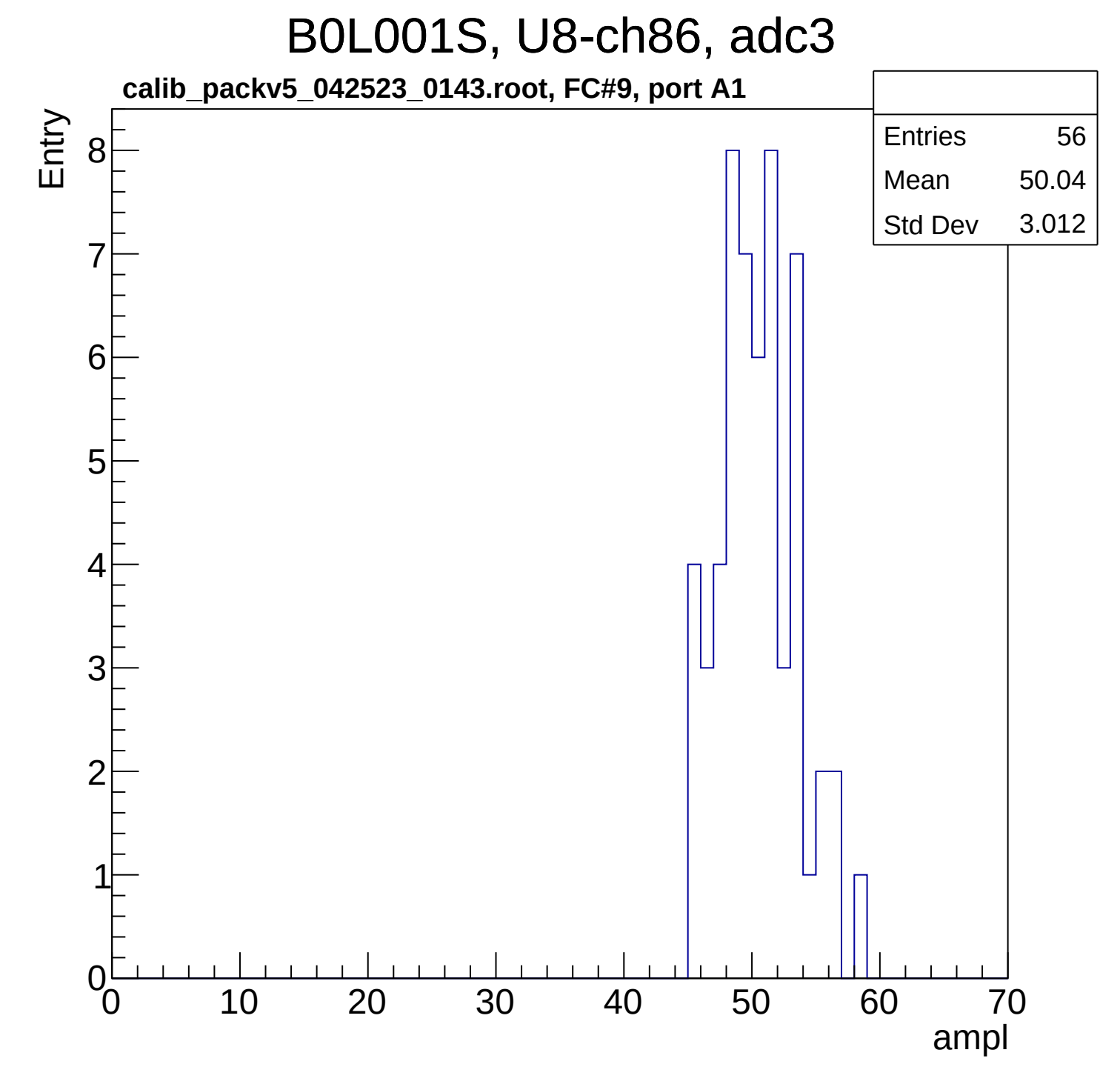
B0L001S, U8-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.04
Std Dev 3.012
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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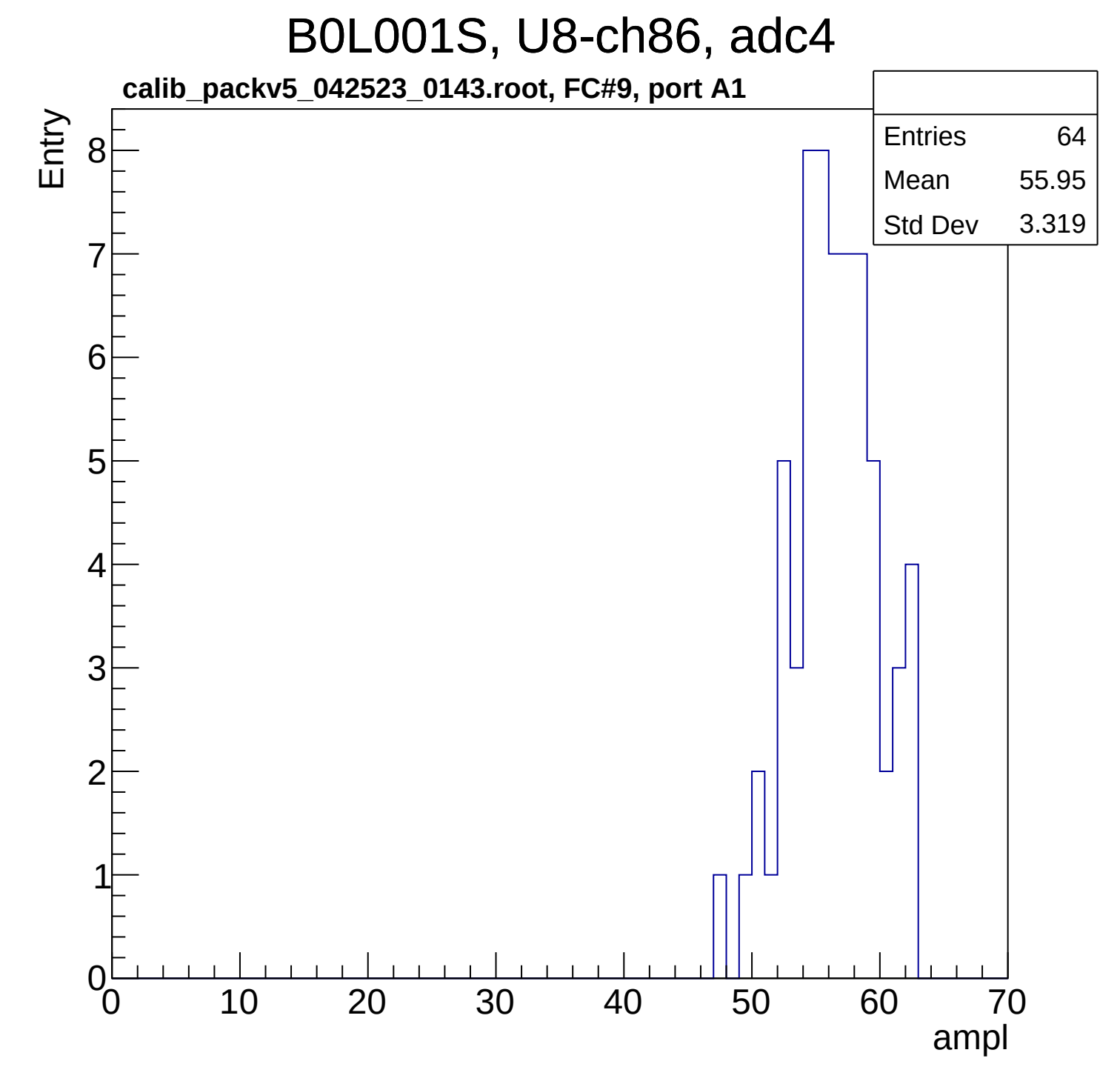
B0L001S, U8-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.95
Std Dev 3.319
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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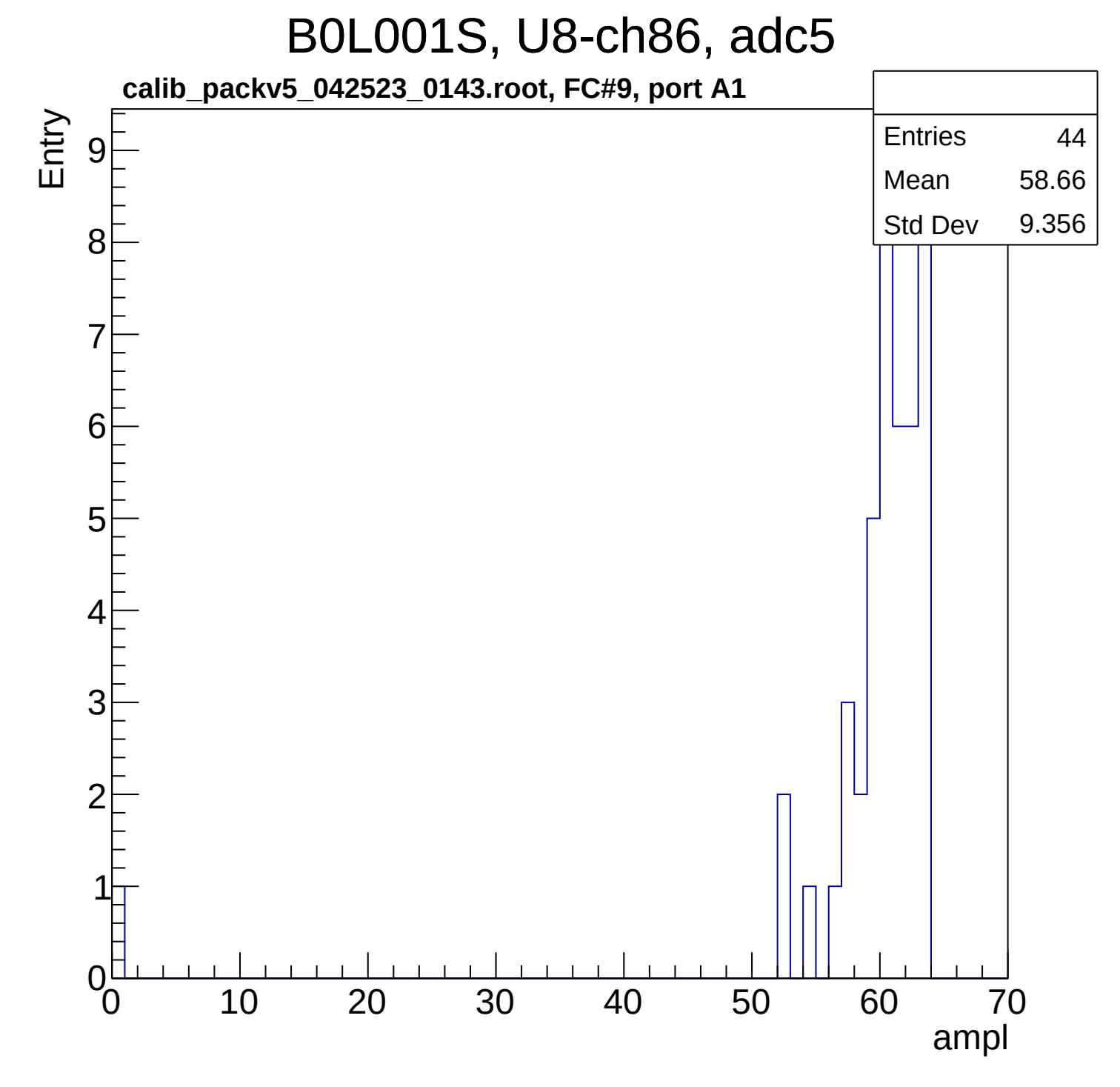
B0L001S, U8-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.66
Std Dev 9.356
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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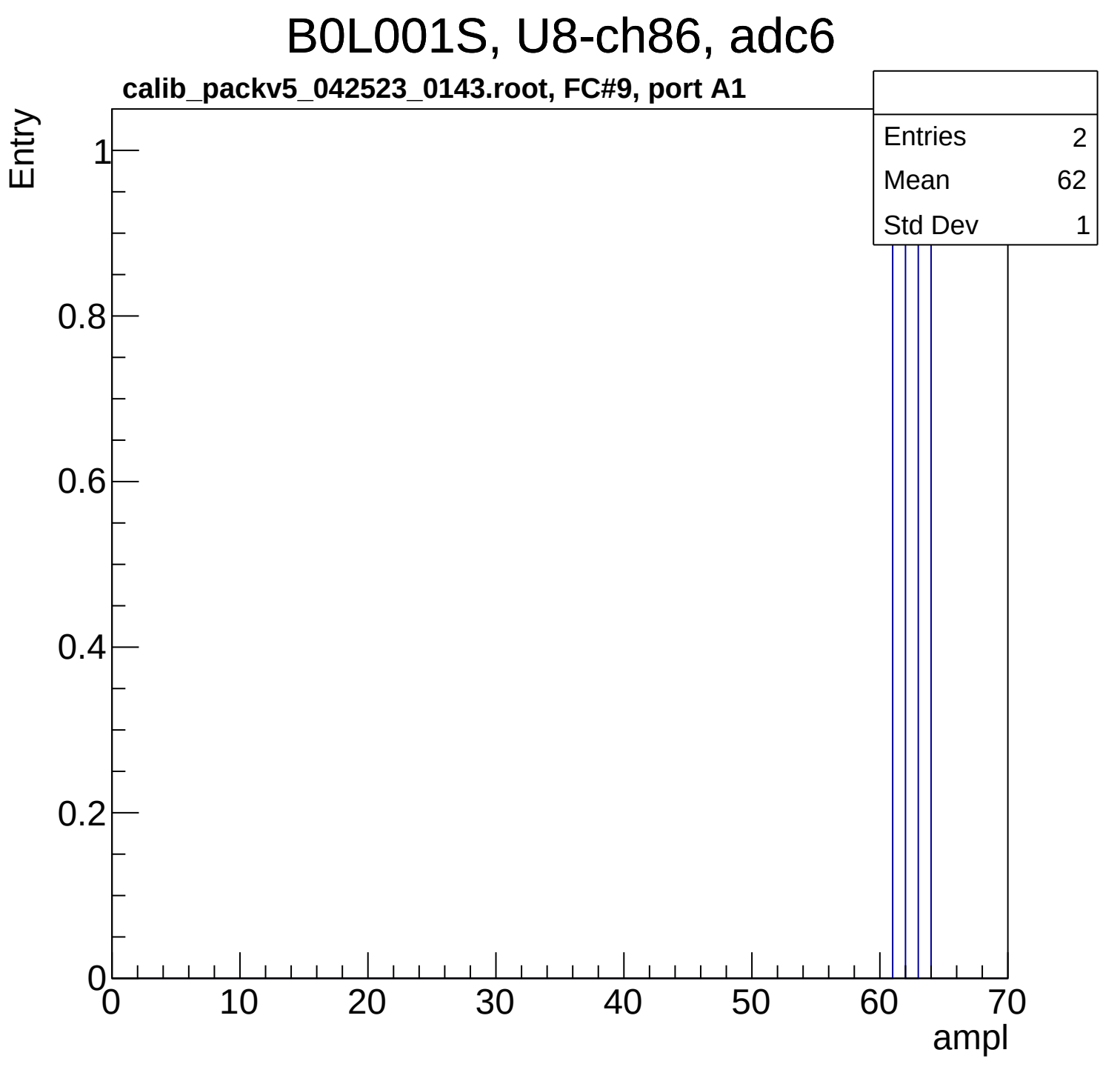
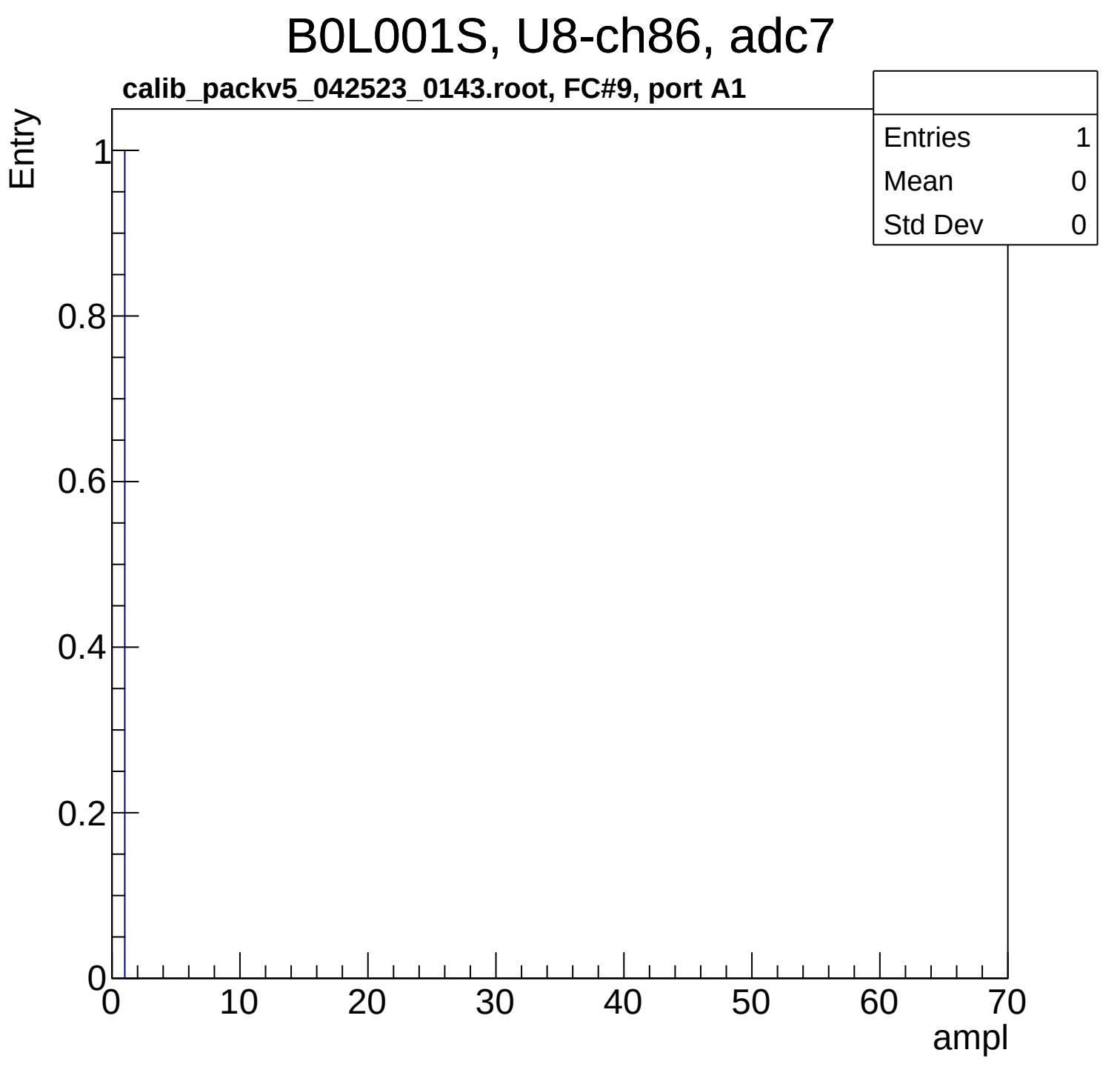
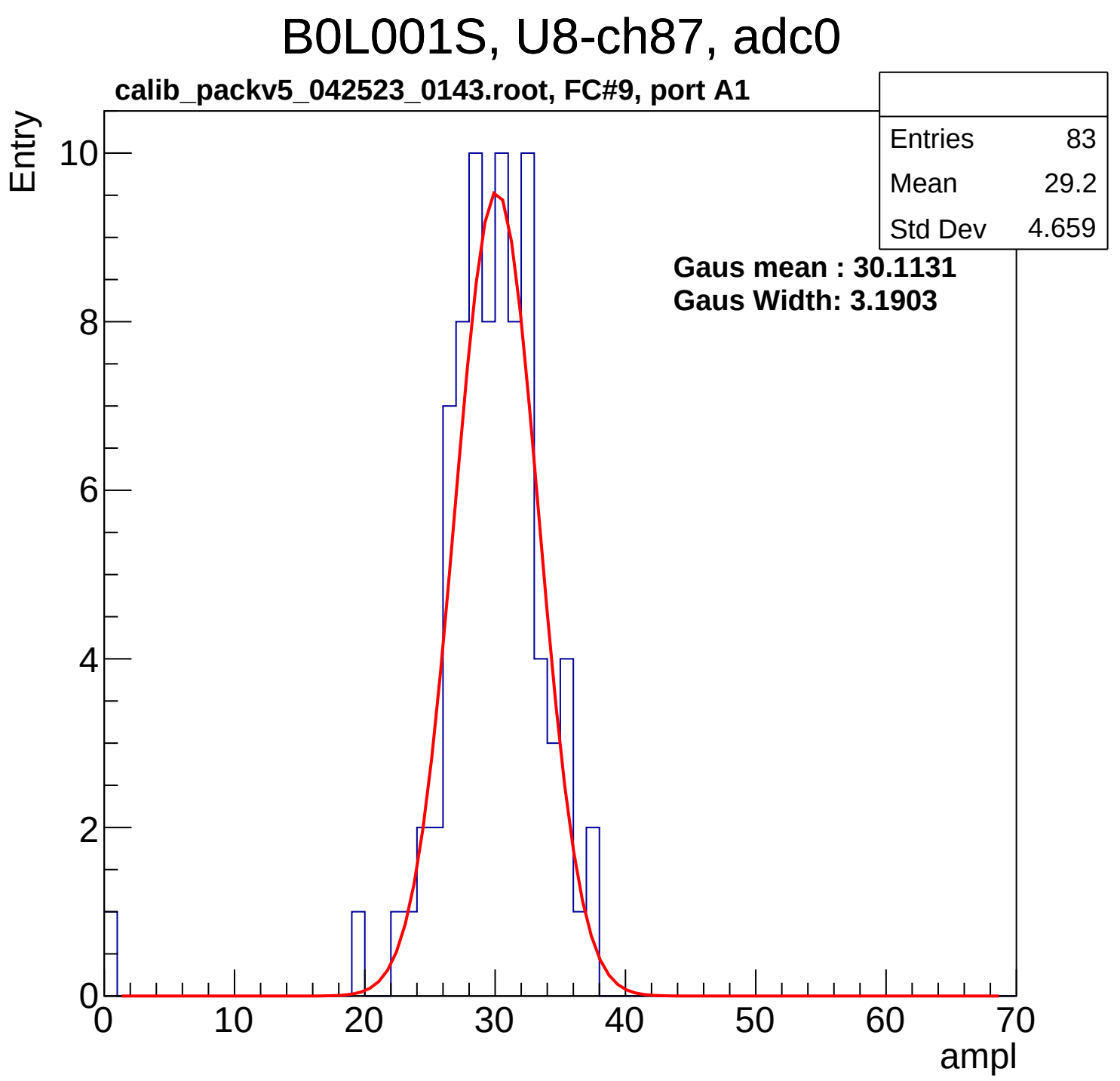
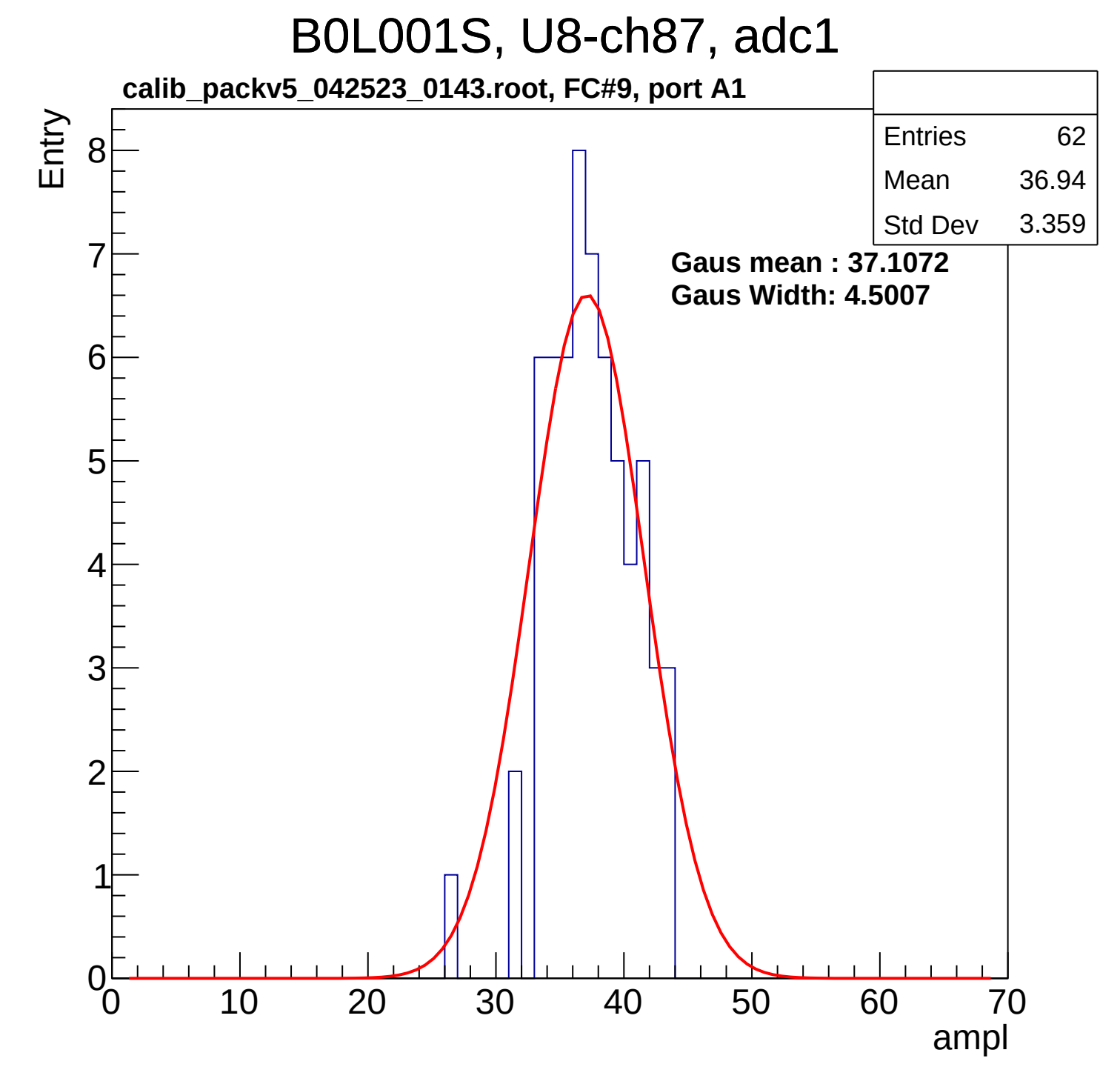

B0L001S, U8-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 29.2
Std Dev 4.659
Gaus mean : 30.1131
Gaus Width: 3.1903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.94
Std Dev 3.359
Gaus mean : 37.1072
Gaus Width: 4.5007
Entry
ampl
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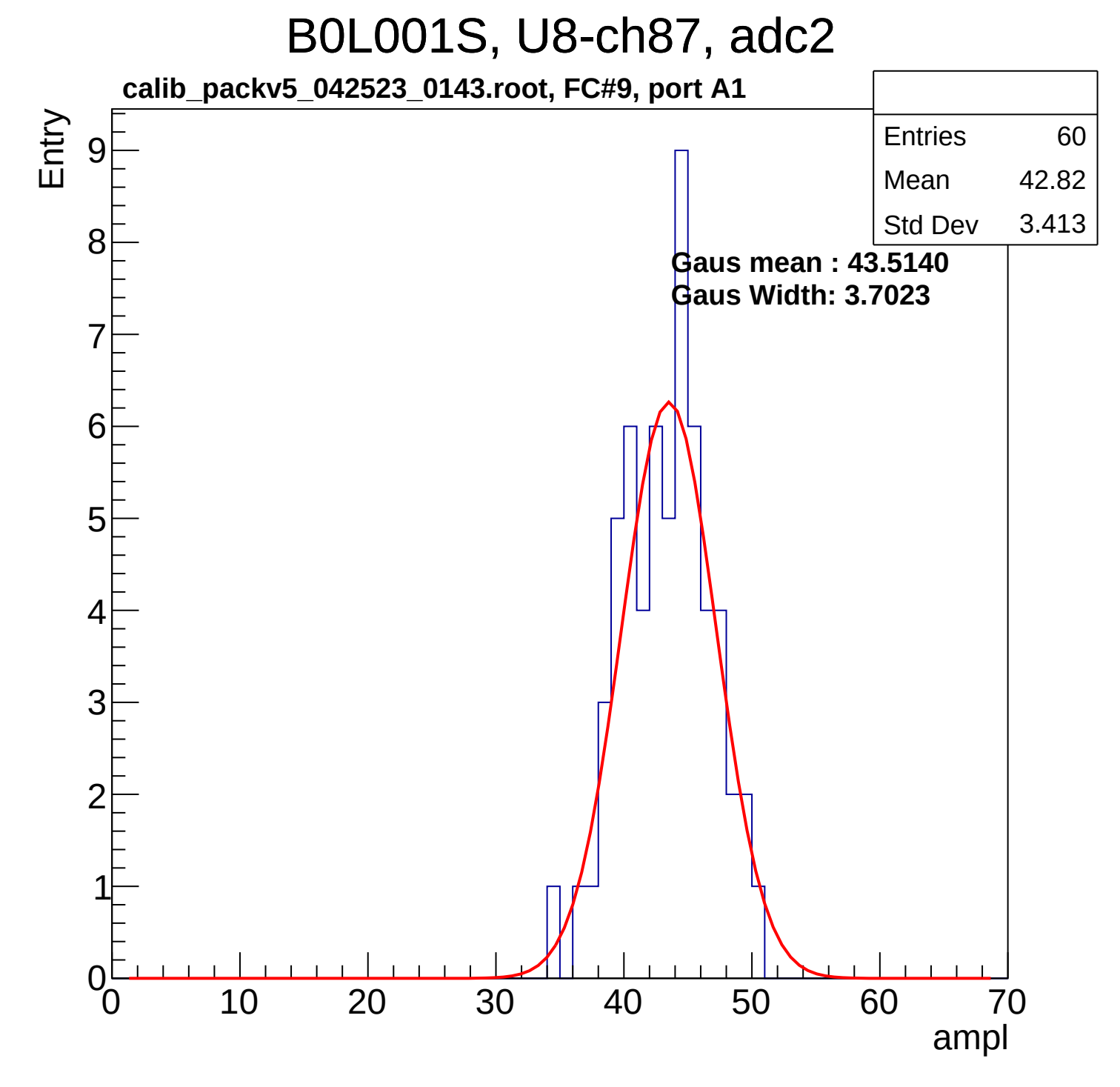
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 42.82
Std Dev 3.413
Gaus mean : 43.5140
Gaus Width: 3.7023
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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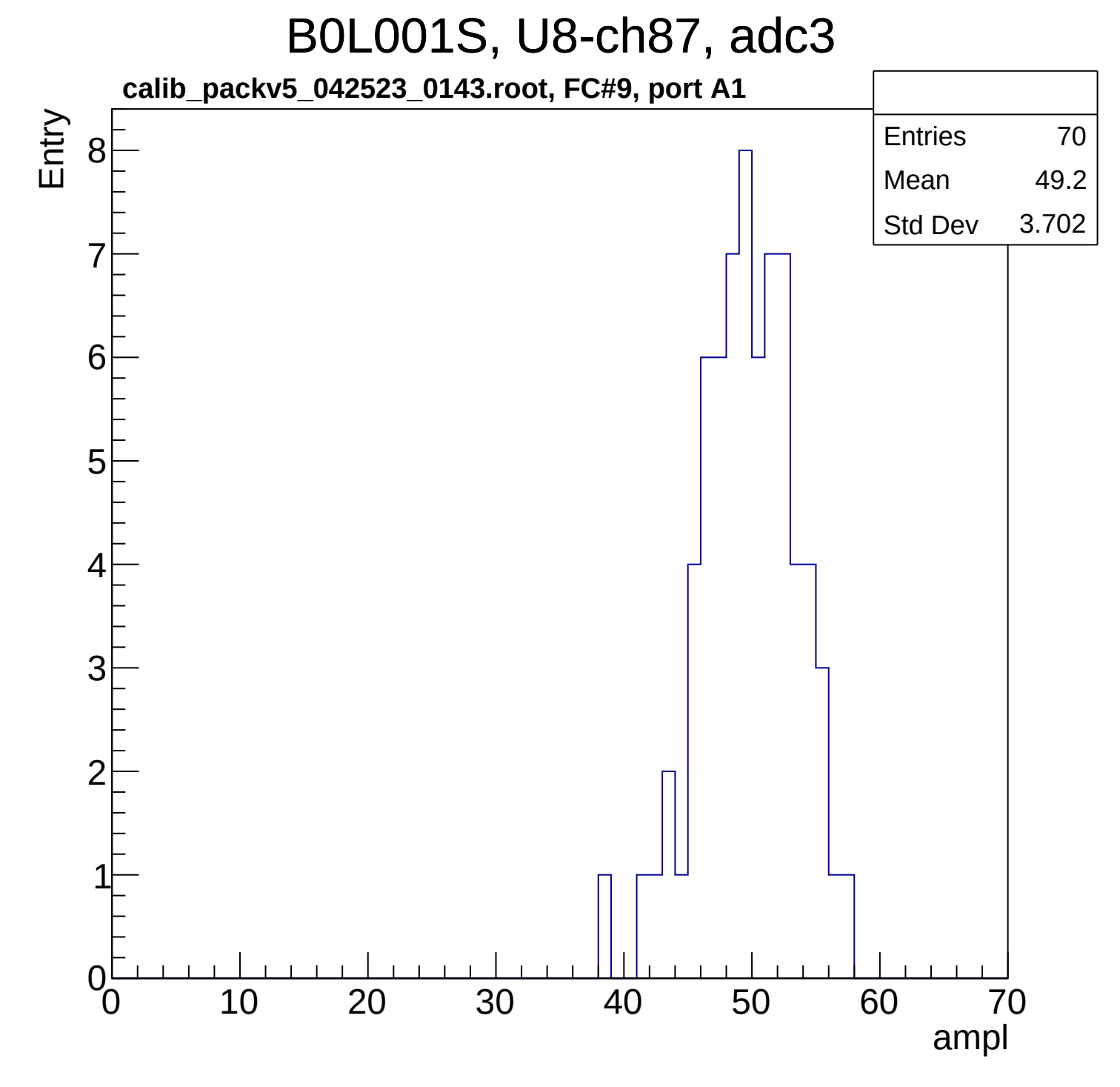
B0L001S, U8-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.2
Std Dev 3.702
Entry
ampl
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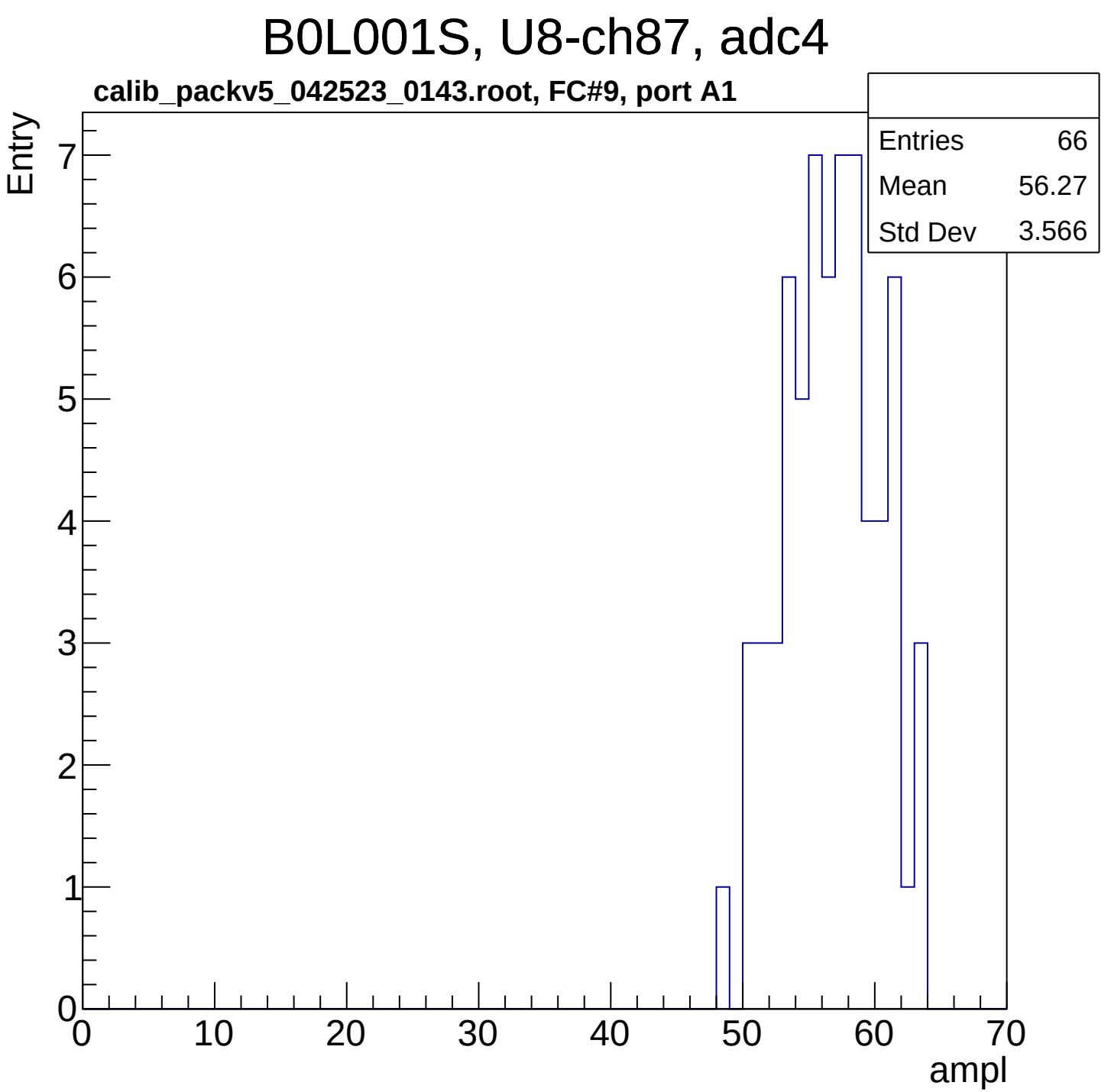
B0L001S, U8-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.27
Std Dev 3.566
Entry
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7
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10
20
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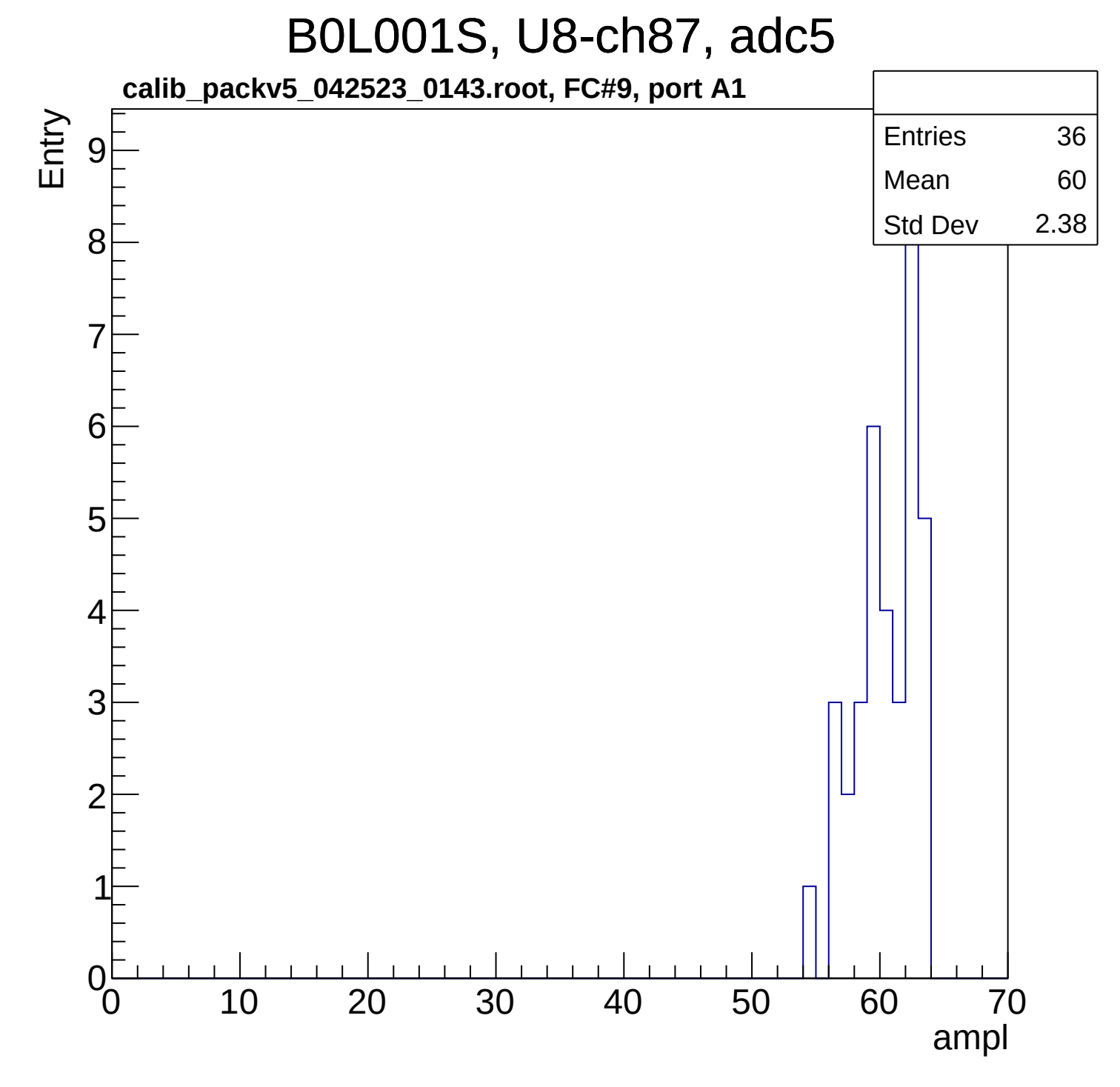
50
60
70
ampl


B0L001S, U8-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60
Std Dev 2.38
Entry
ampl
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3
4
5
6
7
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0
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60
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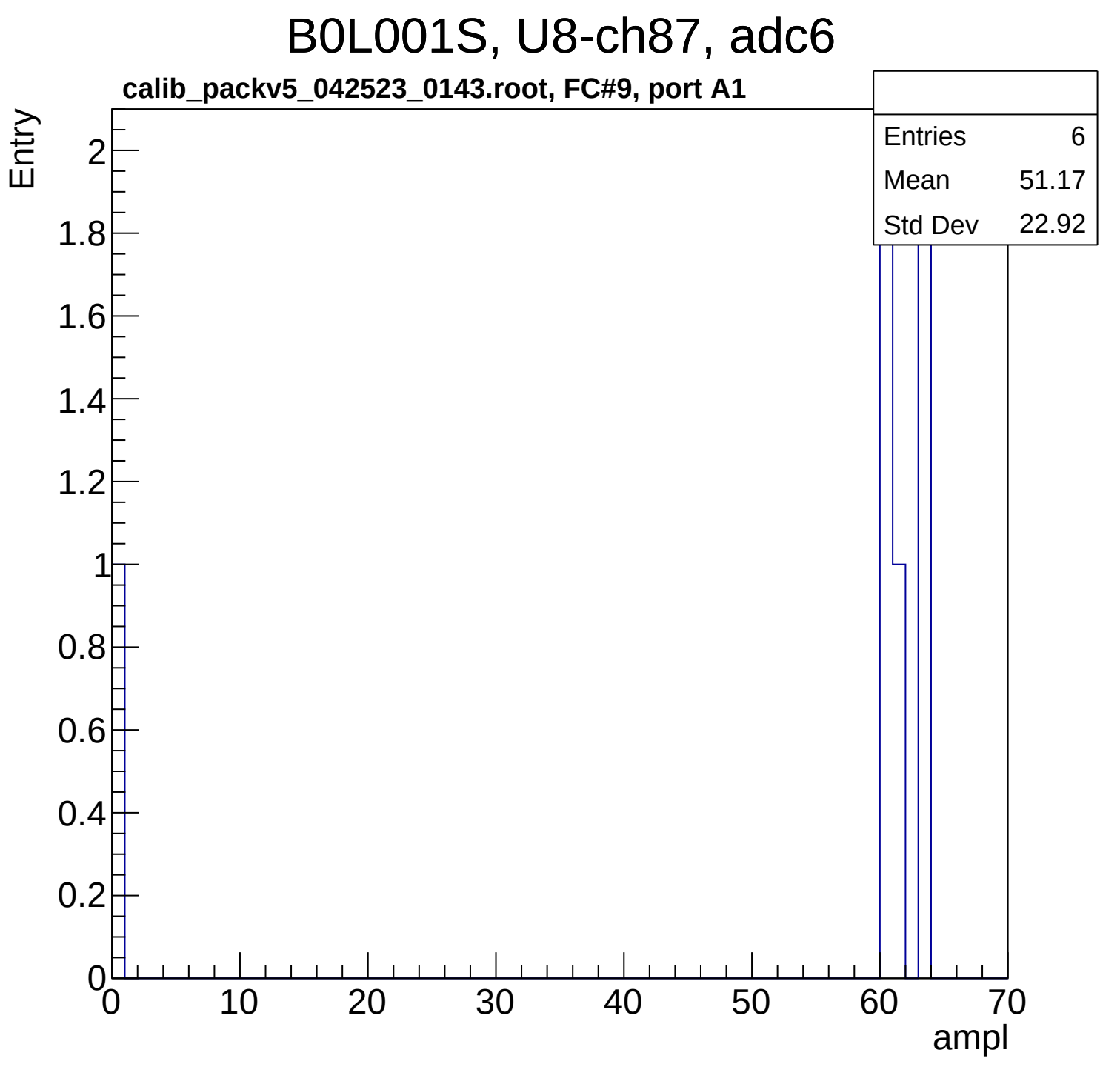
B0L001S, U8-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.17
Std Dev 22.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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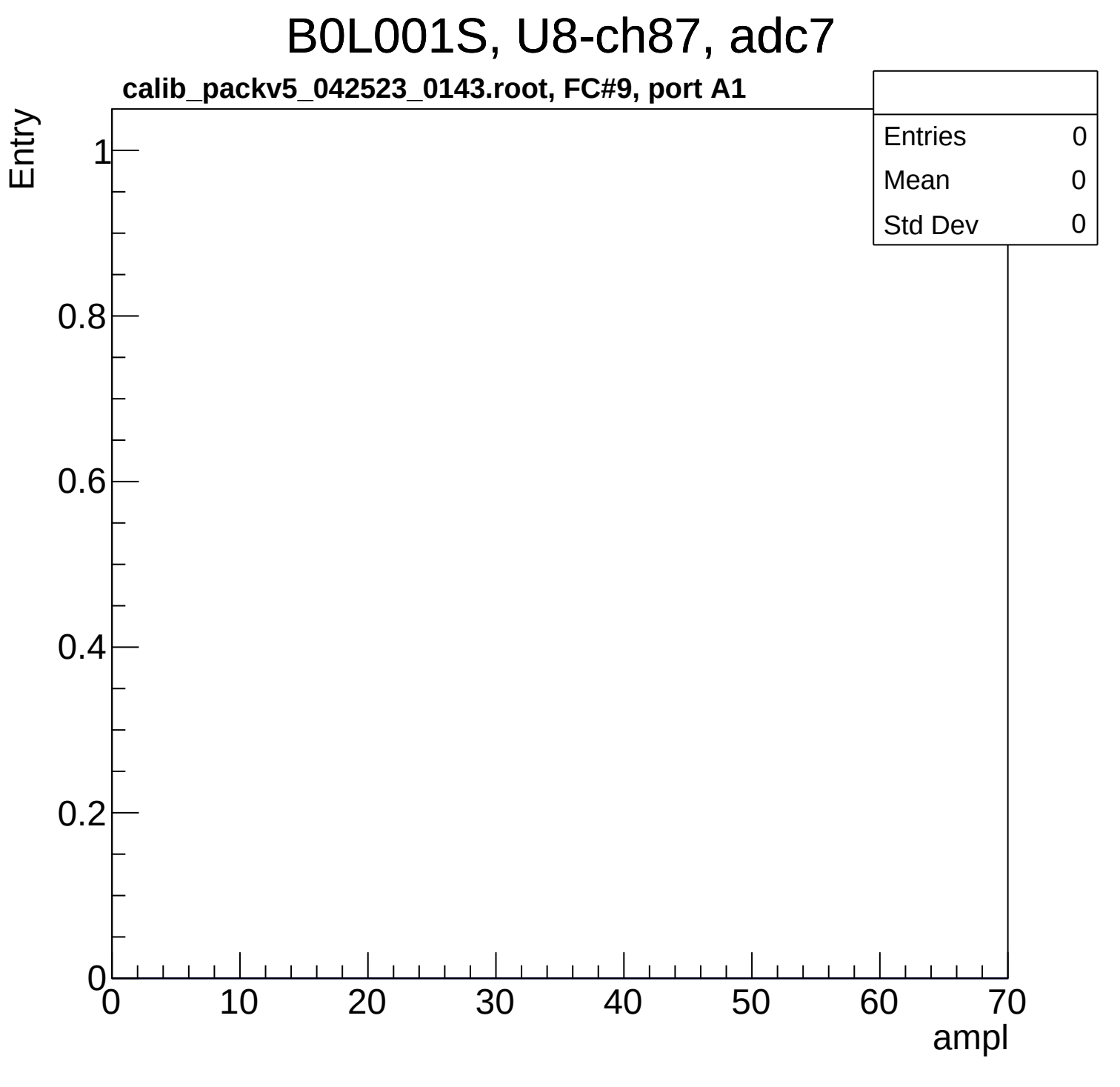
40
50
60
70


B0L001S, U8-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
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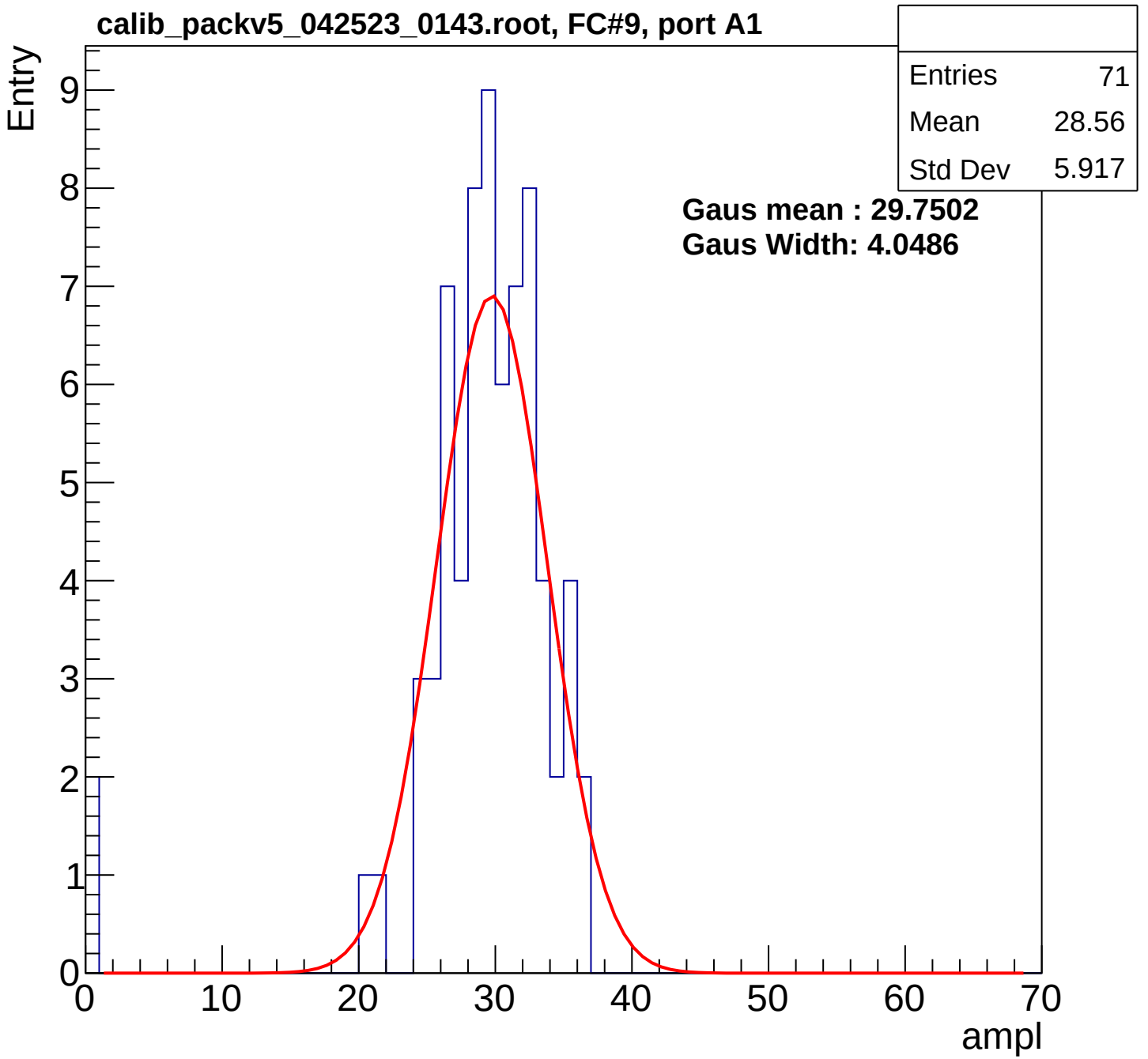
60
70
ampl


B0L001S, U8-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.56
Std Dev 5.917
Gaus mean : 29.7502
Gaus Width: 4.0486
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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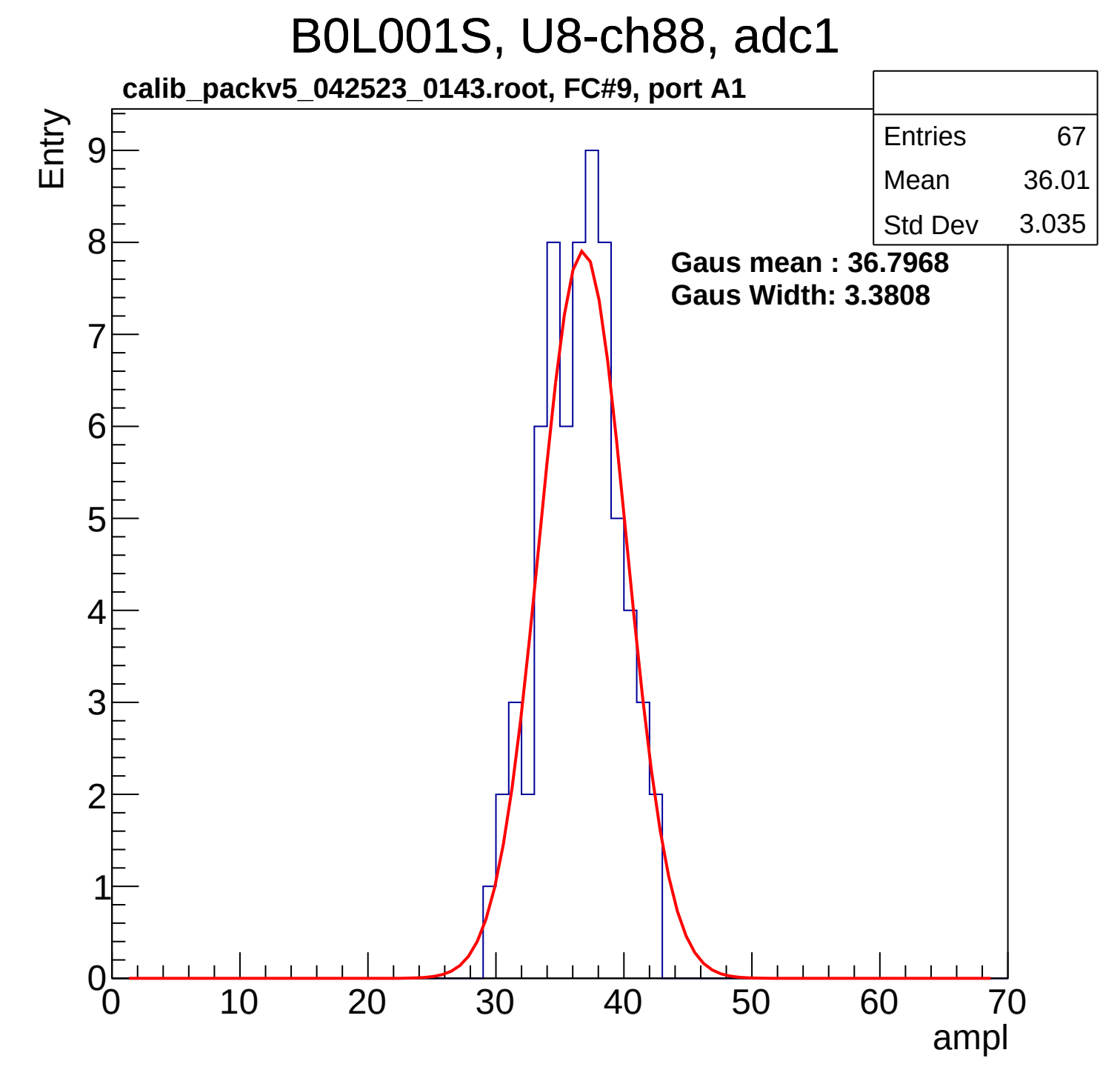
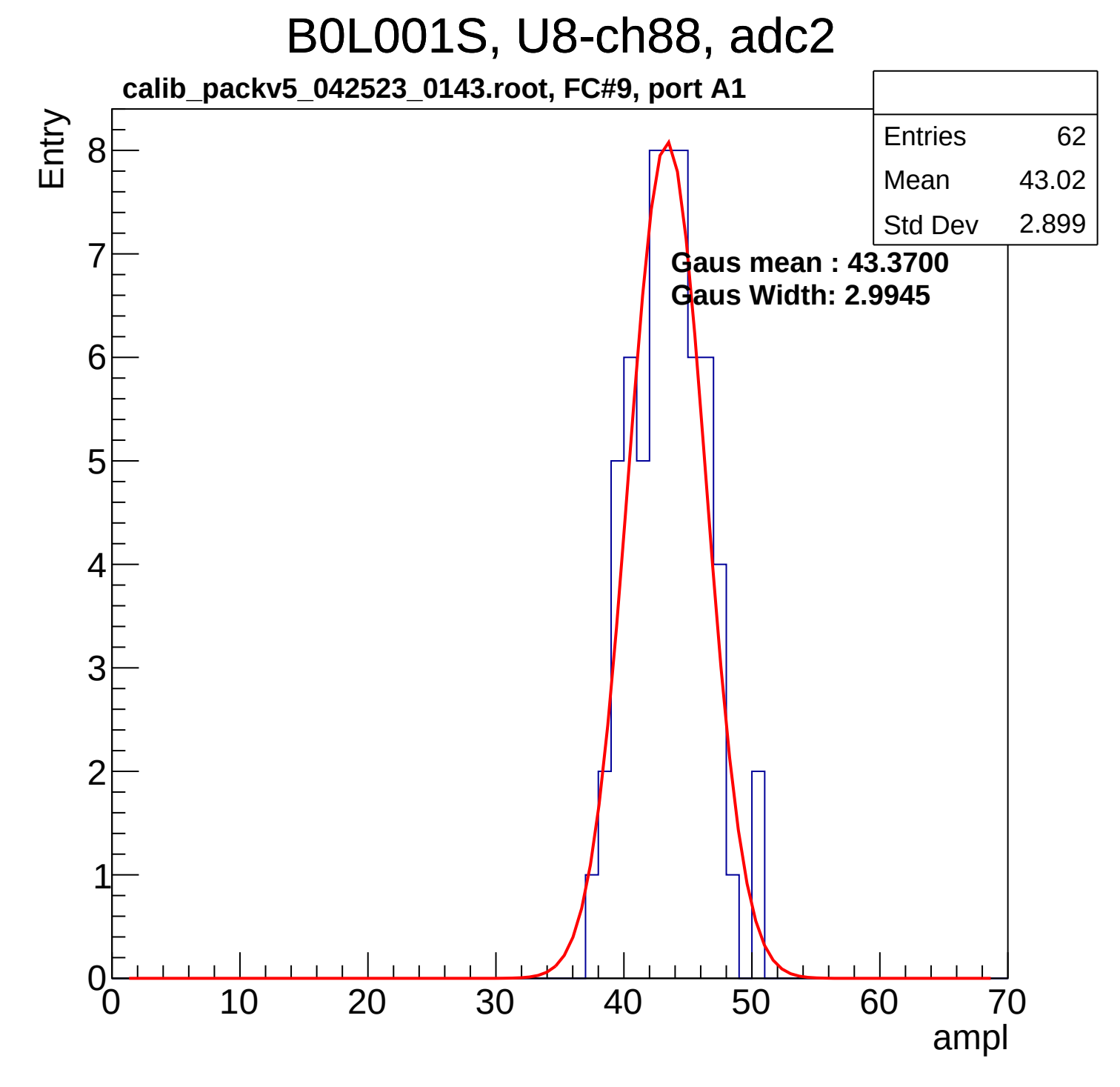
60
70


B0L001S, U8-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.01
Std Dev 3.035
Gaus mean : 36.7968
Gaus Width: 3.3808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.02
Std Dev 2.899
Gaus mean : 43.3700
Gaus Width: 2.9945
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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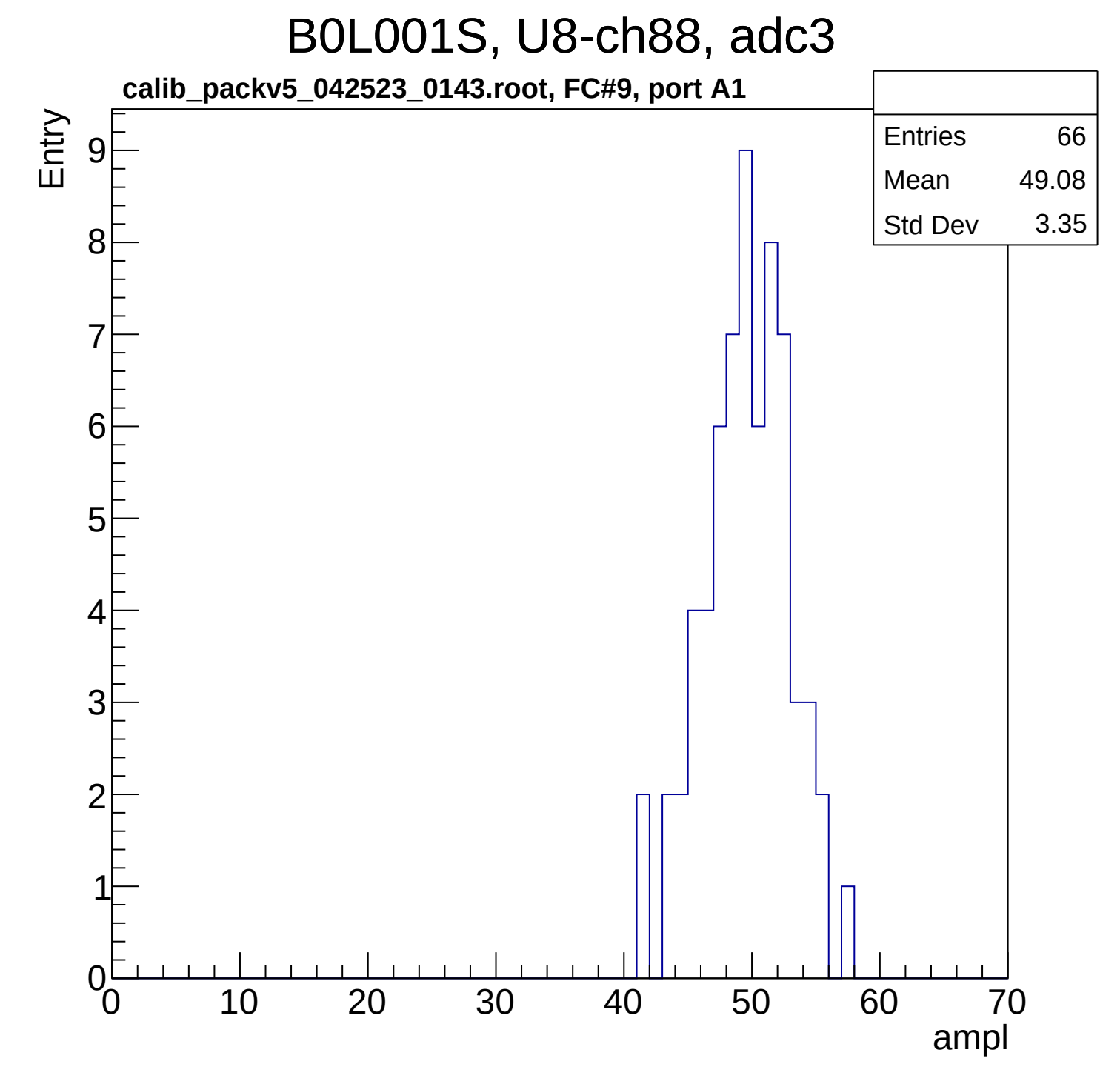
B0L001S, U8-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.08
Std Dev 3.35
Entry
ampl
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2
3
4
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7
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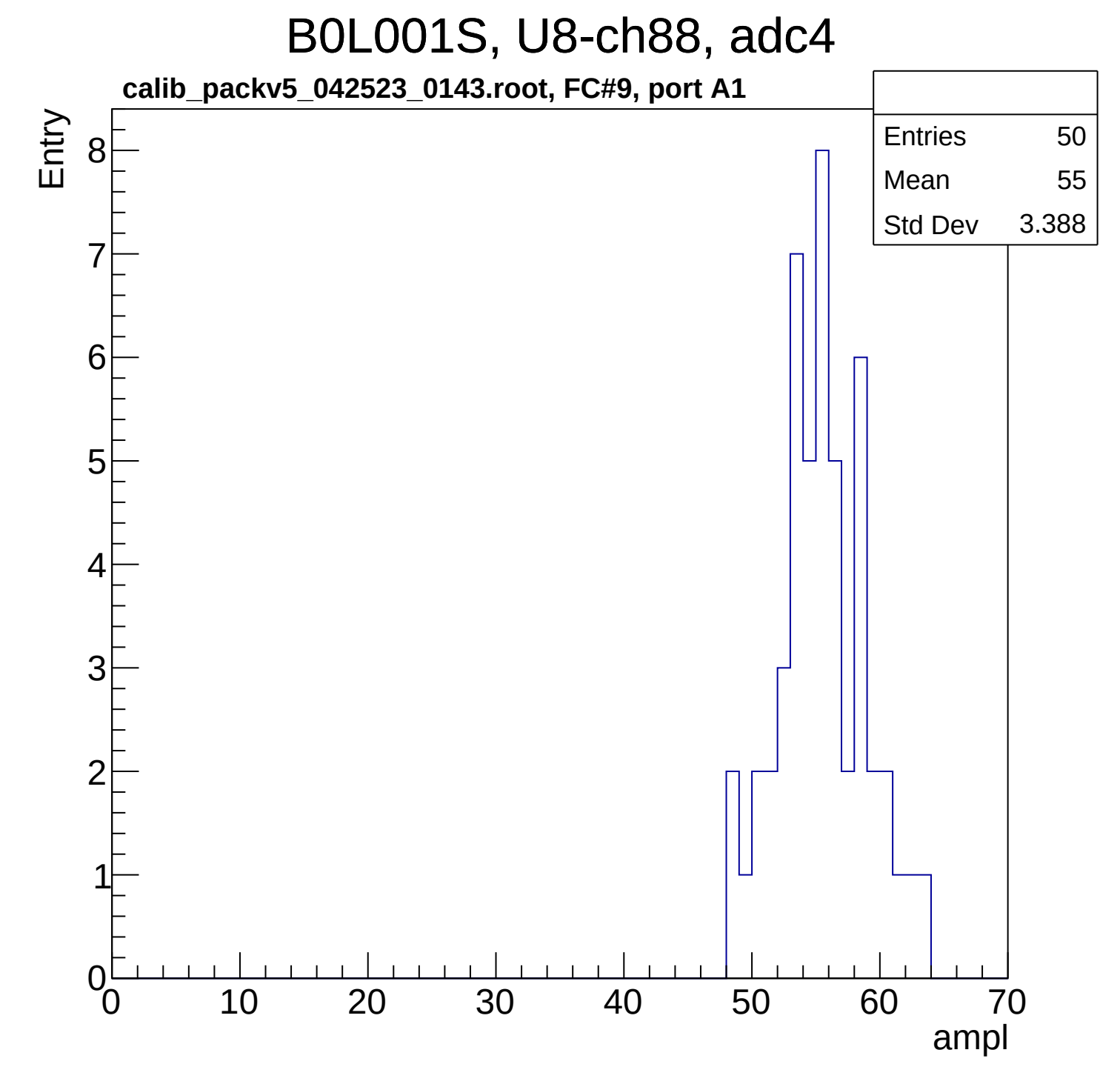
B0L001S, U8-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 55
Std Dev 3.388
Entry
ampl
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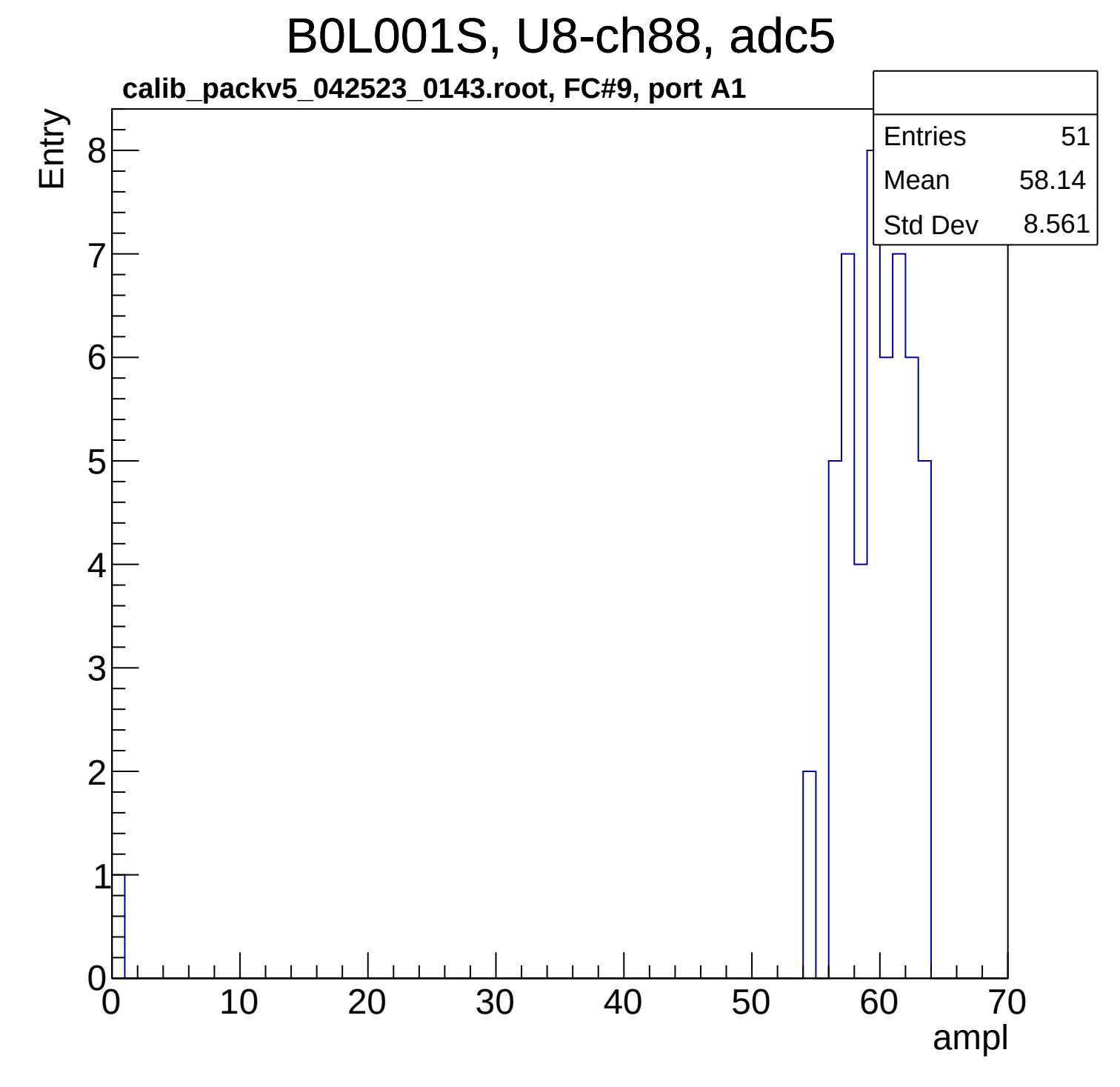
B0L001S, U8-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.14
Std Dev 8.561
Entry
ampl
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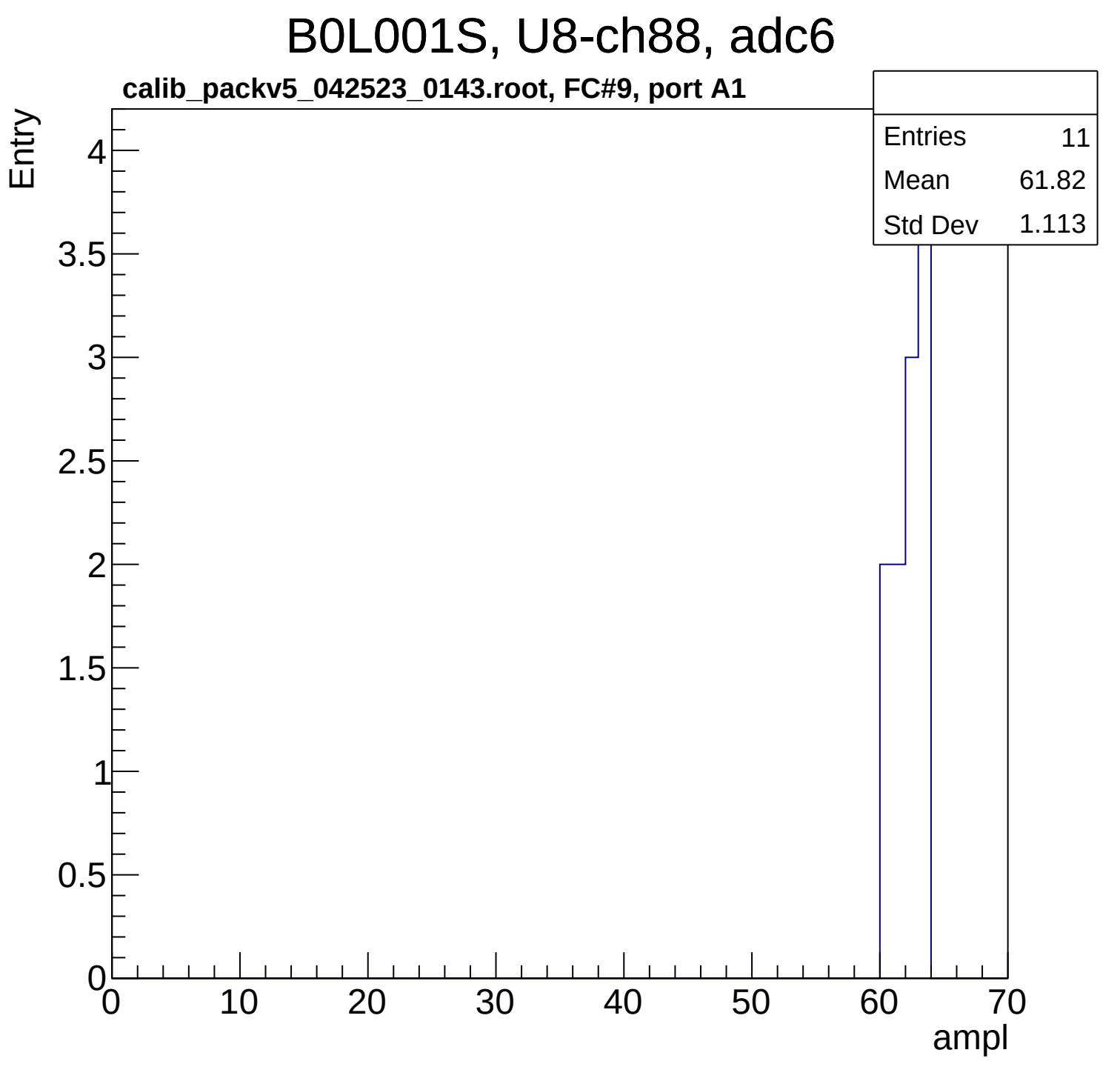
B0L001S, U8-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.82
Std Dev 1.113
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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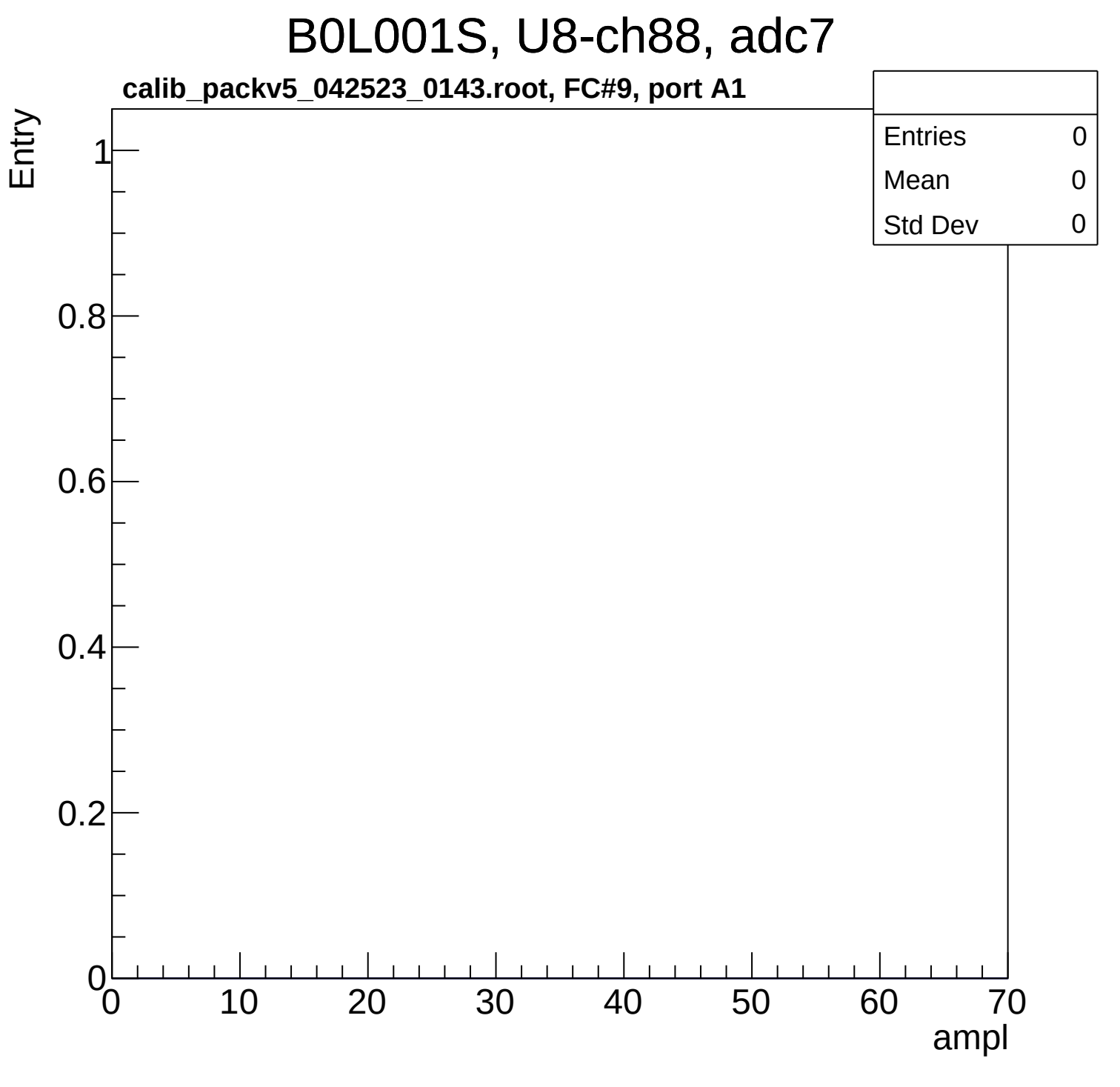
40
50
60
70


B0L001S, U8-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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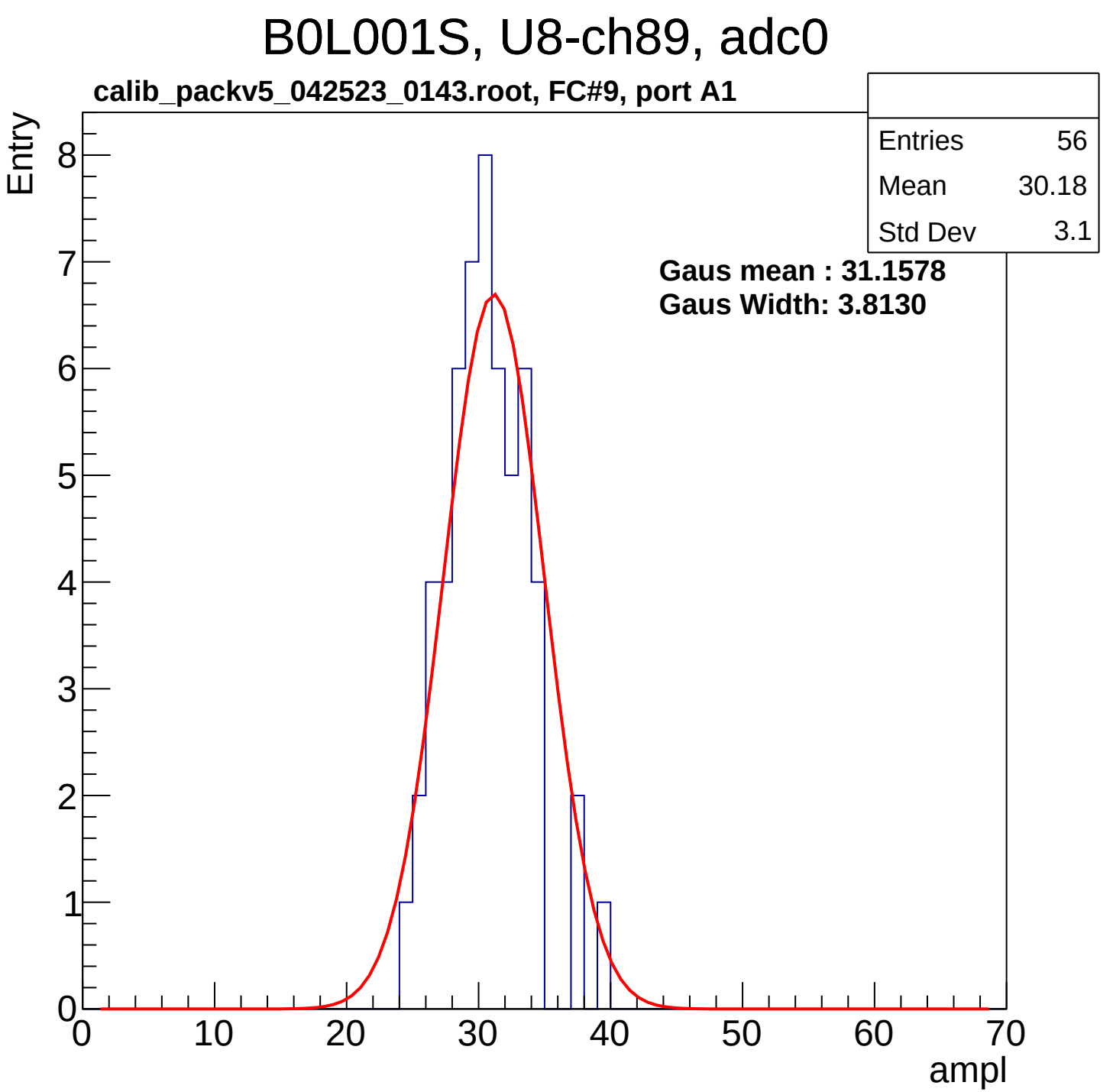
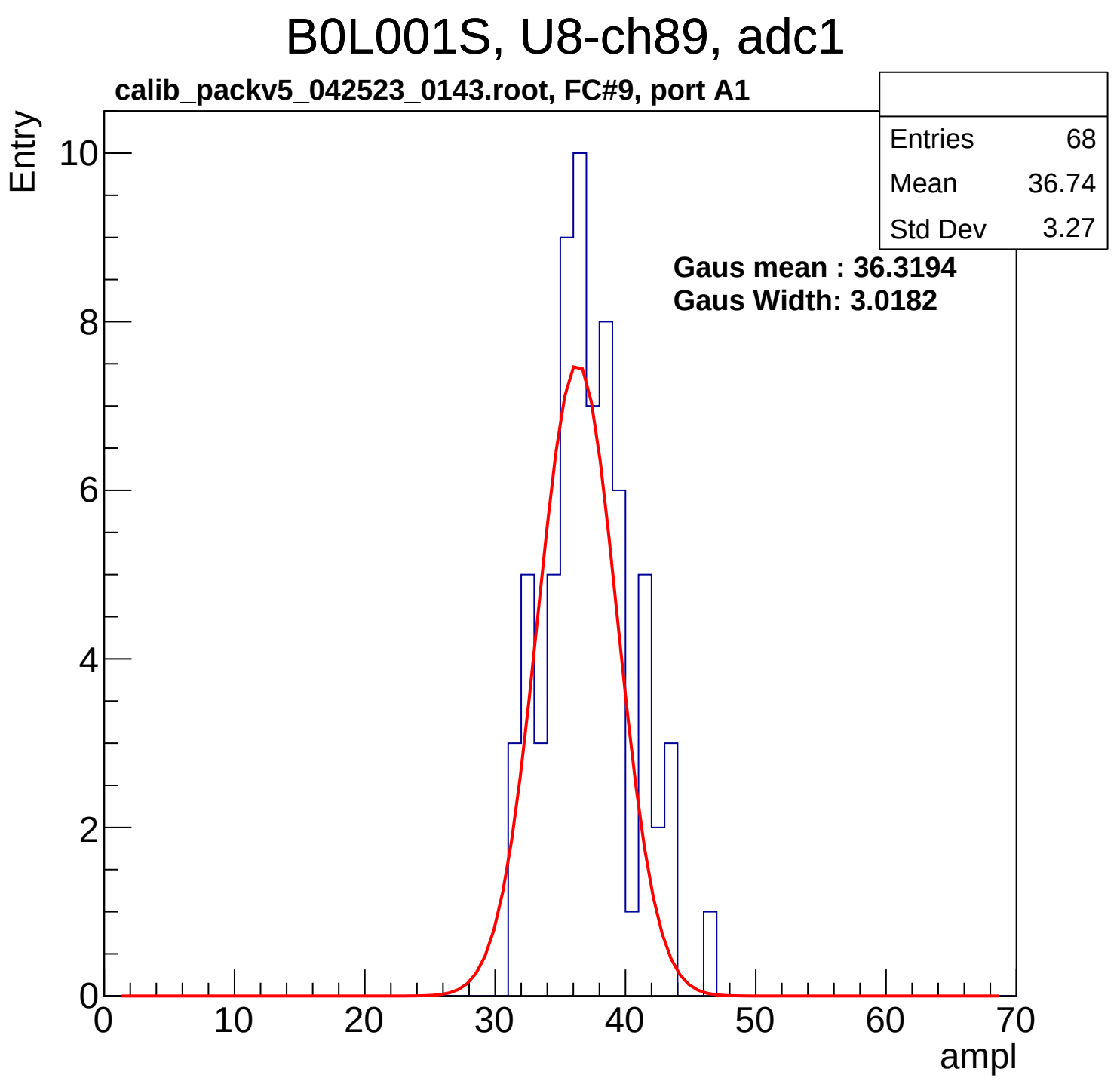
40
50
60
70
ampl


B0L001S, U8-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.18
Std Dev 3.1
Gaus mean : 31.1578
Gaus Width: 3.8130
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.74
Std Dev 3.27
Gaus mean : 36.3194
Gaus Width: 3.0182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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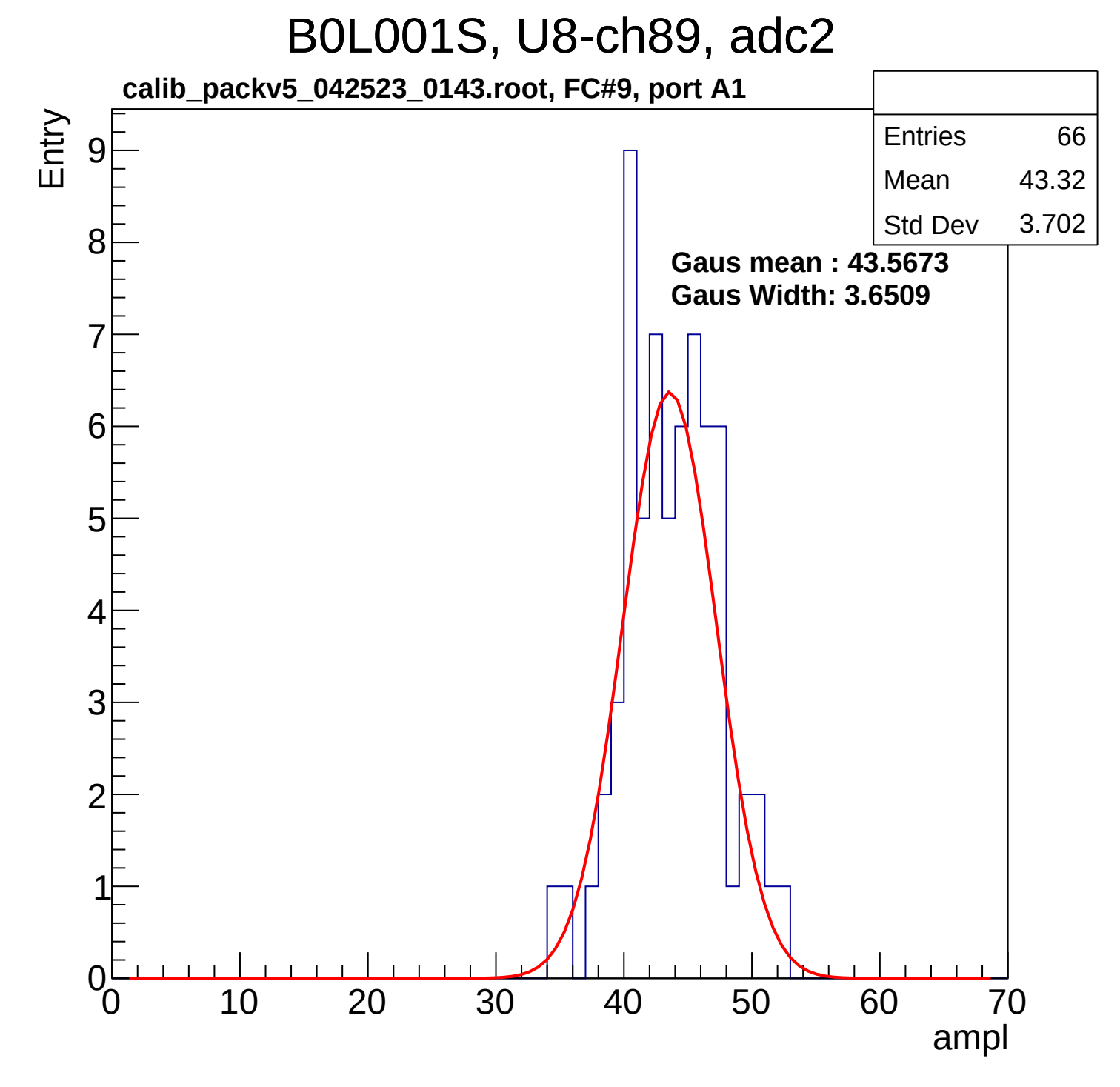
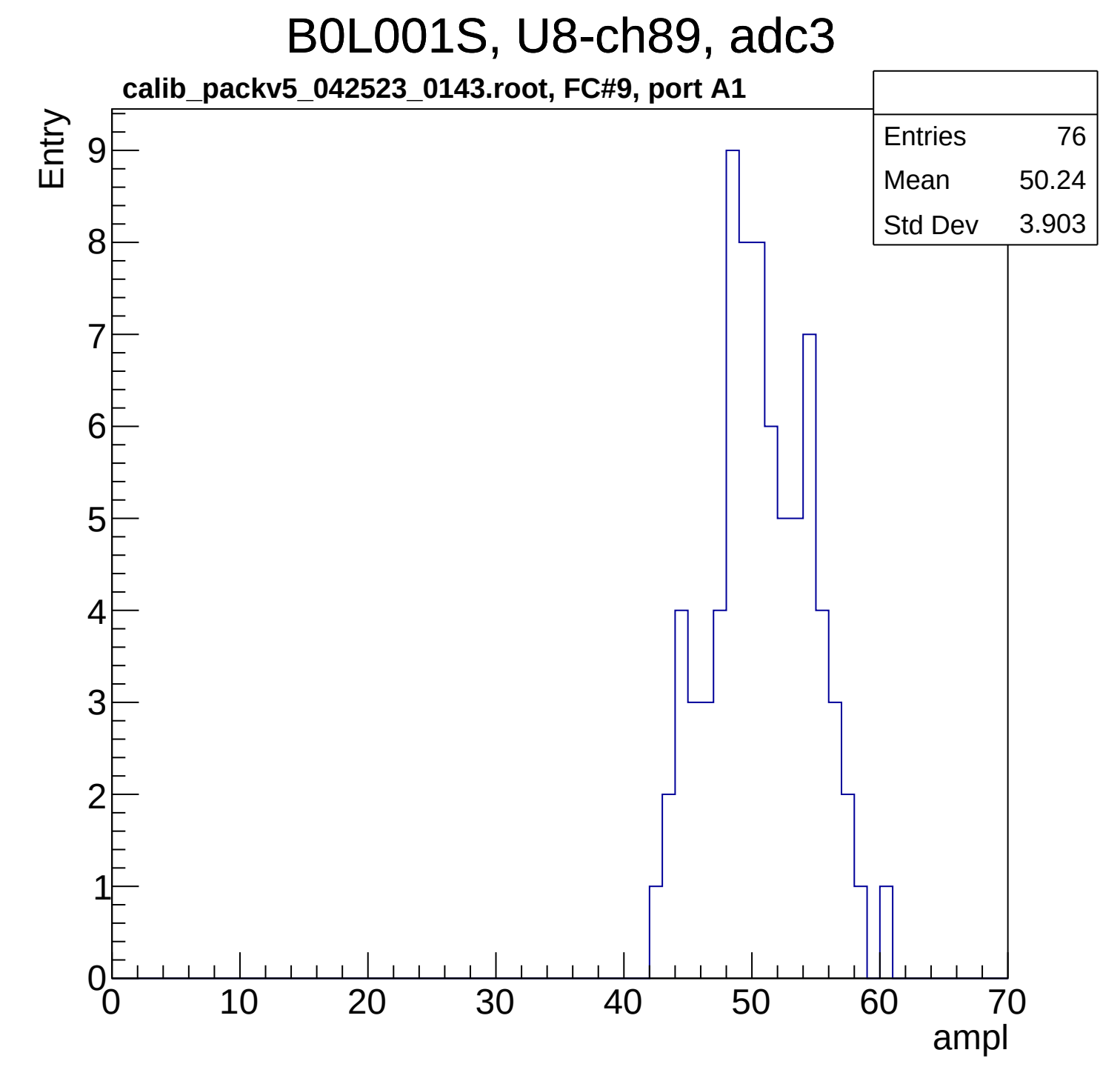

B0L001S, U8-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.32
Std Dev 3.702
Gaus mean : 43.5673
Gaus Width: 3.6509
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U8-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 50.24
Std Dev 3.903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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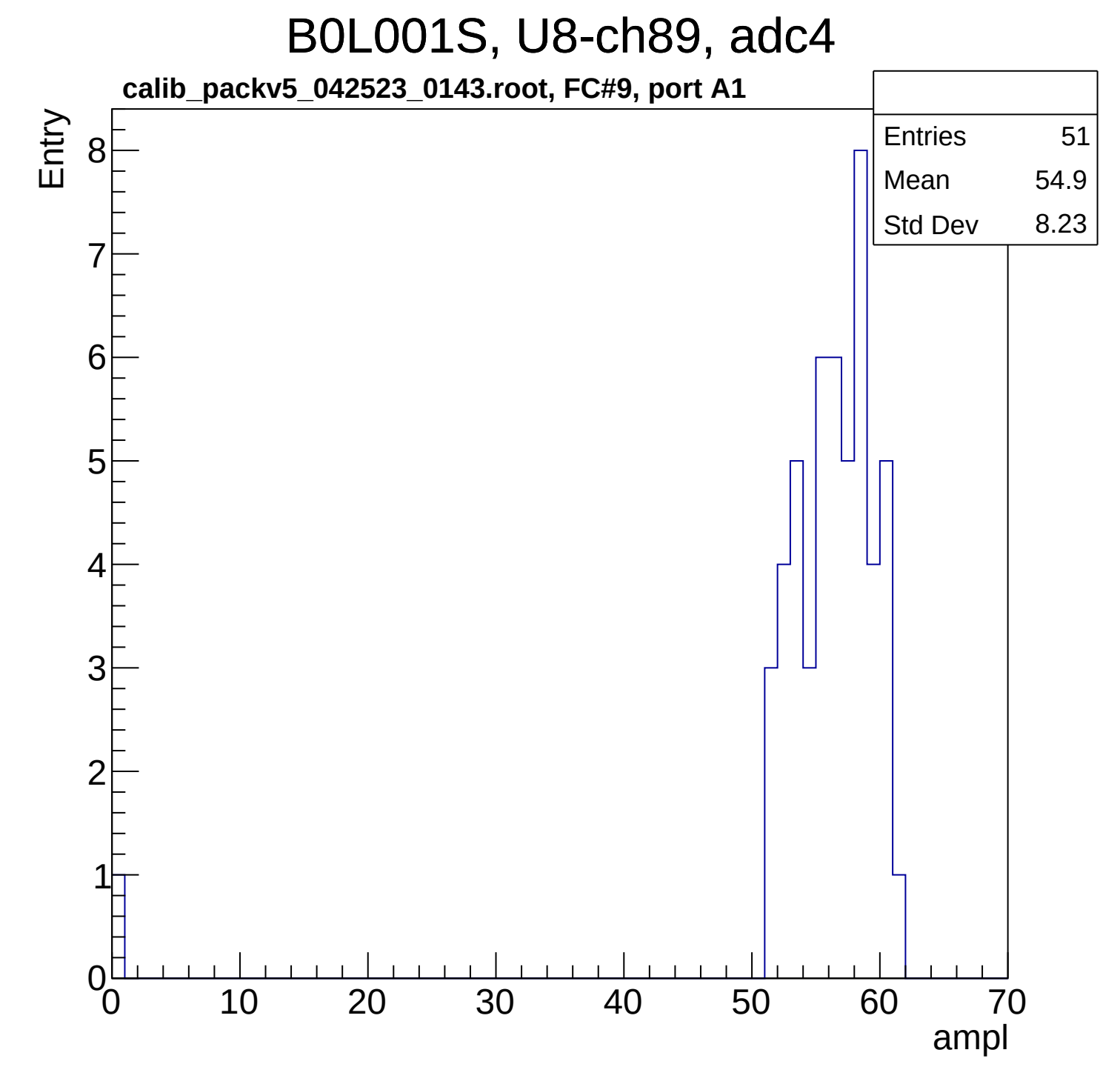
B0L001S, U8-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 54.9
Std Dev 8.23
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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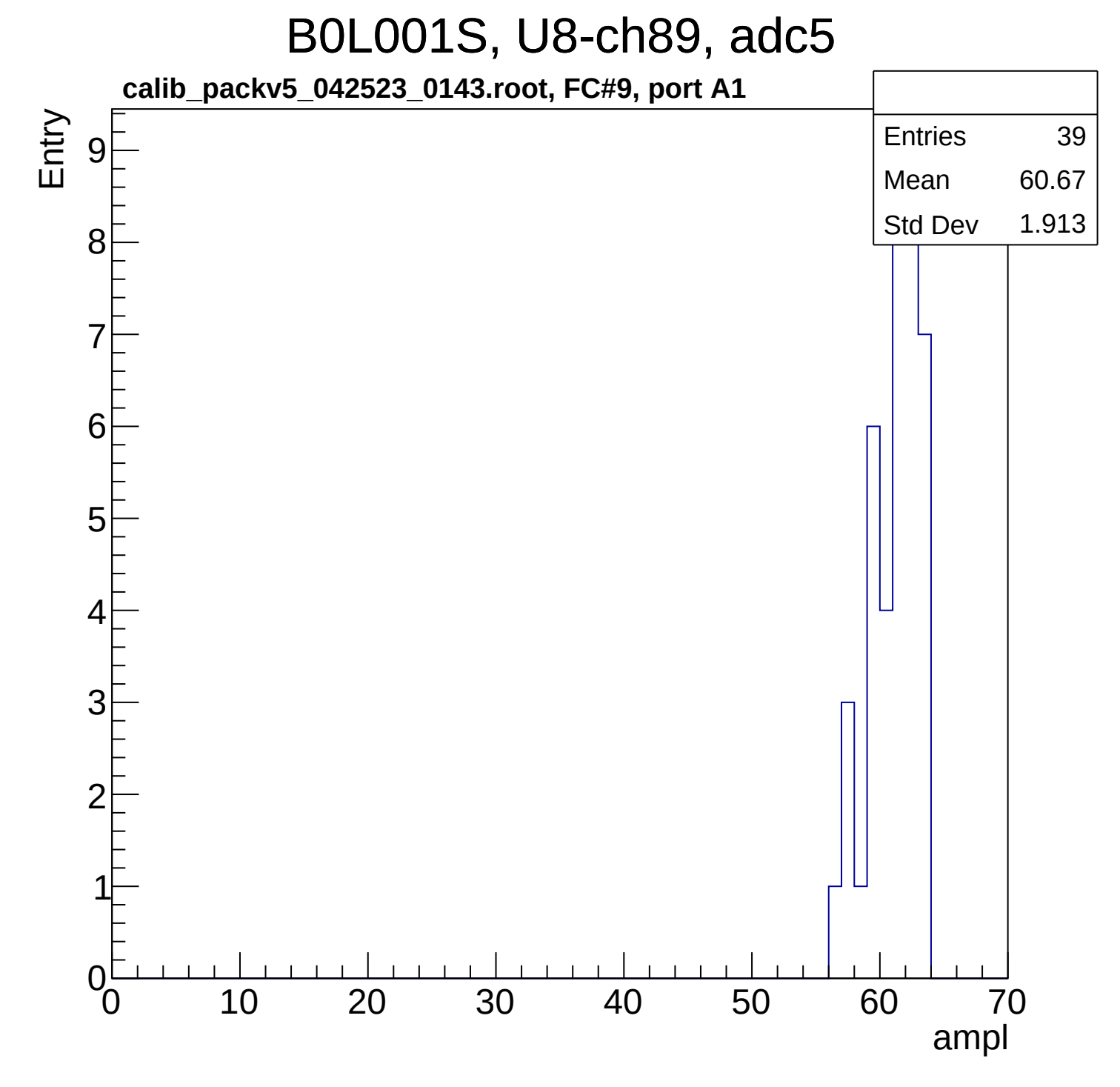
B0L001S, U8-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 60.67
Std Dev 1.913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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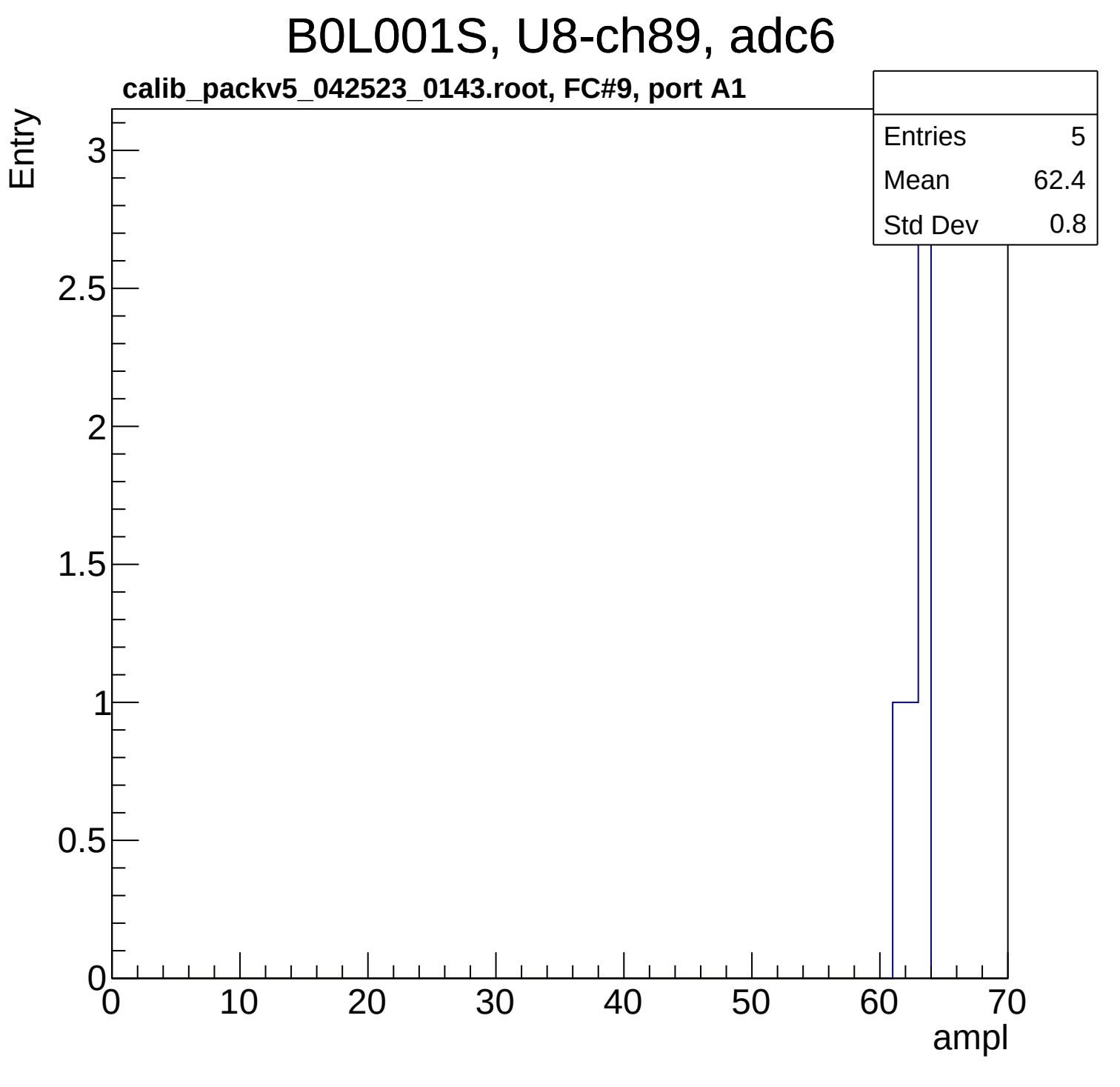
B0L001S, U8-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
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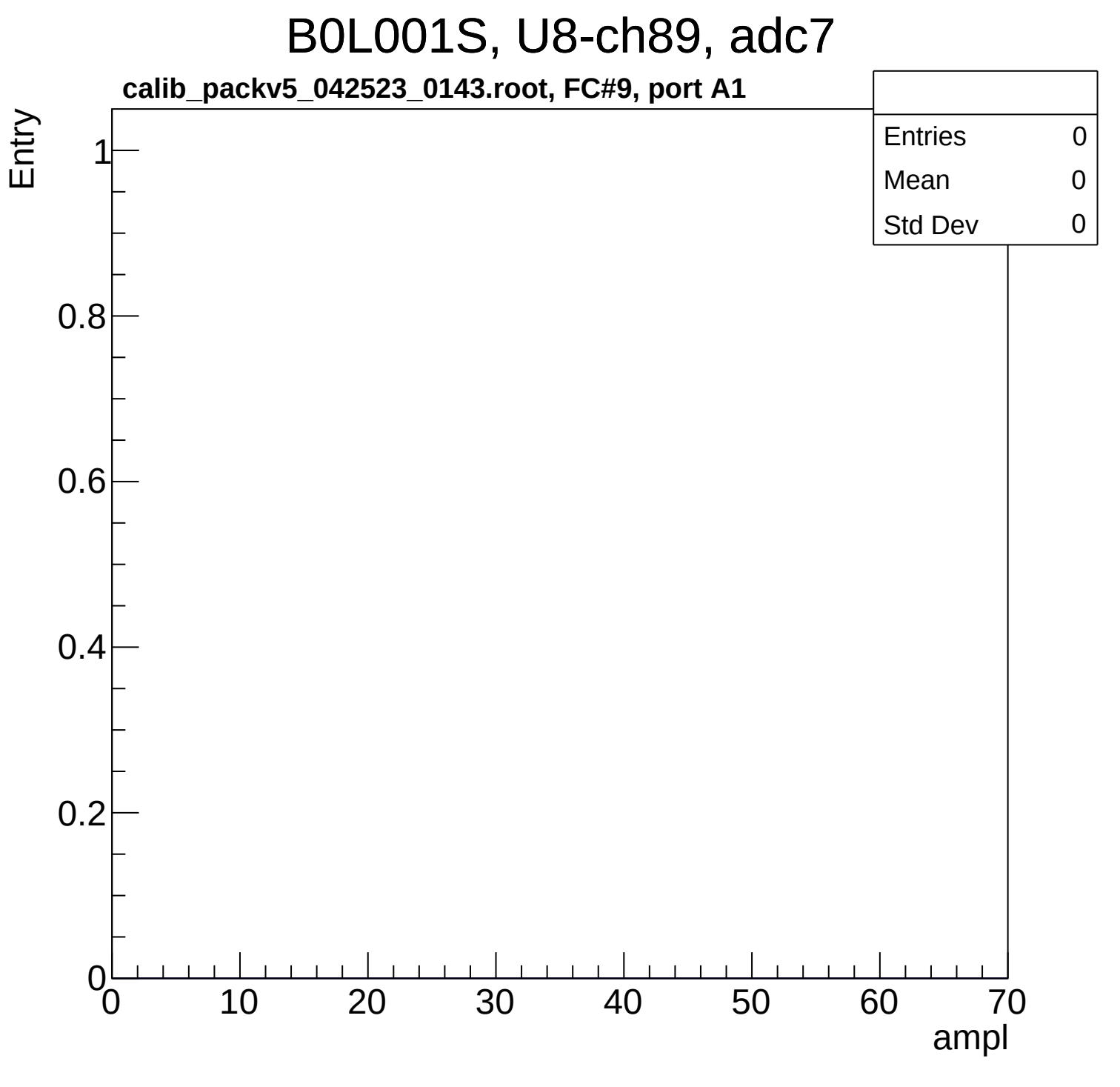
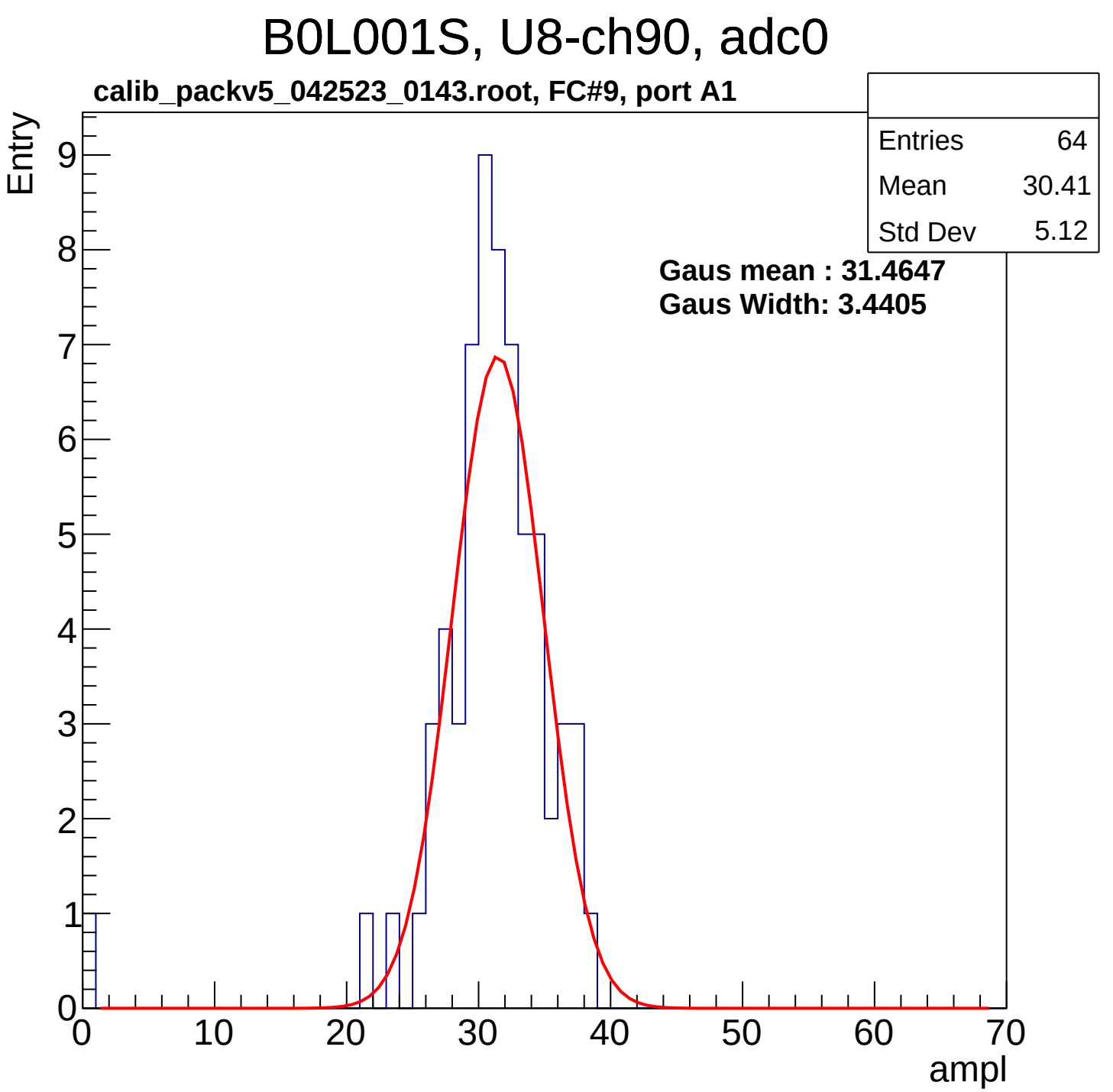
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.41
Std Dev 5.12
Gaus mean : 31.4647
Gaus Width: 3.4405
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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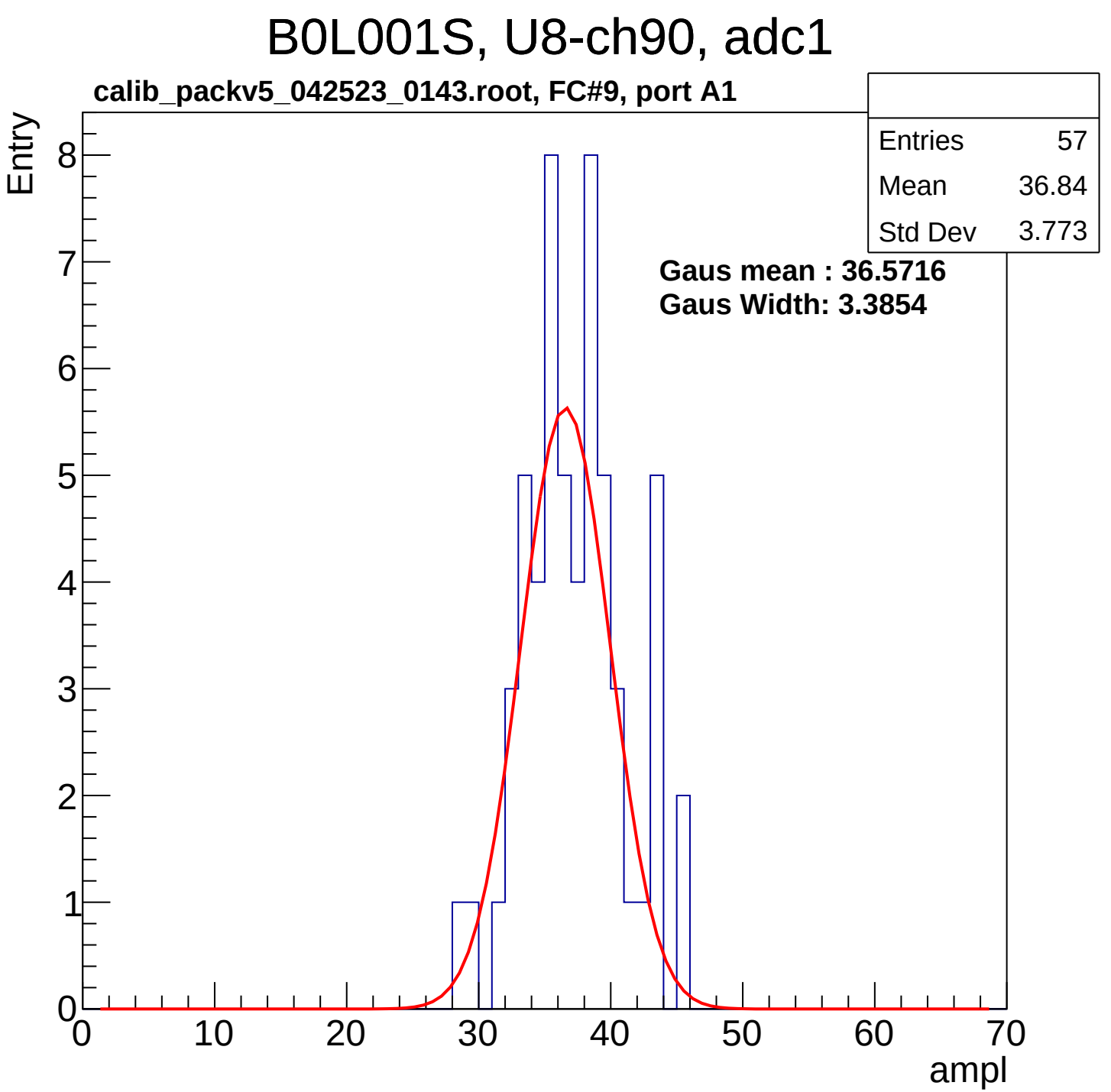
B0L001S, U8-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.84
Std Dev 3.773
Gaus mean : 36.5716
Gaus Width: 3.3854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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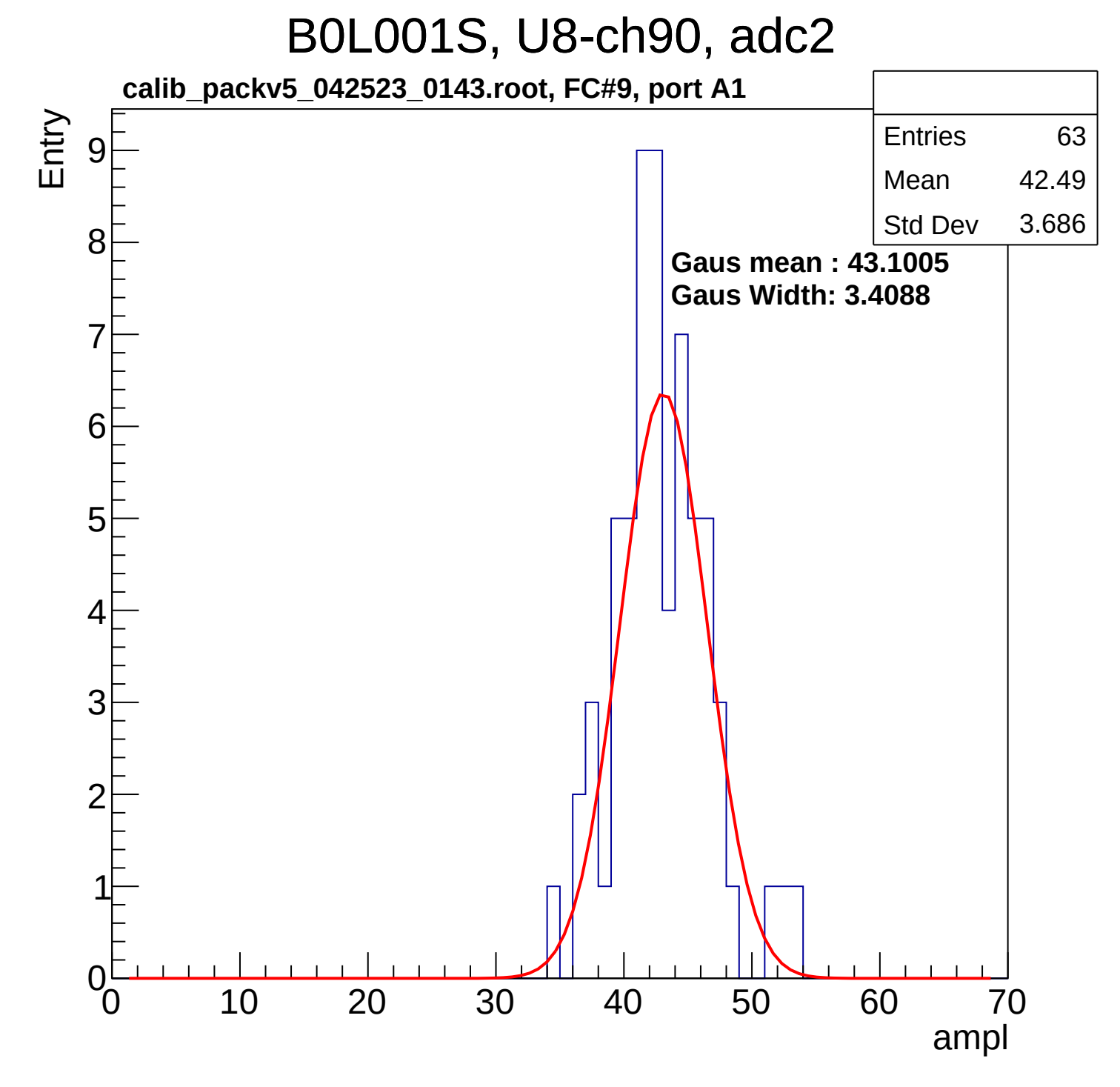
B0L001S, U8-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.49
Std Dev 3.686
Gaus mean : 43.1005
Gaus Width: 3.4088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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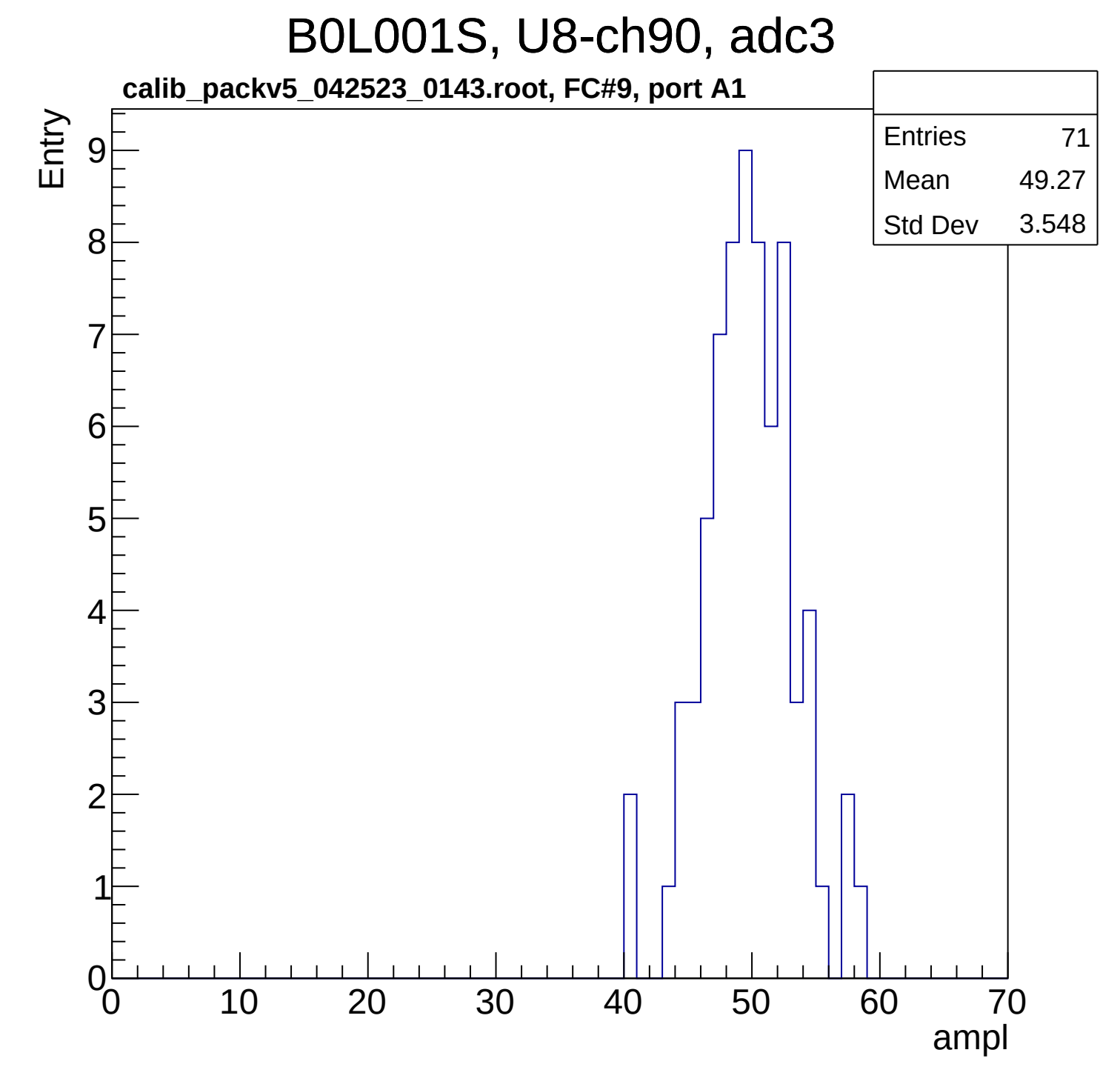
B0L001S, U8-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.27
Std Dev 3.548
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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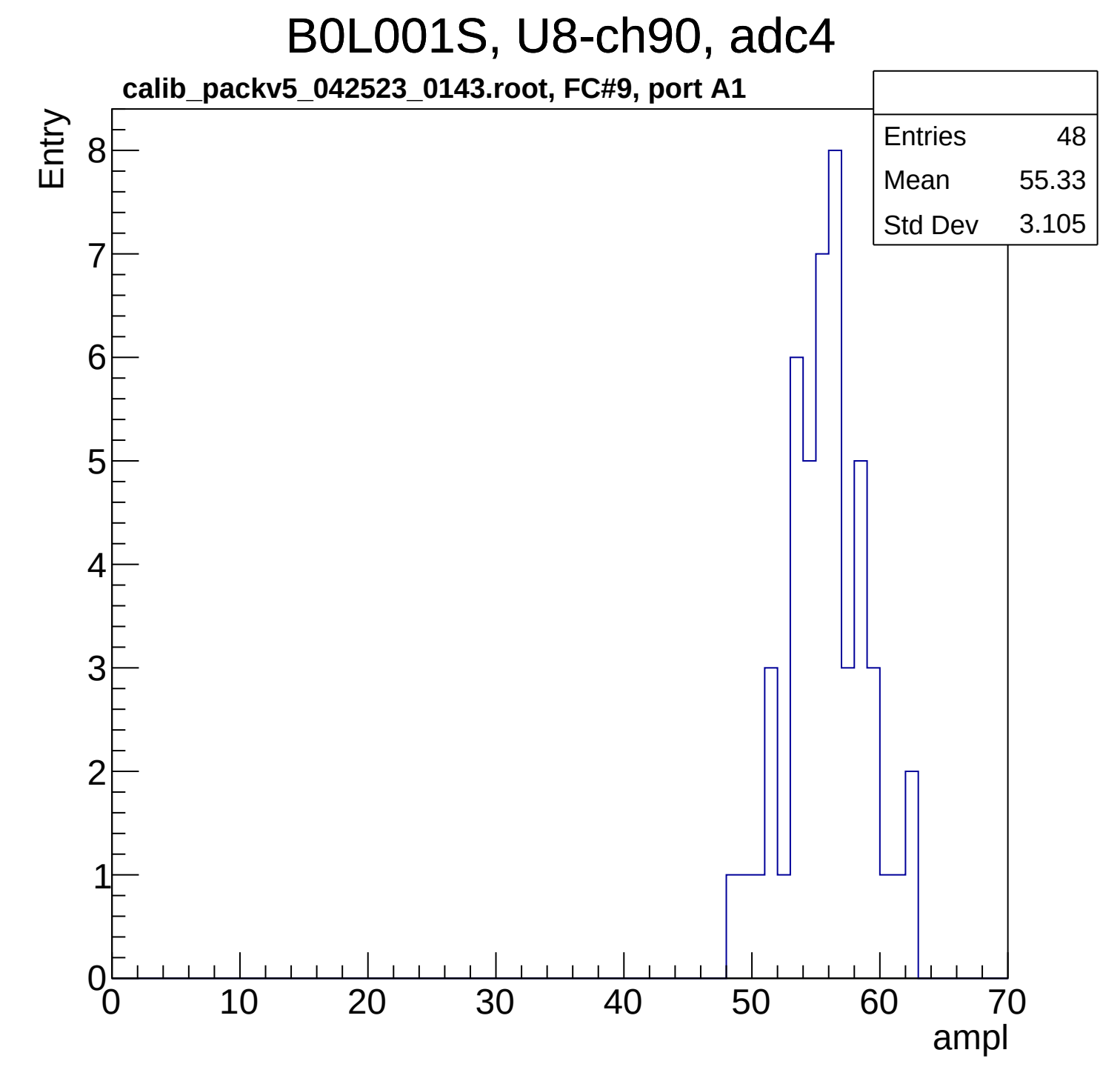
B0L001S, U8-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.33
Std Dev 3.105
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
50
60
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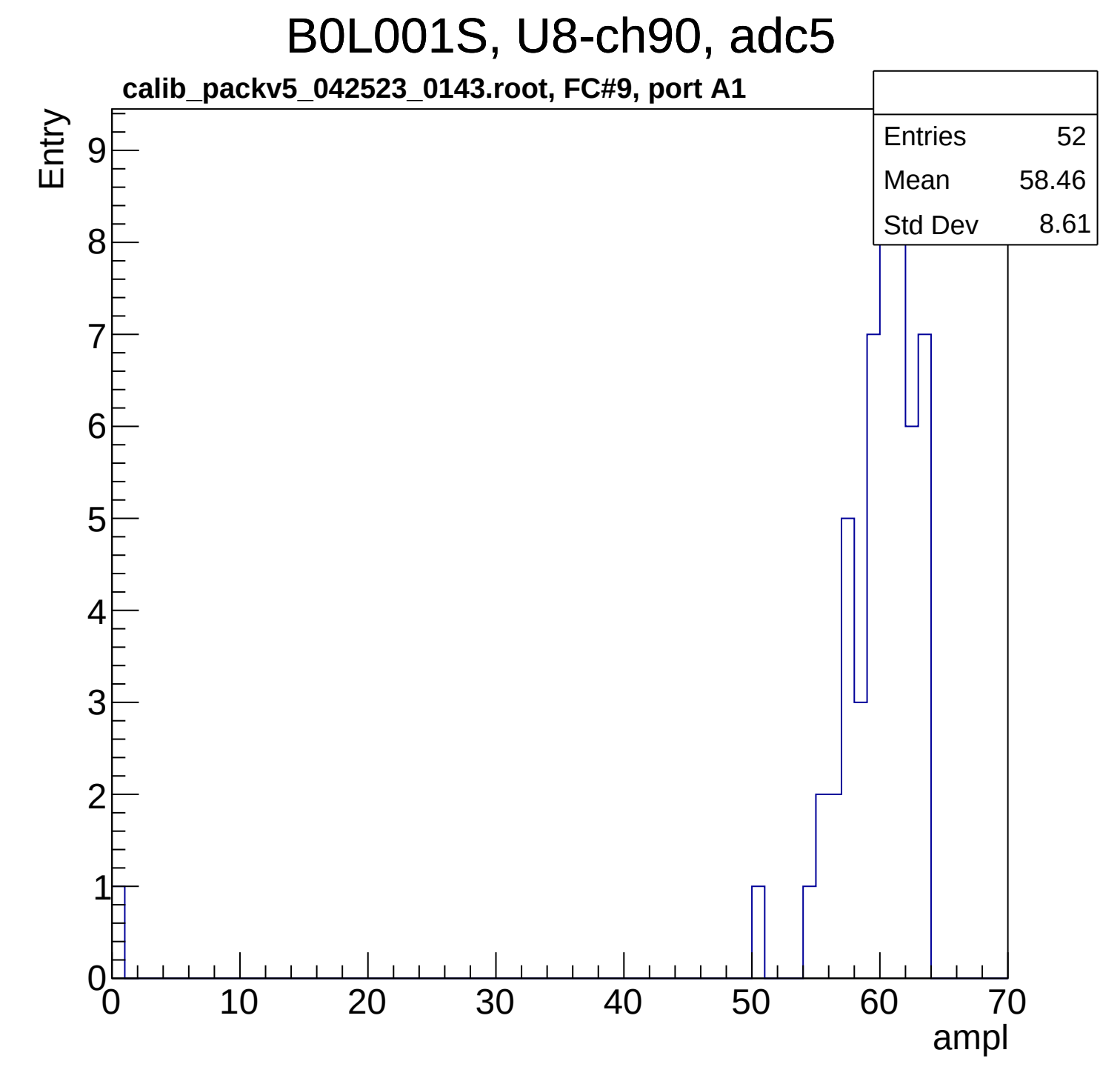
B0L001S, U8-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.46
Std Dev 8.61
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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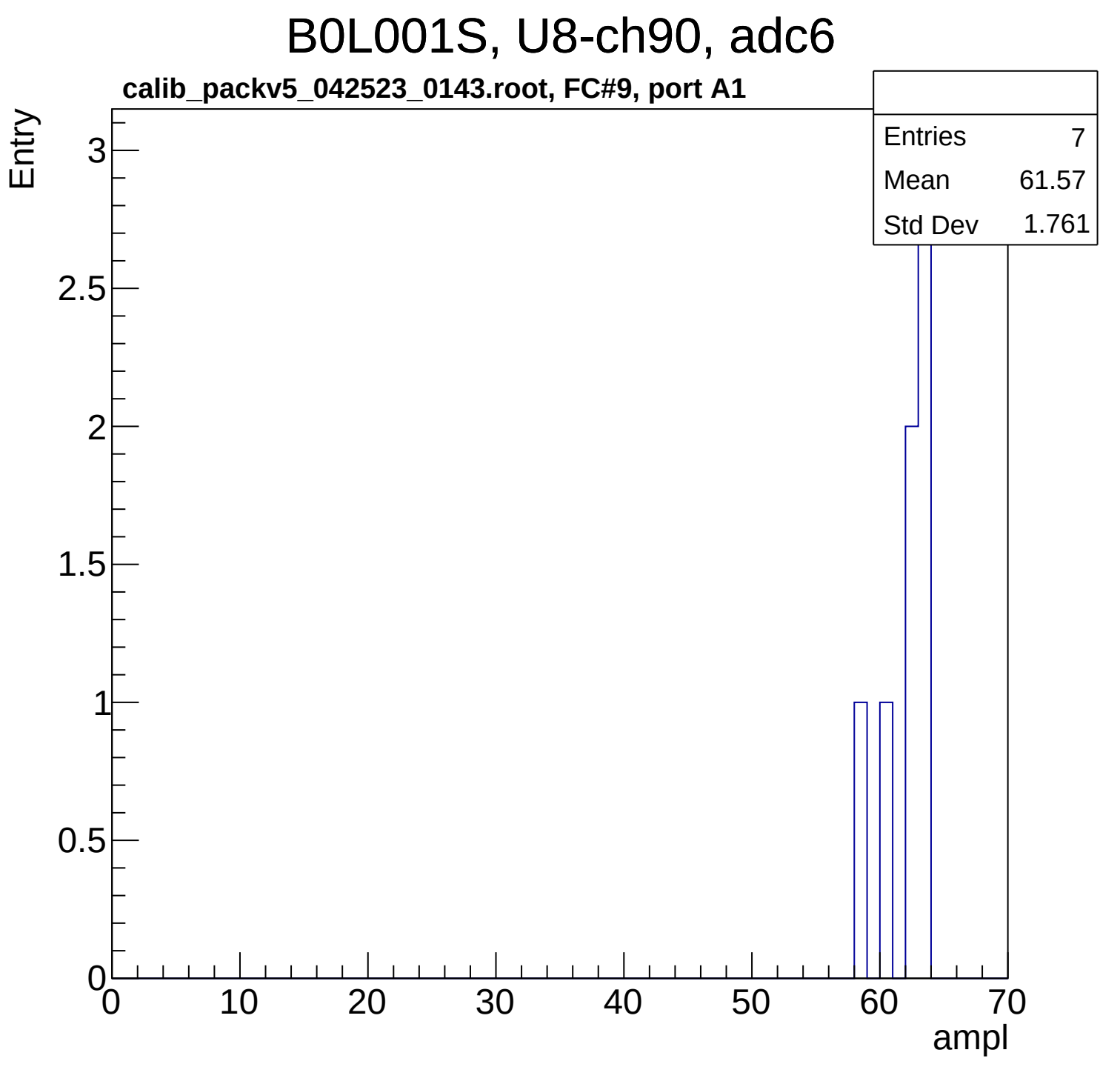
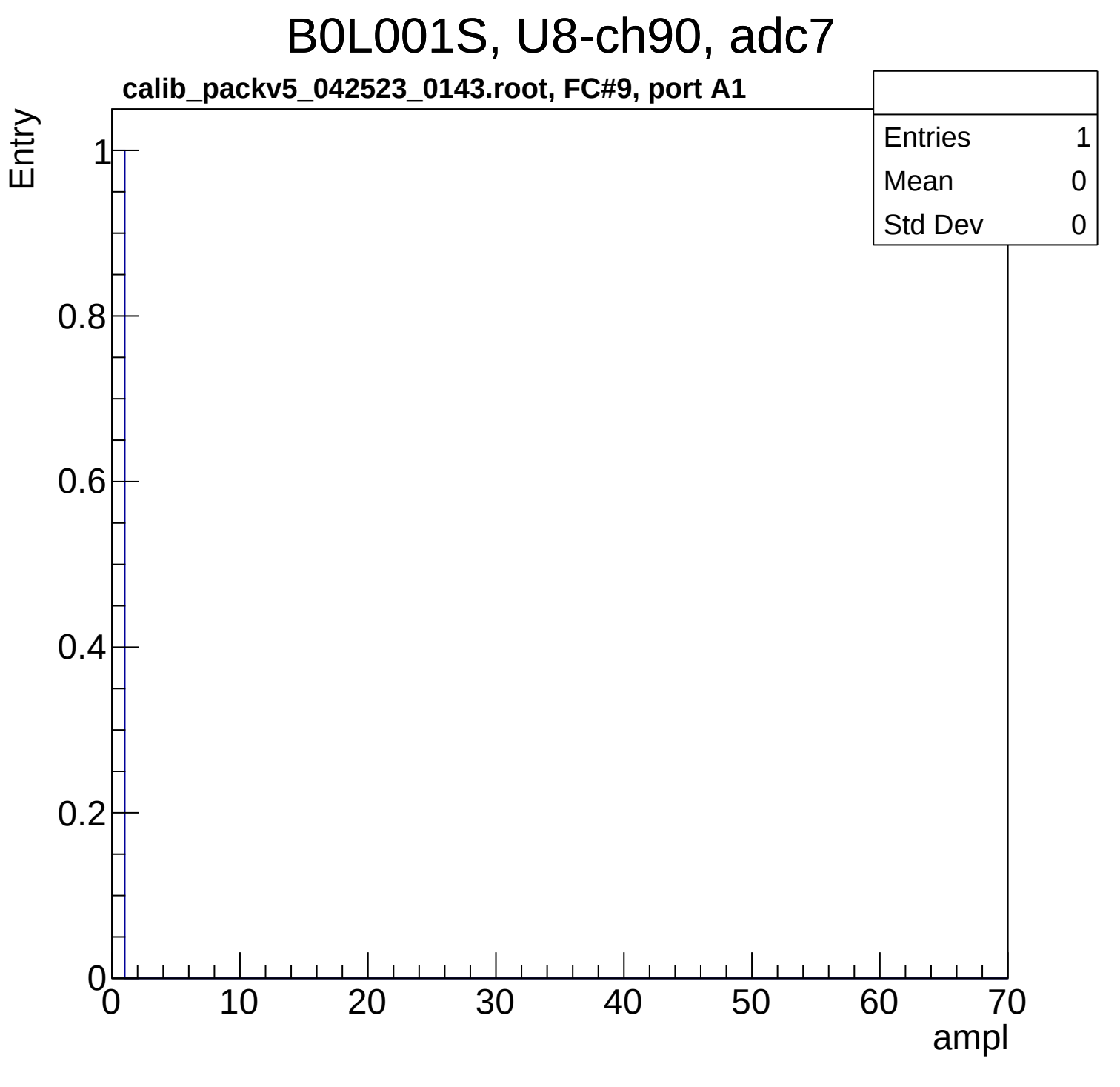
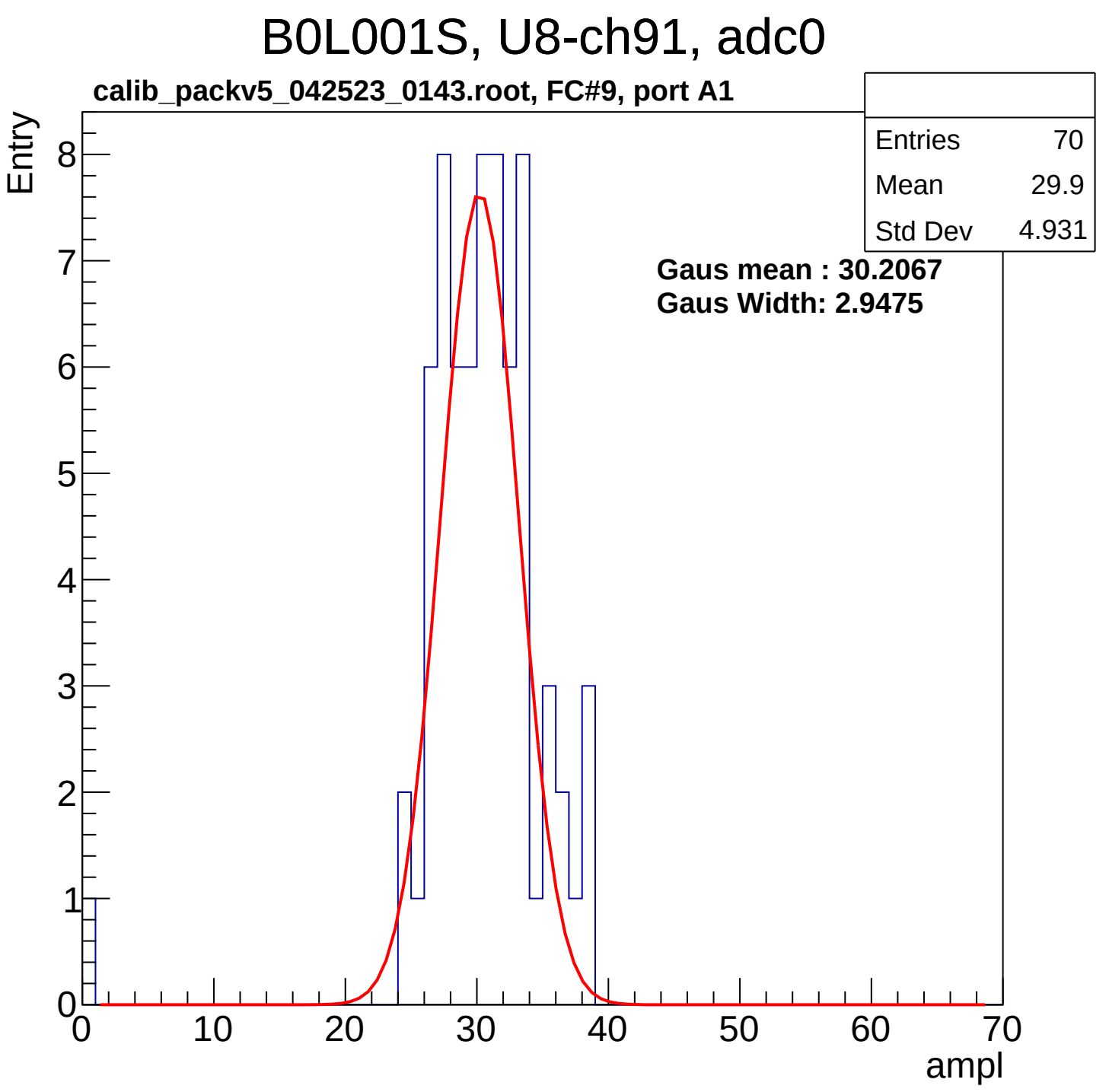
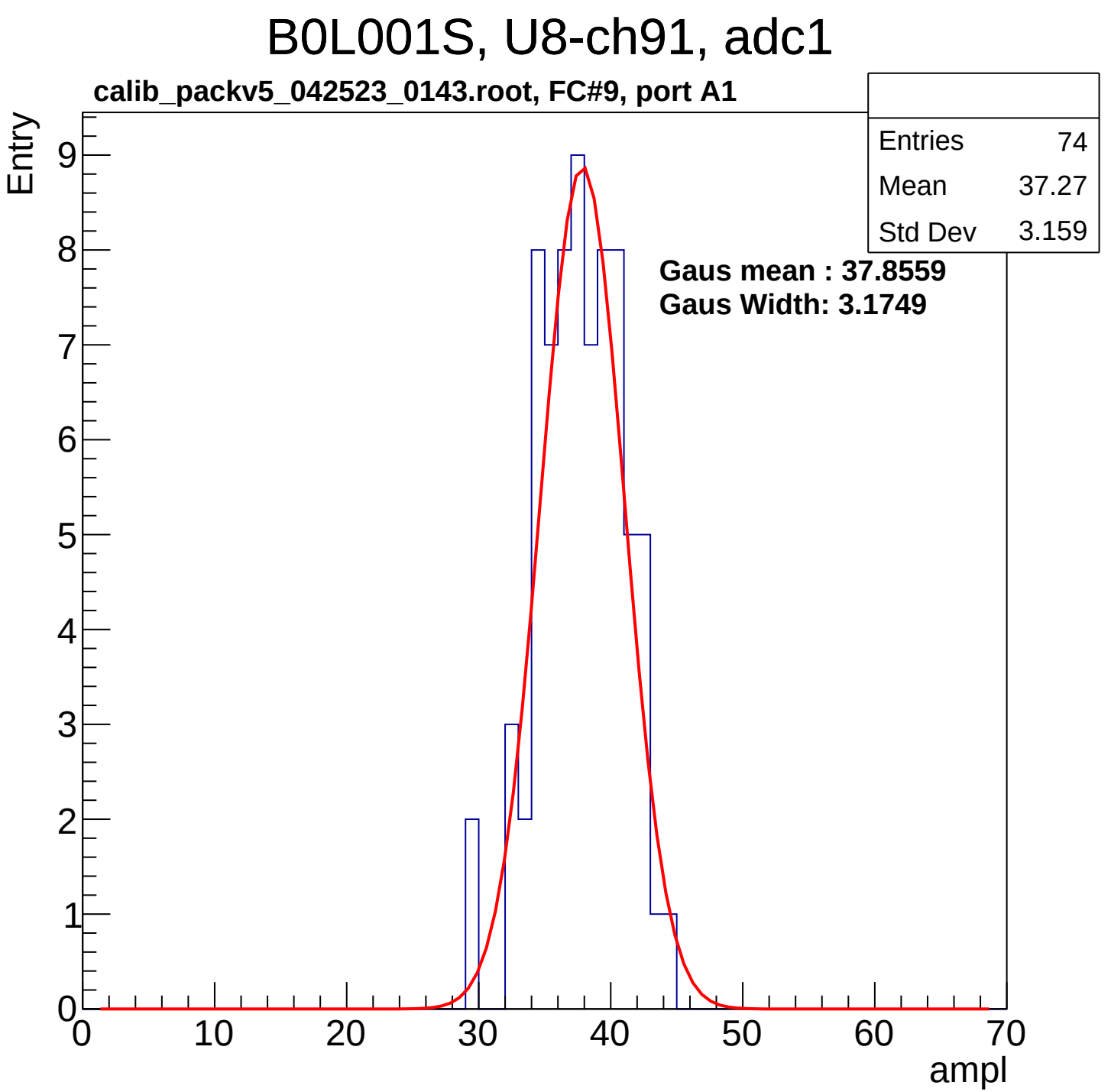

B0L001S, U8-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.57
Std Dev 1.761
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.9
Std Dev 4.931
Gaus mean : 30.2067
Gaus Width: 2.9475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.27
Std Dev 3.159
Gaus mean : 37.8559
Gaus Width: 3.1749
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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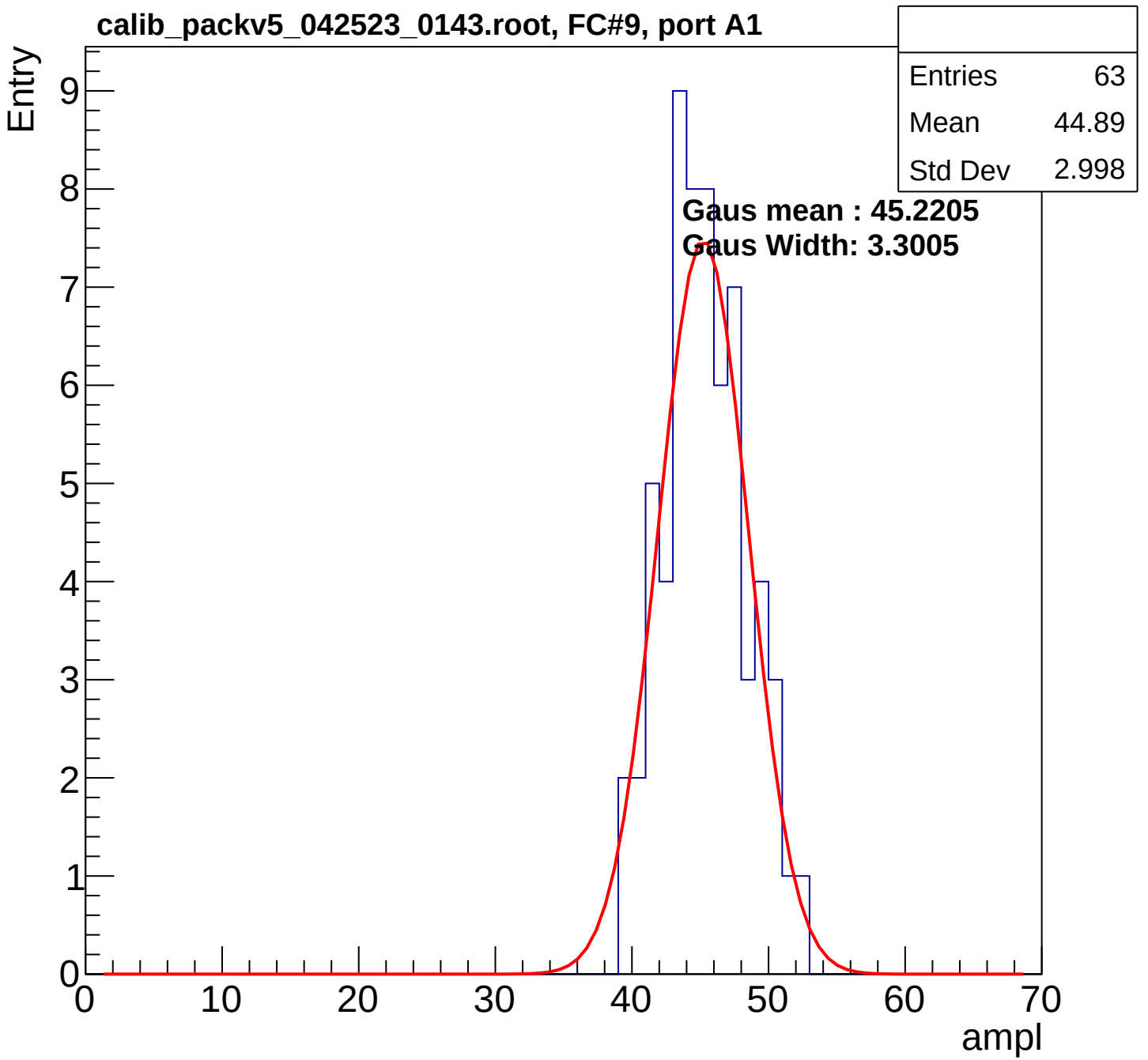
B0L001S, U8-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.89
Std Dev 2.998
Gaus mean : 45.2205
Gaus Width: 3.3005
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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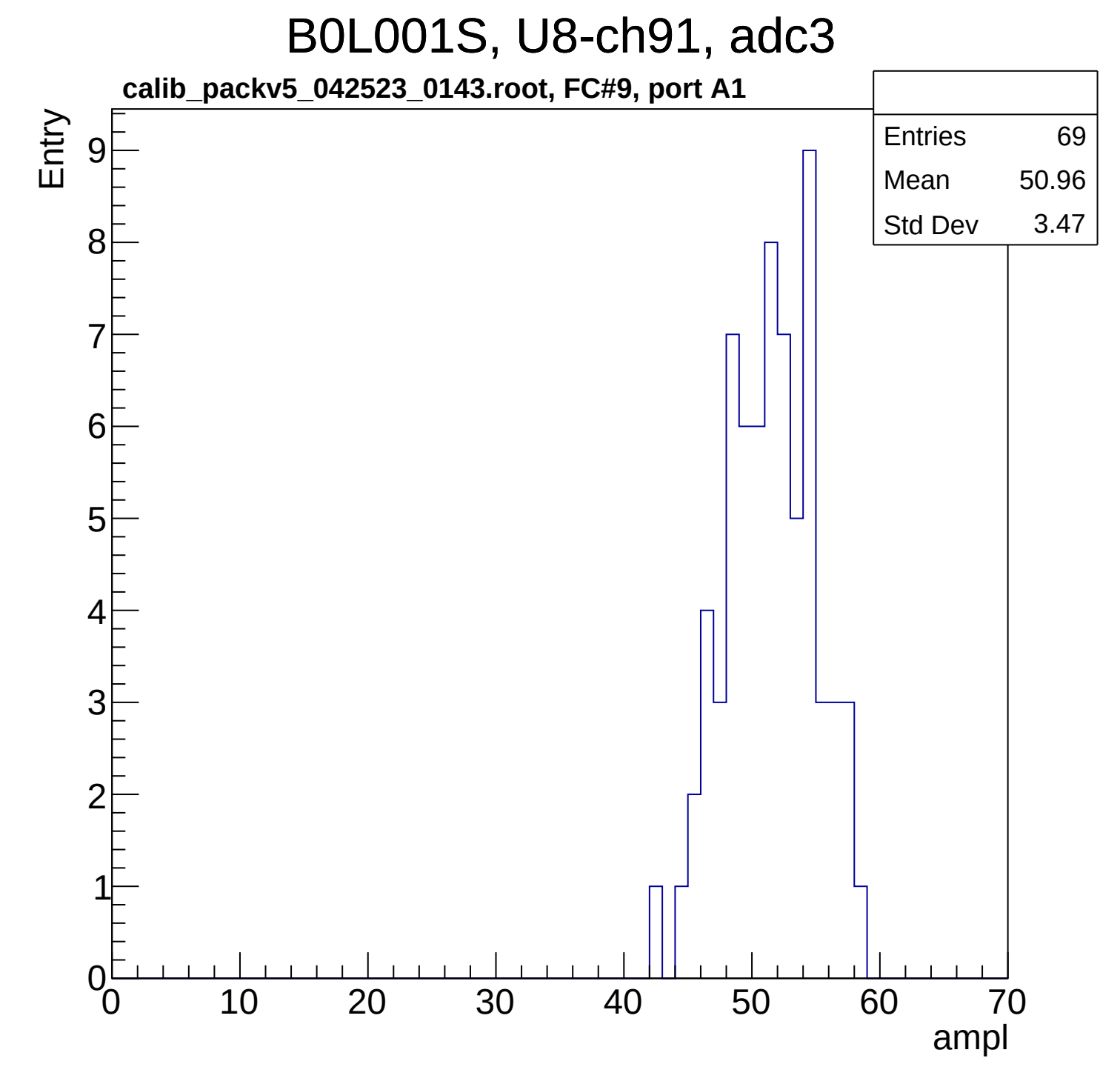
B0L001S, U8-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.96
Std Dev 3.47
Entry
ampl
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3
4
5
6
7
8
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0
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50
60
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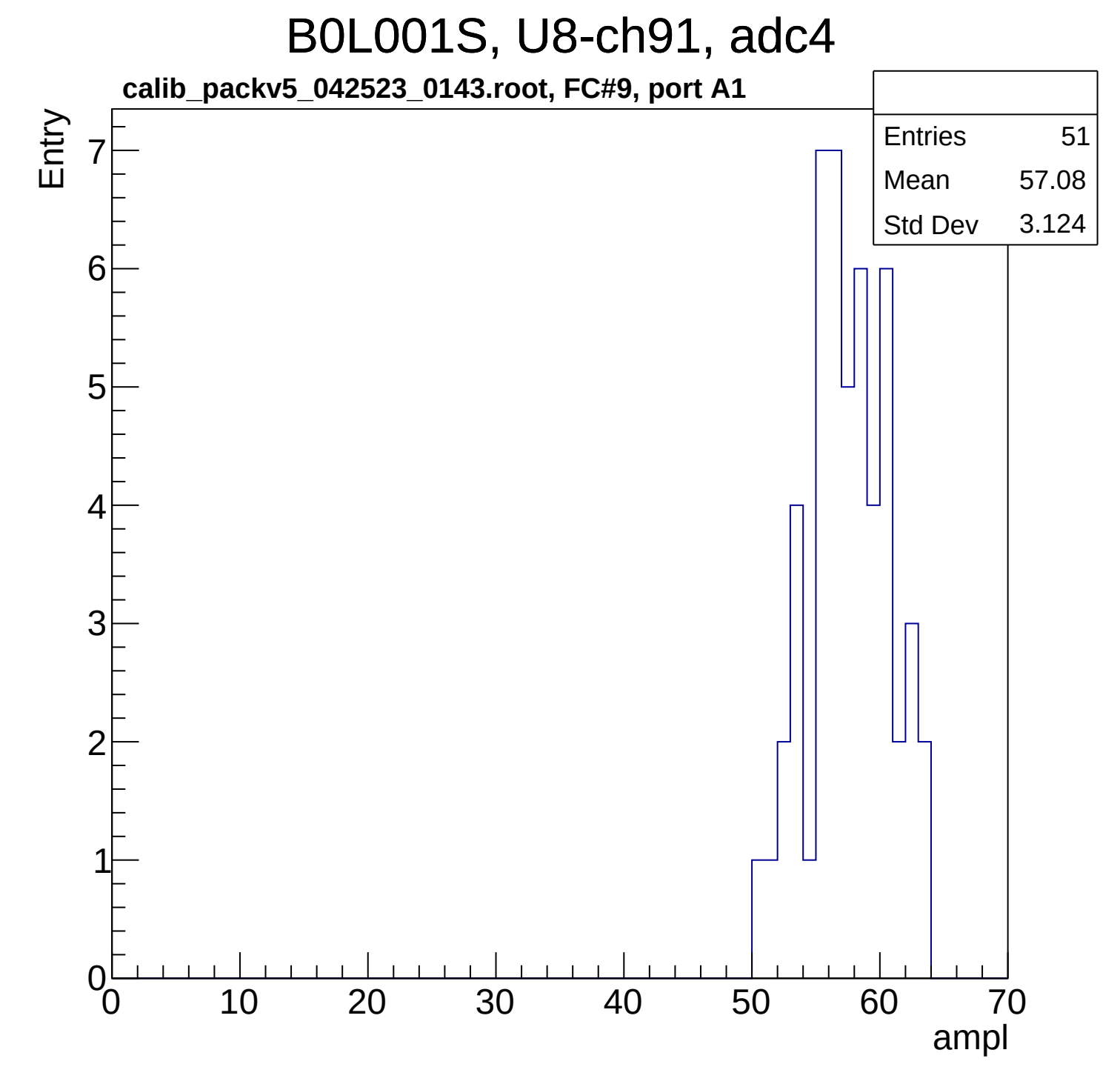
B0L001S, U8-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.08
Std Dev 3.124
Entry
ampl
0
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2
3
4
5
6
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0
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40
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60
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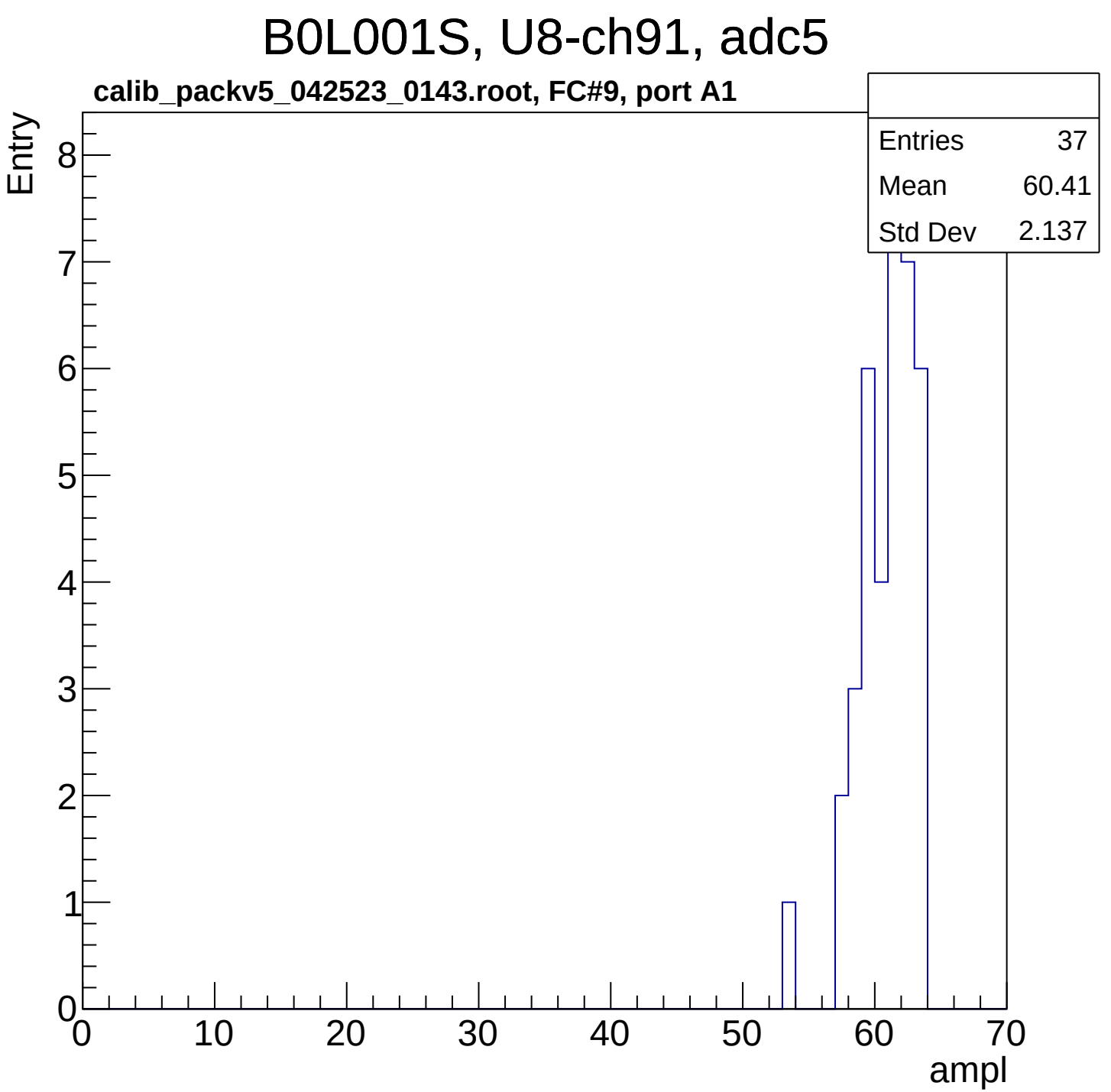
B0L001S, U8-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.41
Std Dev 2.137
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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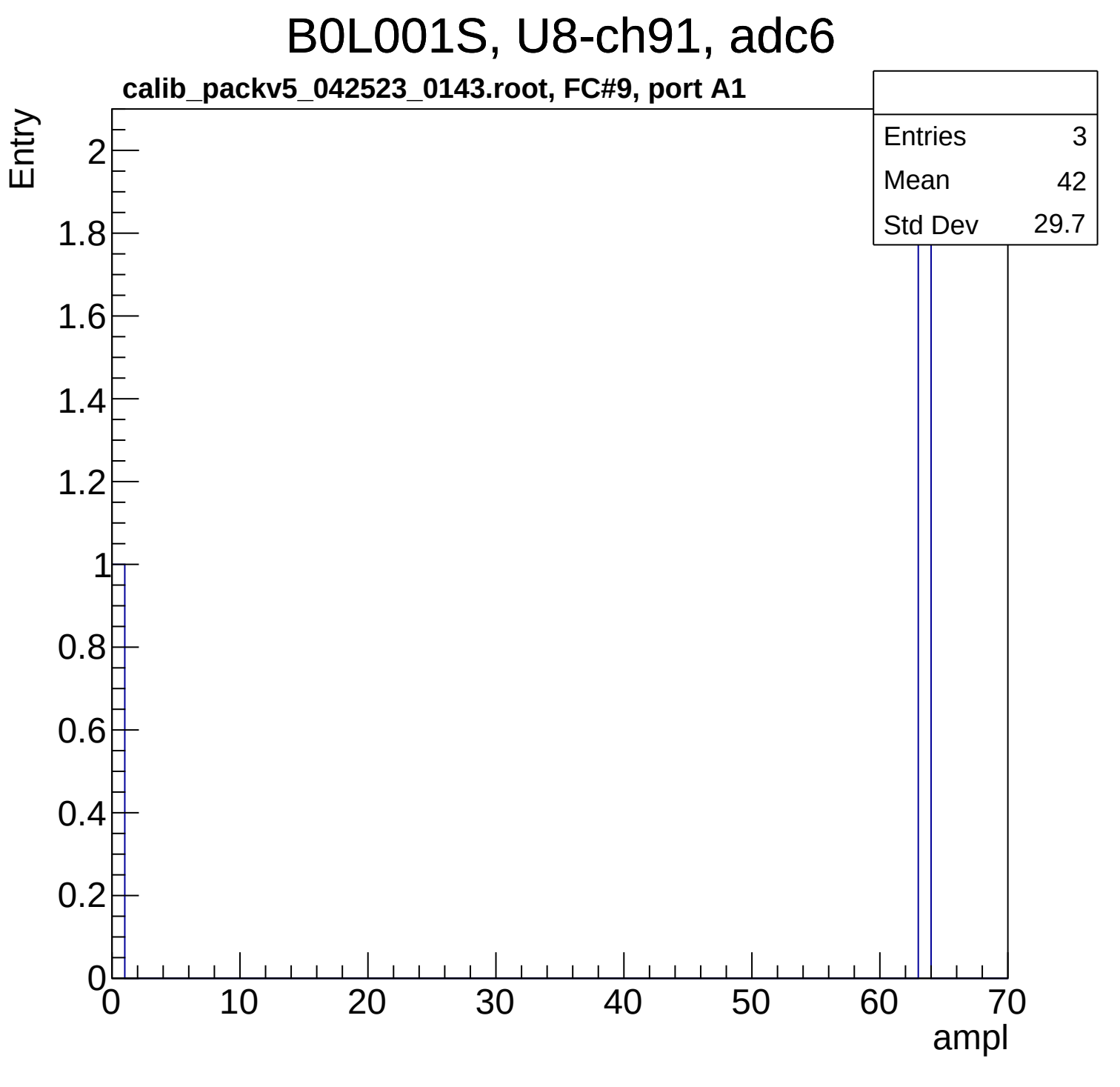
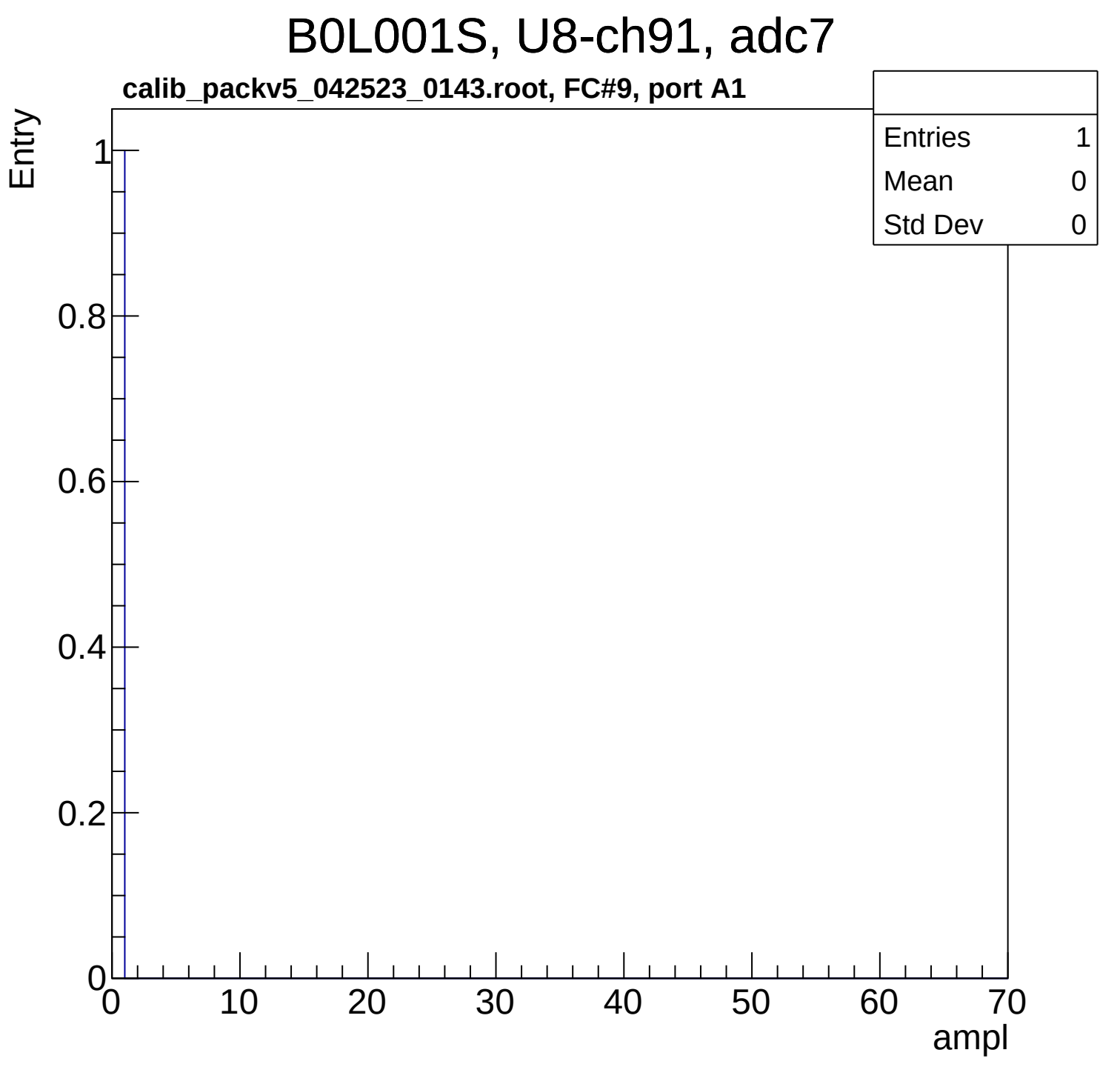
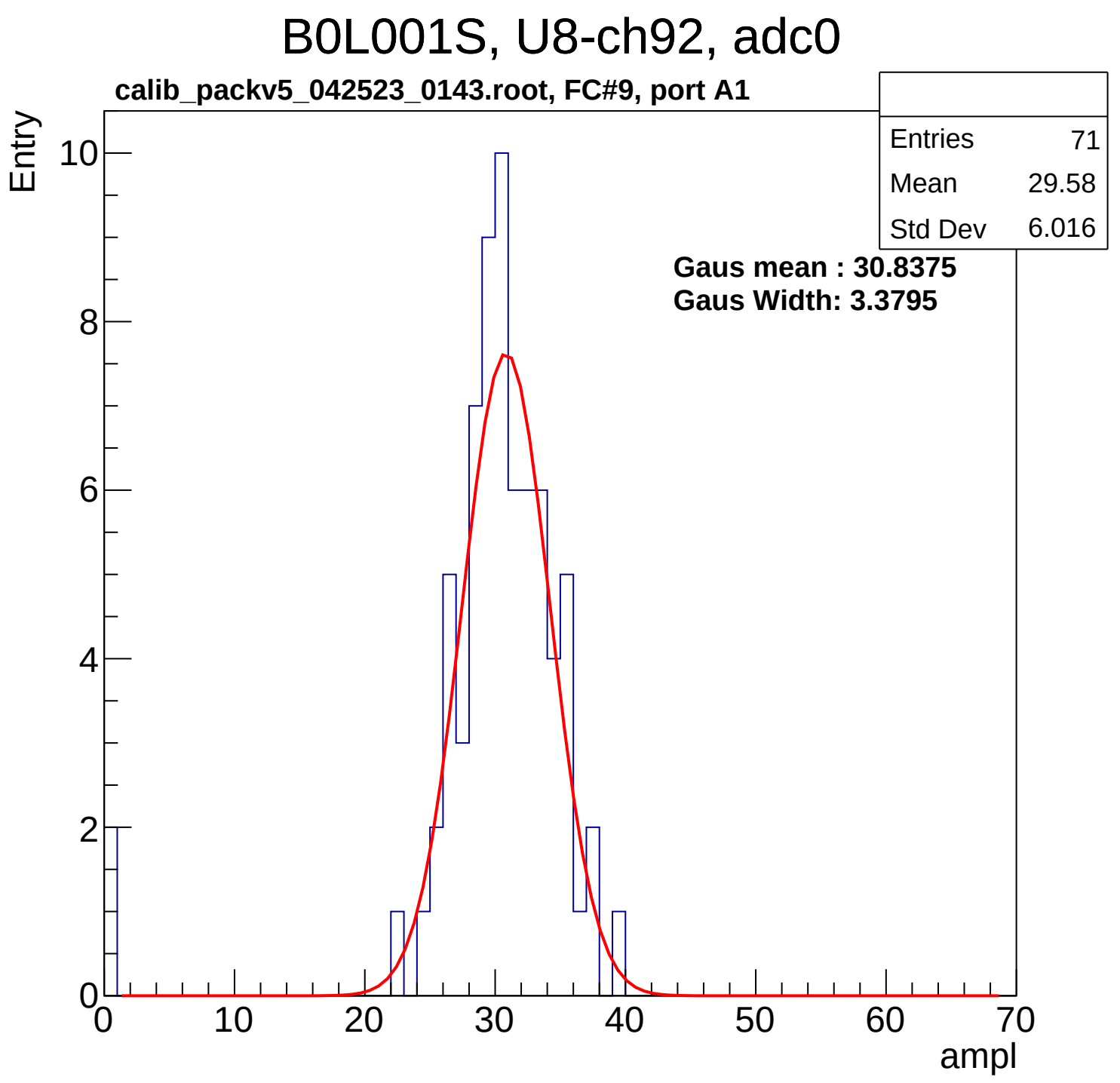

B0L001S, U8-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.58
Std Dev 6.016
Gaus mean : 30.8375
Gaus Width: 3.3795
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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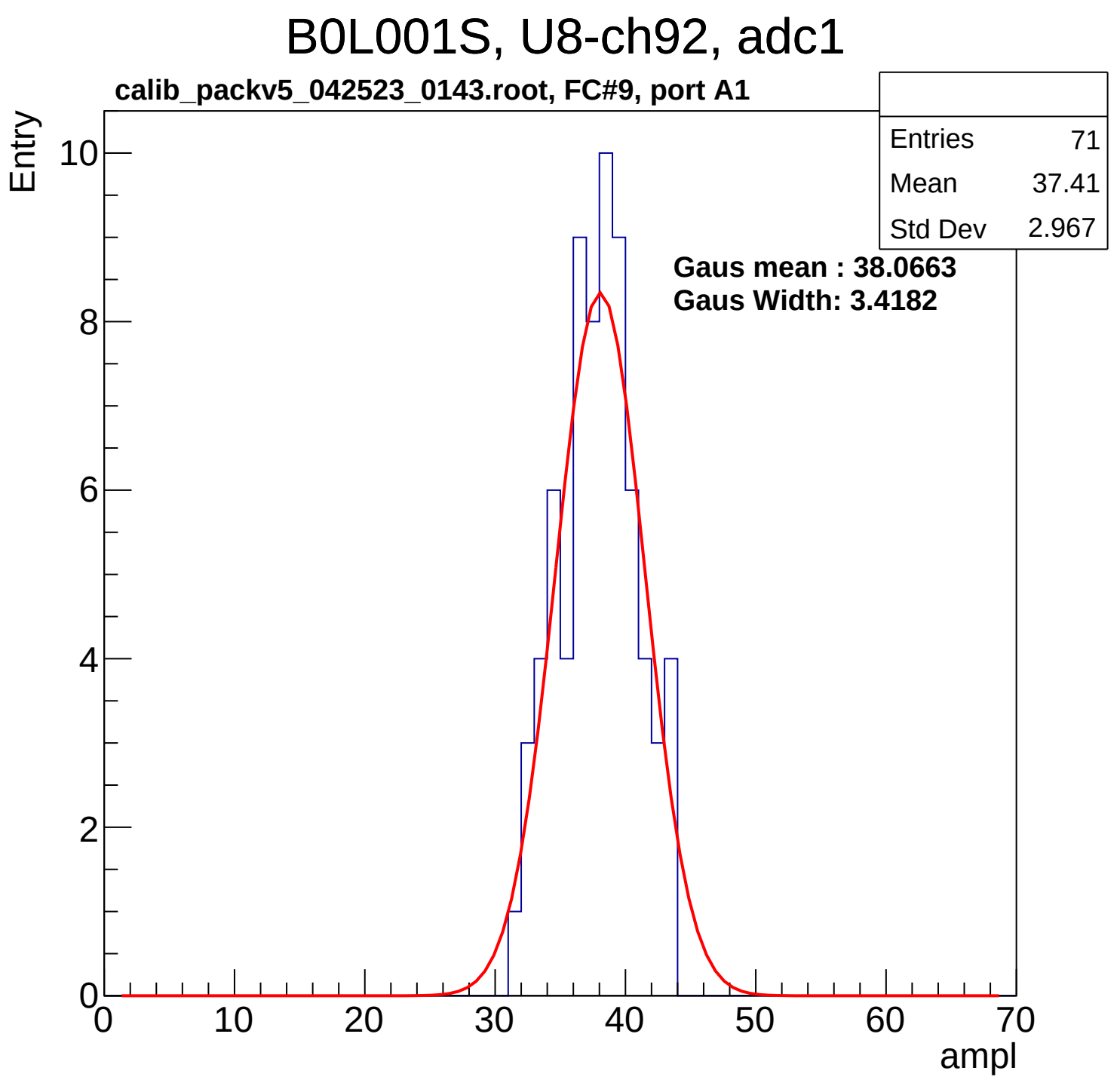
B0L001S, U8-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.41
Std Dev 2.967
Gaus mean : 38.0663
Gaus Width: 3.4182
Entry
ampl
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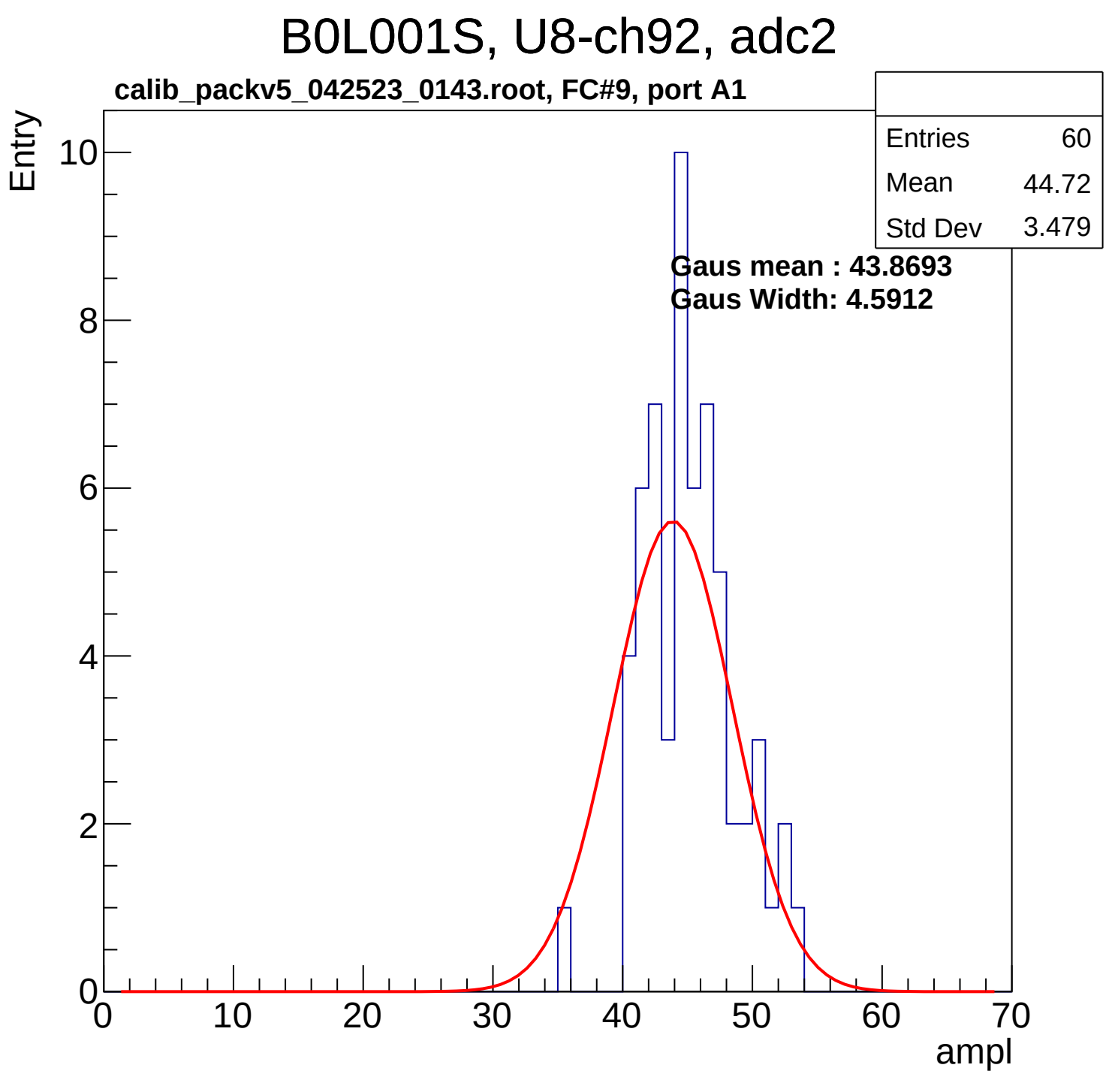
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.72
Std Dev 3.479
Gaus mean : 43.8693
Gaus Width: 4.5912
Entry
ampl
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4
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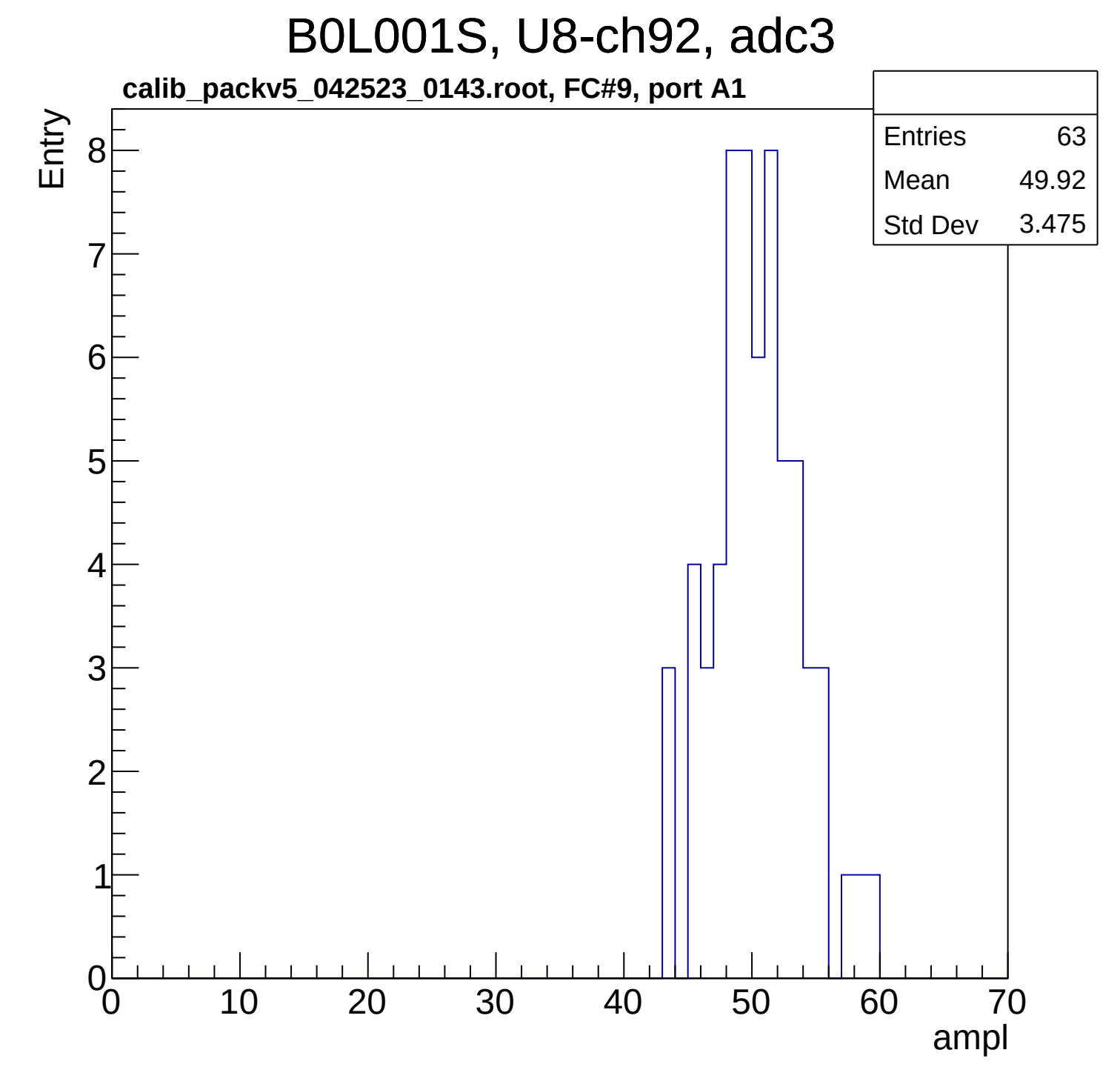
B0L001S, U8-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.92
Std Dev 3.475
Entry
ampl
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7
8
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60
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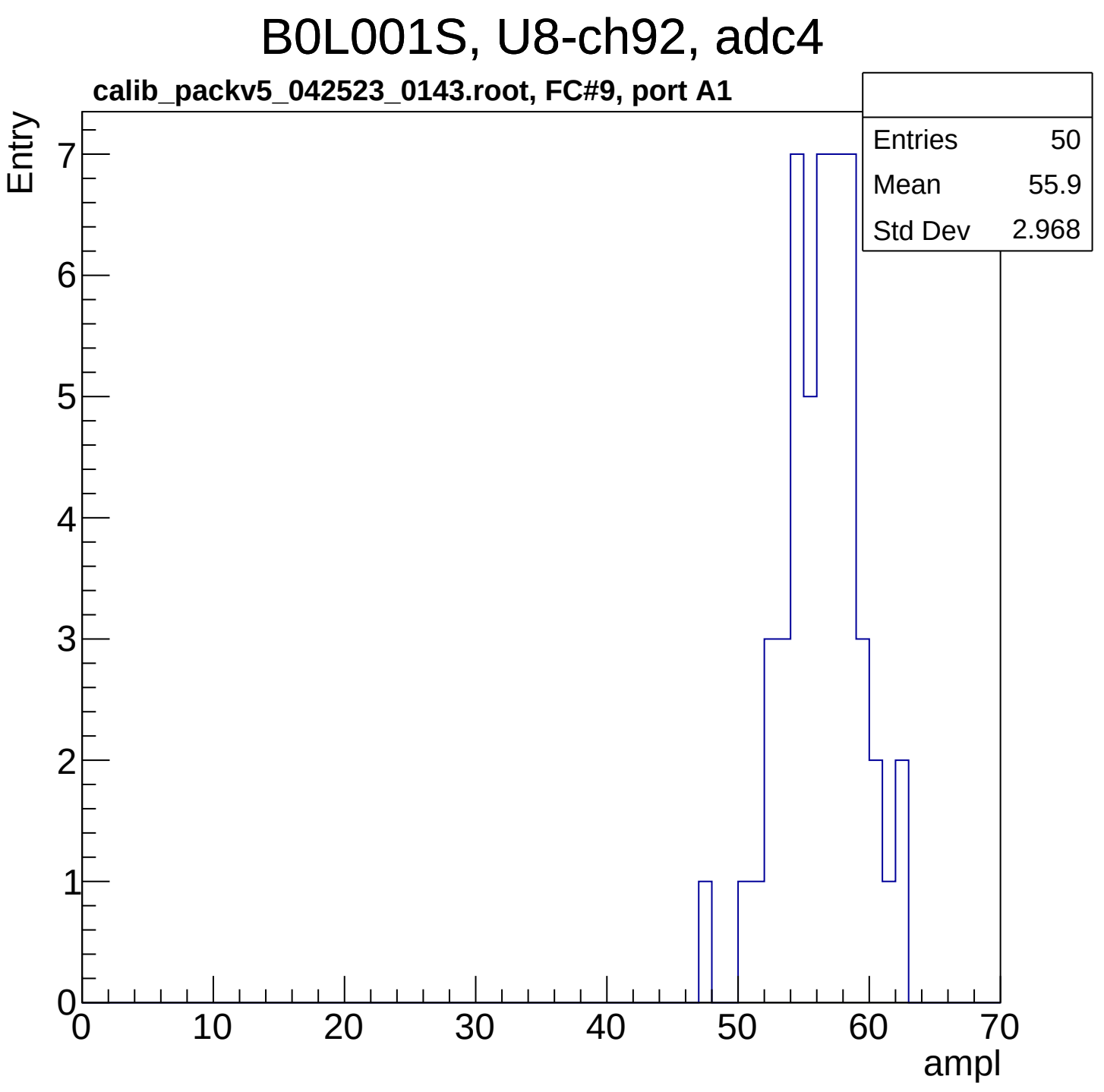
B0L001S, U8-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 55.9
Std Dev 2.968
Entry
ampl
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1
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5
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0
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40
50
60
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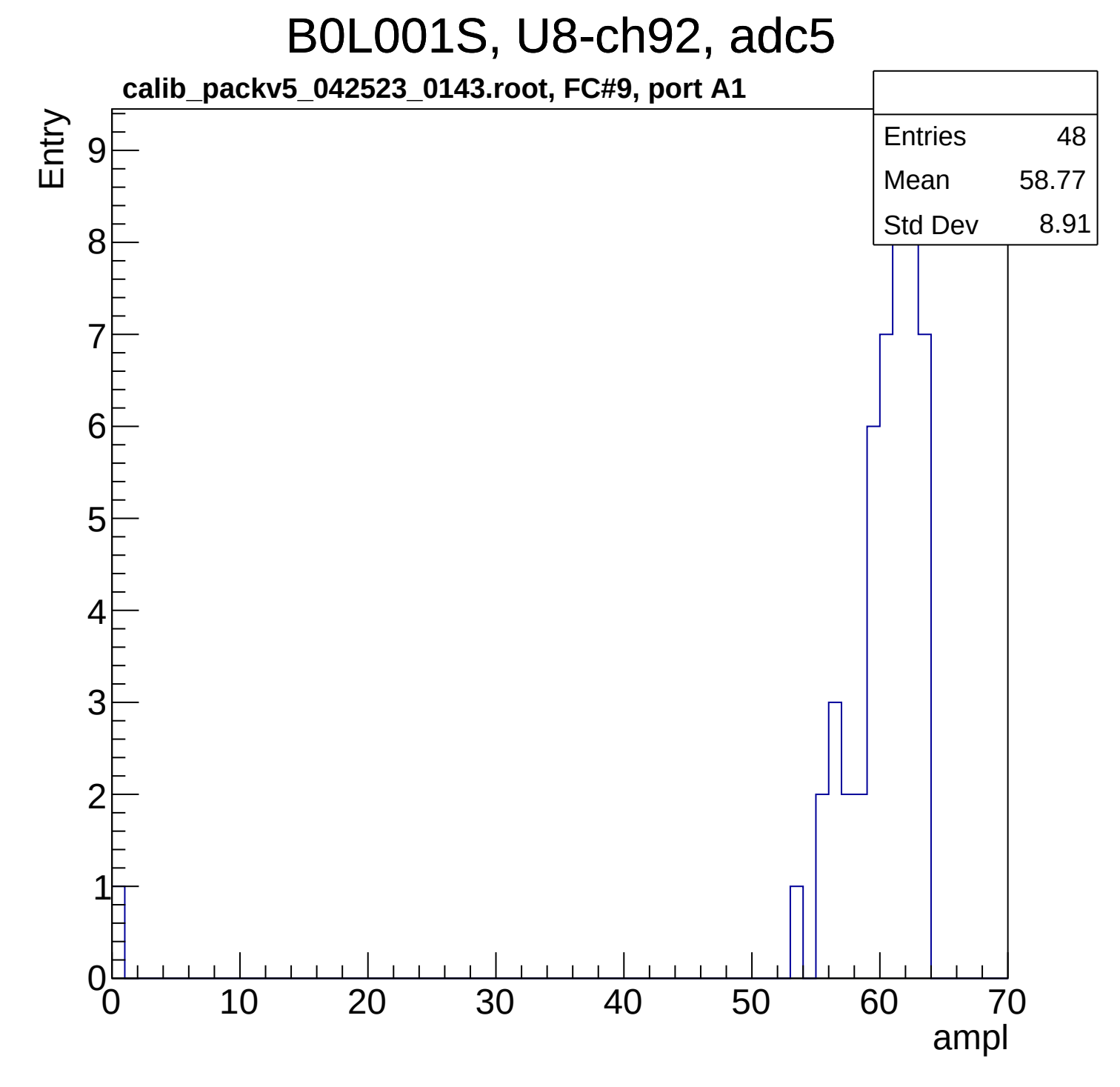
B0L001S, U8-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.77
Std Dev 8.91
Entry
ampl
0
1
2
3
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6
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8
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0
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60
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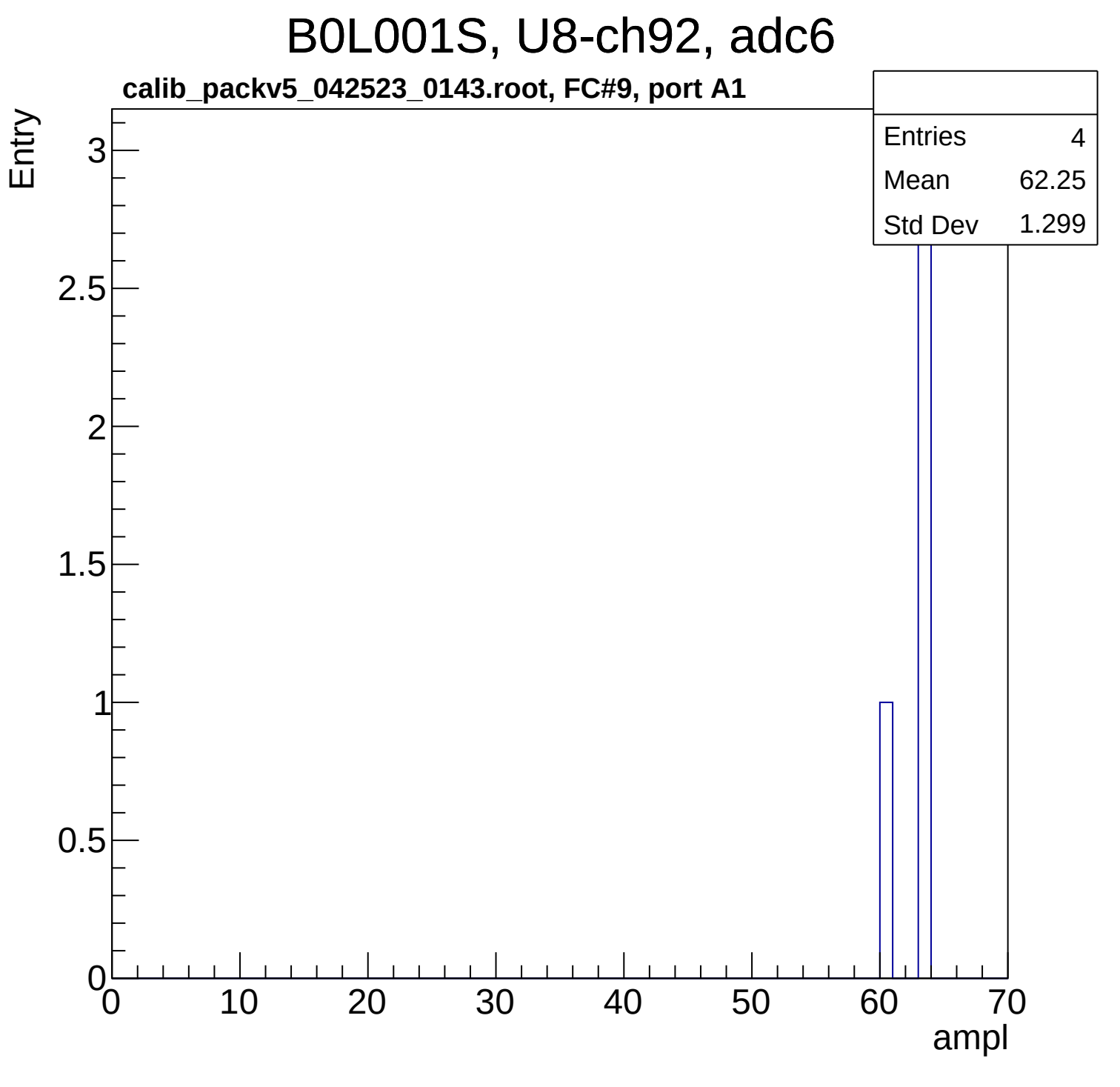
B0L001S, U8-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
0
0.5
1
1.5
2
2.5
3
0
10
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50
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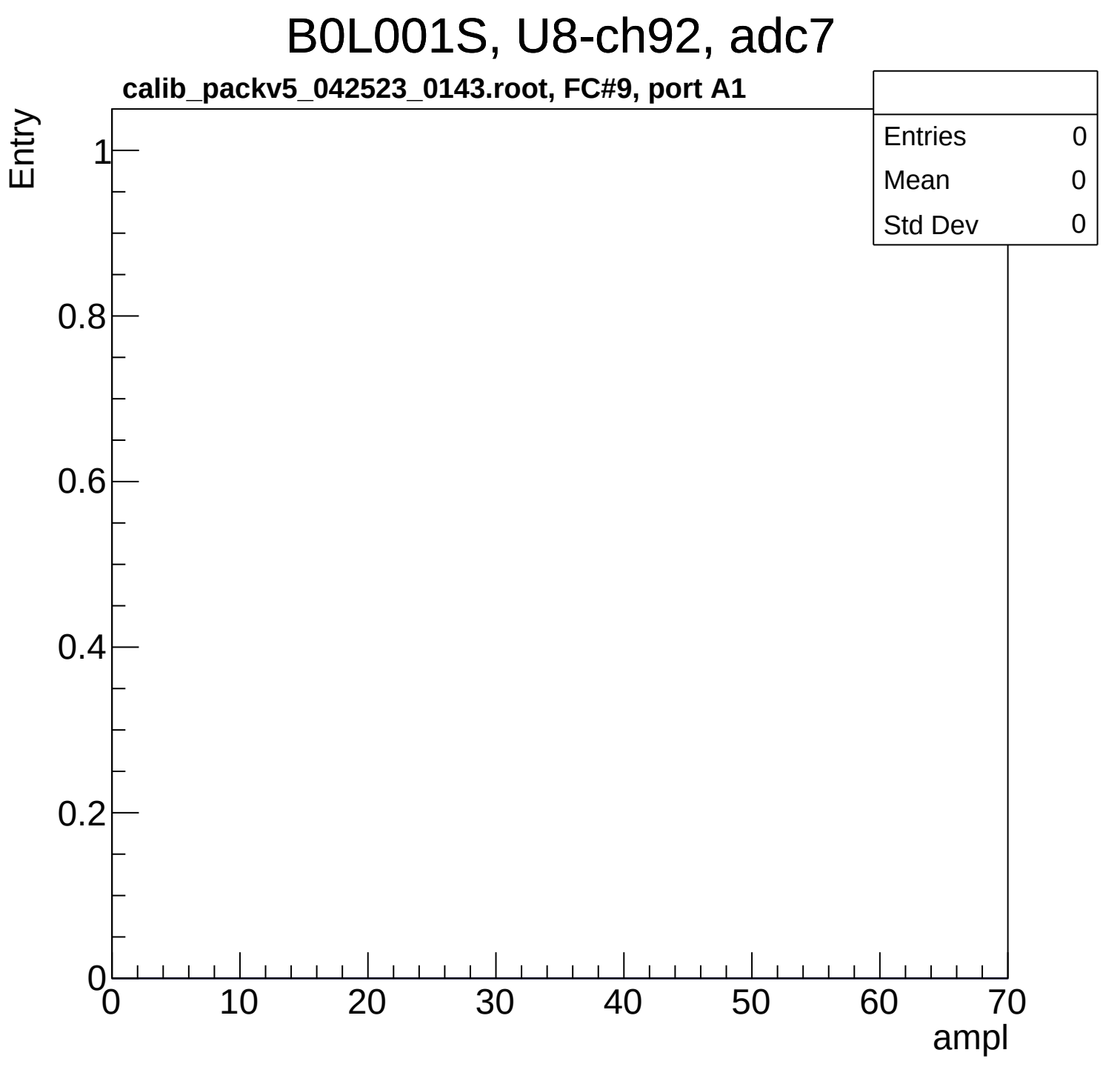
ampl


B0L001S, U8-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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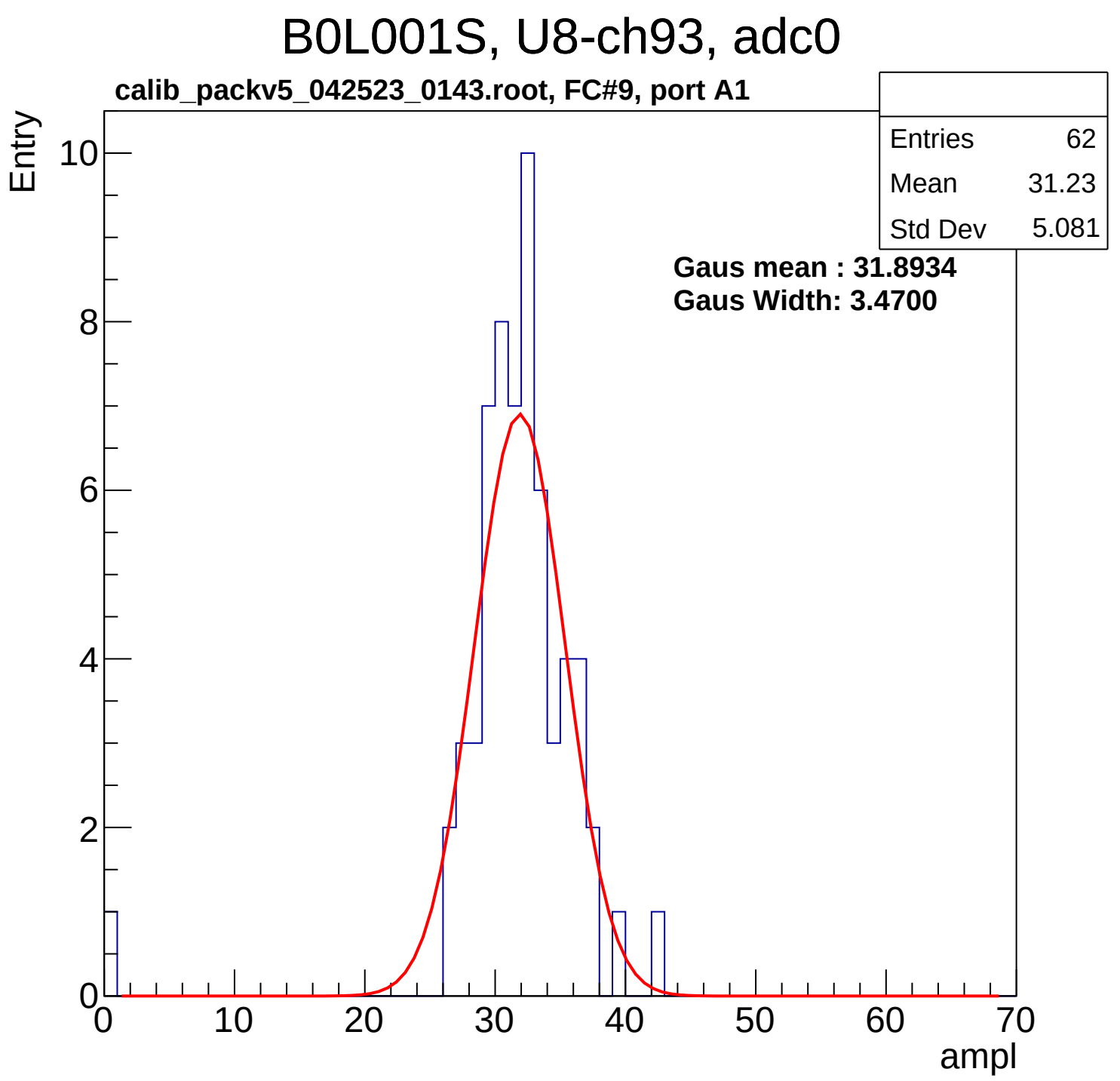
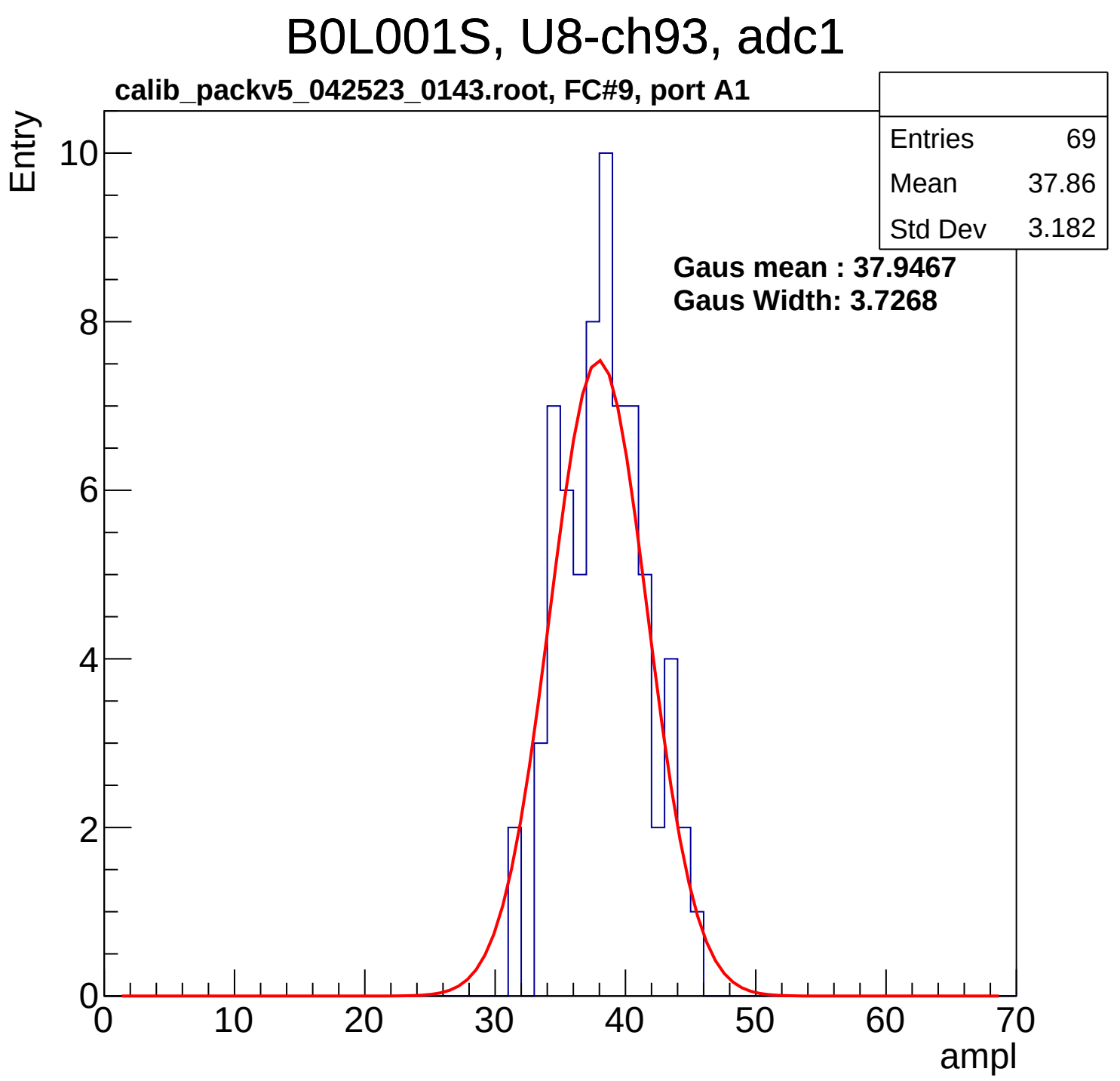
40
50
60
70
ampl


B0L001S, U8-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.23
Std Dev 5.081
Gaus mean : 31.8934
Gaus Width: 3.4700
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.86
Std Dev 3.182
Gaus mean : 37.9467
Gaus Width: 3.7268
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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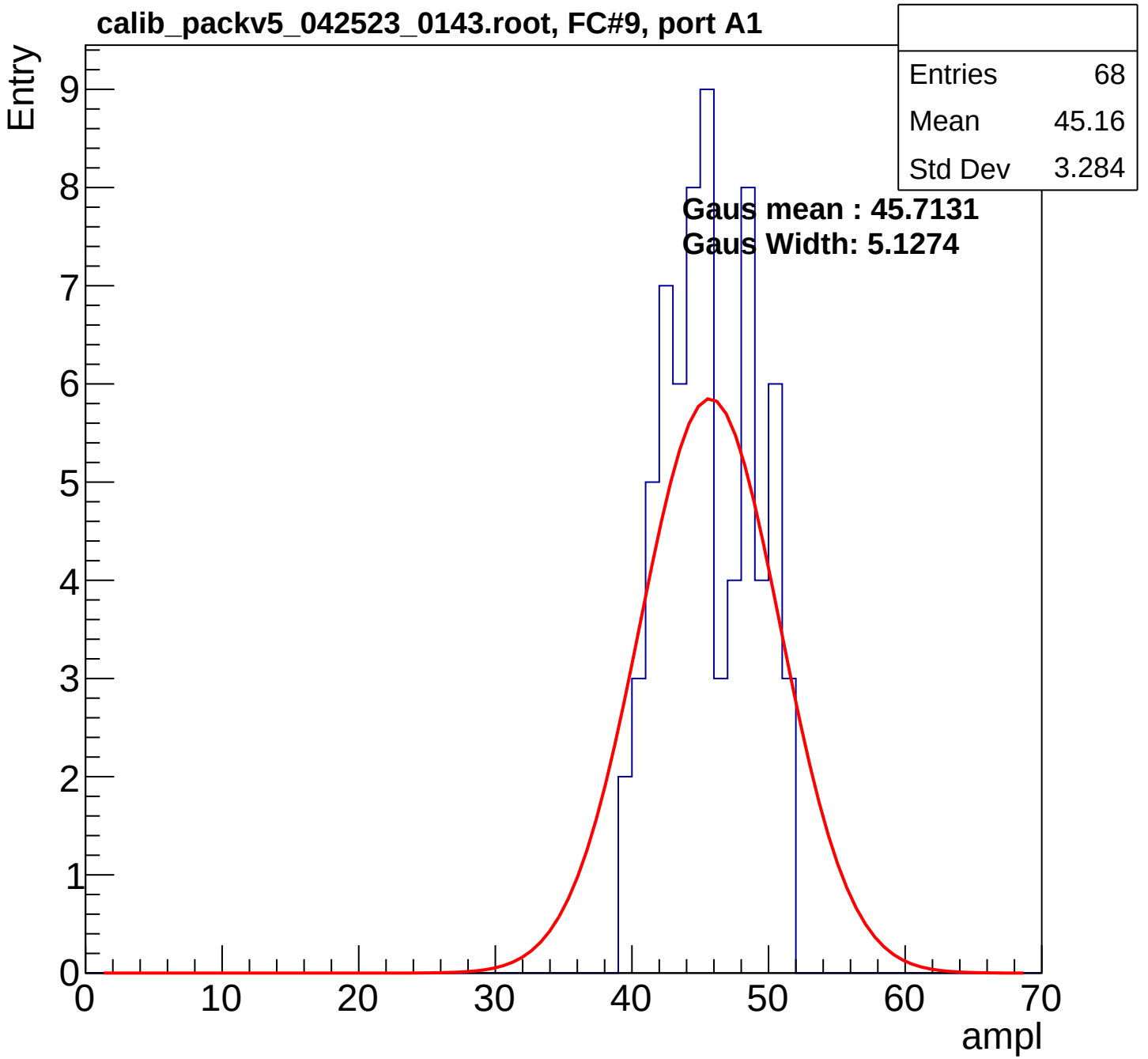
B0L001S, U8-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 45.16
Std Dev 3.284
Gaus mean : 45.7131
Gaus Width: 5.1274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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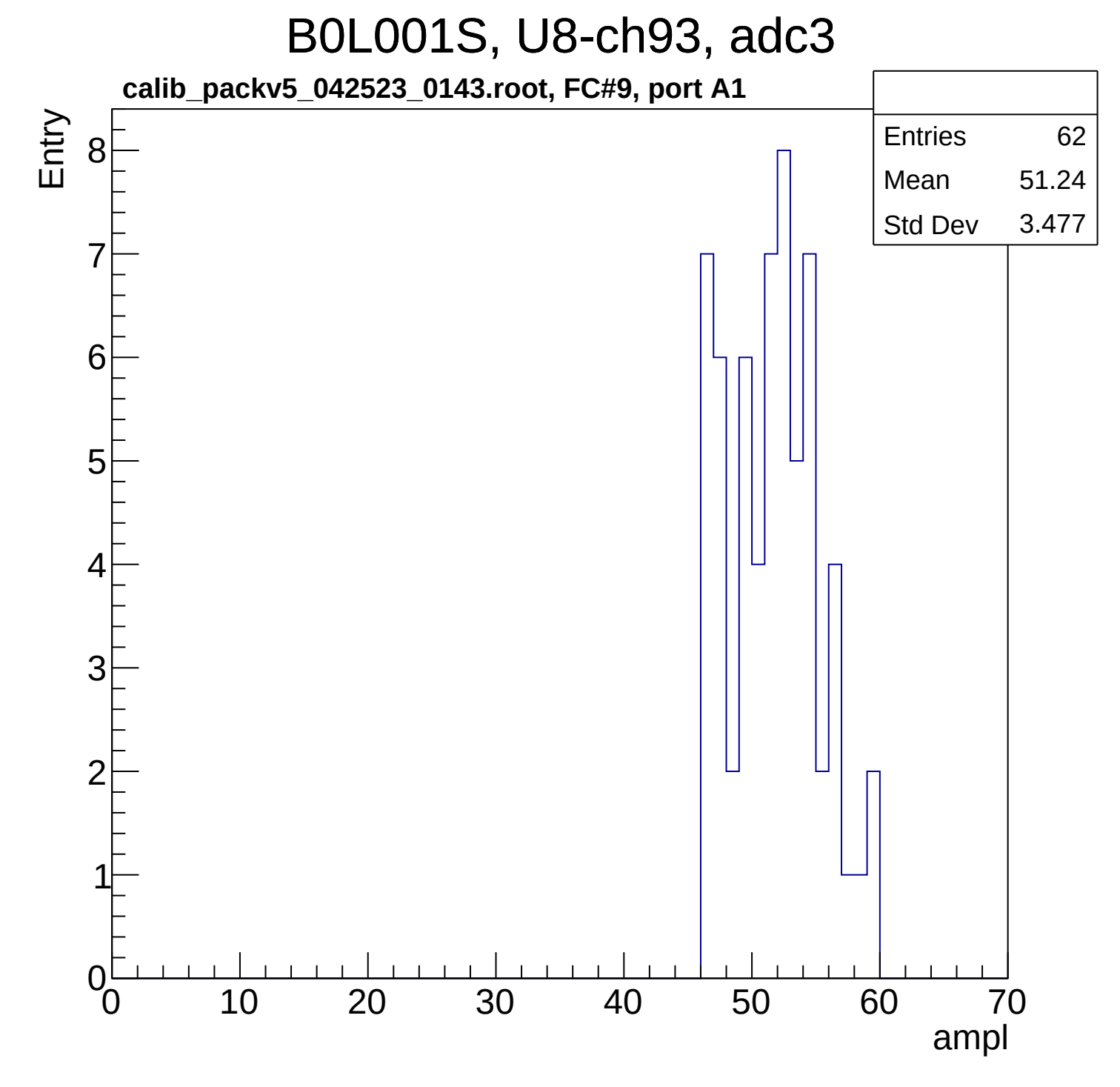
B0L001S, U8-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.24
Std Dev 3.477
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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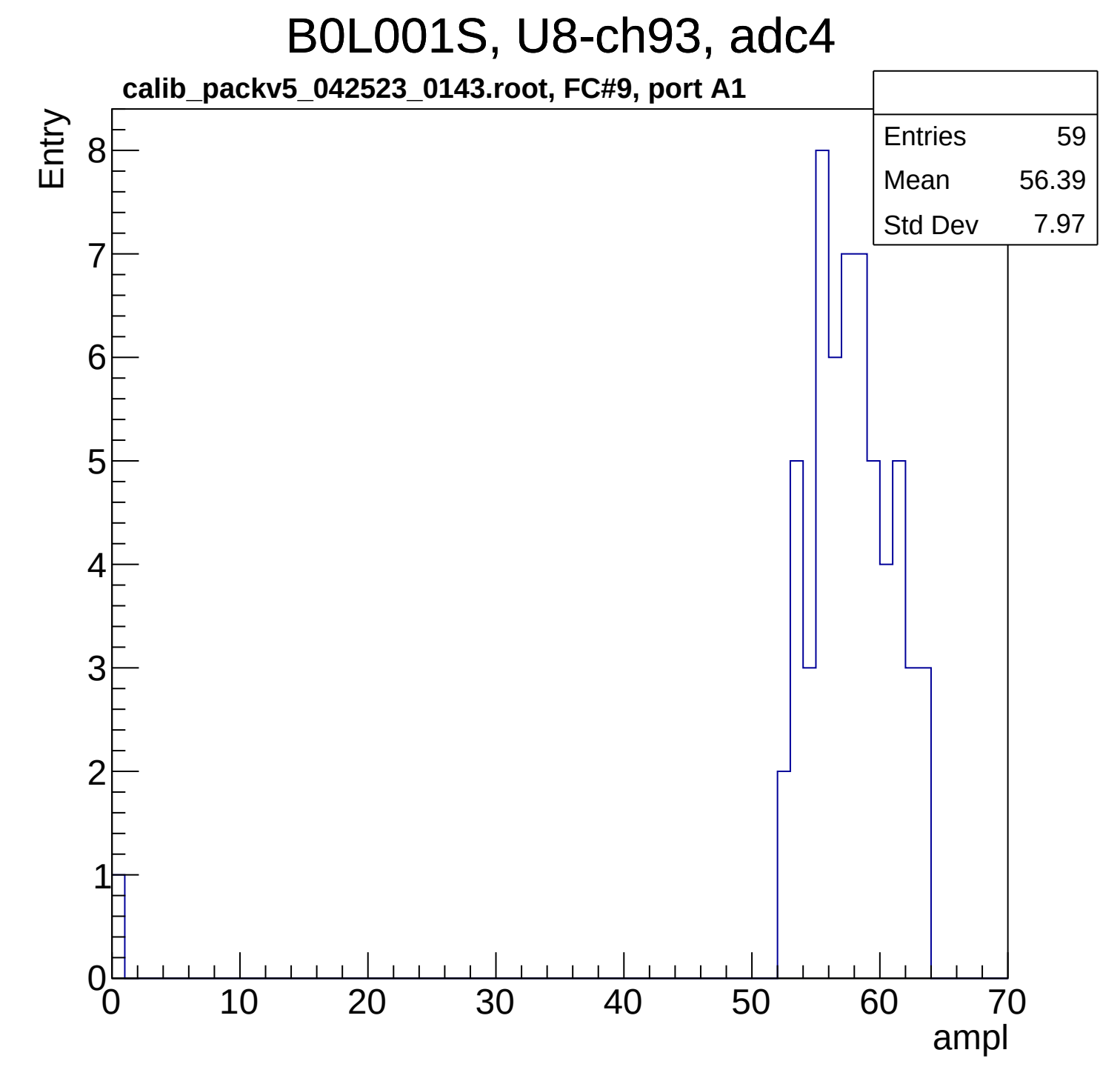
B0L001S, U8-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.39
Std Dev 7.97
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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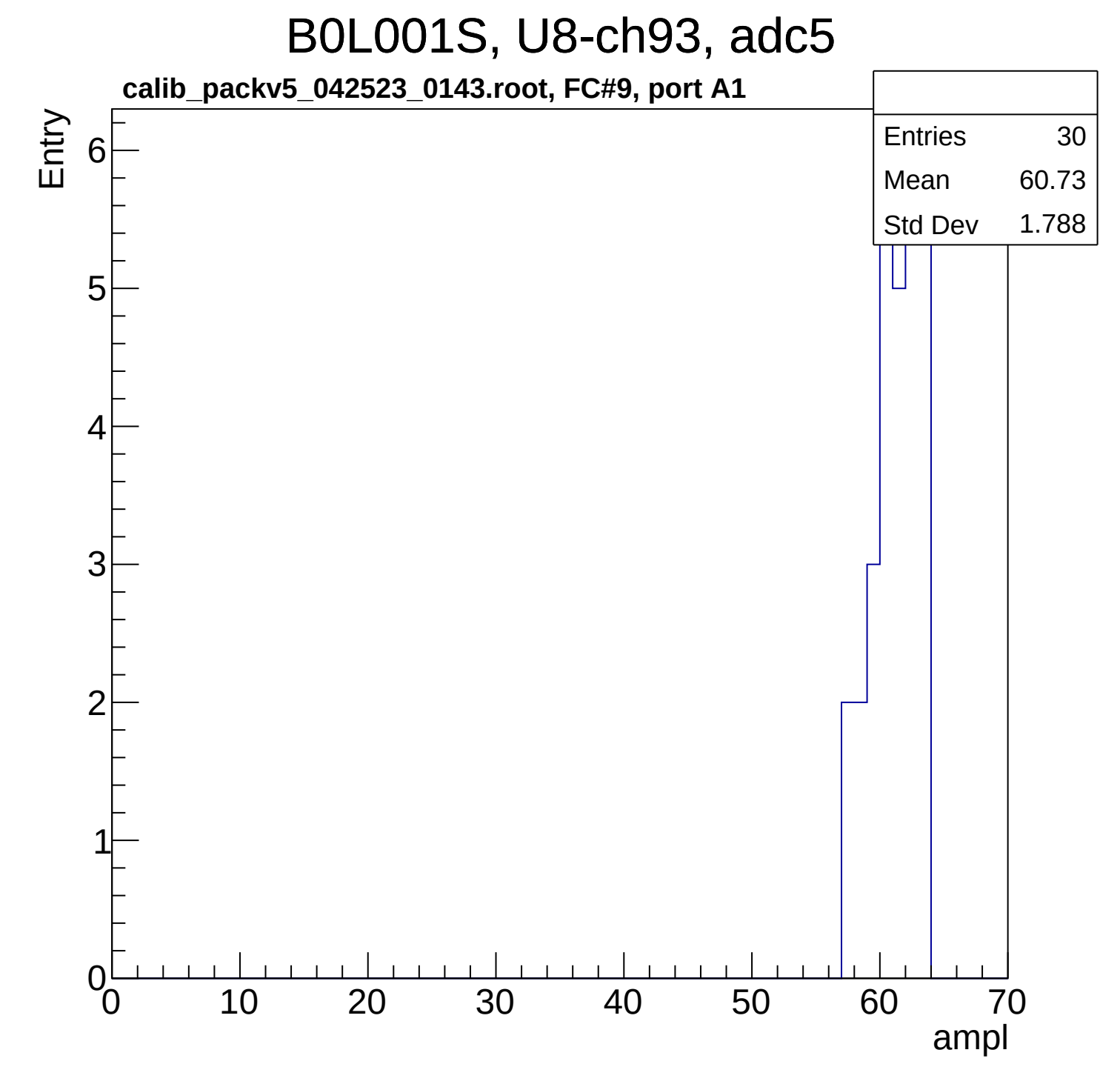
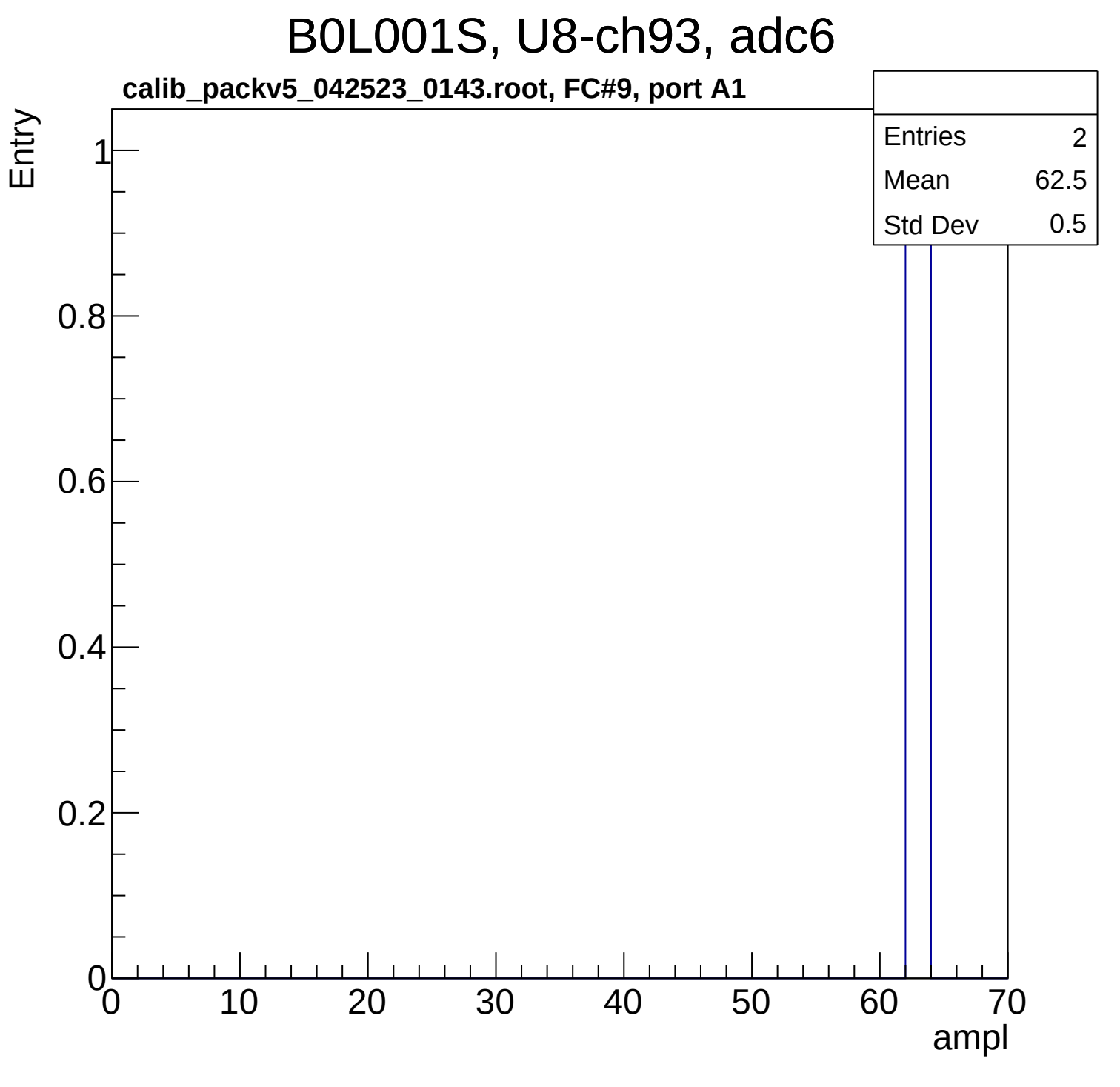
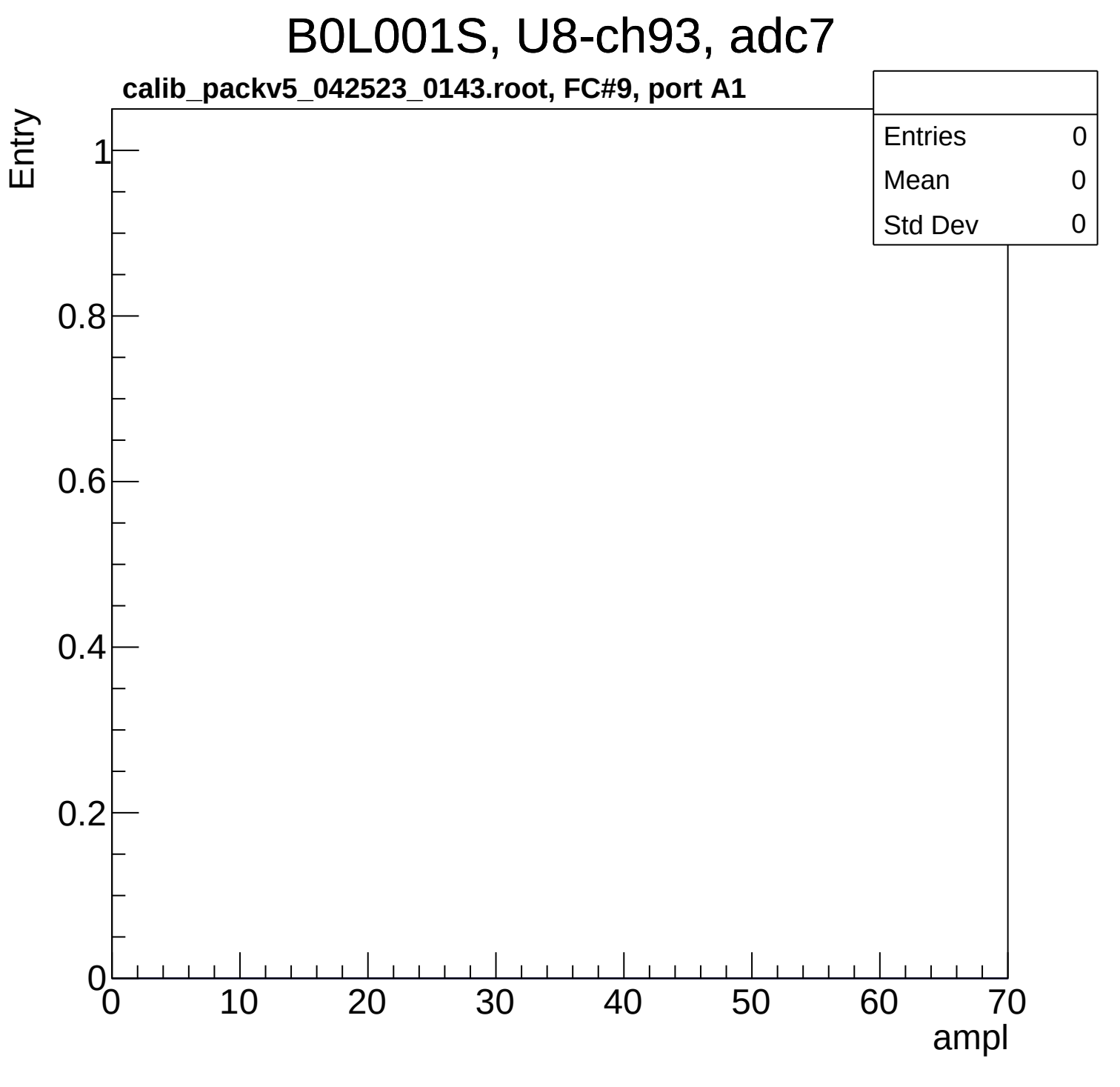
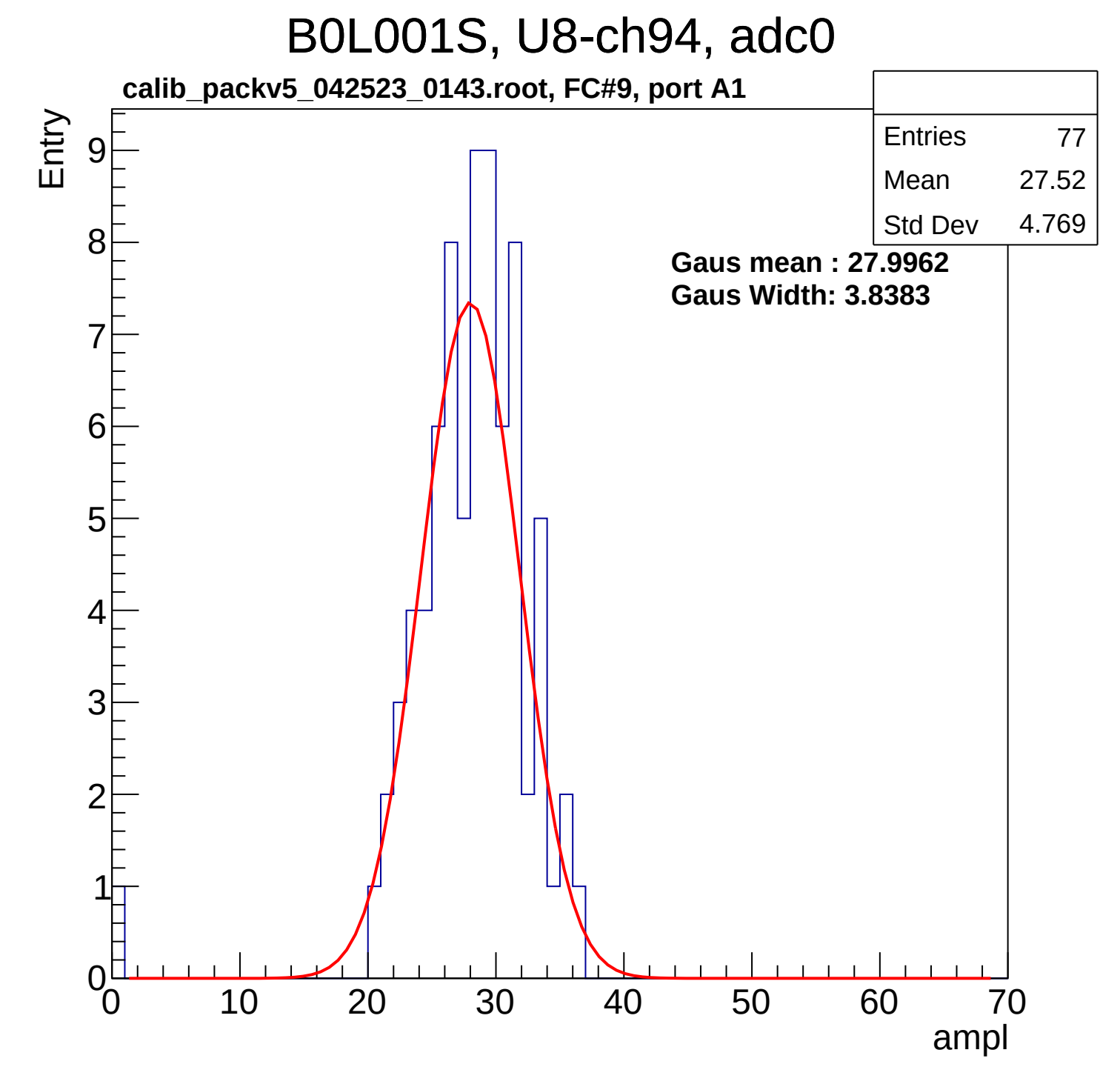

B0L001S, U8-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.73
Std Dev 1.788
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 27.52
Std Dev 4.769
Gaus mean : 27.9962
Gaus Width: 3.8383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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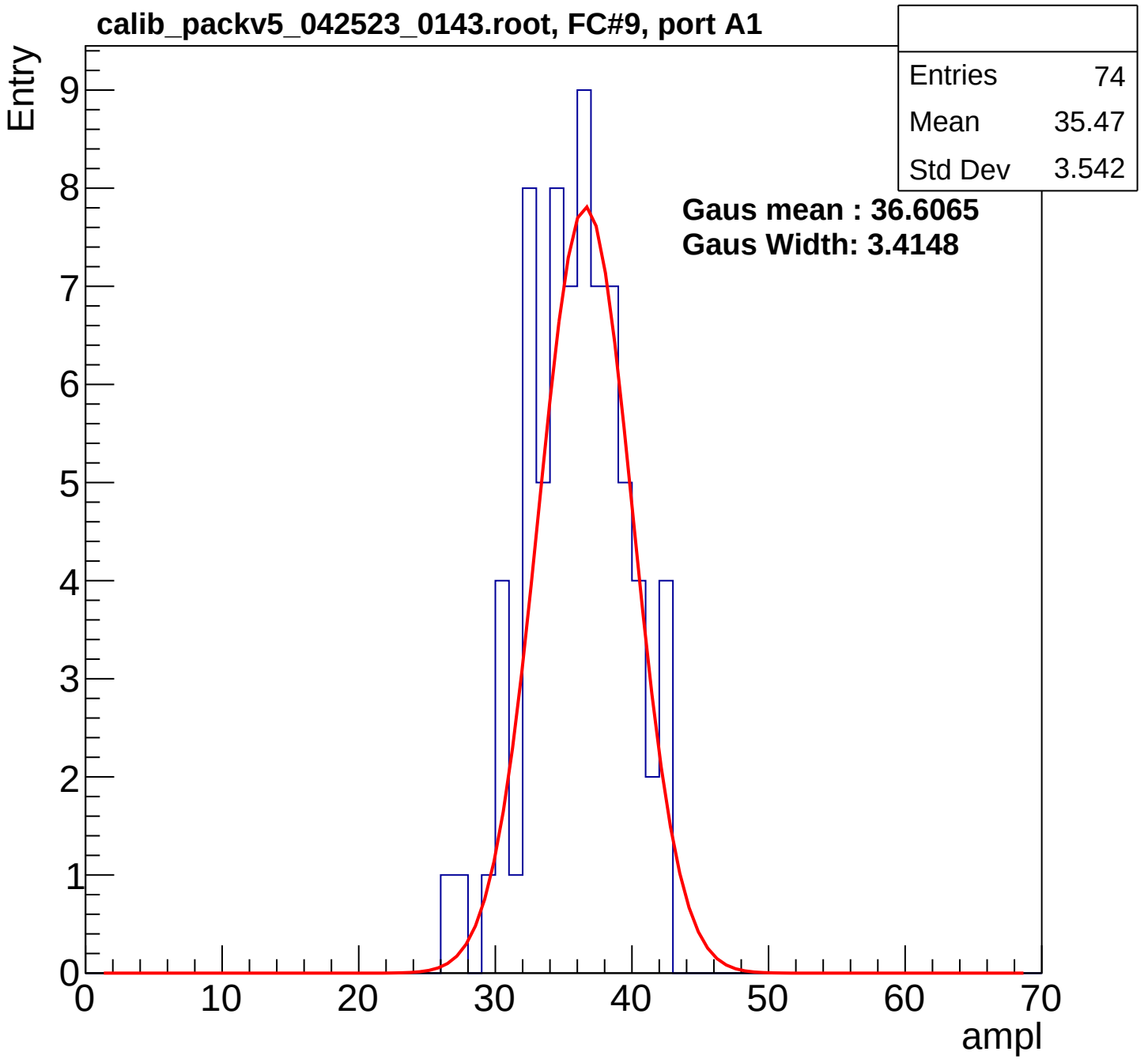
B0L001S, U8-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 35.47
Std Dev 3.542
Gaus mean : 36.6065
Gaus Width: 3.4148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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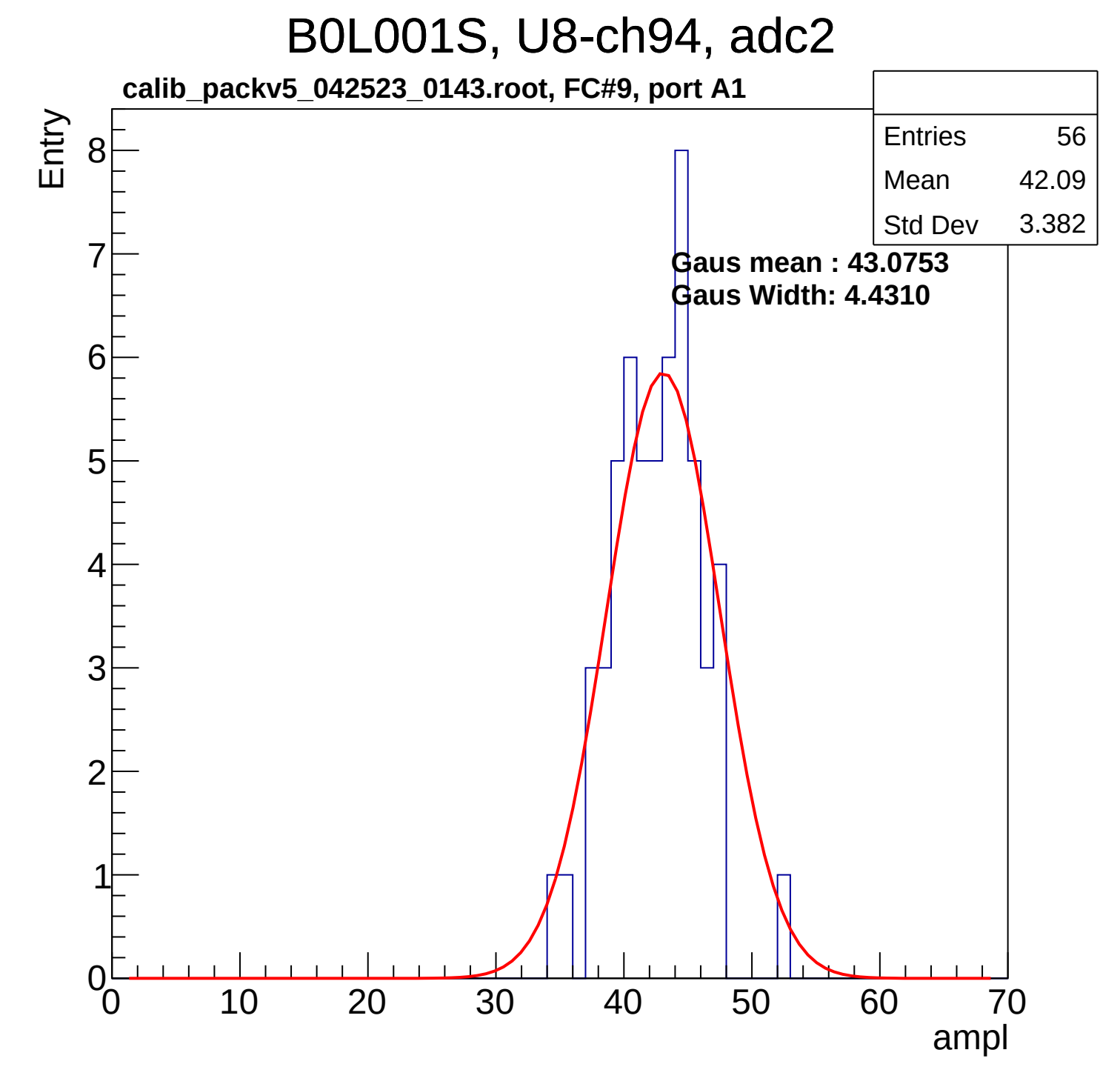
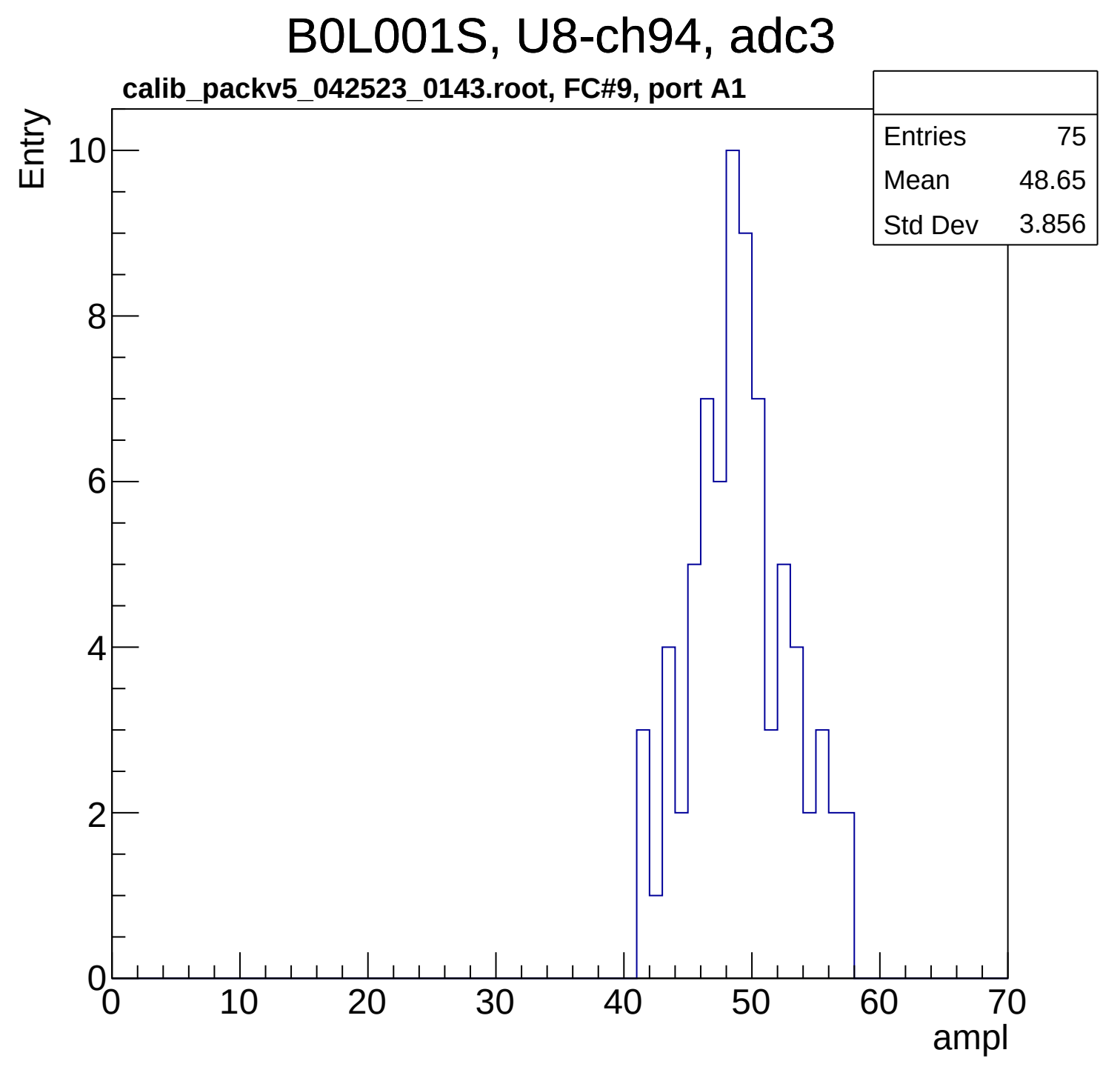
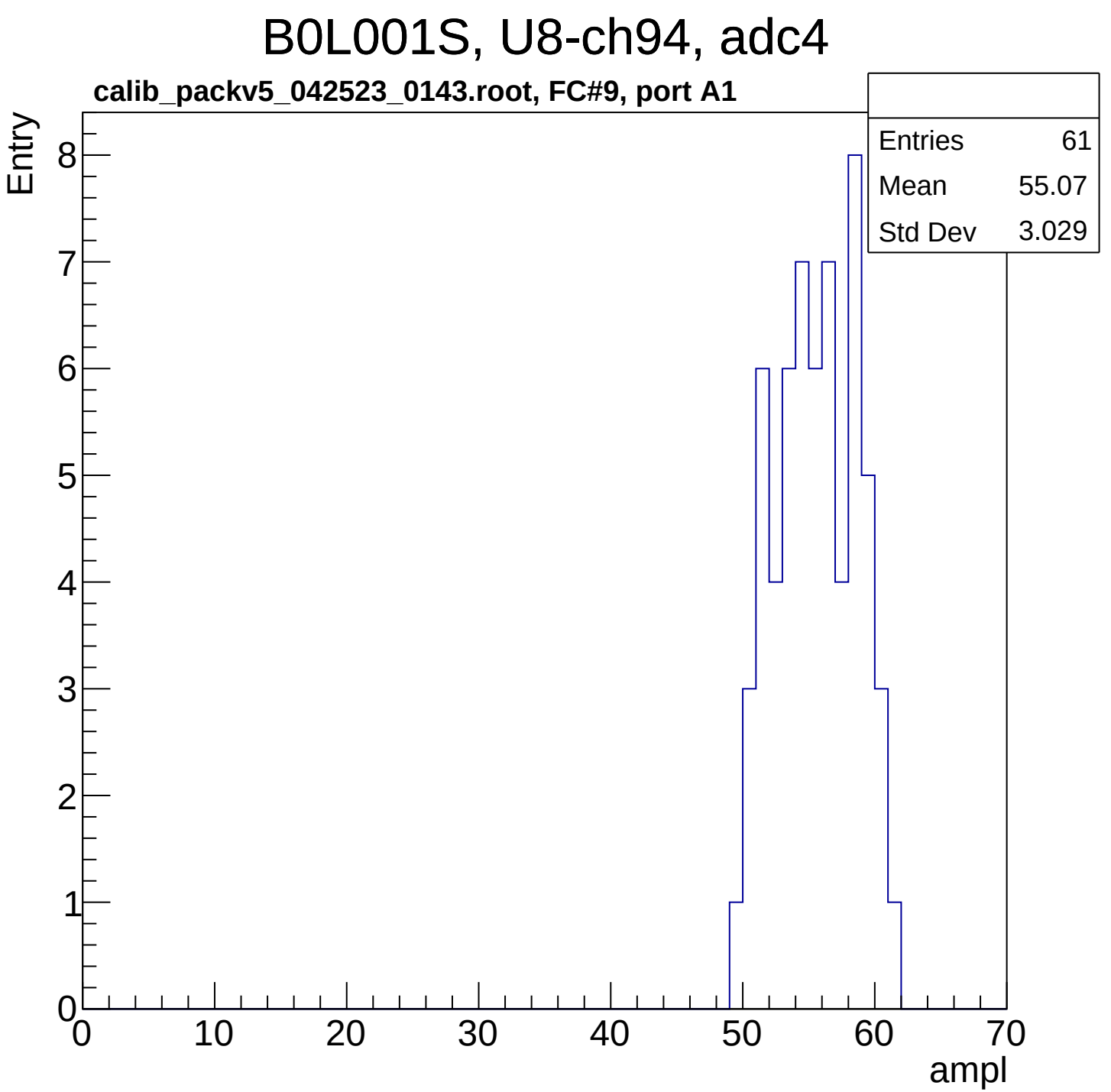
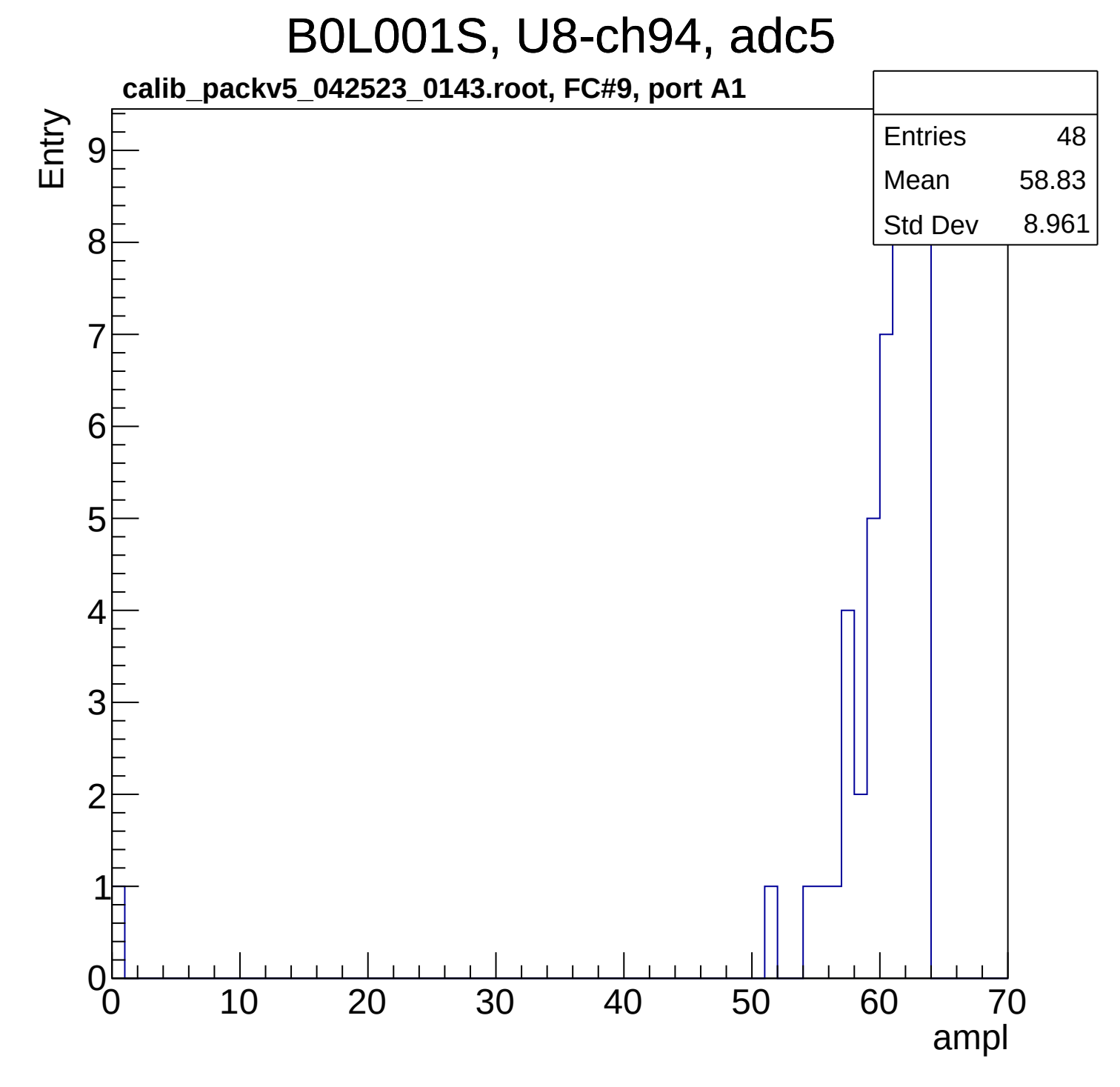

B0L001S, U8-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.09
Std Dev 3.382
Gaus mean : 43.0753
Gaus Width: 4.4310
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 48.65
Std Dev 3.856
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 55.07
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.83
Std Dev 8.961
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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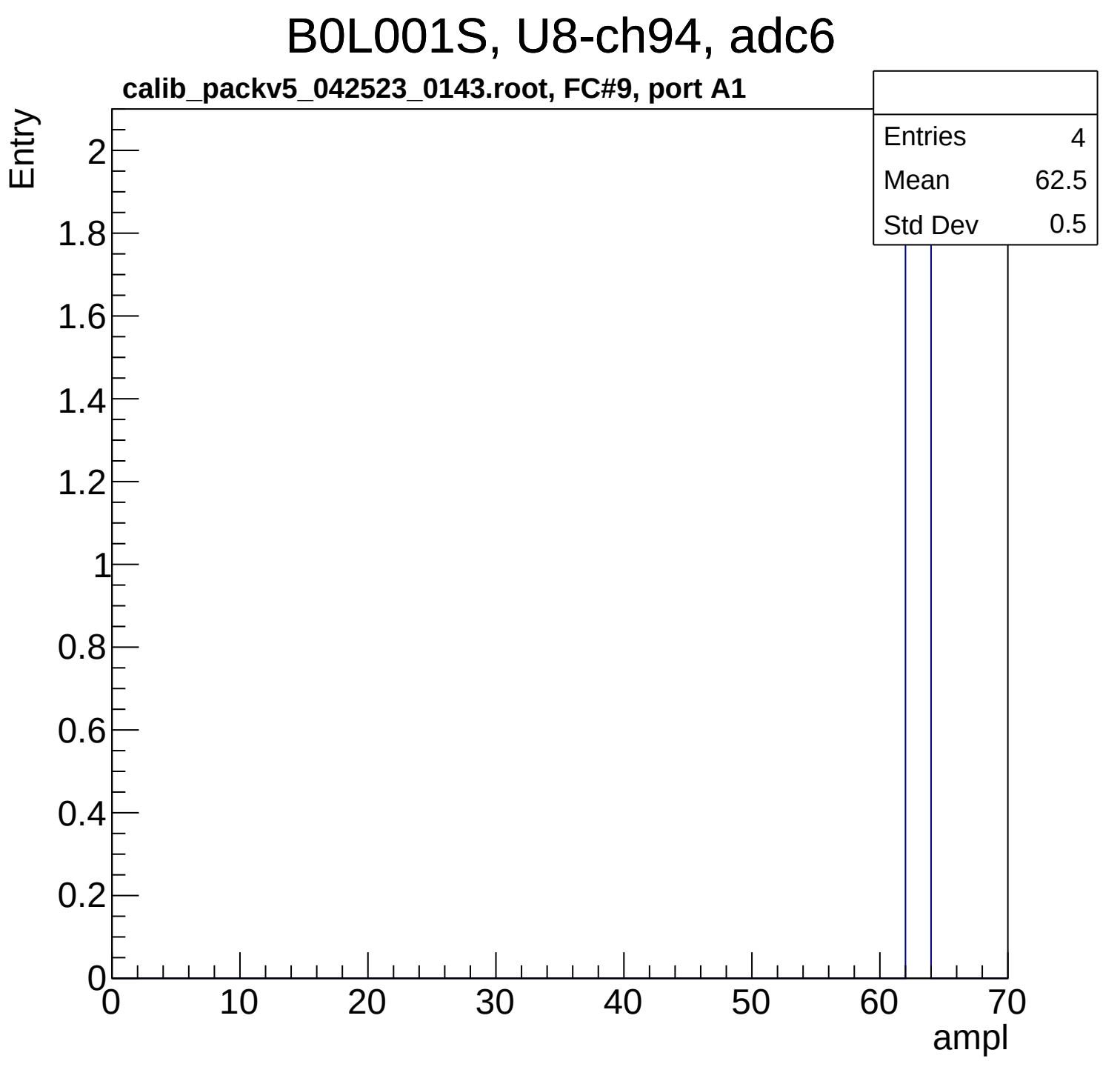
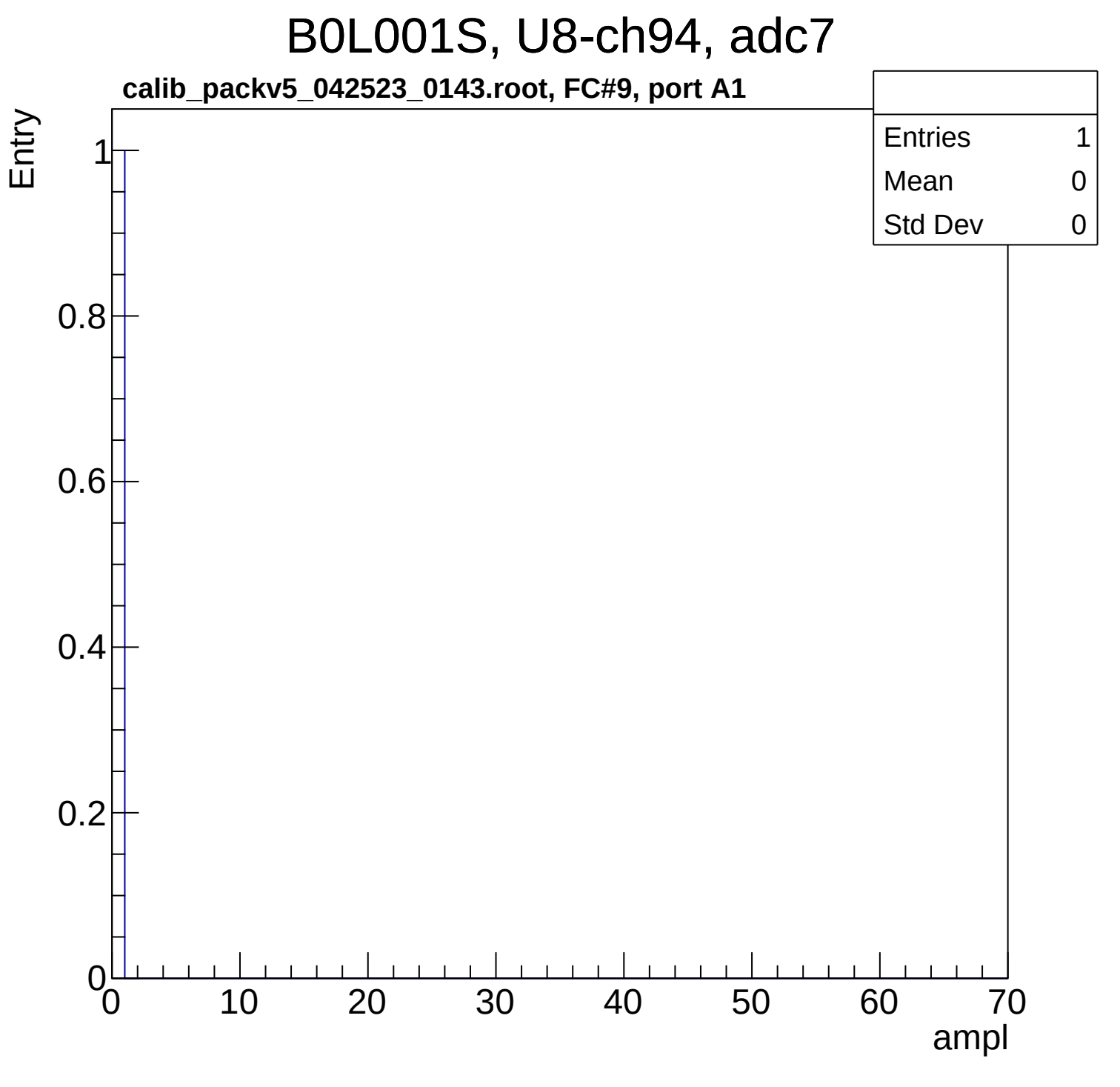
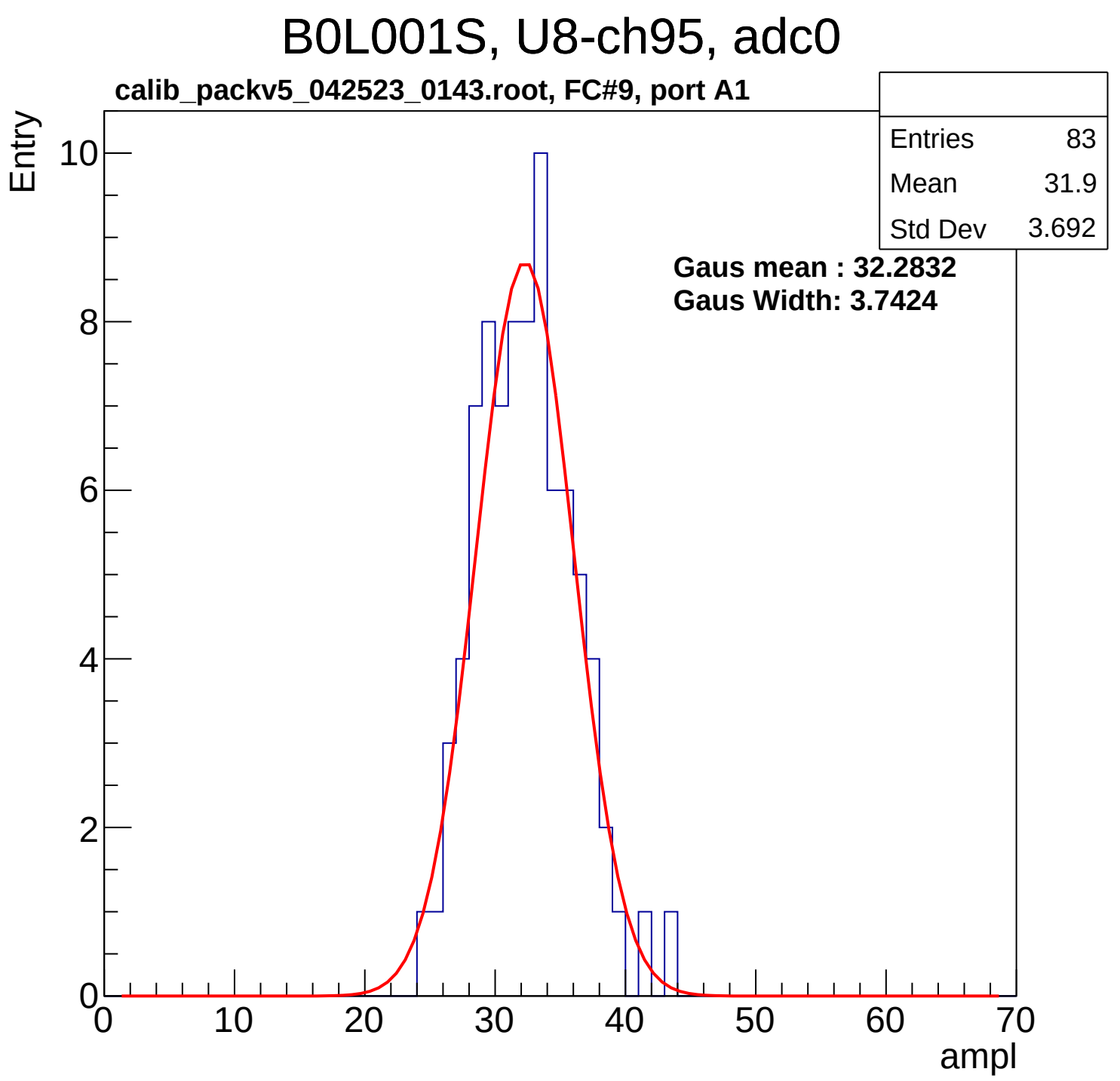
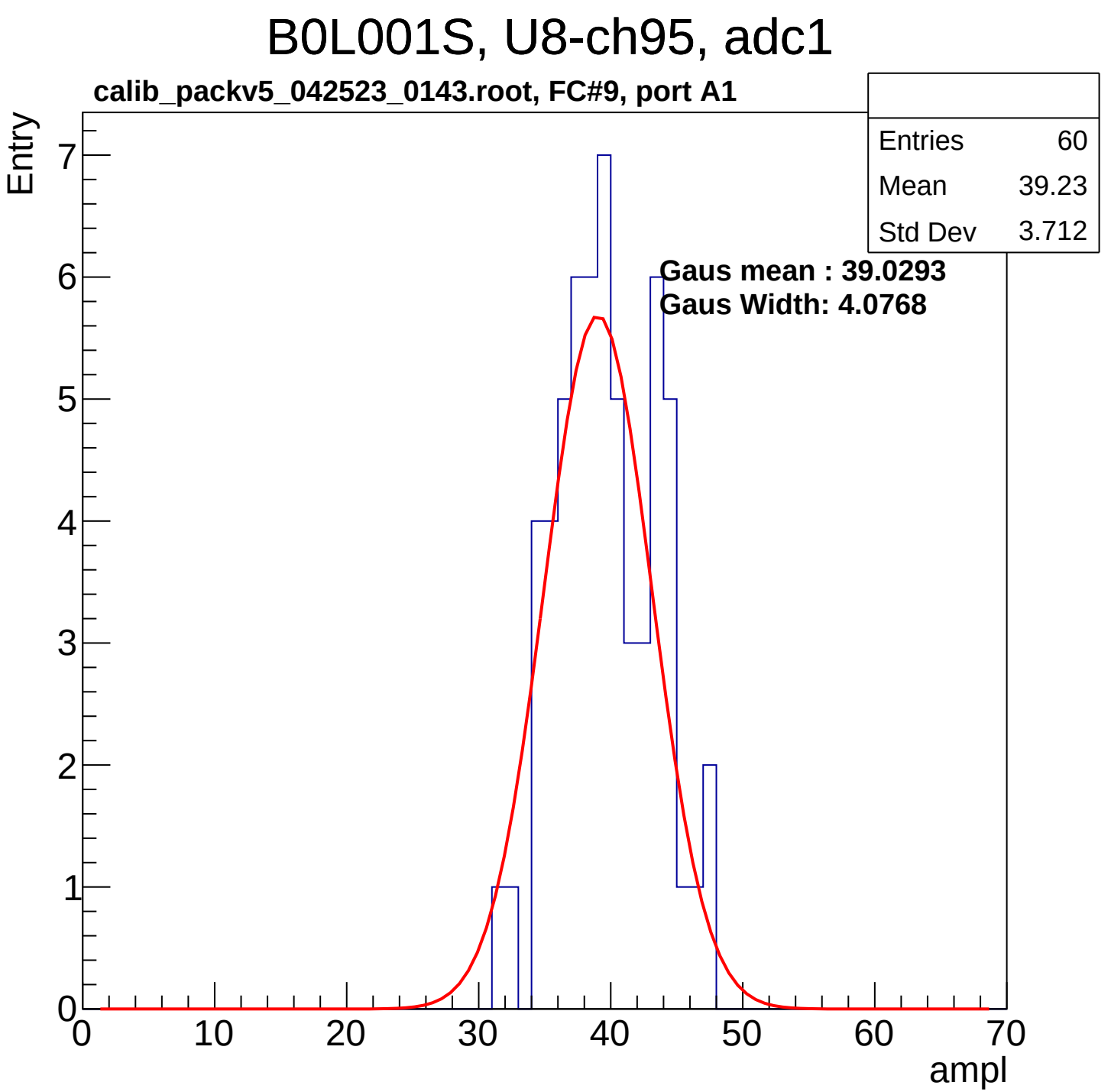
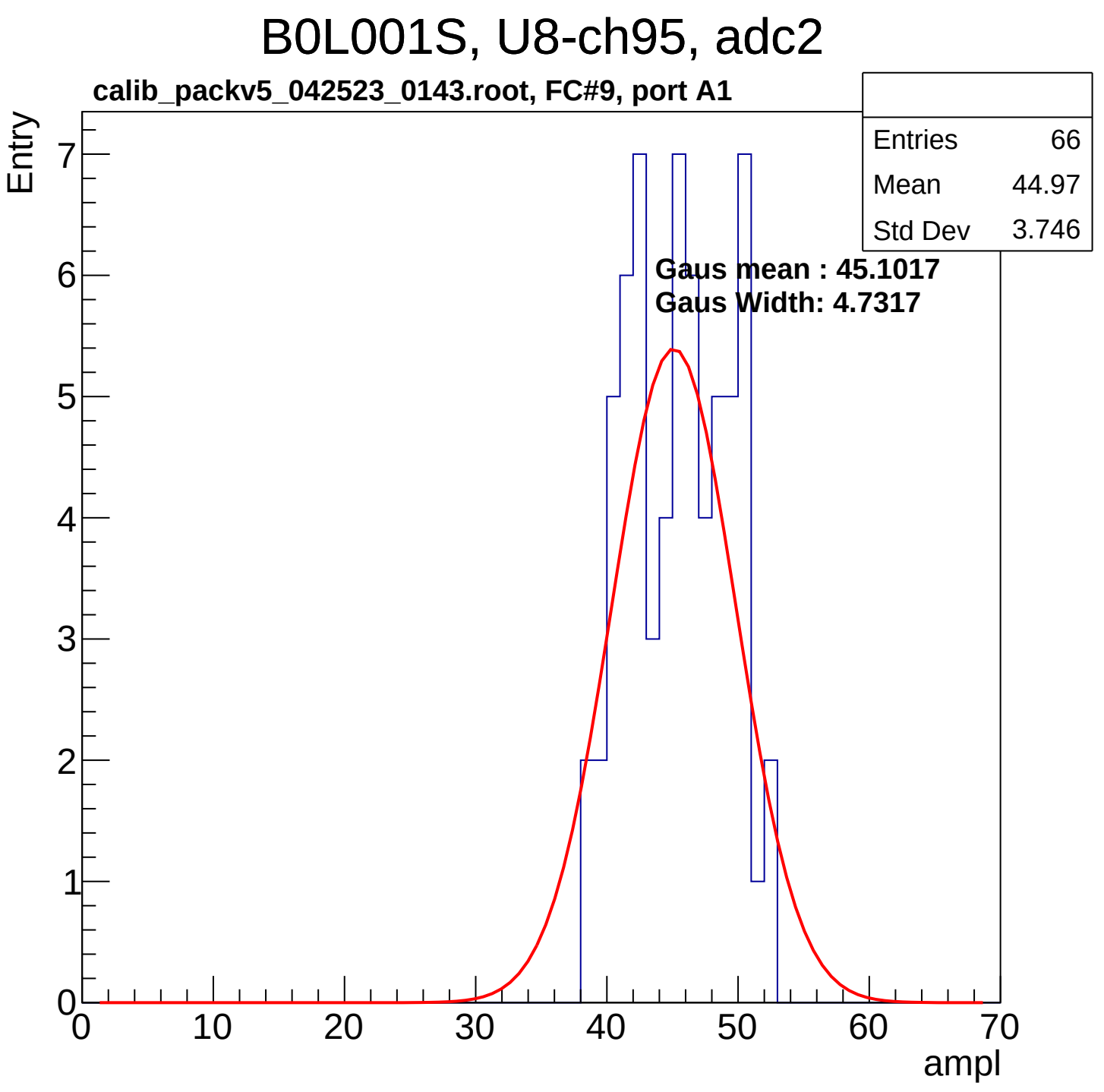

B0L001S, U8-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 31.9
Std Dev 3.692
Gaus mean : 32.2832
Gaus Width: 3.7424
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 39.23
Std Dev 3.712
Gaus mean : 39.0293
Gaus Width: 4.0768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.97
Std Dev 3.746
Gaus mean : 45.1017
Gaus Width: 4.7317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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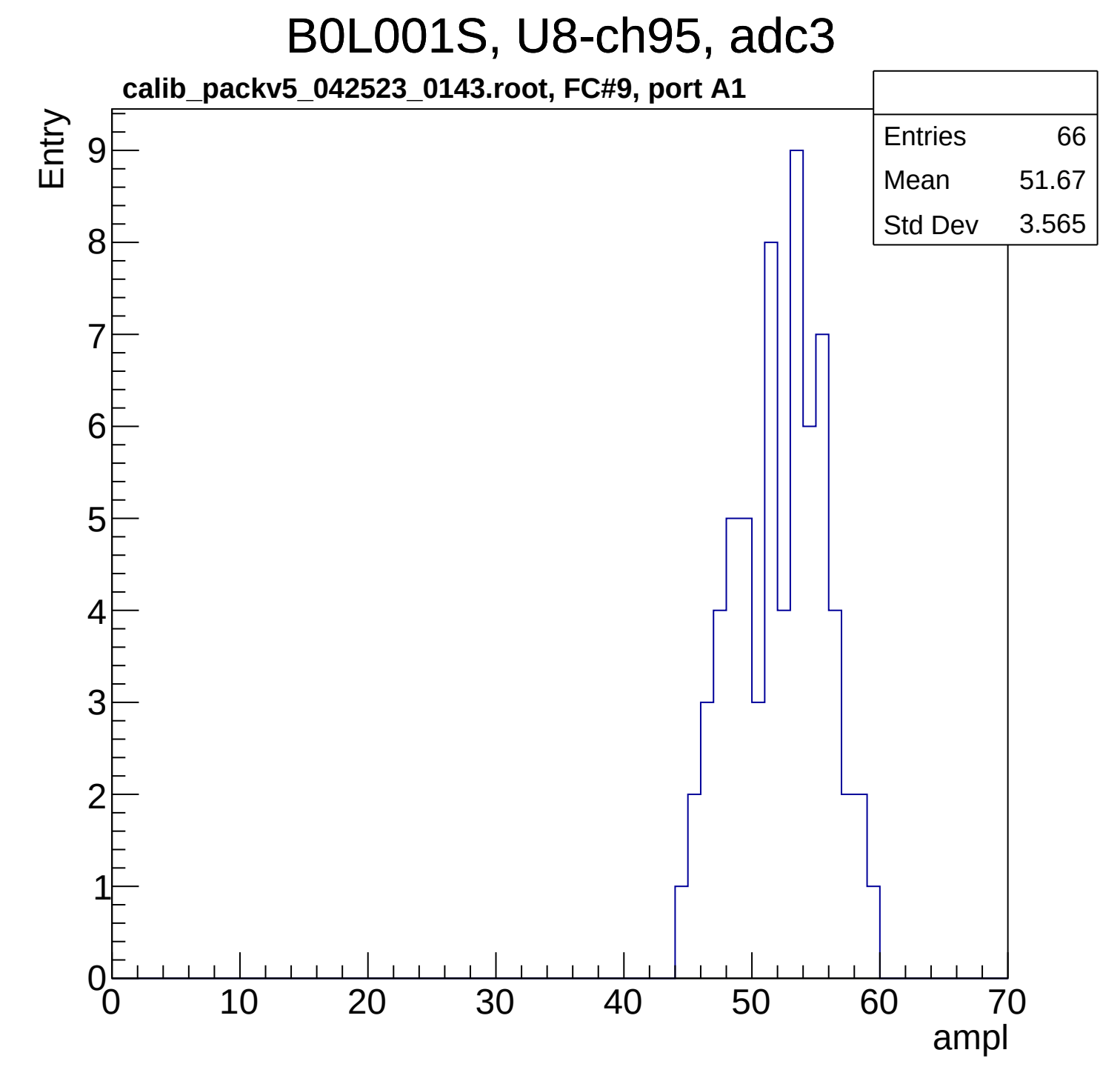
B0L001S, U8-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.67
Std Dev 3.565
Entry
ampl
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3
4
5
6
7
8
9
0
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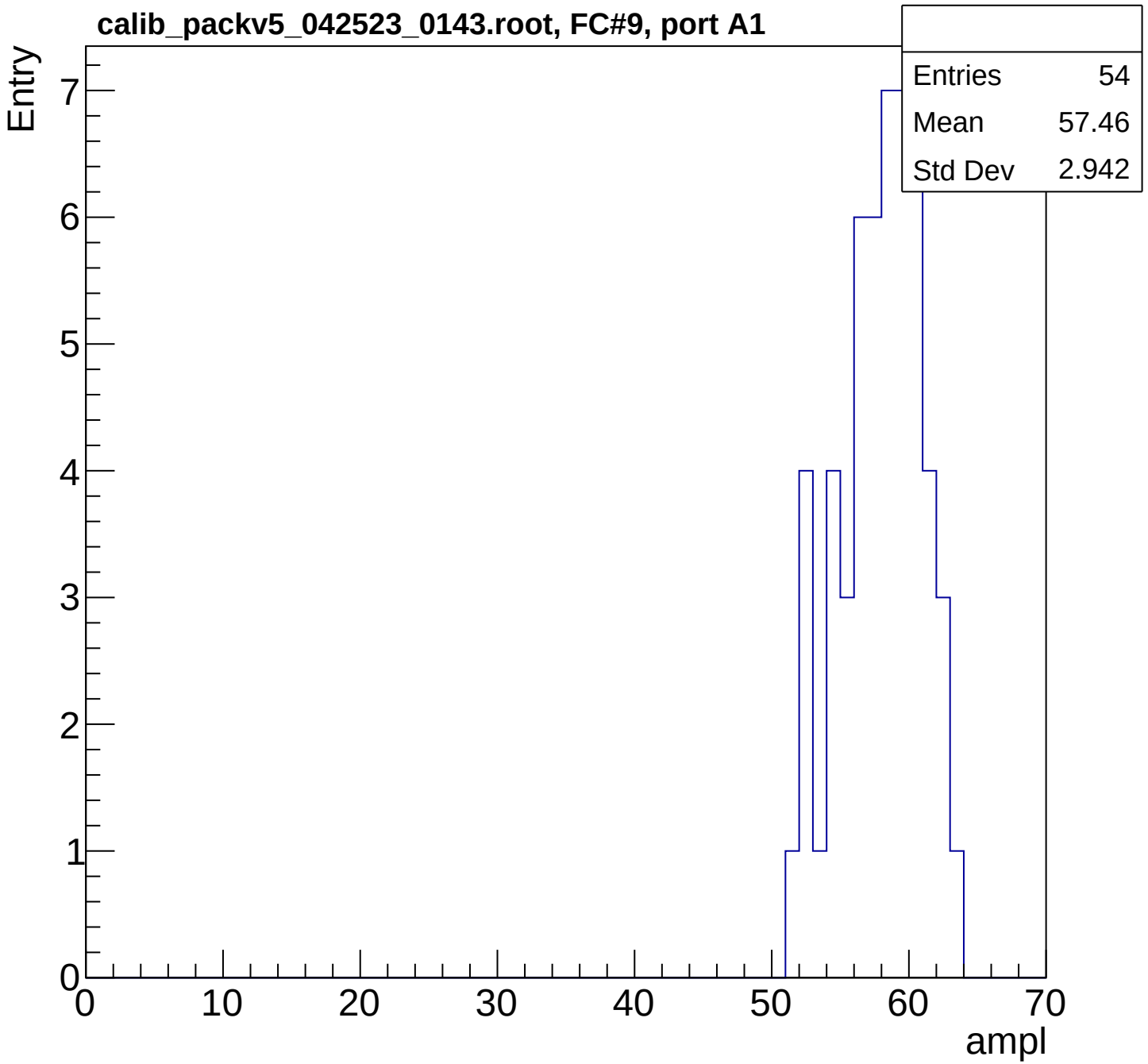
60
70


B0L001S, U8-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.46
Std Dev 2.942
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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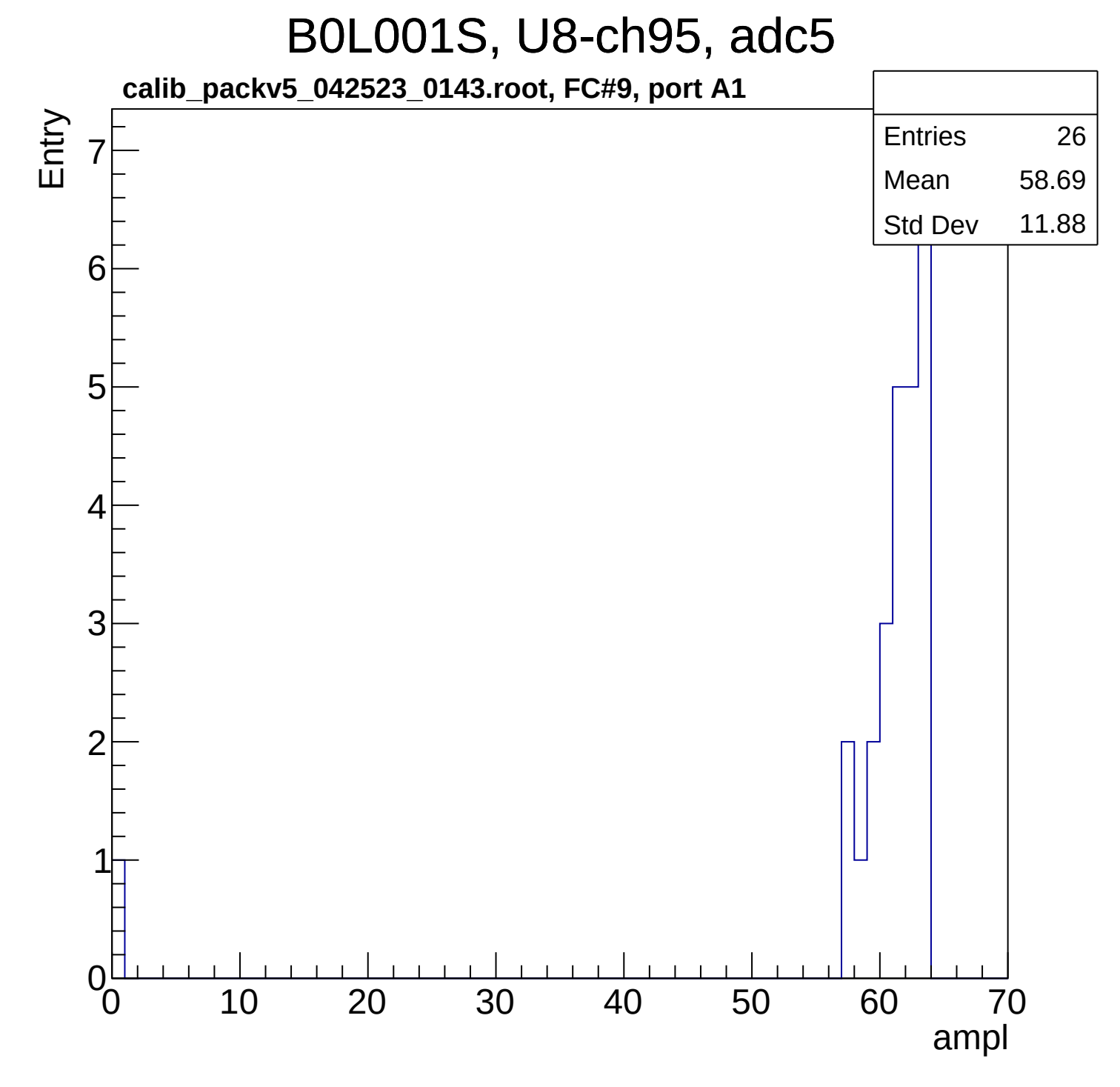
40
50
60
70


B0L001S, U8-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 58.69
Std Dev 11.88
Entry
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1
2
3
4
5
6
7
0
10
20
30
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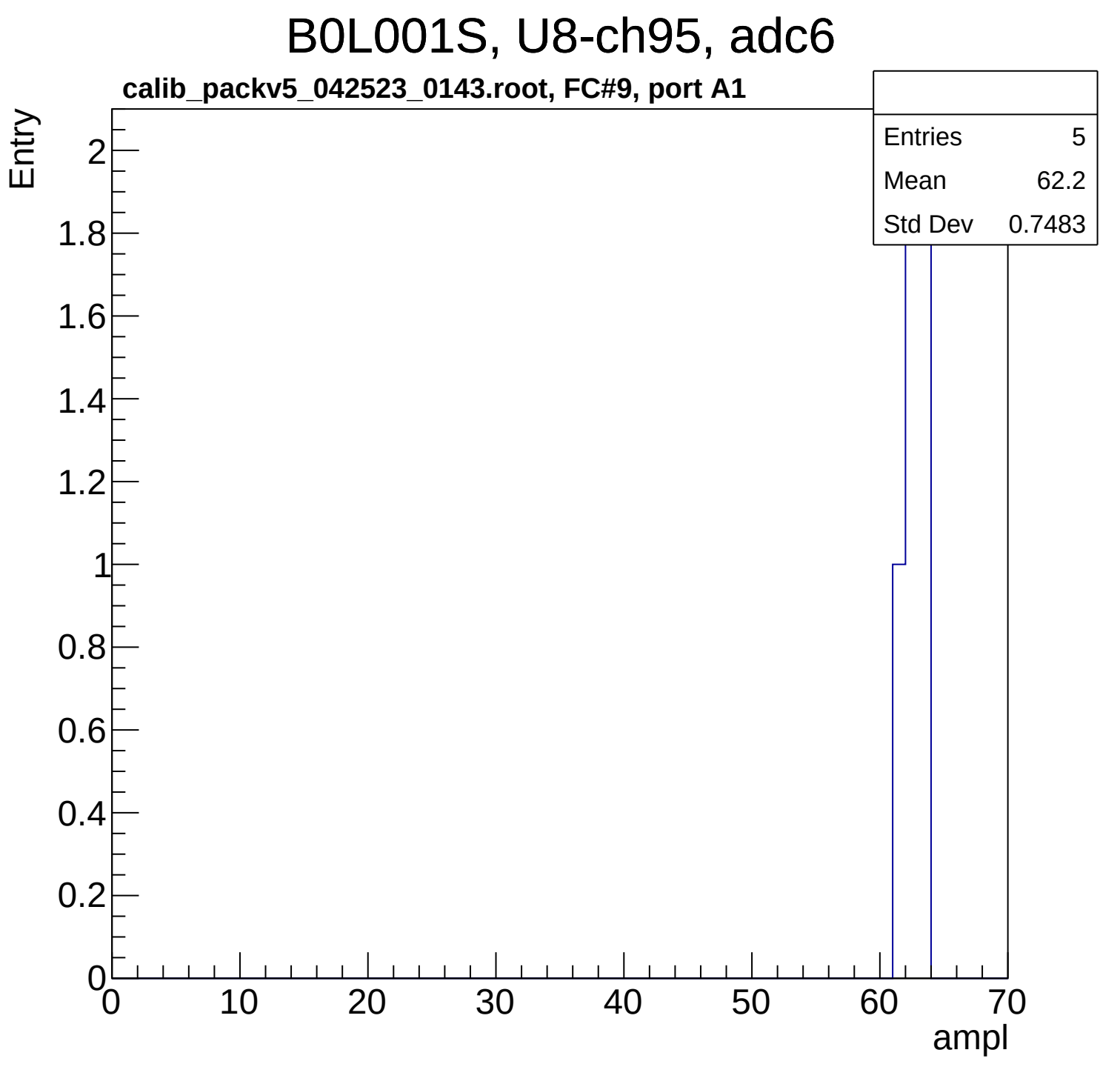
50
60
70
ampl


B0L001S, U8-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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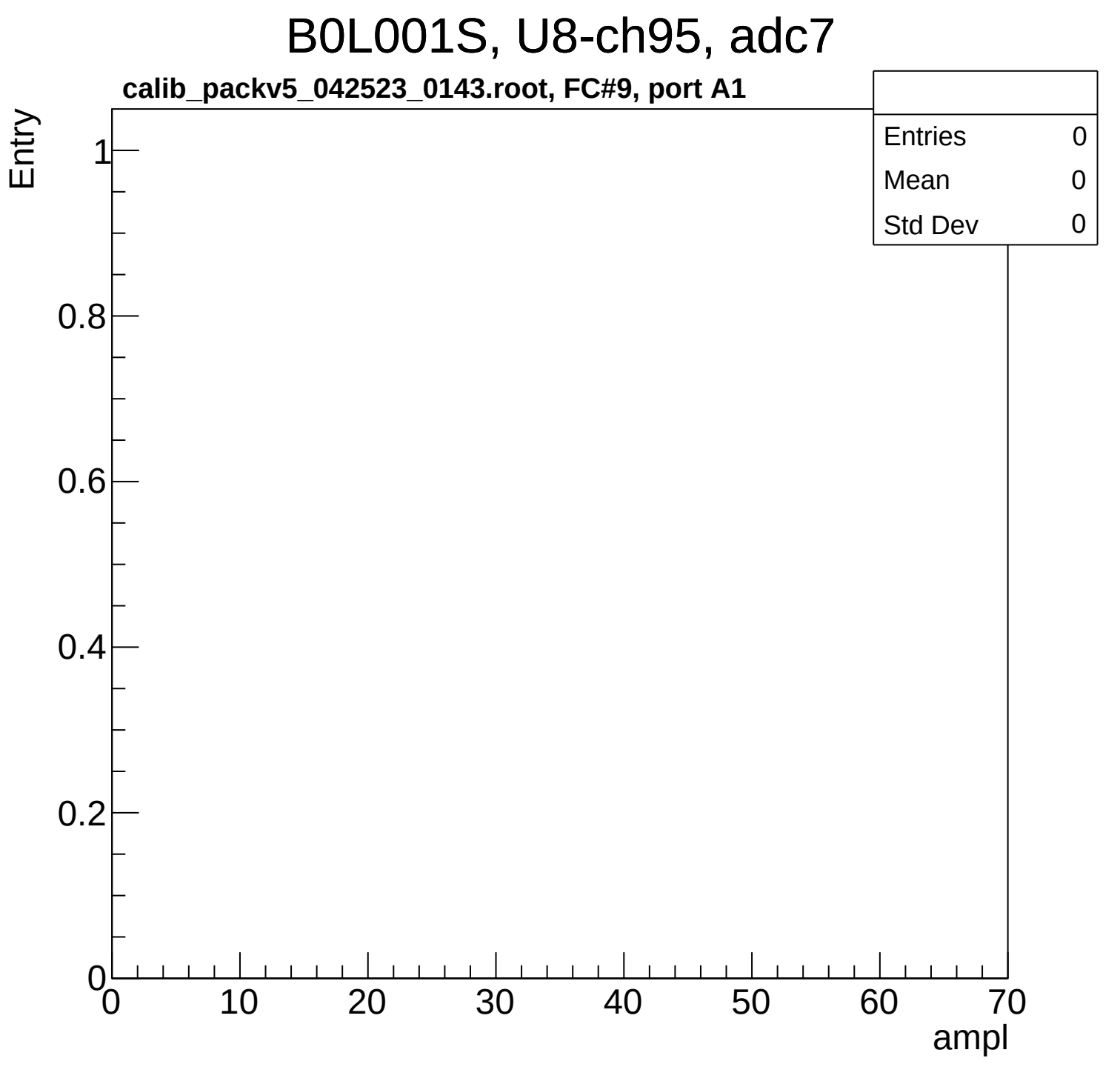
B0L001S, U8-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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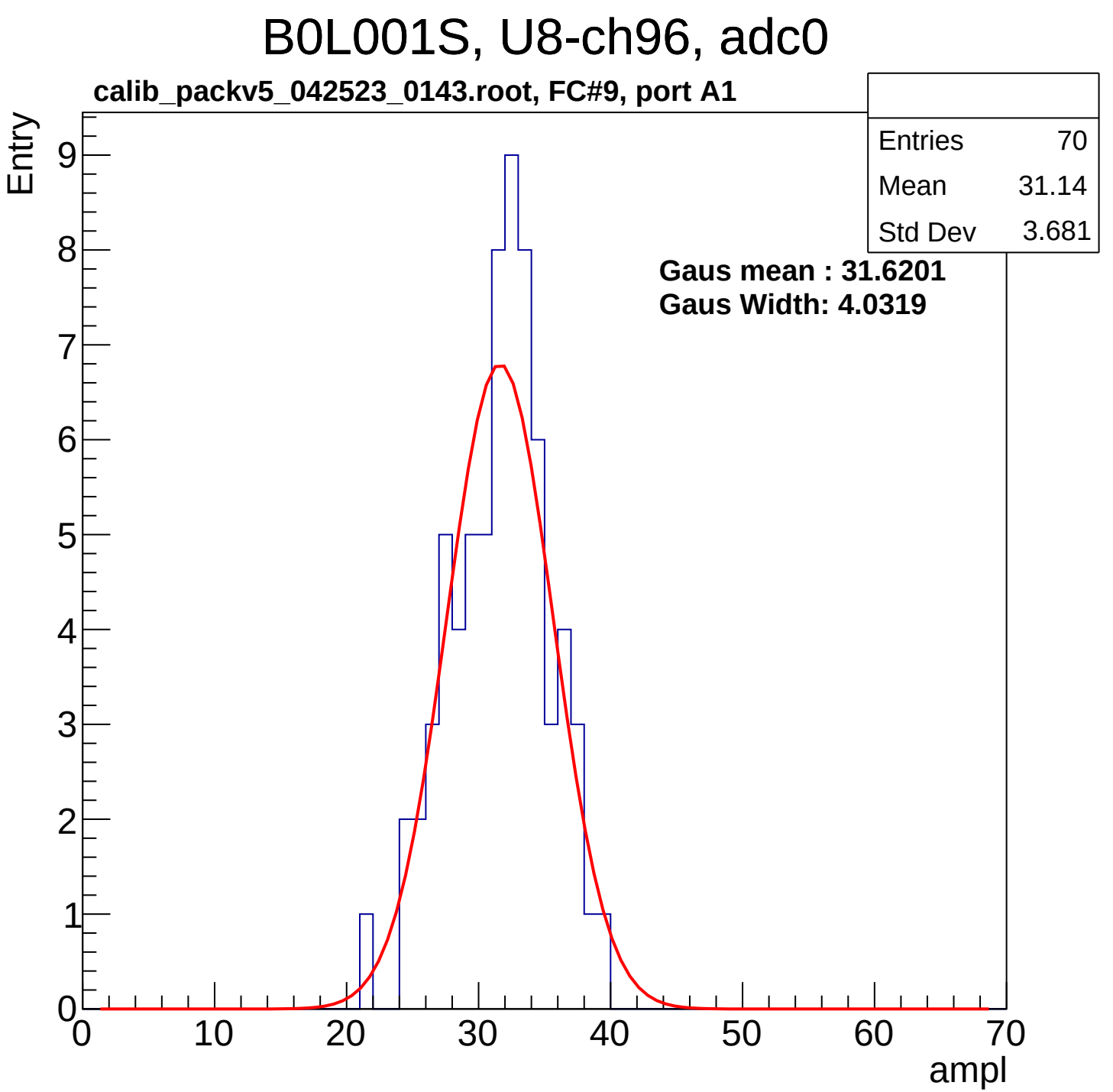
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.14
Std Dev 3.681
Gaus mean : 31.6201
Gaus Width: 4.0319
Entry
ampl
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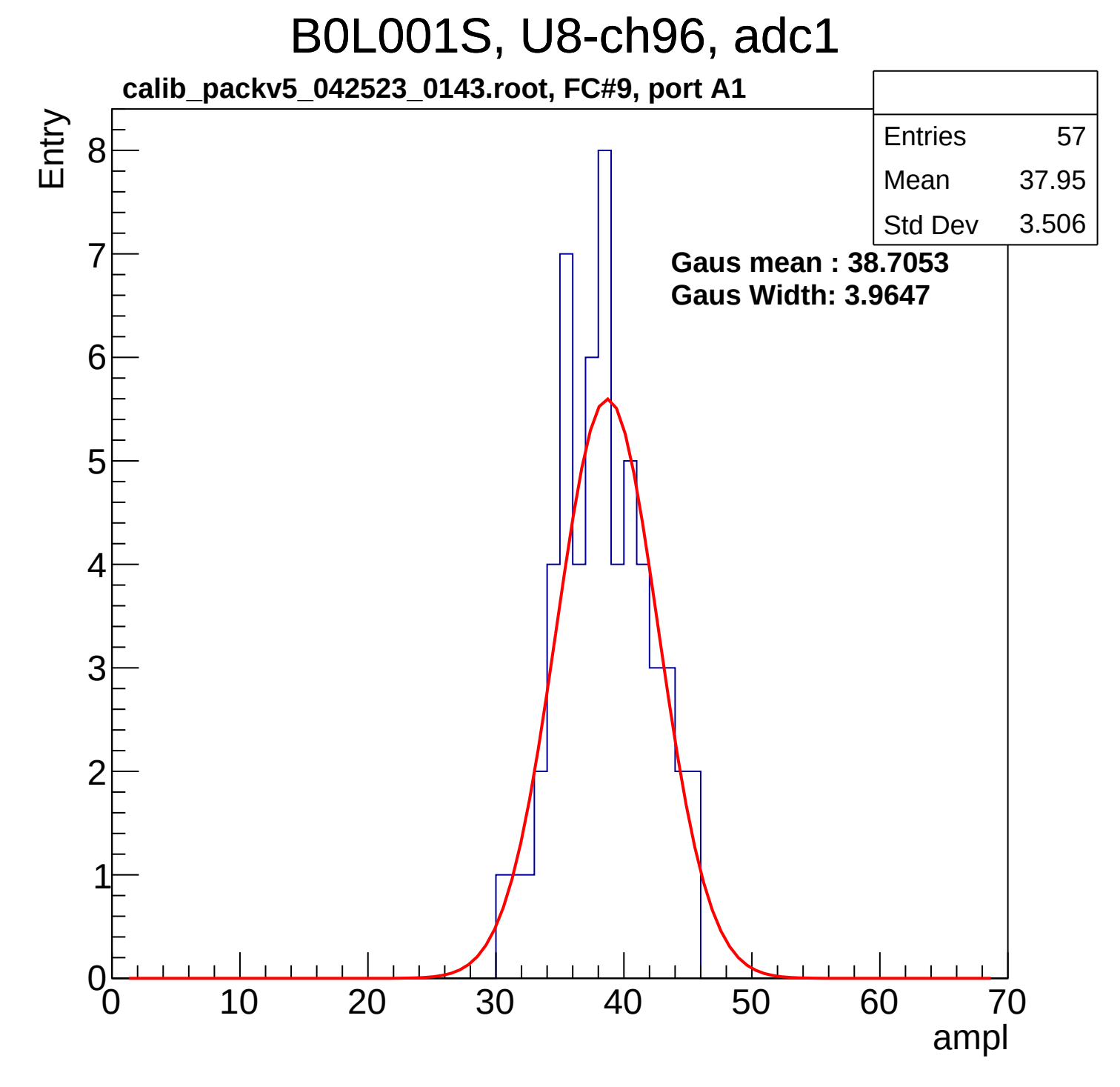
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.95
Std Dev 3.506
Gaus mean : 38.7053
Gaus Width: 3.9647
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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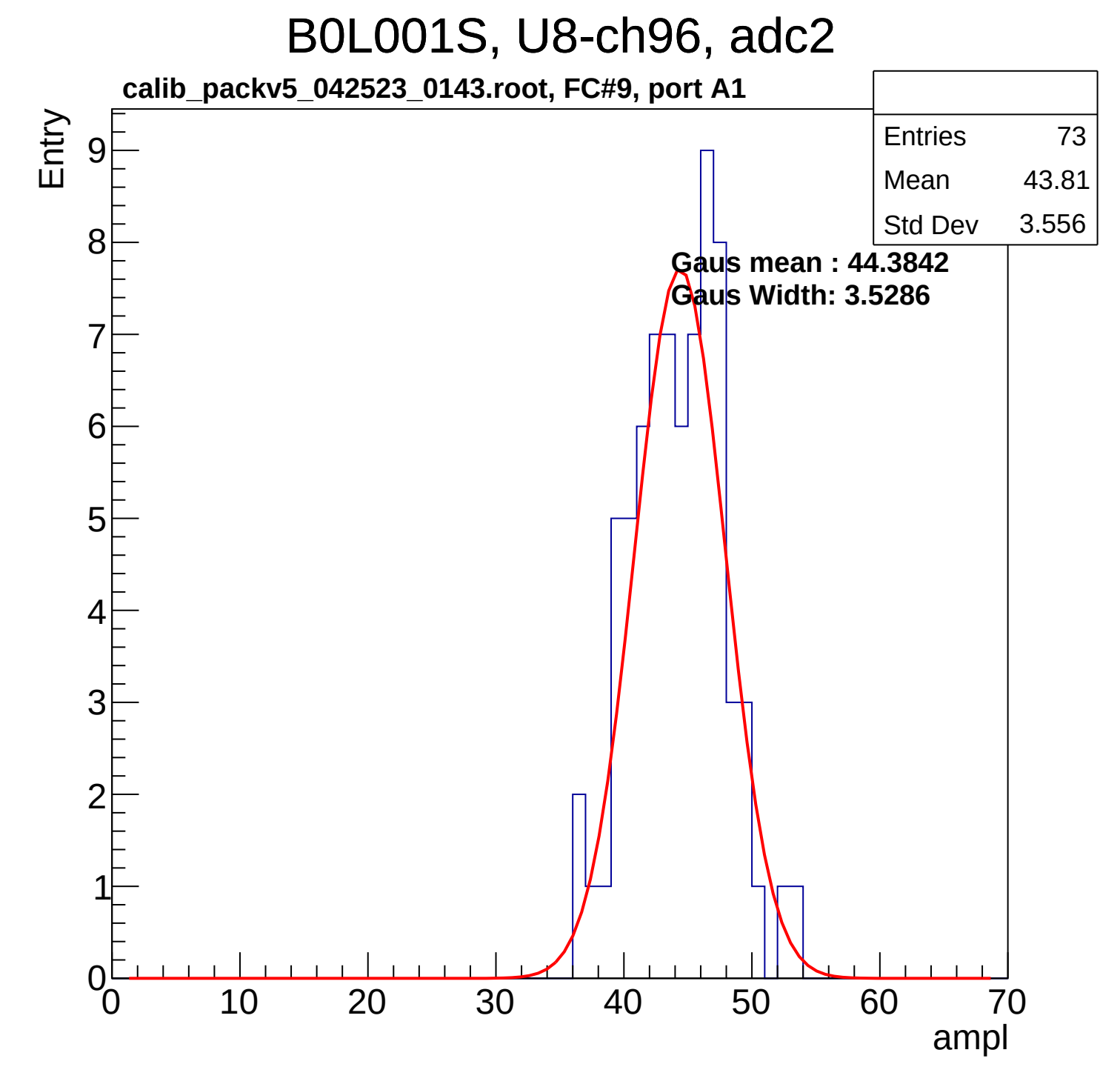
B0L001S, U8-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.81
Std Dev 3.556
Gaus mean : 44.3842
Gaus Width: 3.5286
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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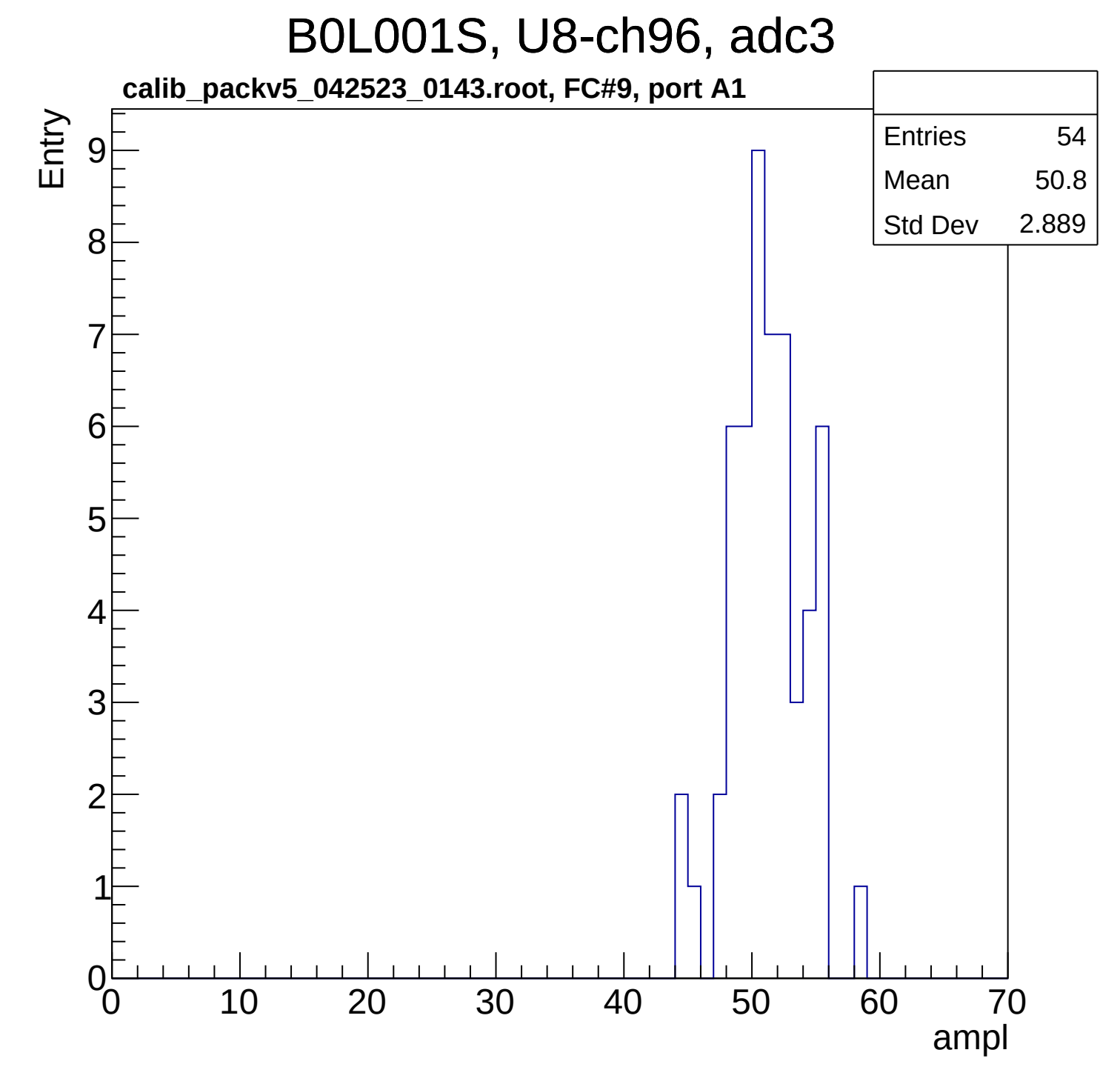
B0L001S, U8-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 50.8
Std Dev 2.889
Entry
ampl
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3
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5
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7
8
9
0
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60
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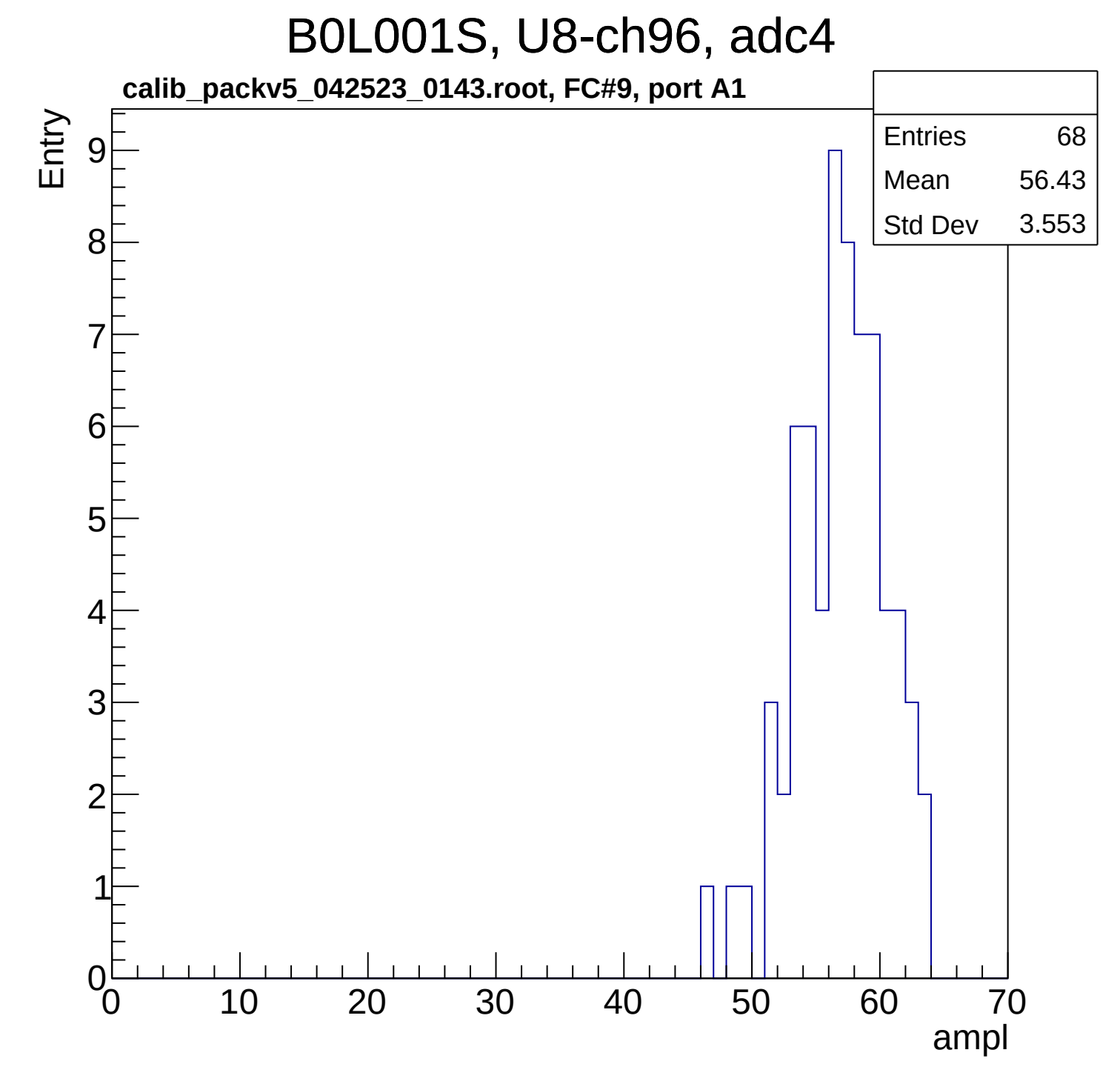
B0L001S, U8-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56.43
Std Dev 3.553
Entry
ampl
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2
3
4
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6
7
8
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0
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40
50
60
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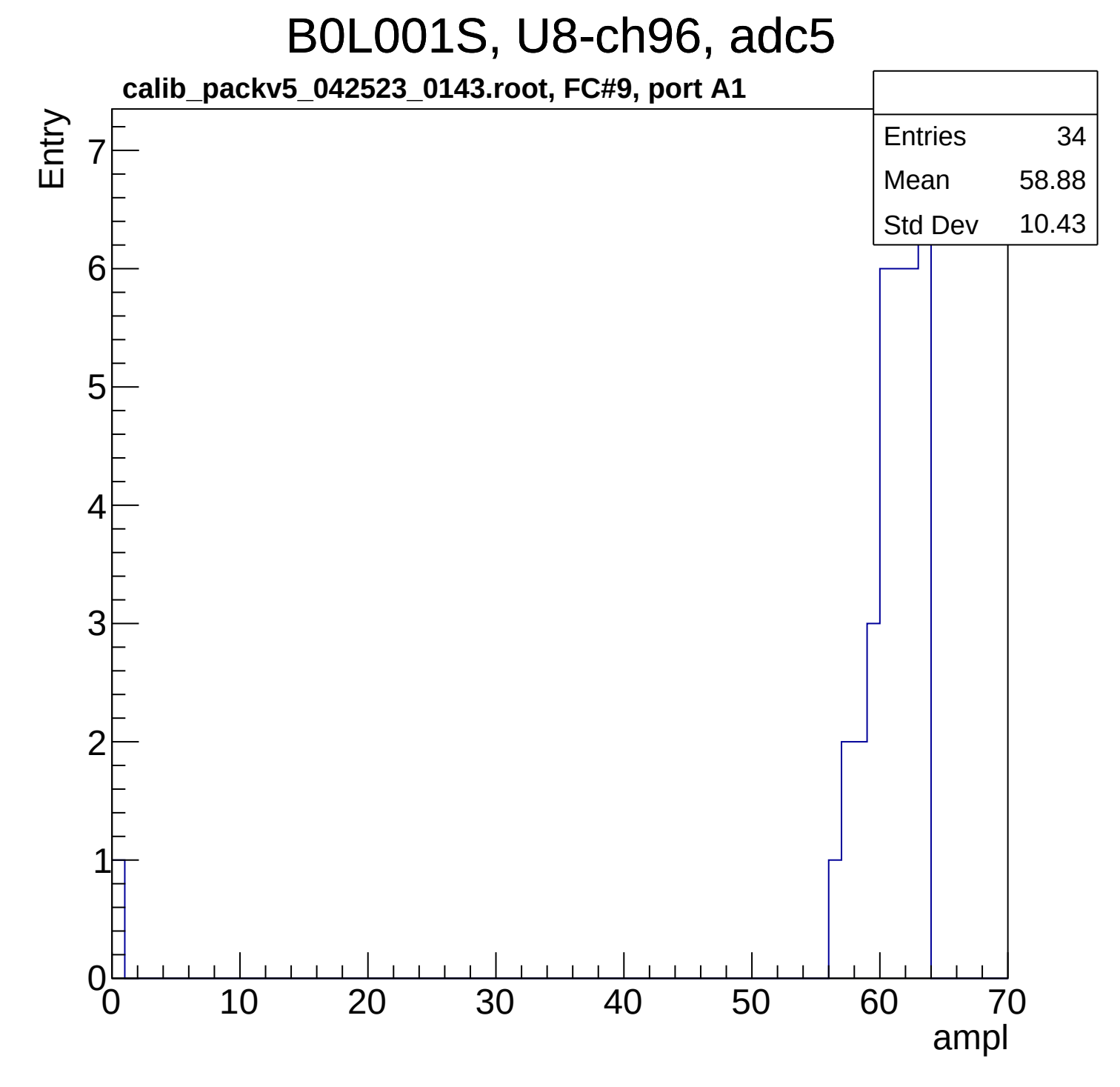
B0L001S, U8-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.88
Std Dev 10.43
Entry
ampl
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1
2
3
4
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0
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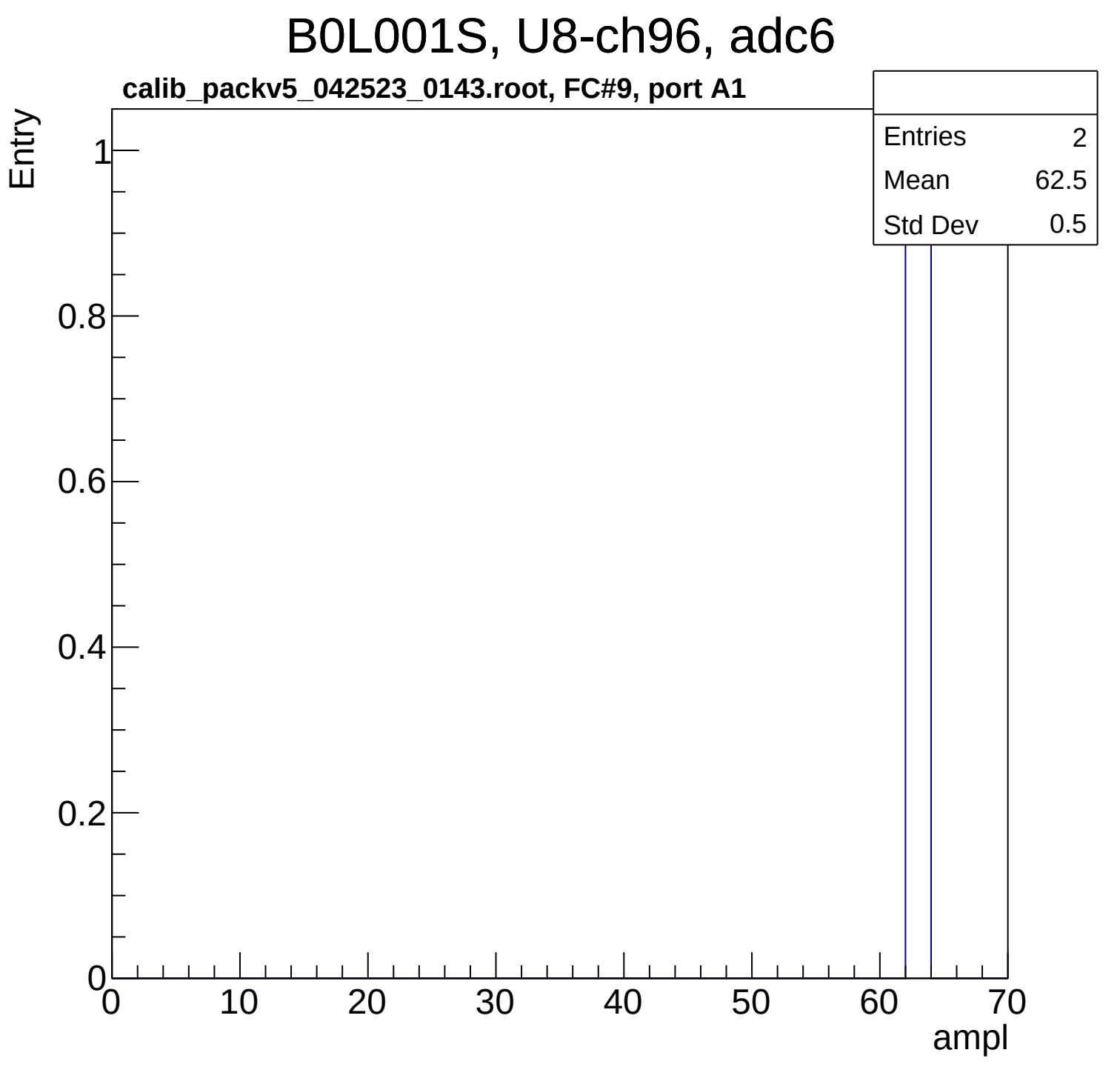
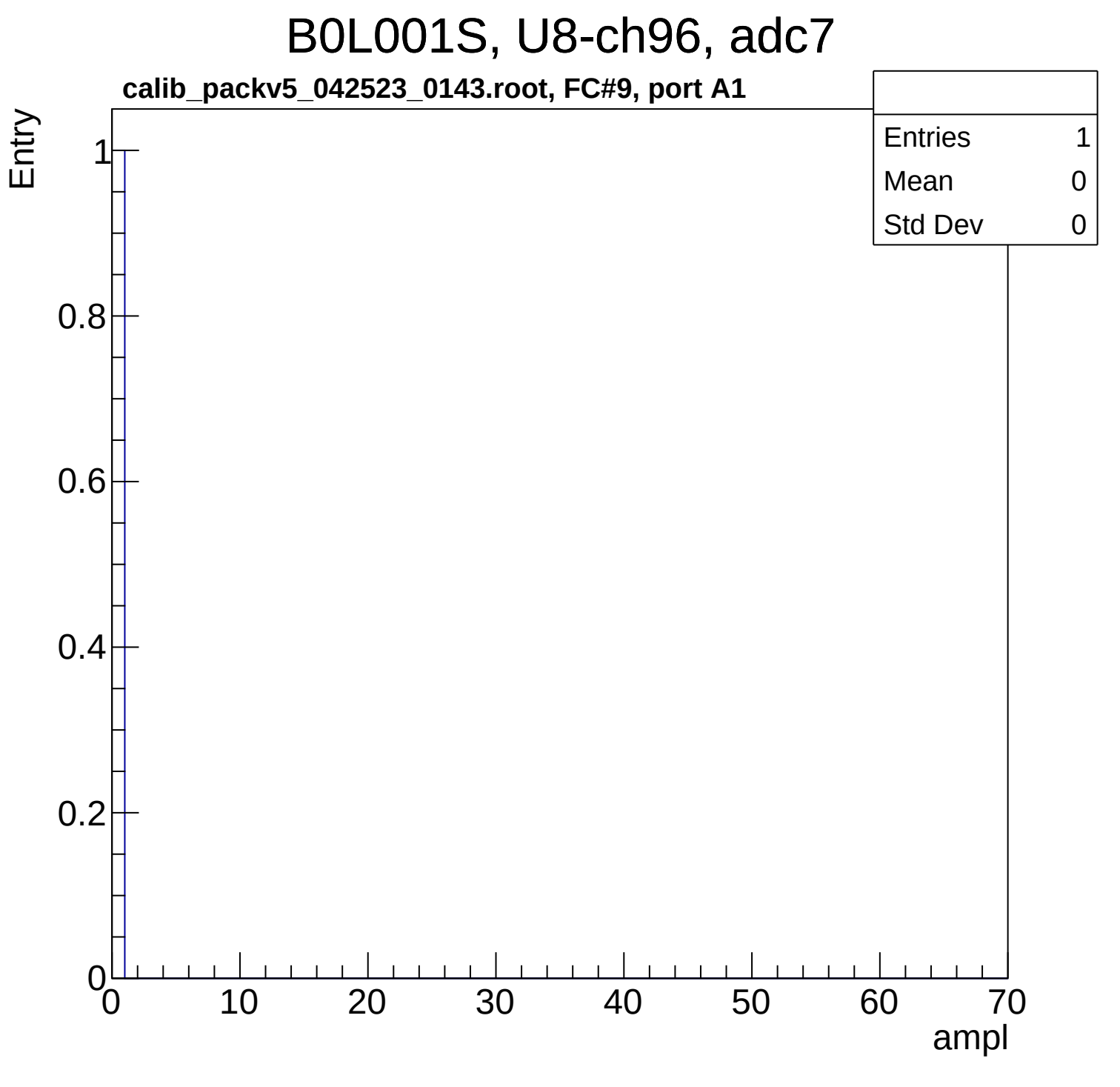

B0L001S, U8-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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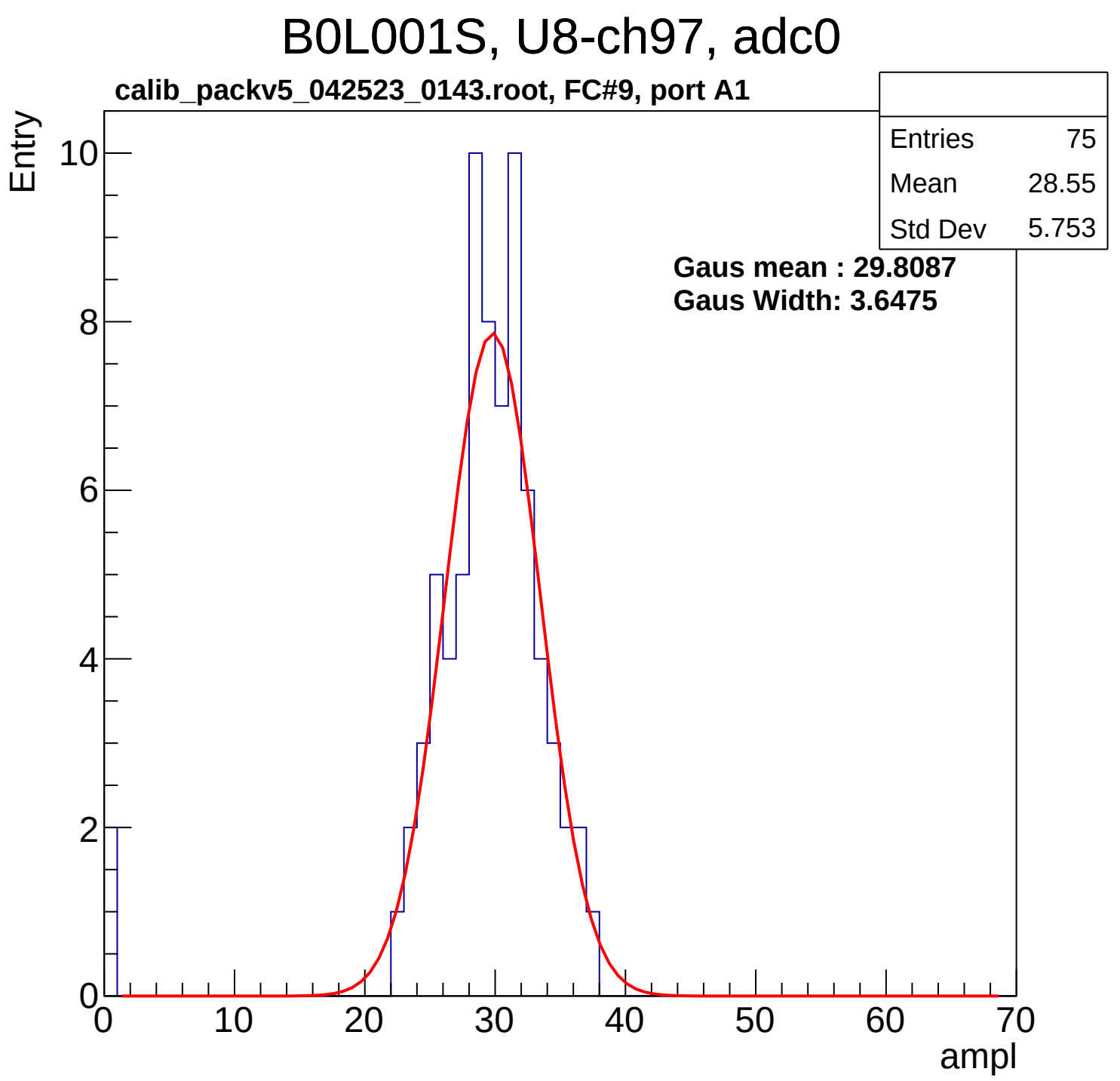
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 28.55
Std Dev 5.753
Gaus mean : 29.8087
Gaus Width: 3.6475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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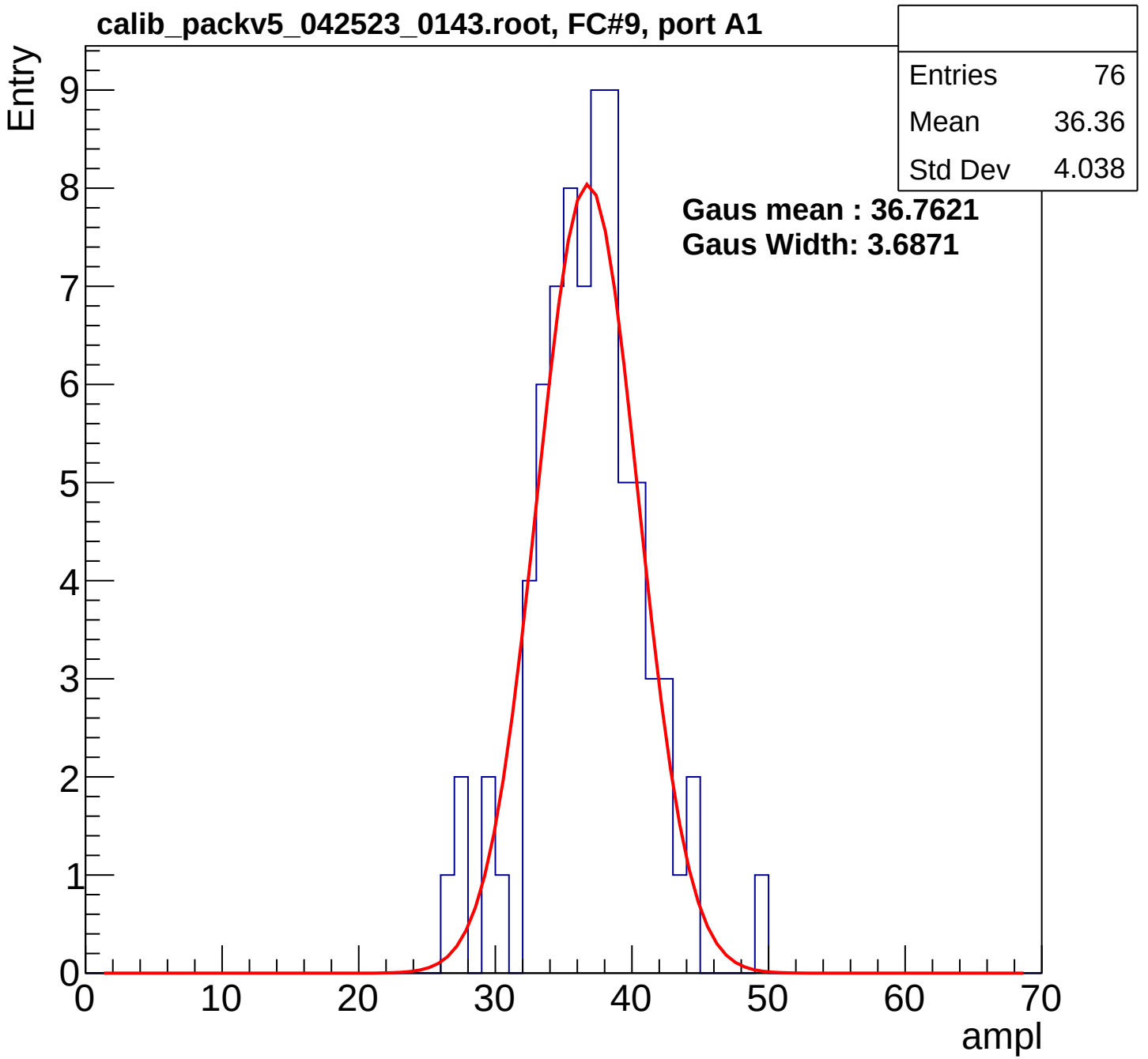
B0L001S, U8-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.36
Std Dev 4.038
Gaus mean : 36.7621
Gaus Width: 3.6871
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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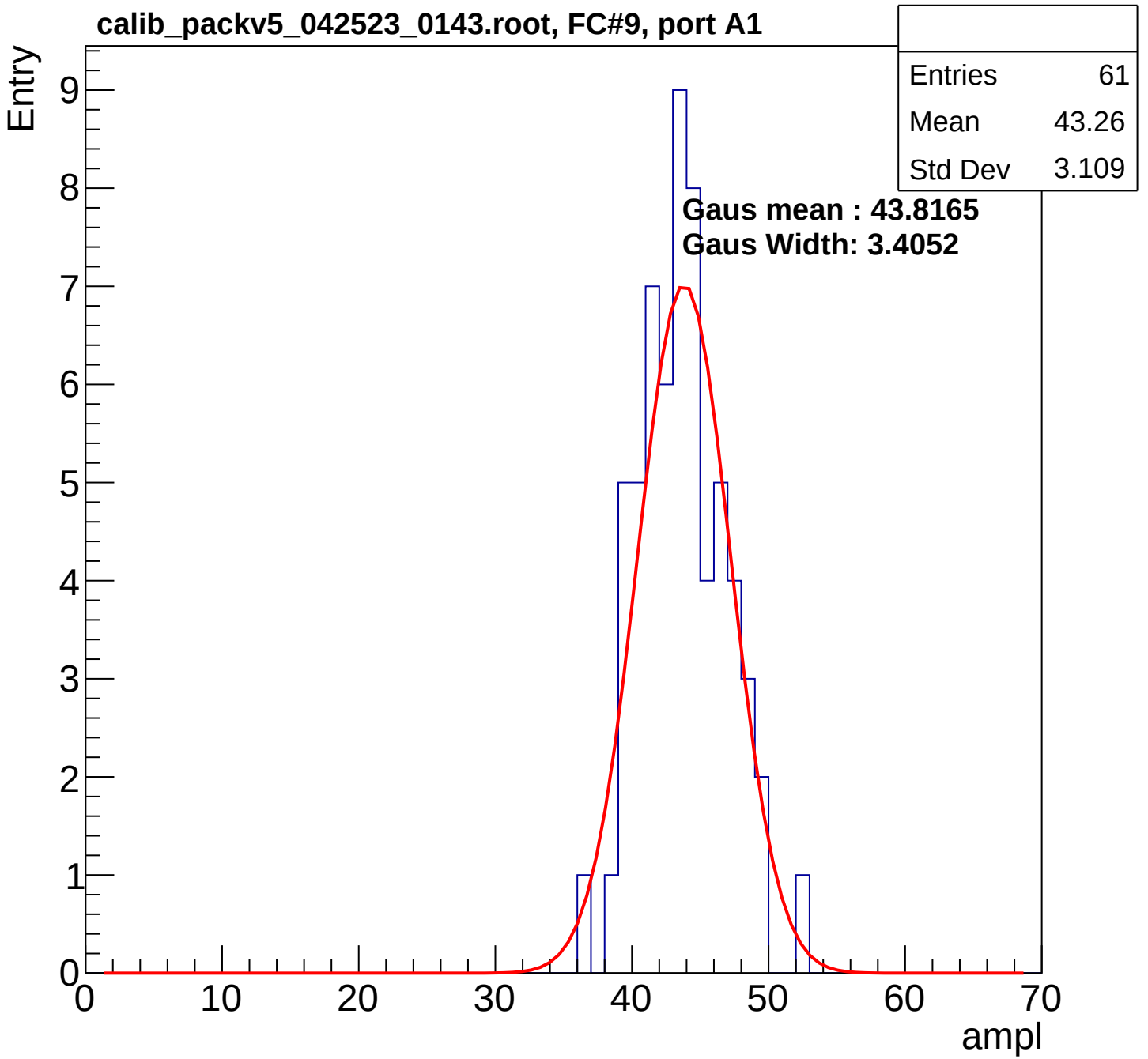
B0L001S, U8-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.26
Std Dev 3.109
Gaus mean : 43.8165
Gaus Width: 3.4052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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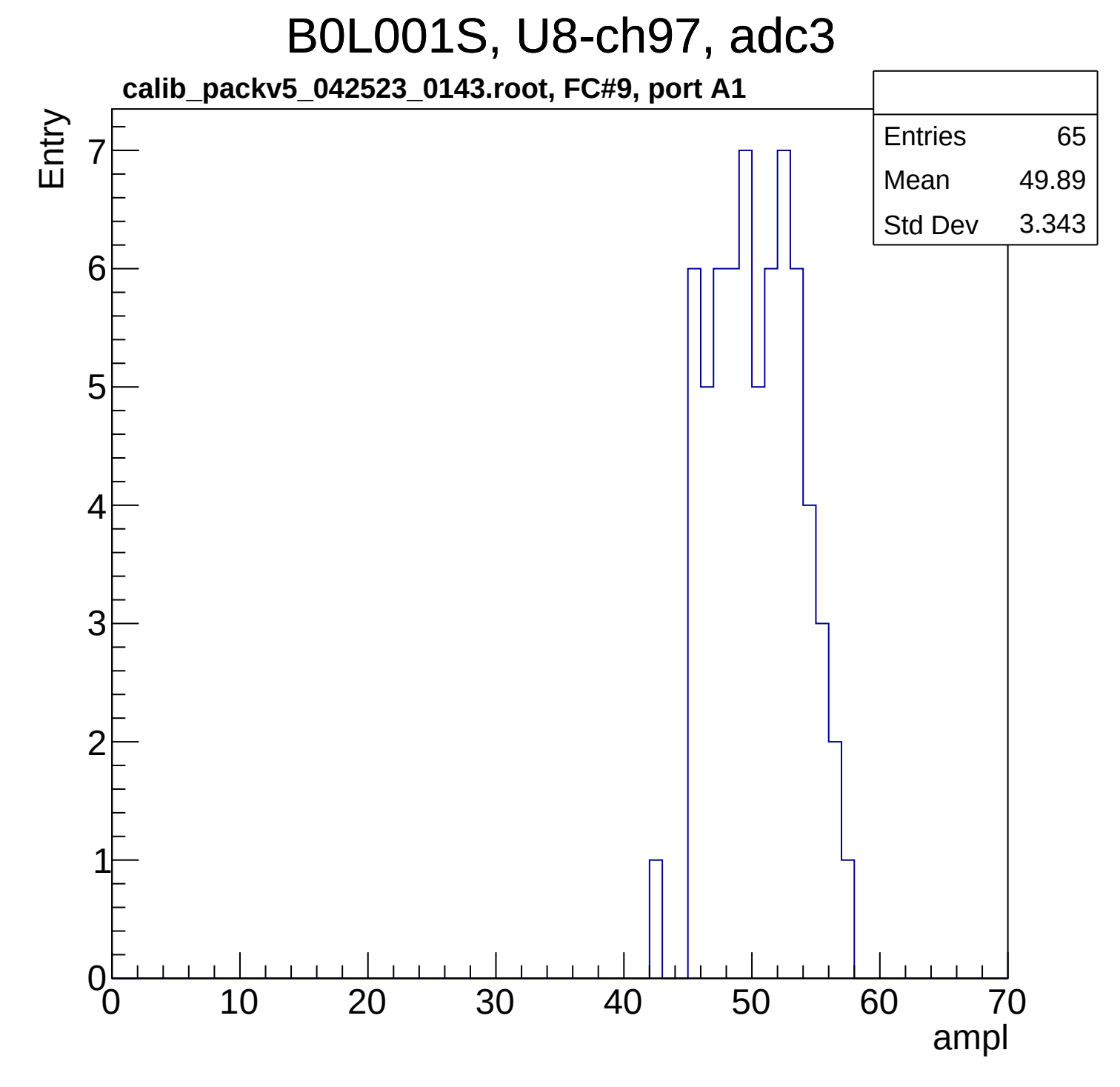
B0L001S, U8-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.89
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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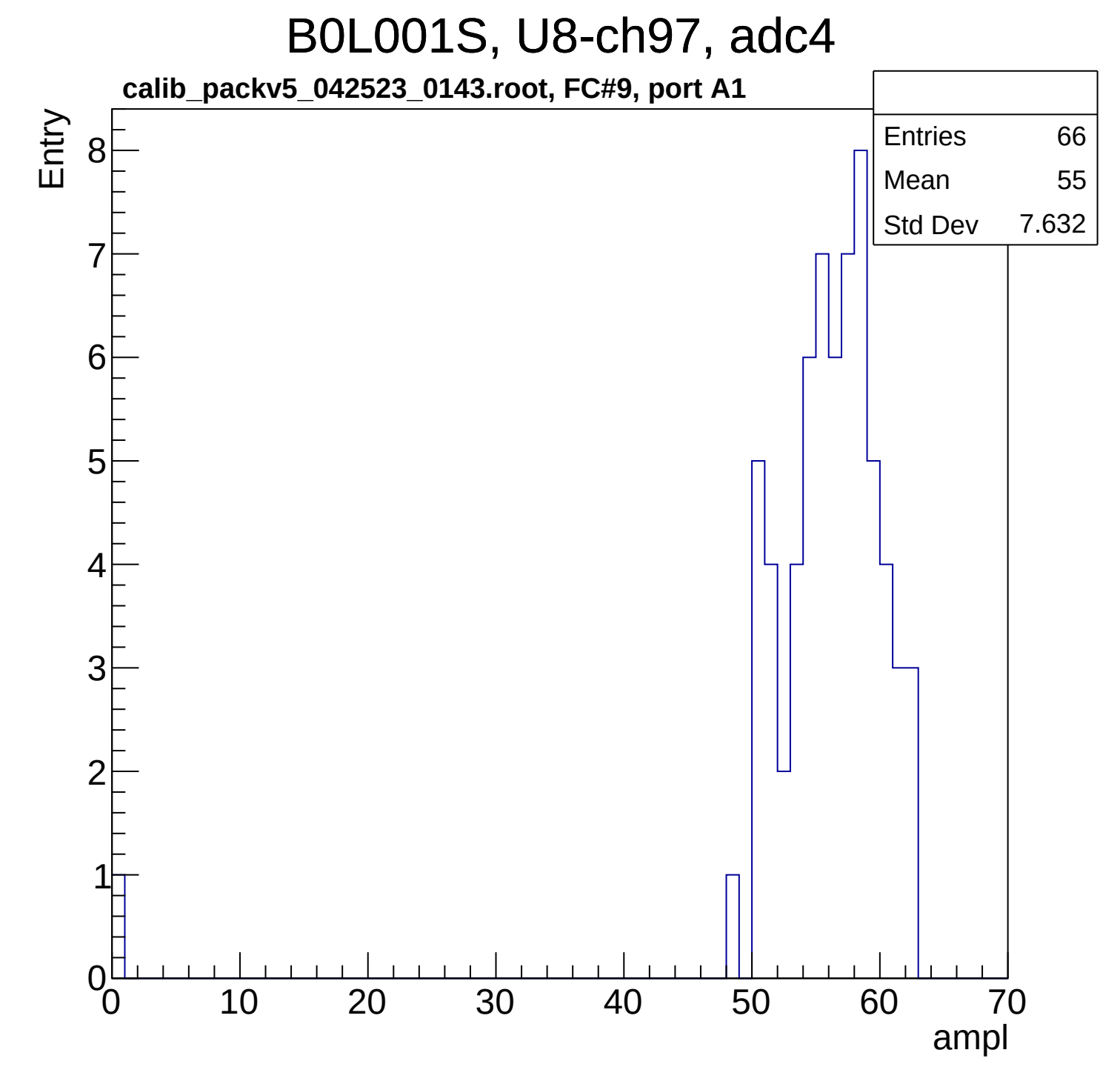
B0L001S, U8-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55
Std Dev 7.632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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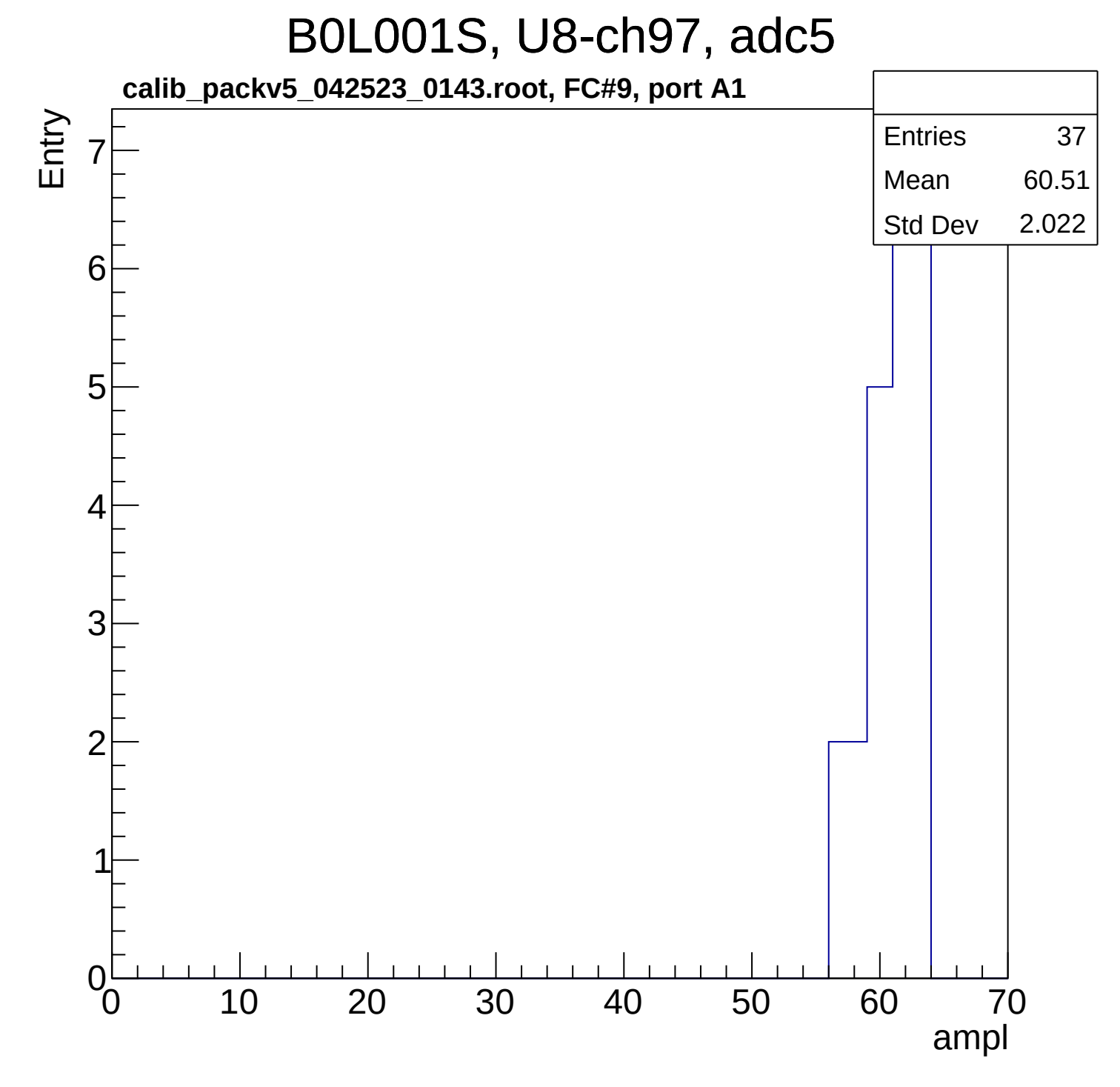
B0L001S, U8-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.51
Std Dev 2.022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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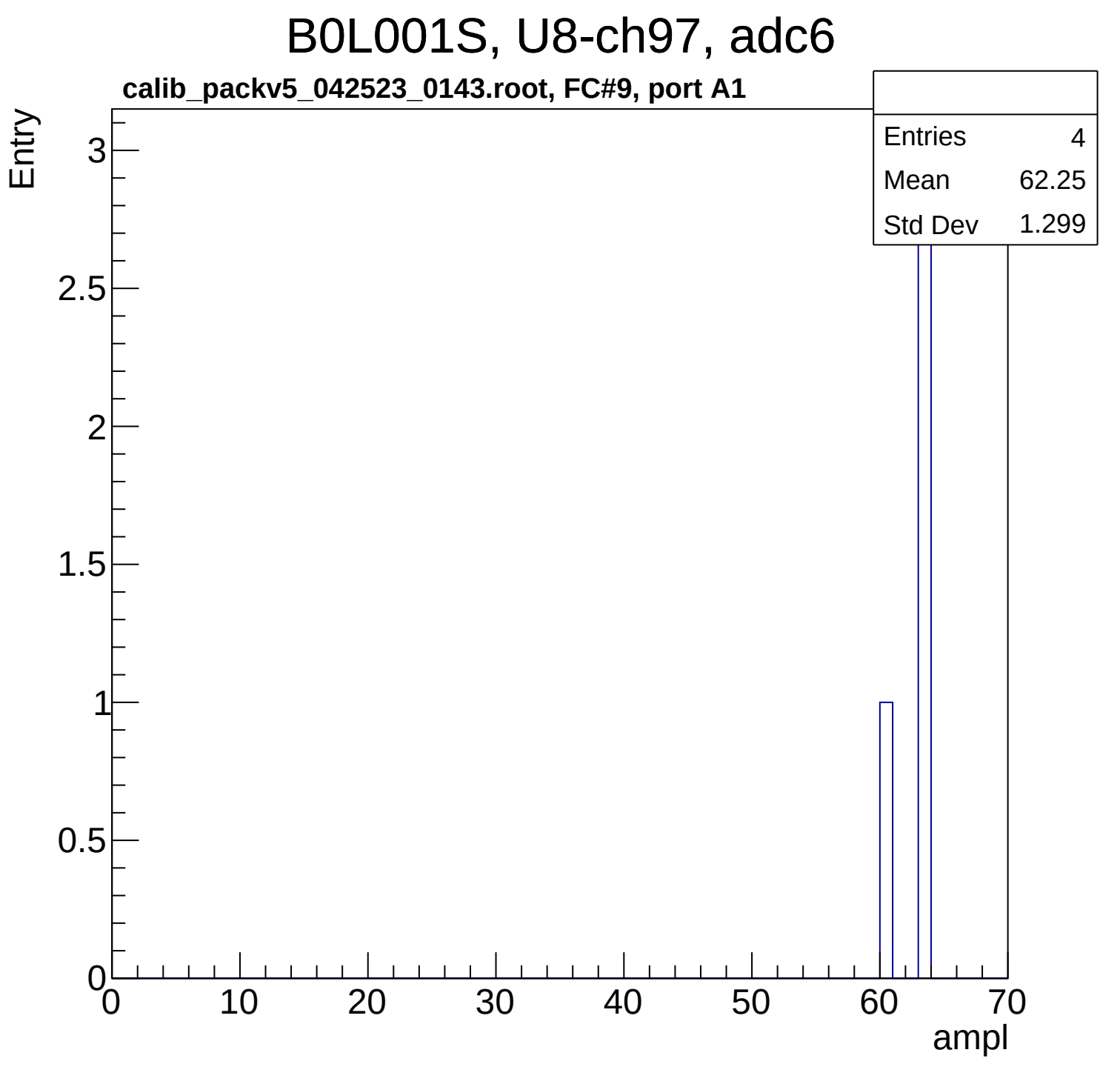
B0L001S, U8-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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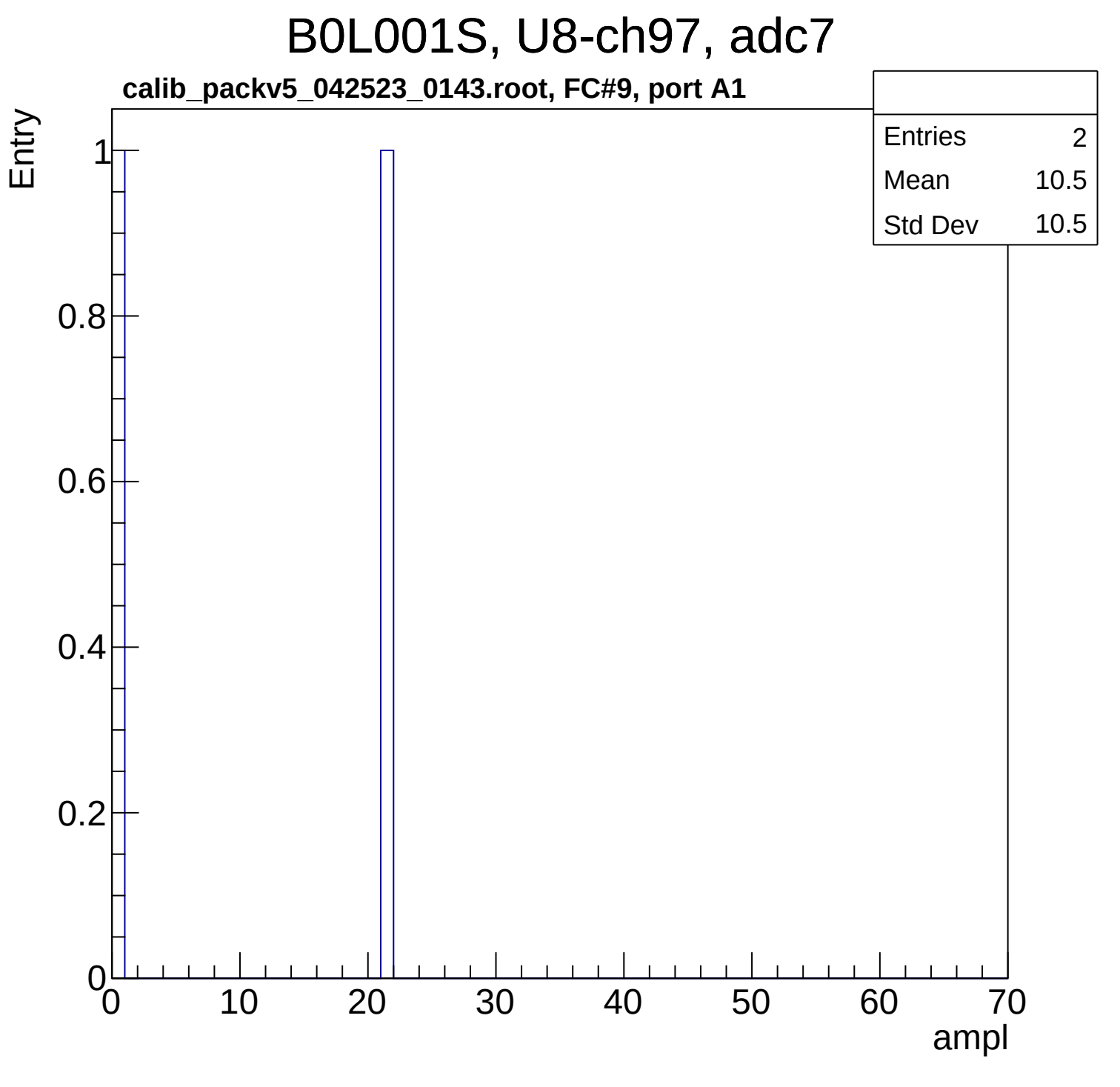
B0L001S, U8-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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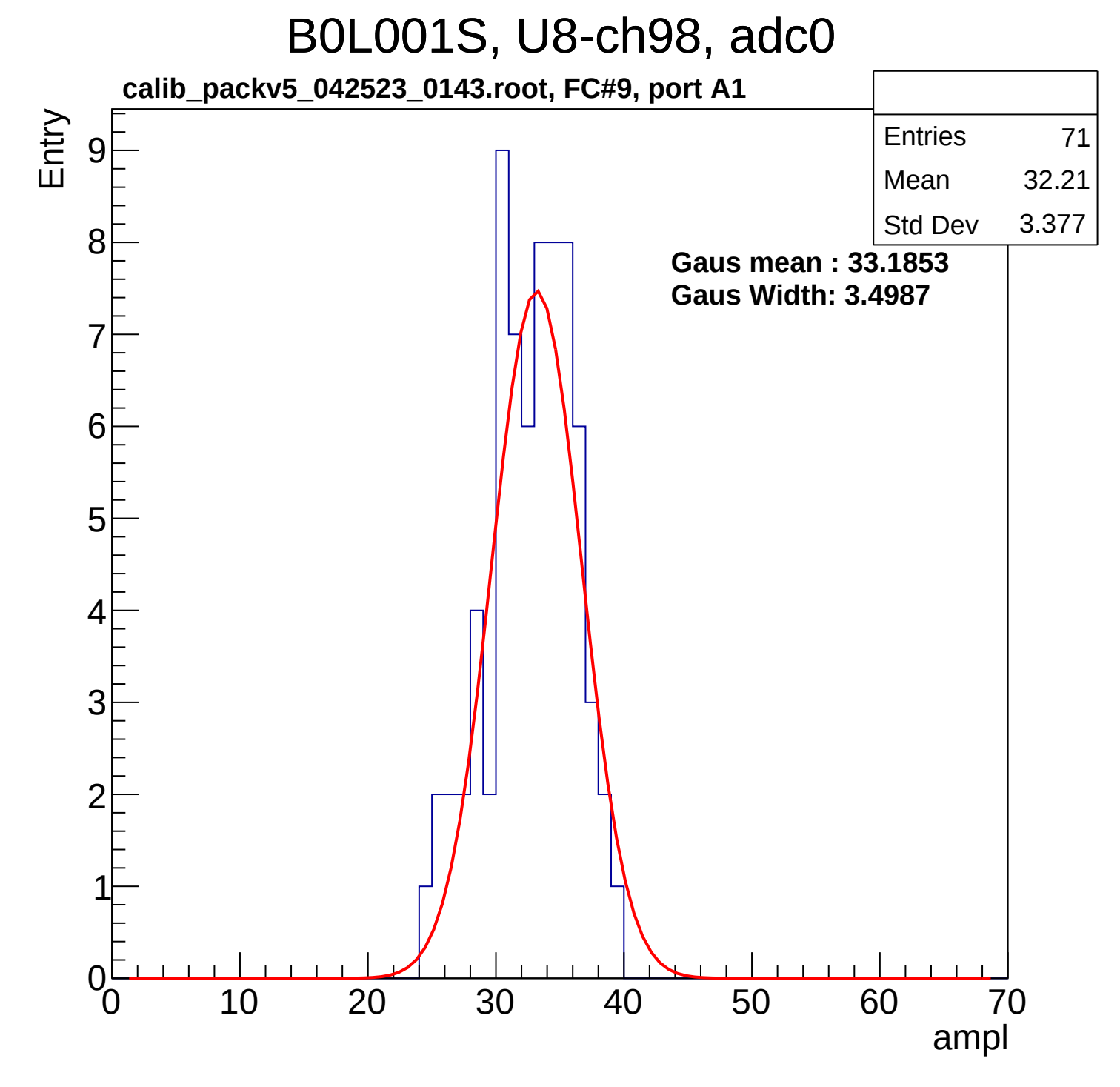
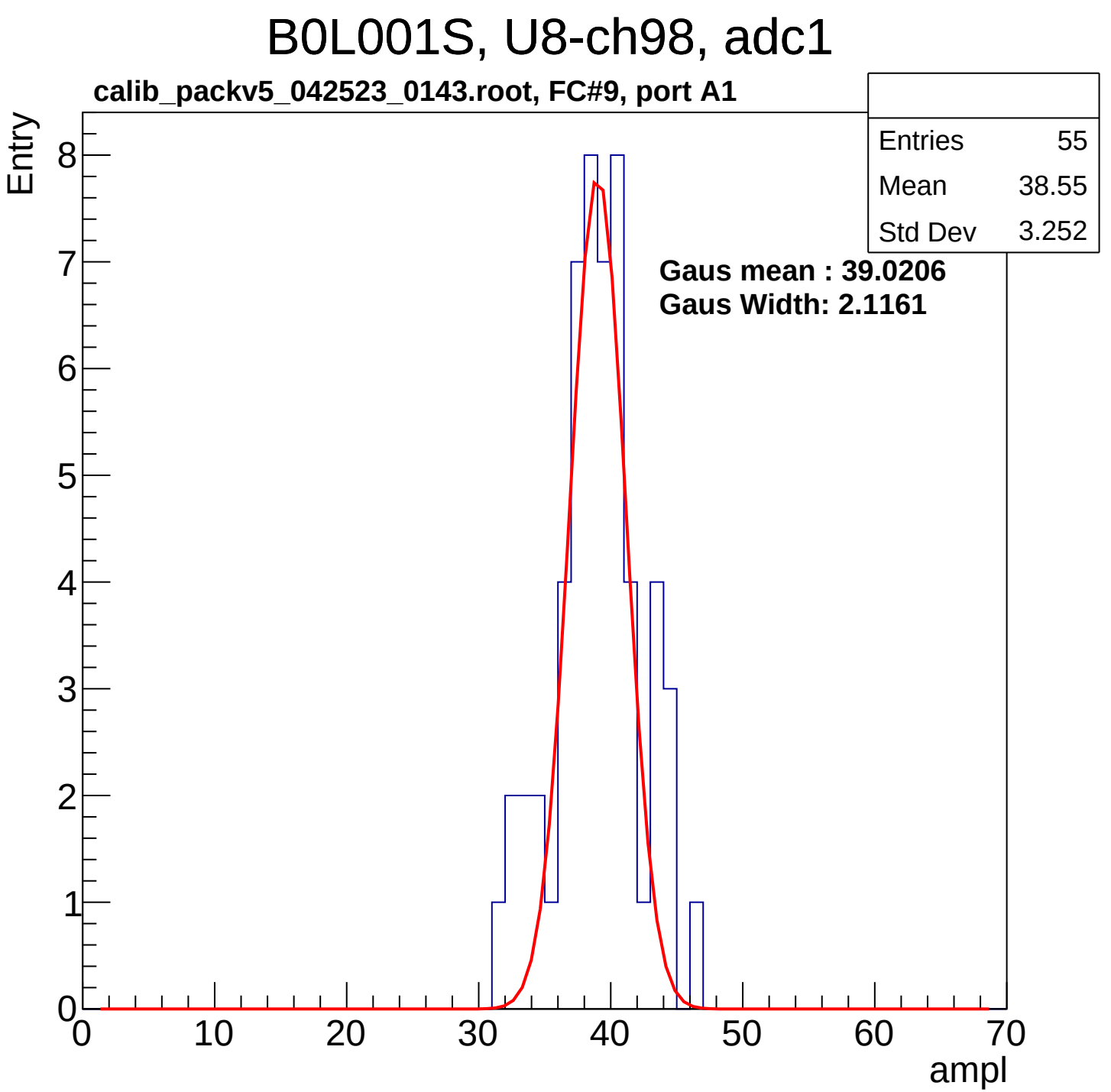
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.21
Std Dev 3.377
Gaus mean : 33.1853
Gaus Width: 3.4987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 38.55
Std Dev 3.252
Gaus mean : 39.0206
Gaus Width: 2.1161
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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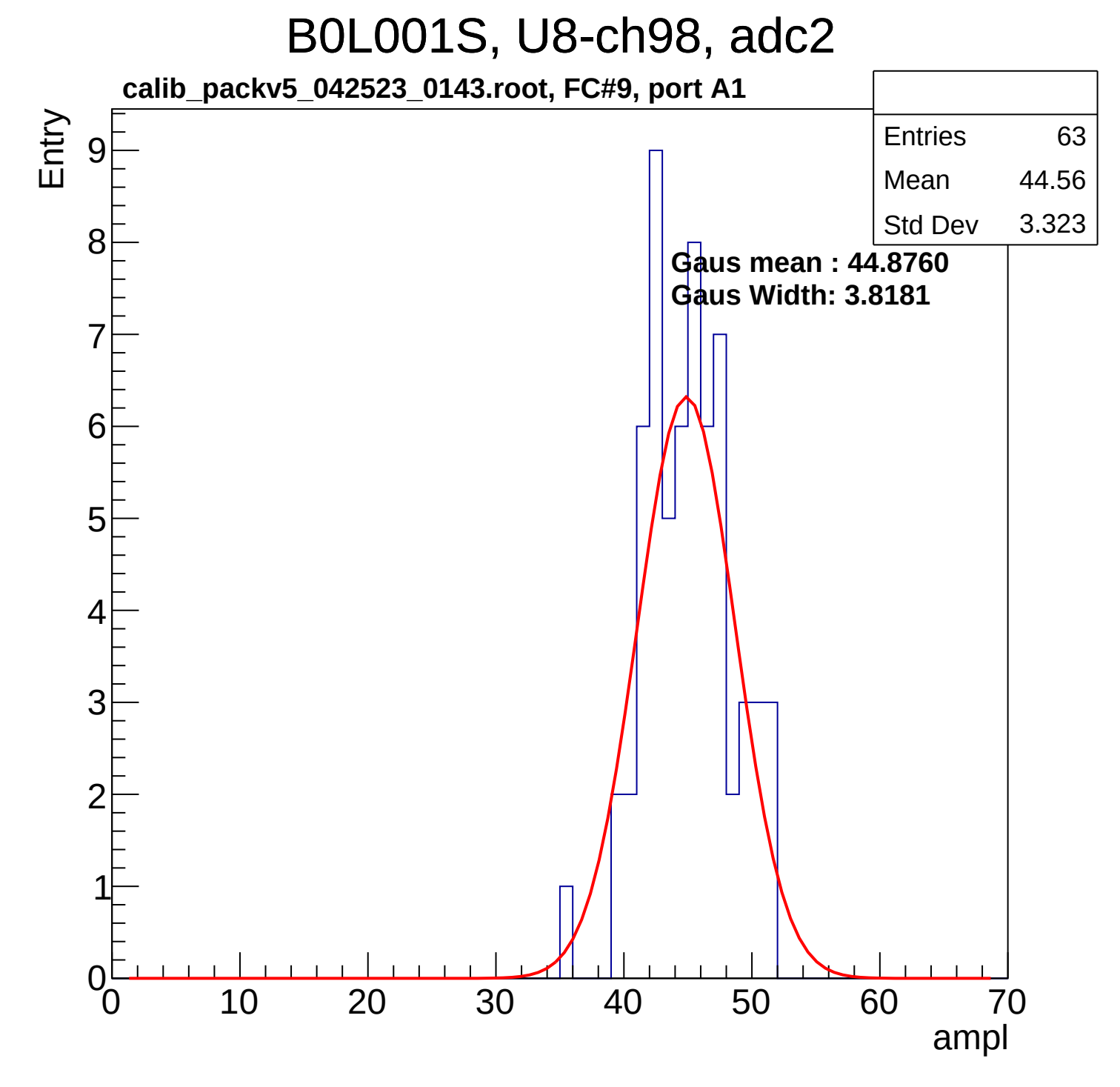
B0L001S, U8-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.56
Std Dev 3.323
Gaus mean : 44.8760
Gaus Width: 3.8181
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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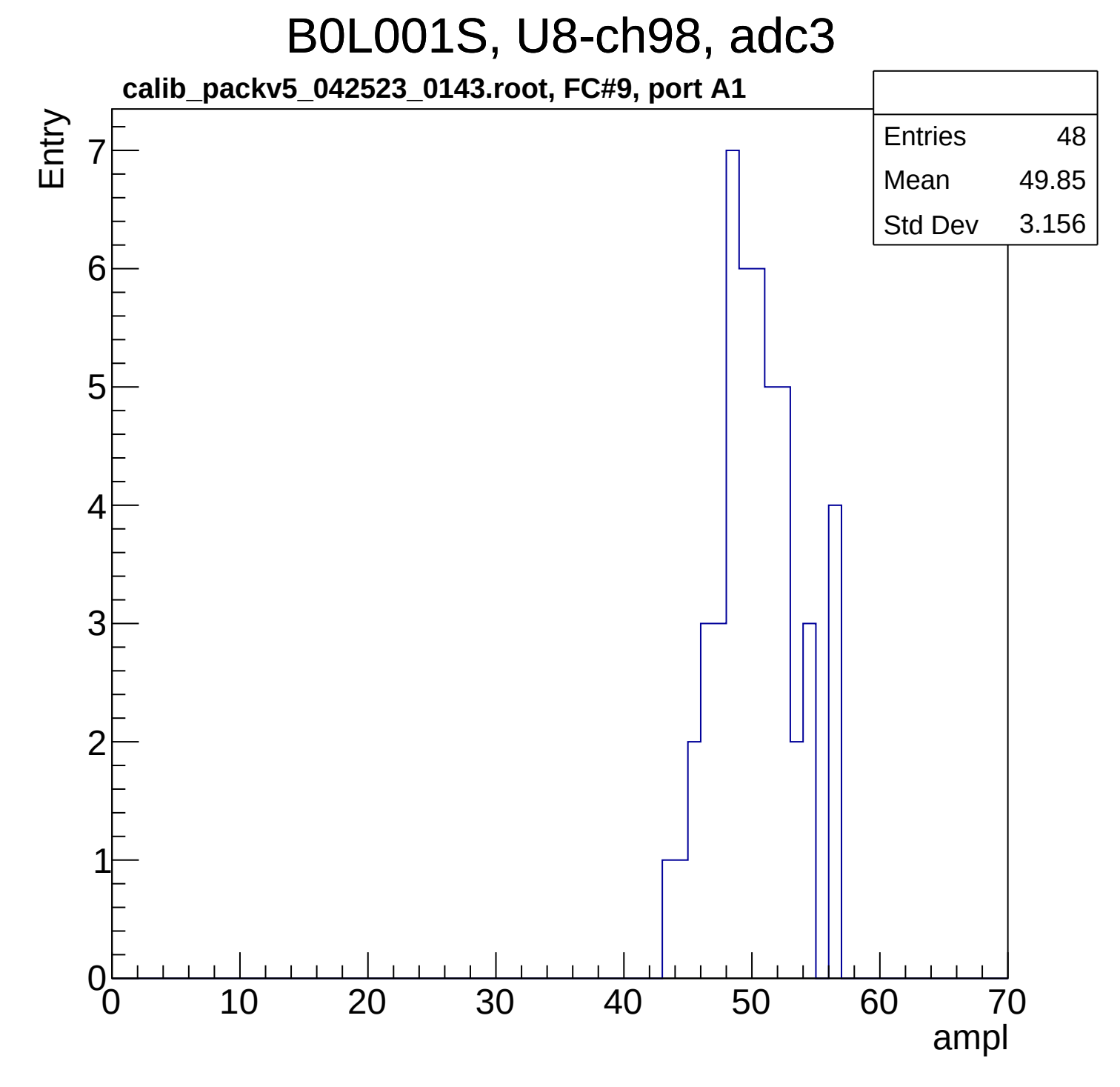
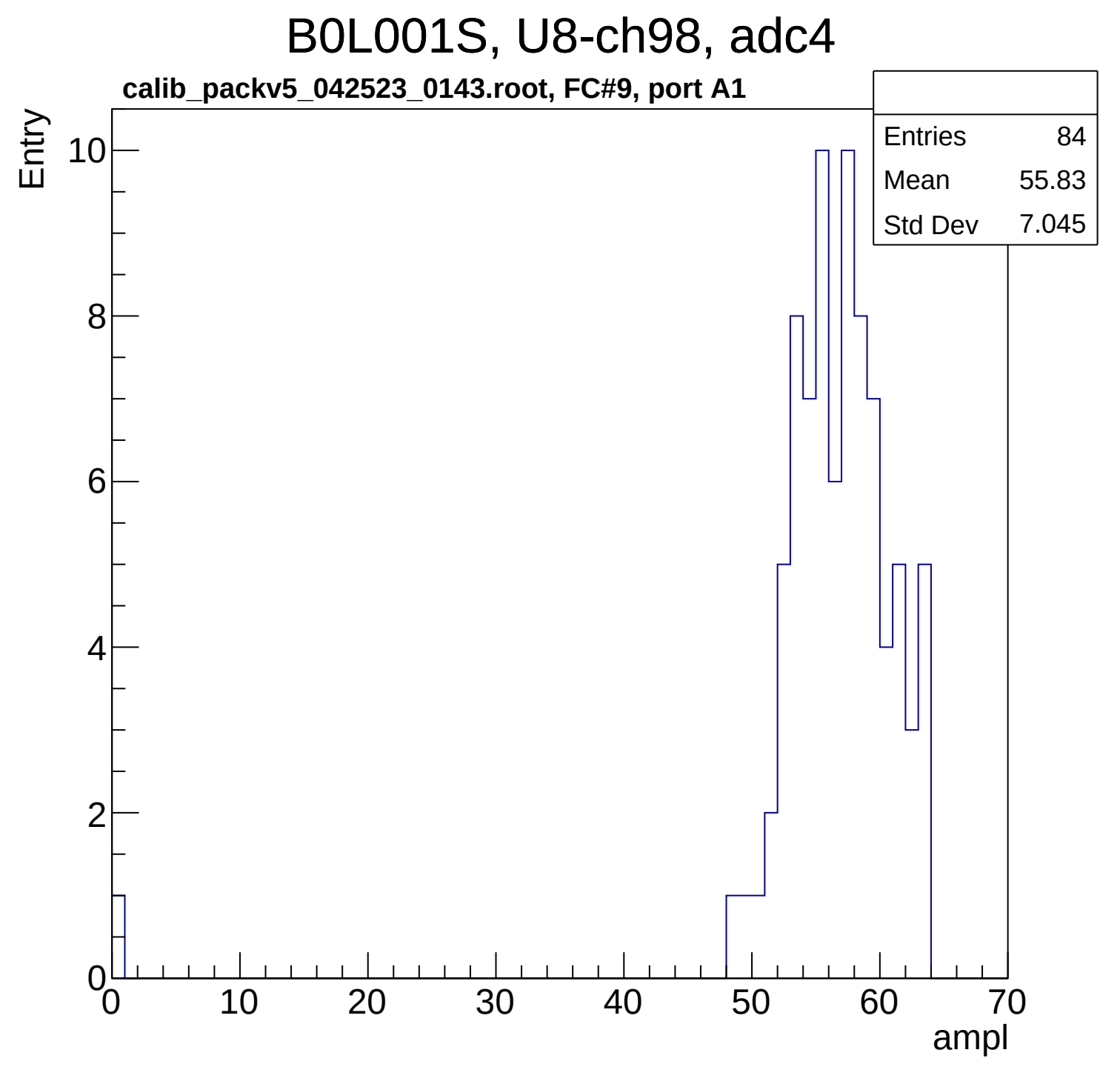
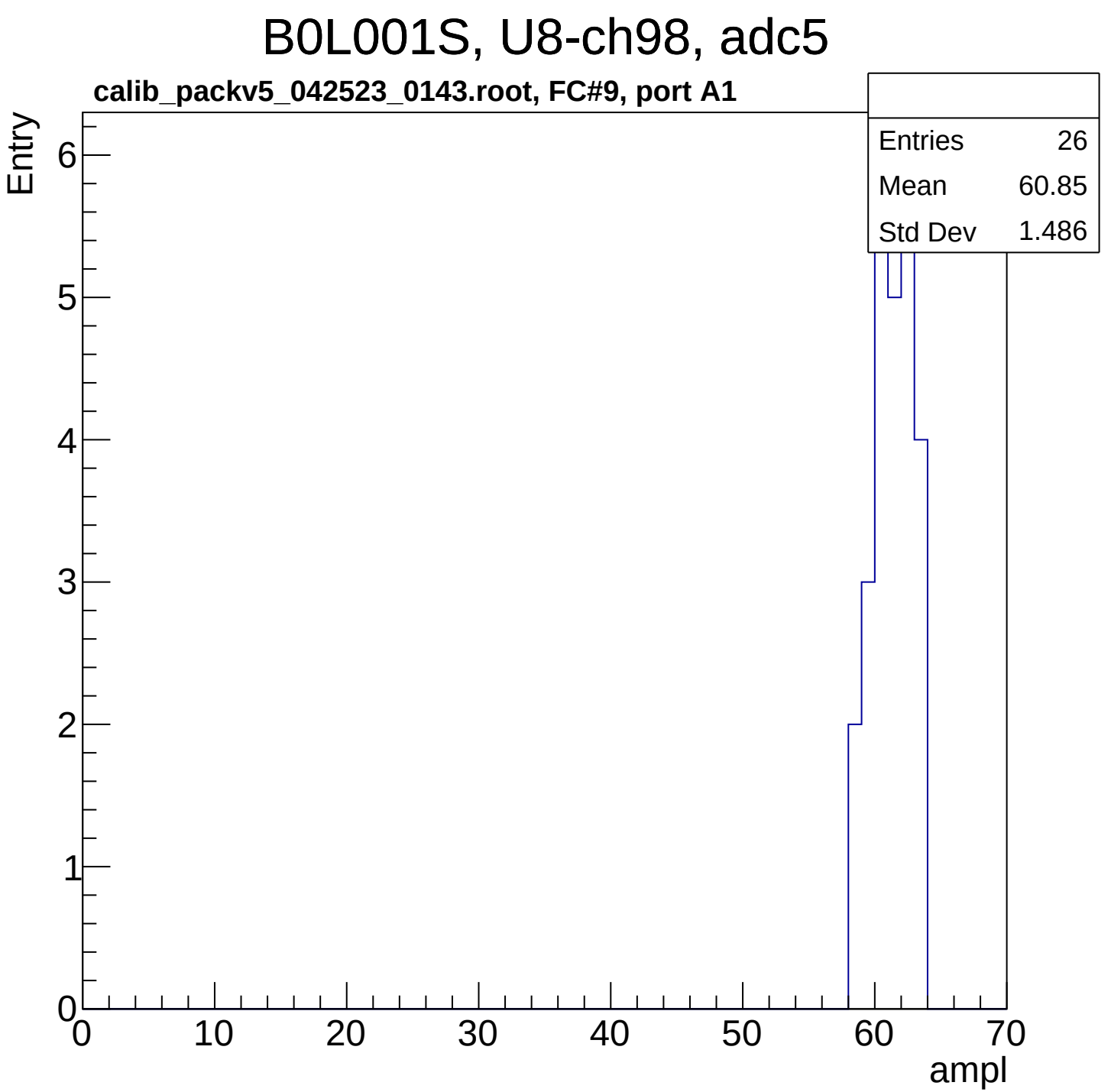

B0L001S, U8-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 49.85
Std Dev 3.156
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 55.83
Std Dev 7.045
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.85
Std Dev 1.486
Entry
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2
3
4
5
6
0
10
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30
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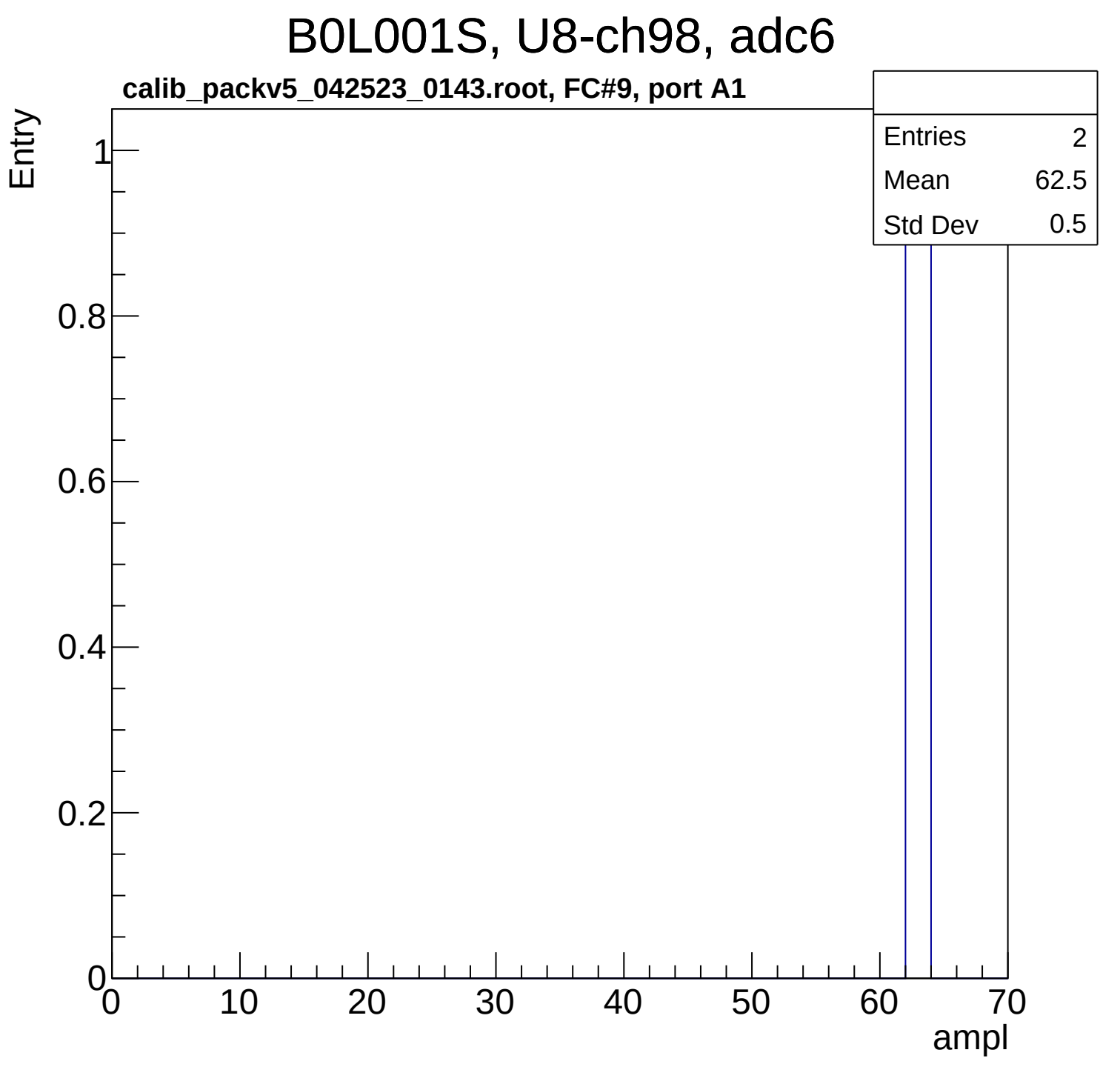
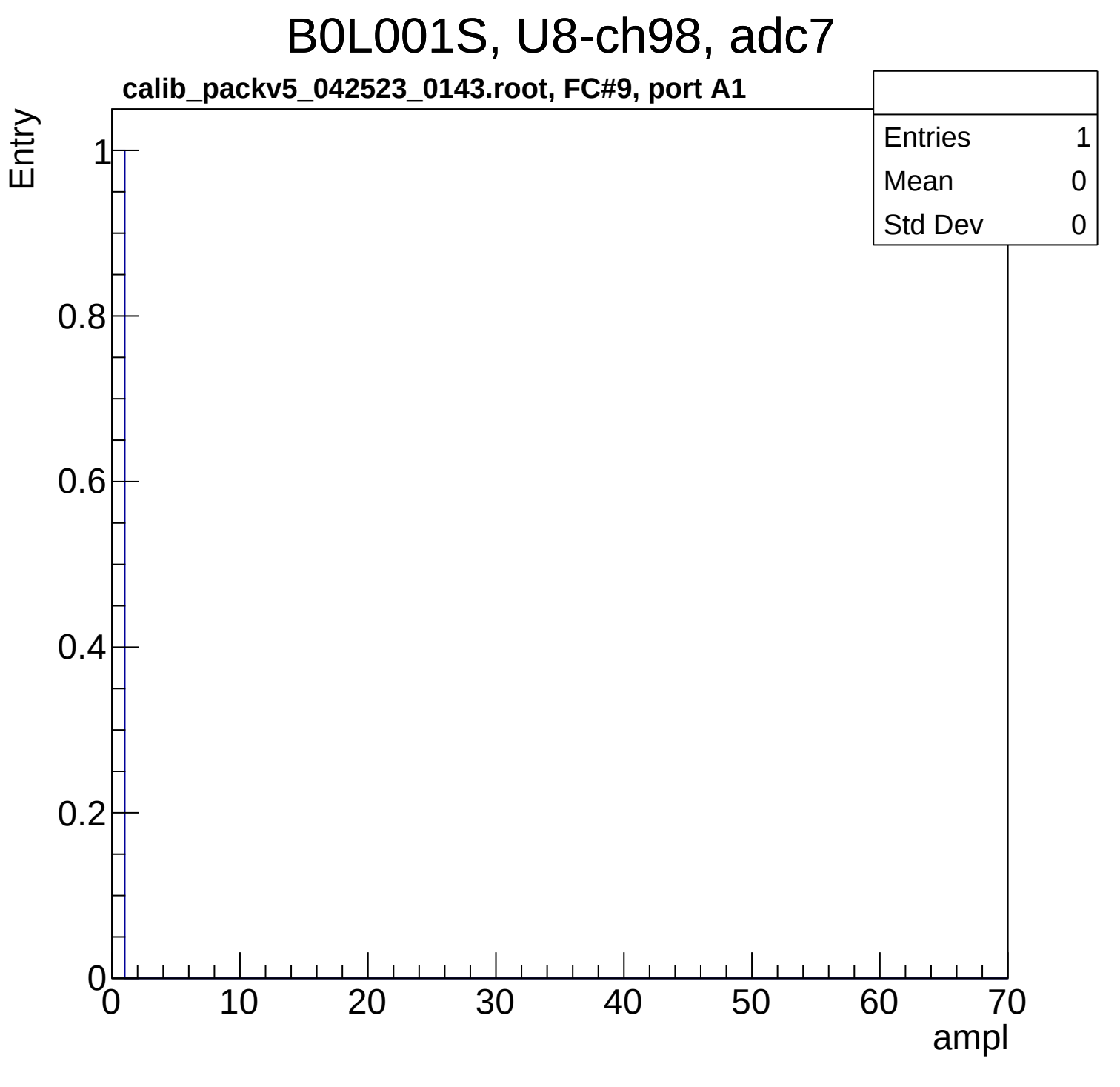
50
60
70
ampl


B0L001S, U8-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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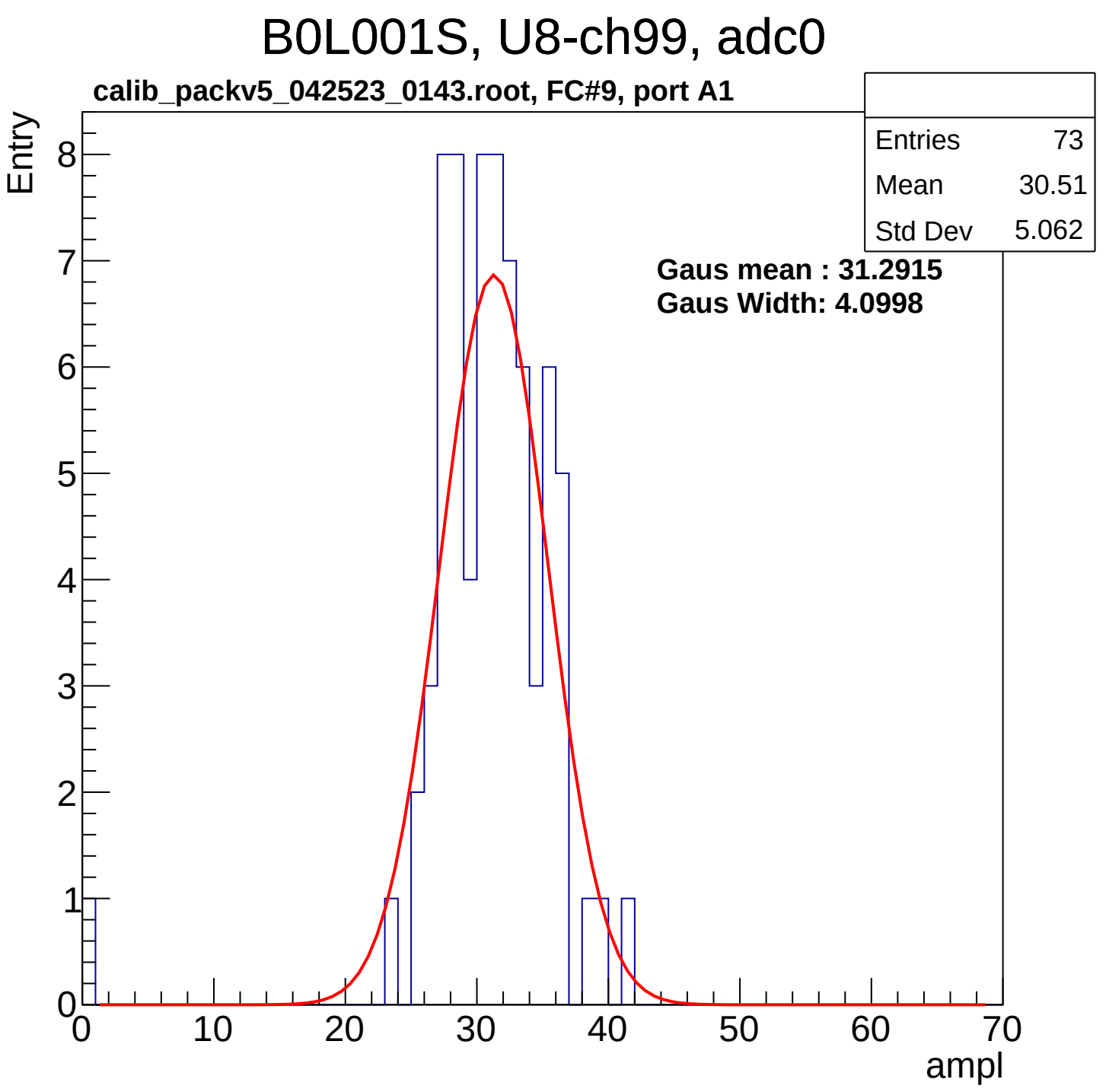
50
60
70
ampl


B0L001S, U8-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.51
Std Dev 5.062
Gaus mean : 31.2915
Gaus Width: 4.0998
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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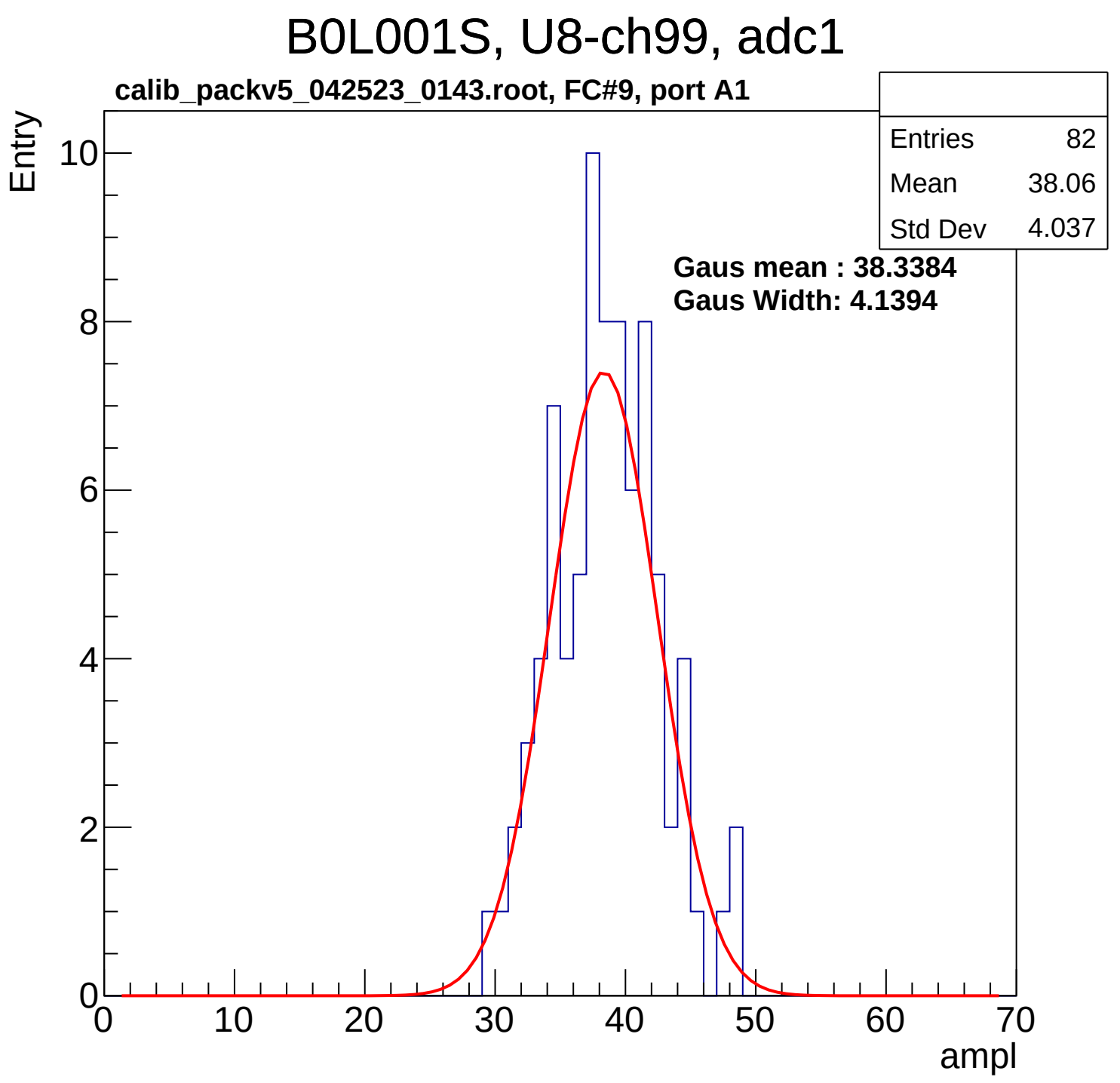
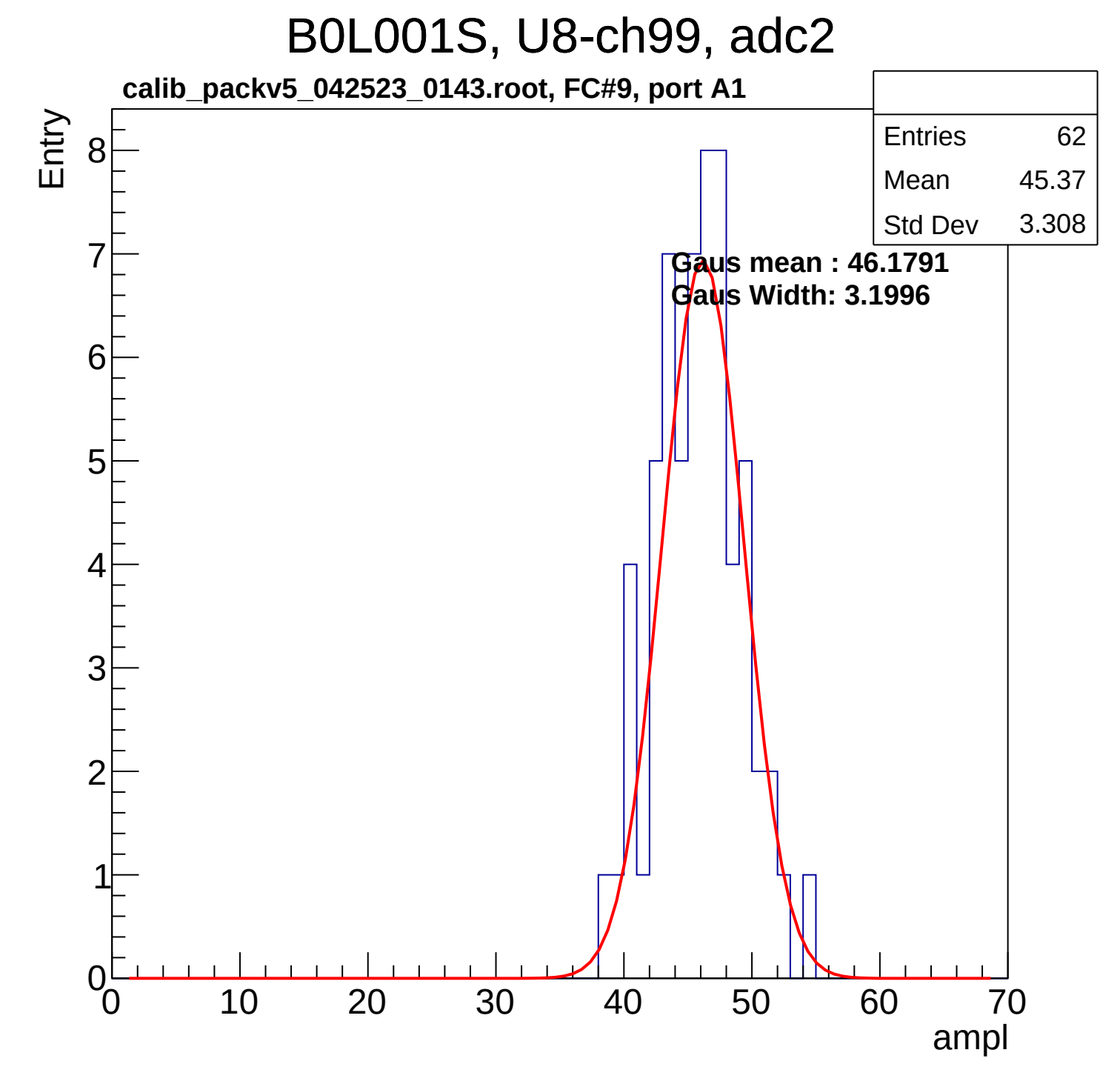
60
70


B0L001S, U8-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 38.06
Std Dev 4.037
Gaus mean : 38.3384
Gaus Width: 4.1394
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 45.37
Std Dev 3.308
Gaus mean : 46.1791
Gaus Width: 3.1996
Entry
ampl
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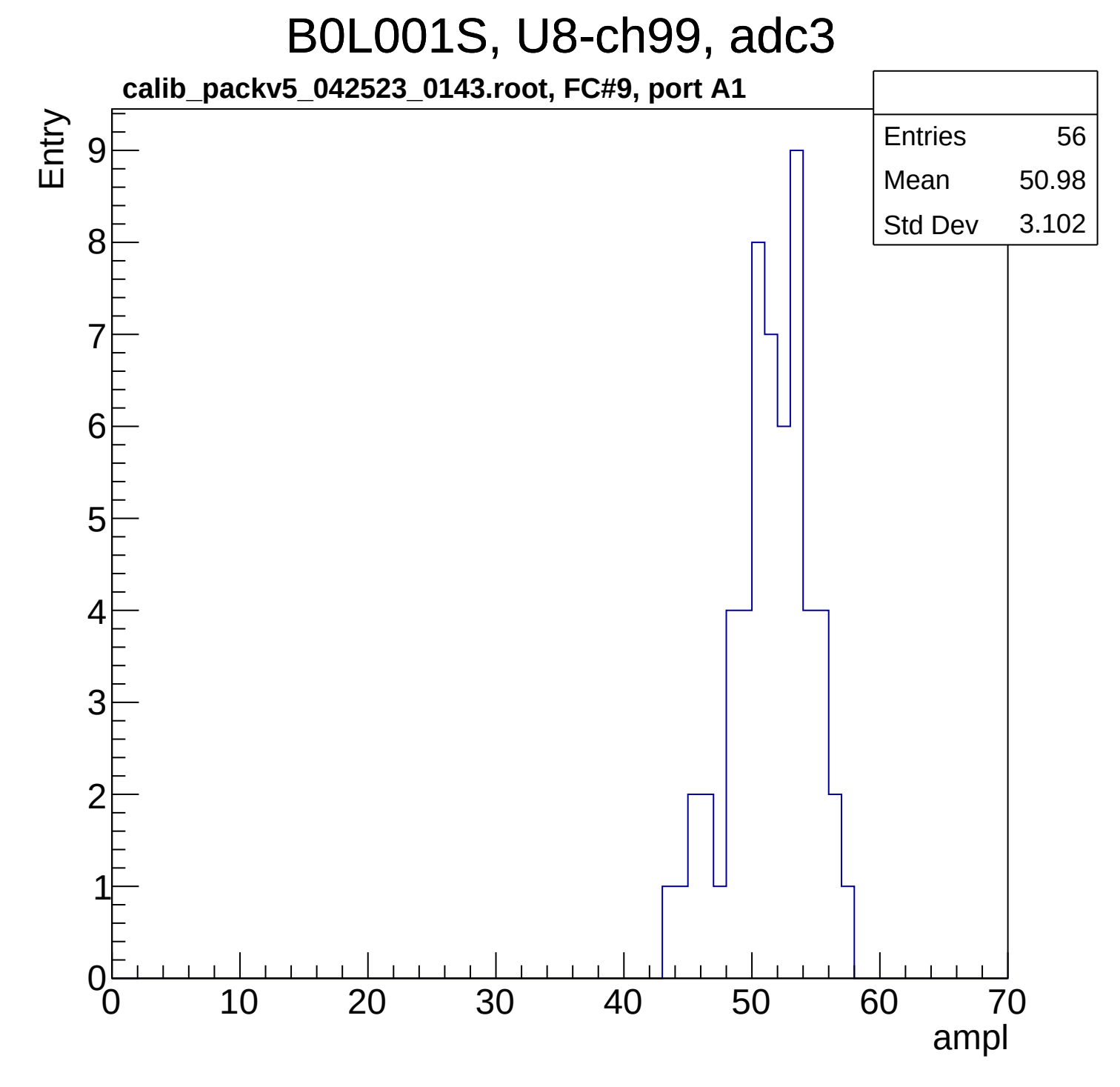
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.98
Std Dev 3.102
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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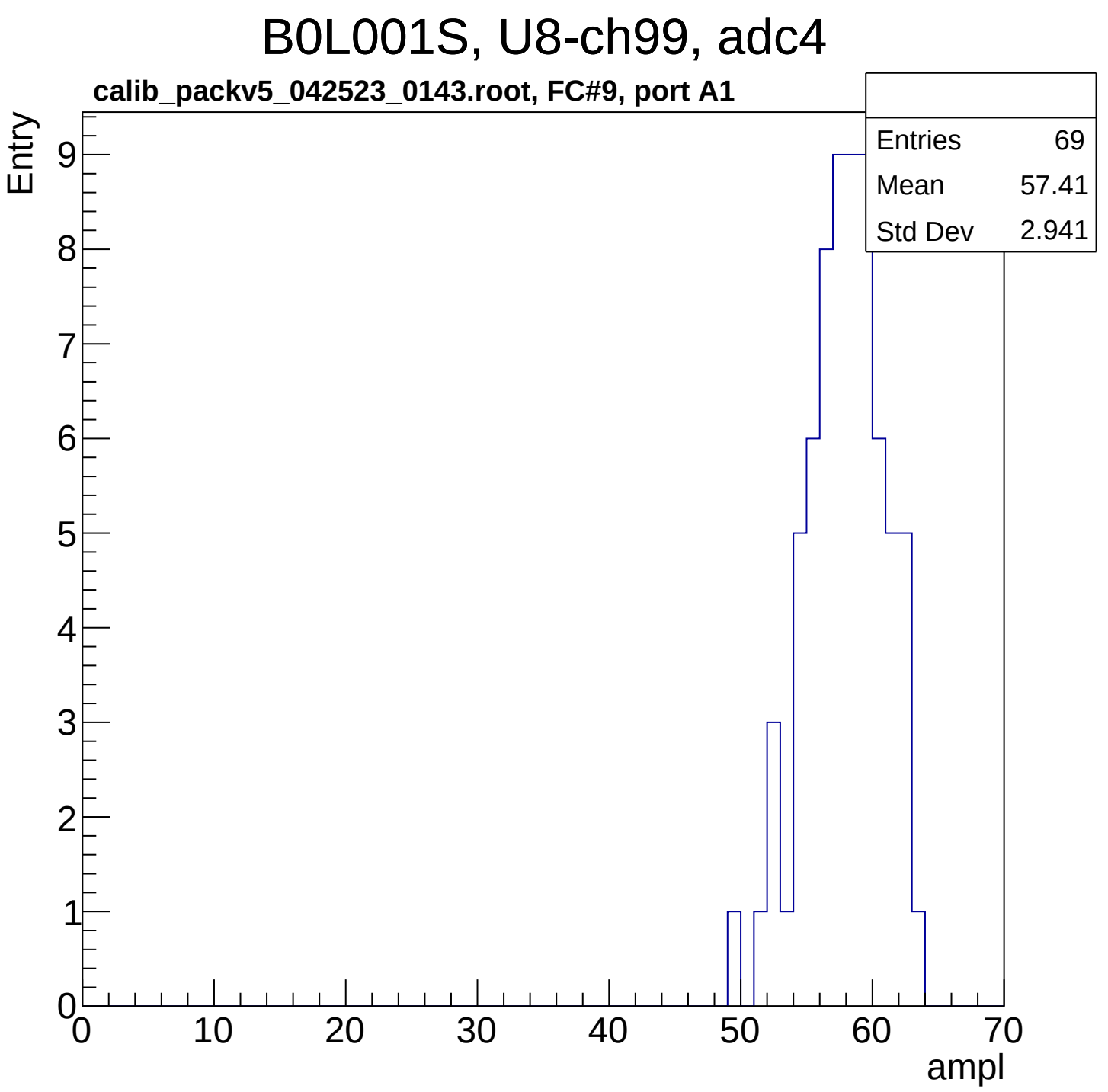
B0L001S, U8-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 57.41
Std Dev 2.941
Entry
ampl
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3
4
5
6
7
8
9
0
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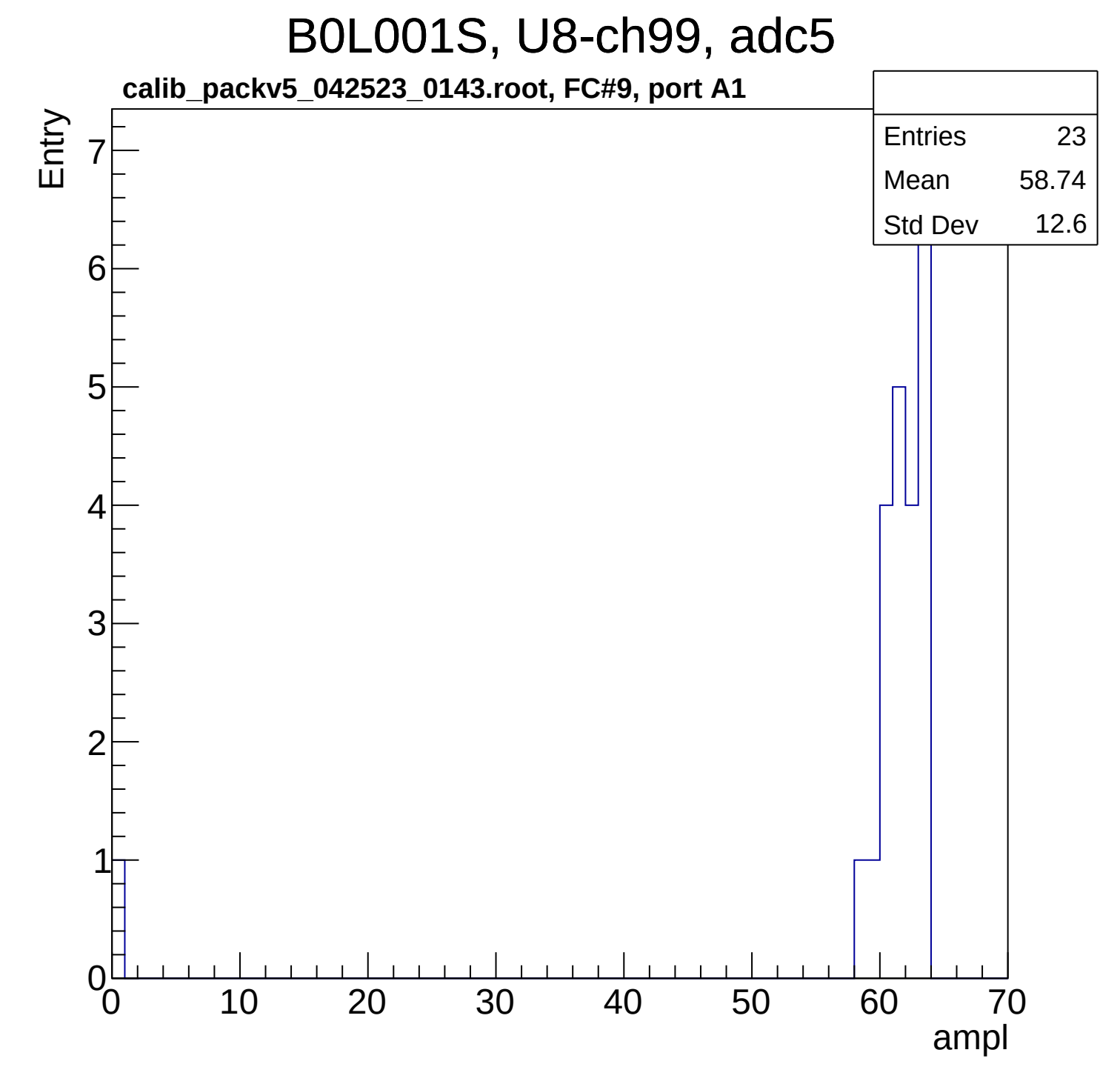
B0L001S, U8-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 58.74
Std Dev 12.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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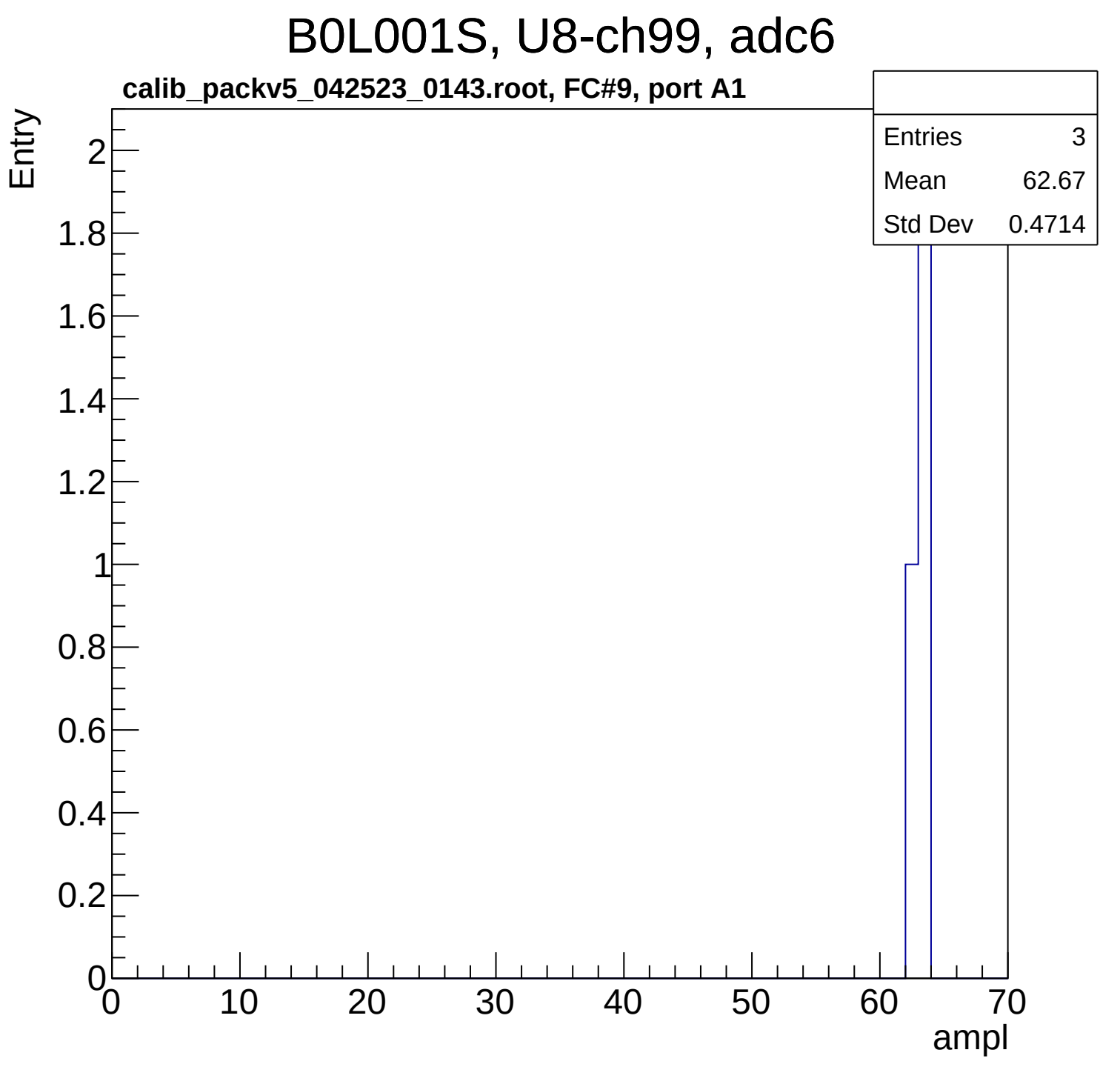
B0L001S, U8-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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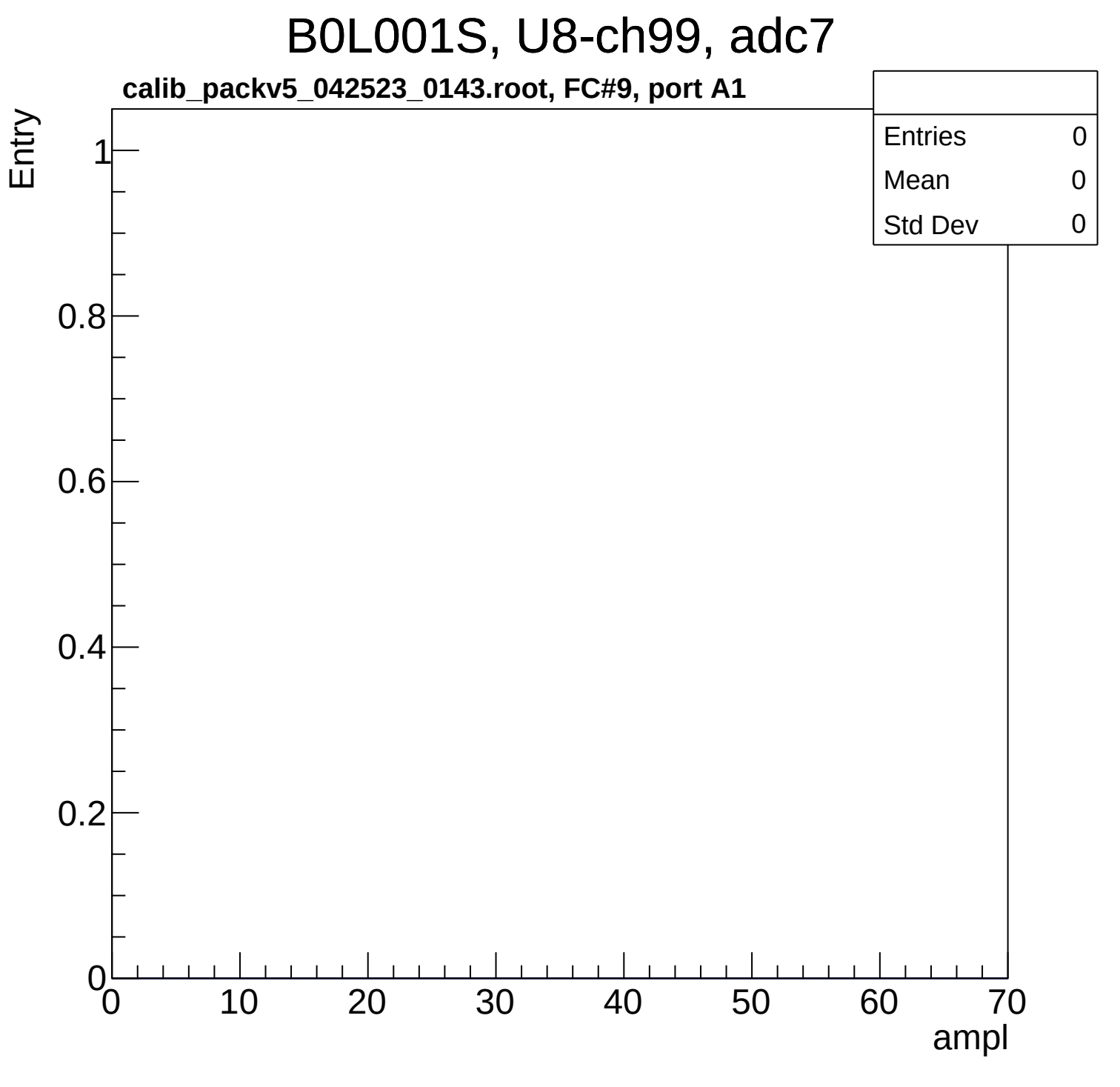
B0L001S, U8-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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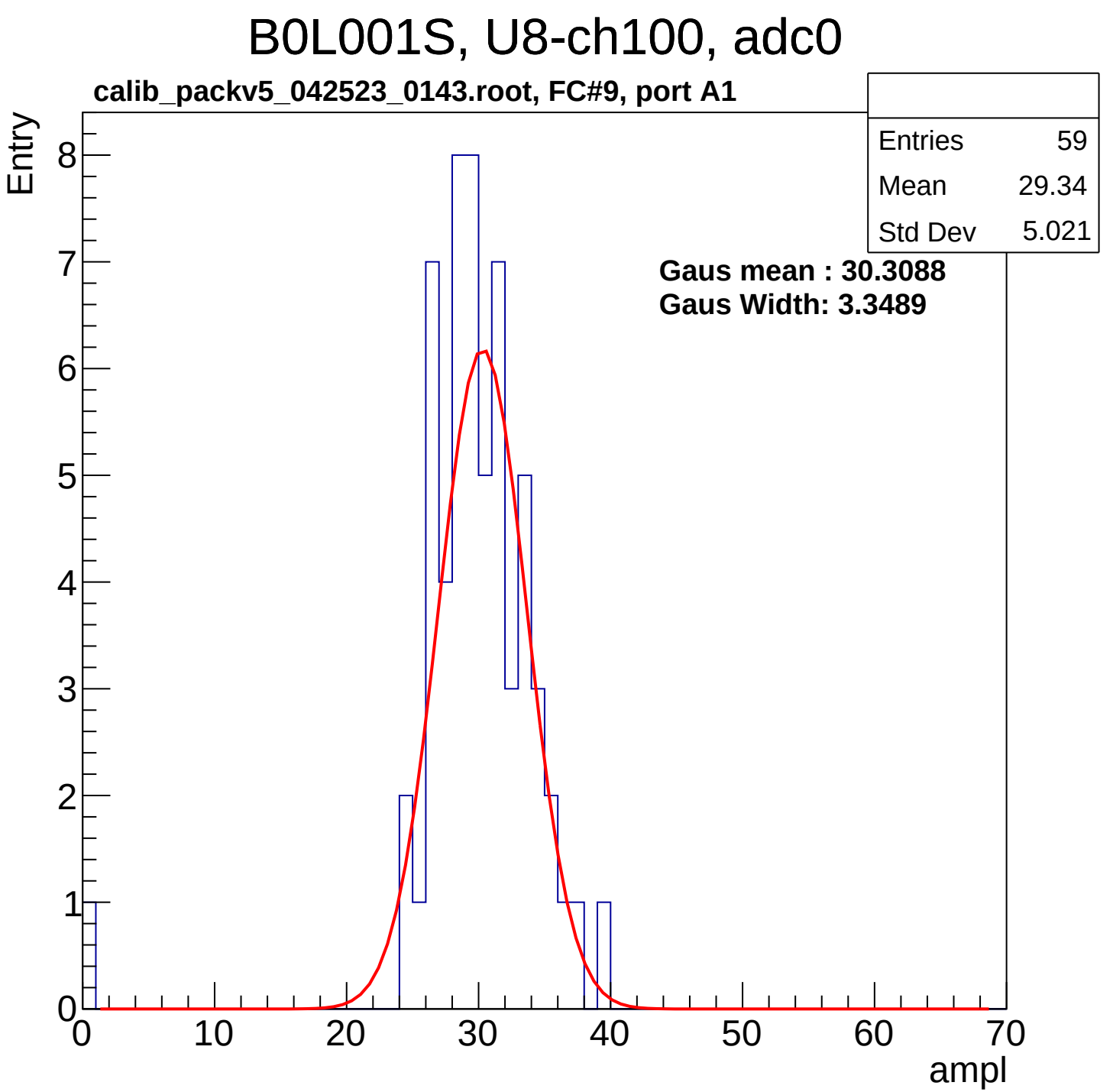
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.34
Std Dev 5.021
Gaus mean : 30.3088
Gaus Width: 3.3489
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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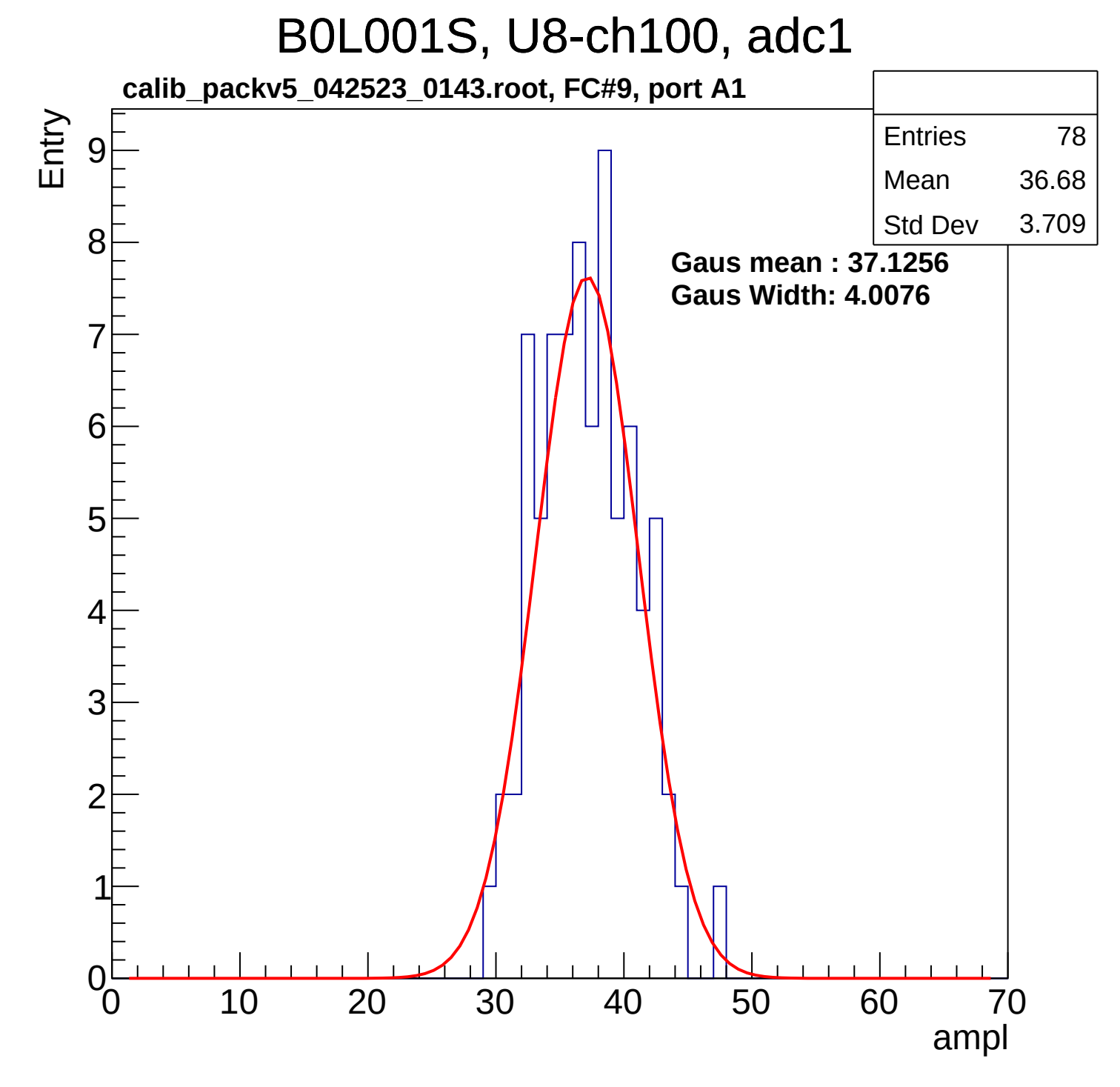
B0L001S, U8-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 36.68
Std Dev 3.709
Gaus mean : 37.1256
Gaus Width: 4.0076
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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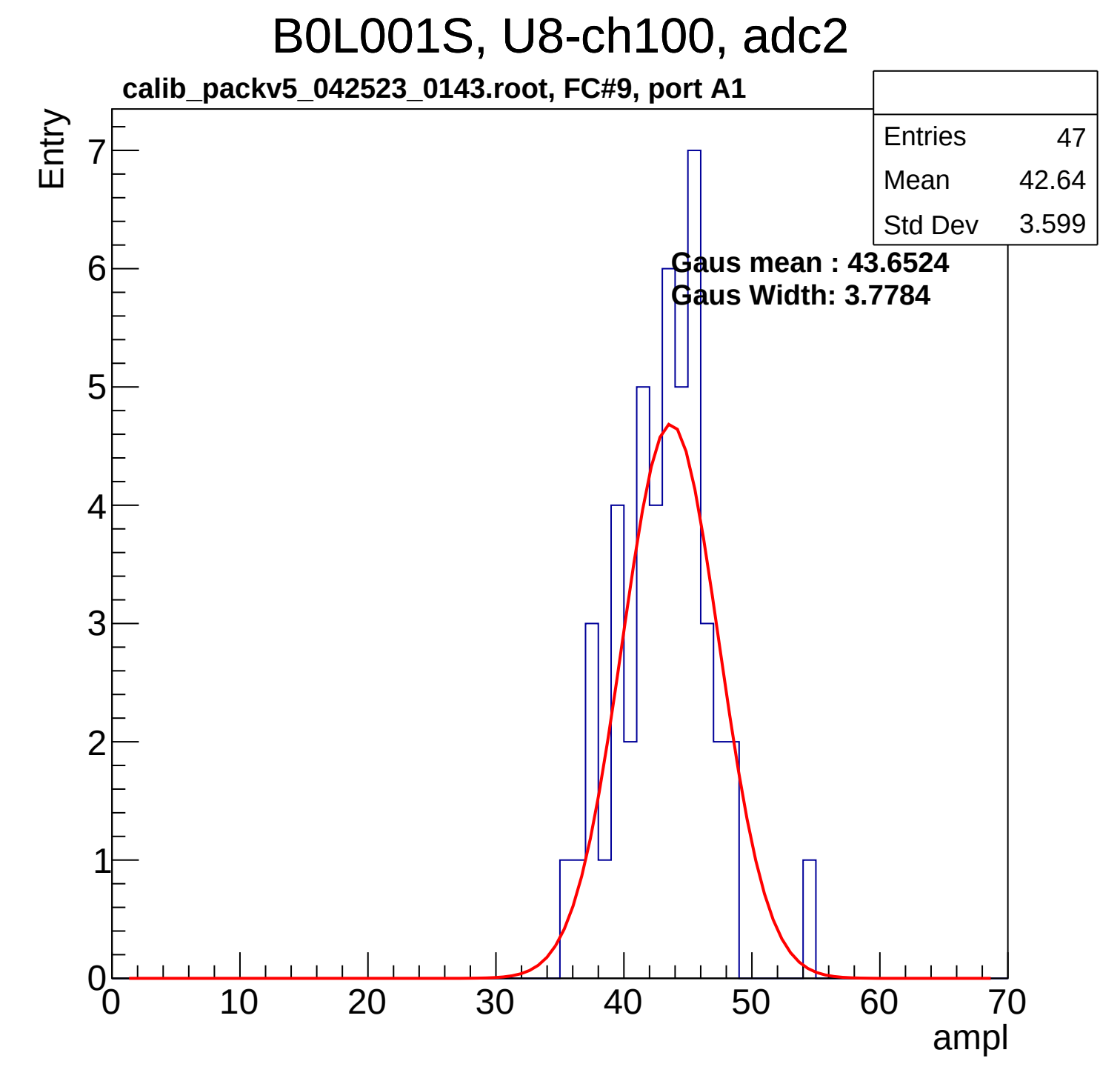
B0L001S, U8-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 42.64
Std Dev 3.599
Gaus mean : 43.6524
Gaus Width: 3.7784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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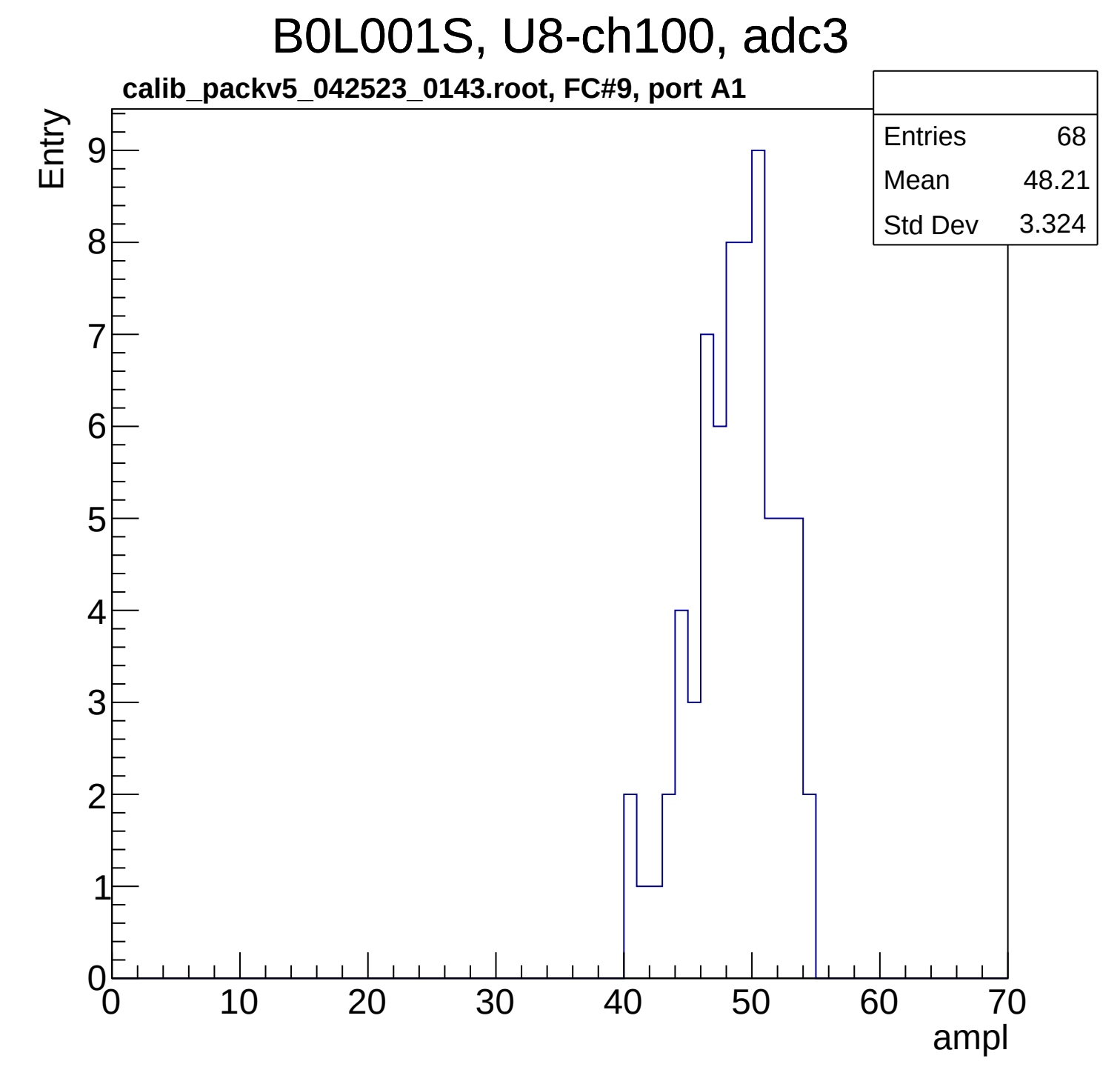
B0L001S, U8-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.21
Std Dev 3.324
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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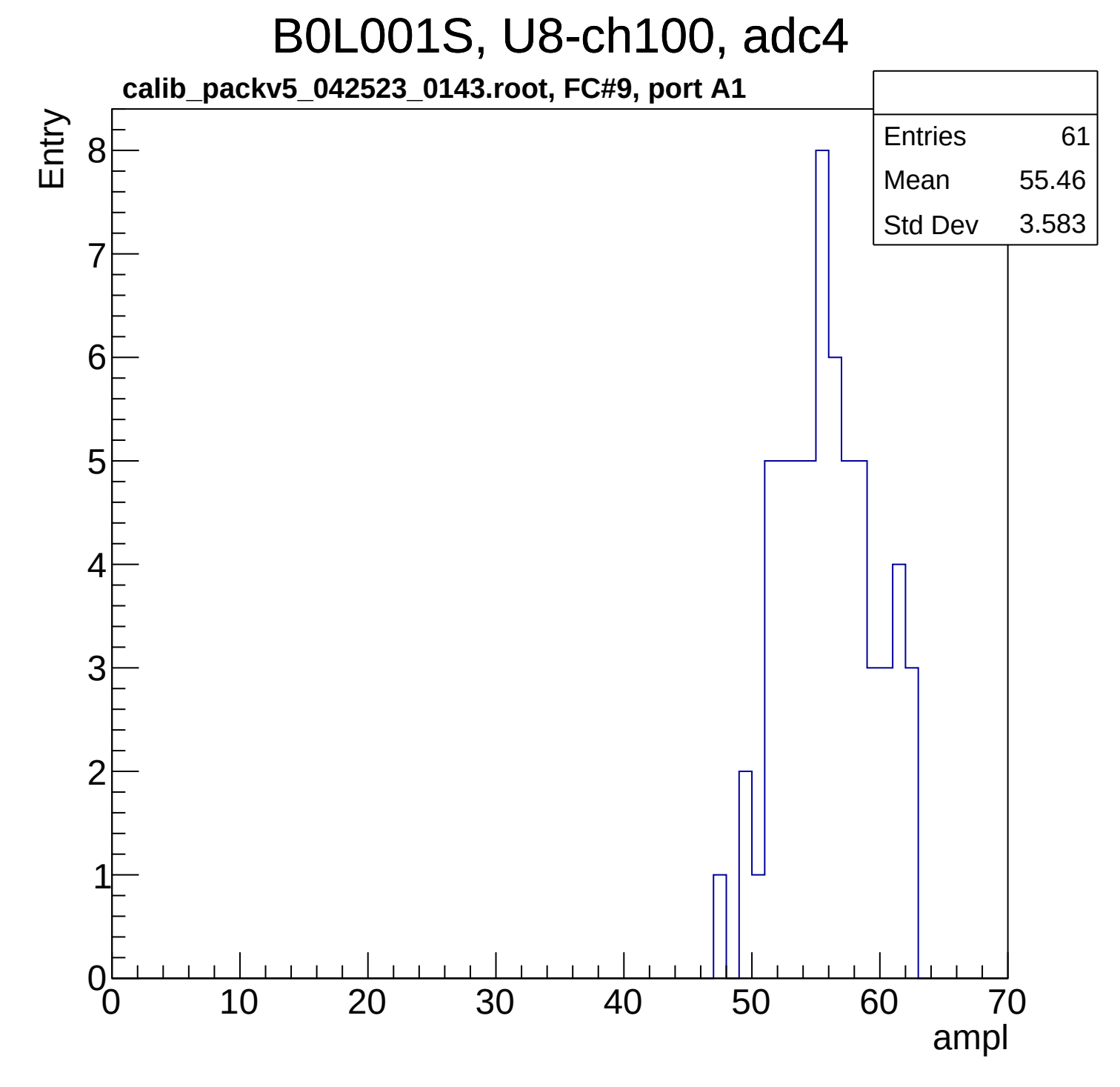
B0L001S, U8-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 55.46
Std Dev 3.583
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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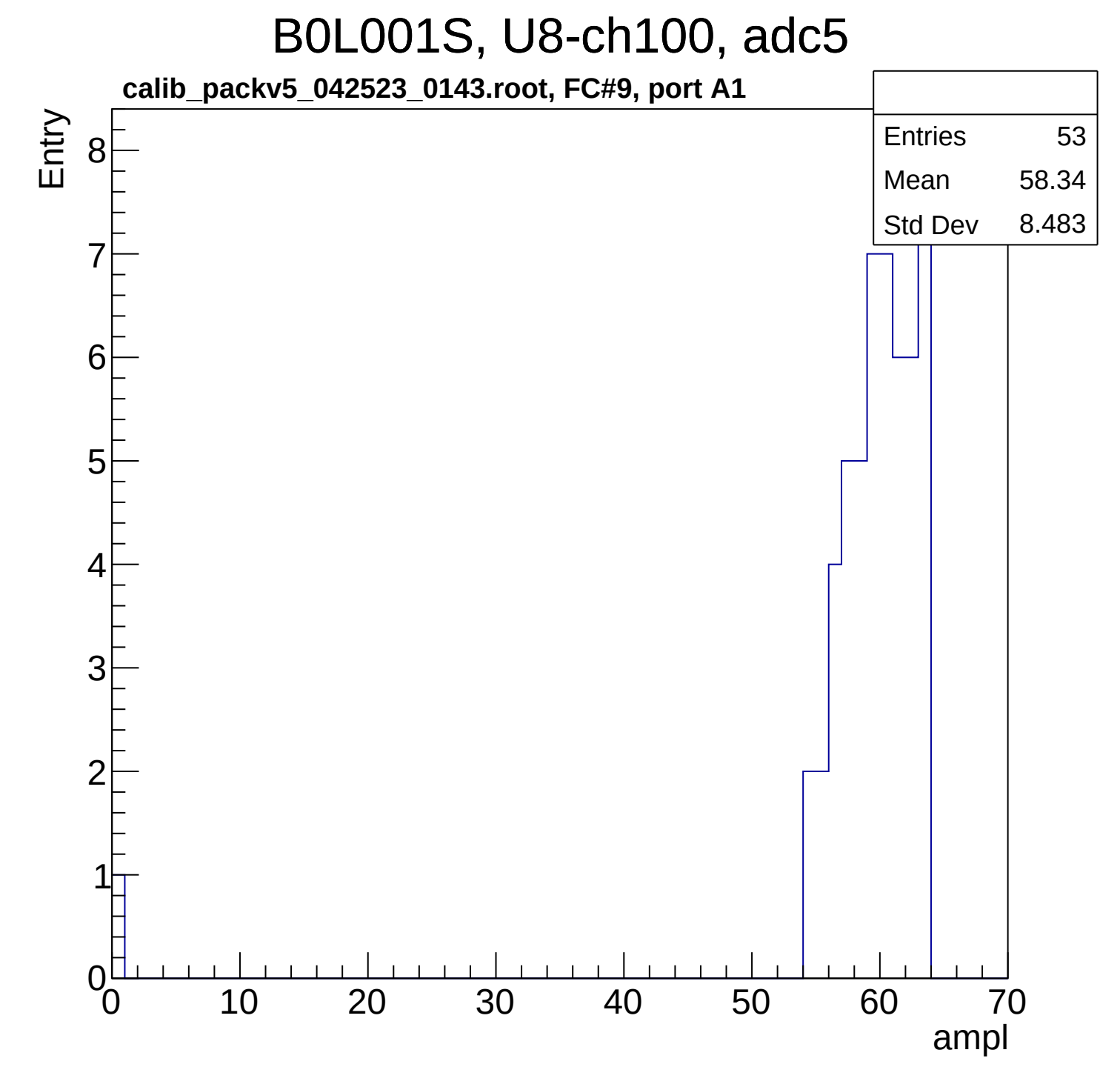
B0L001S, U8-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.34
Std Dev 8.483
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
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60
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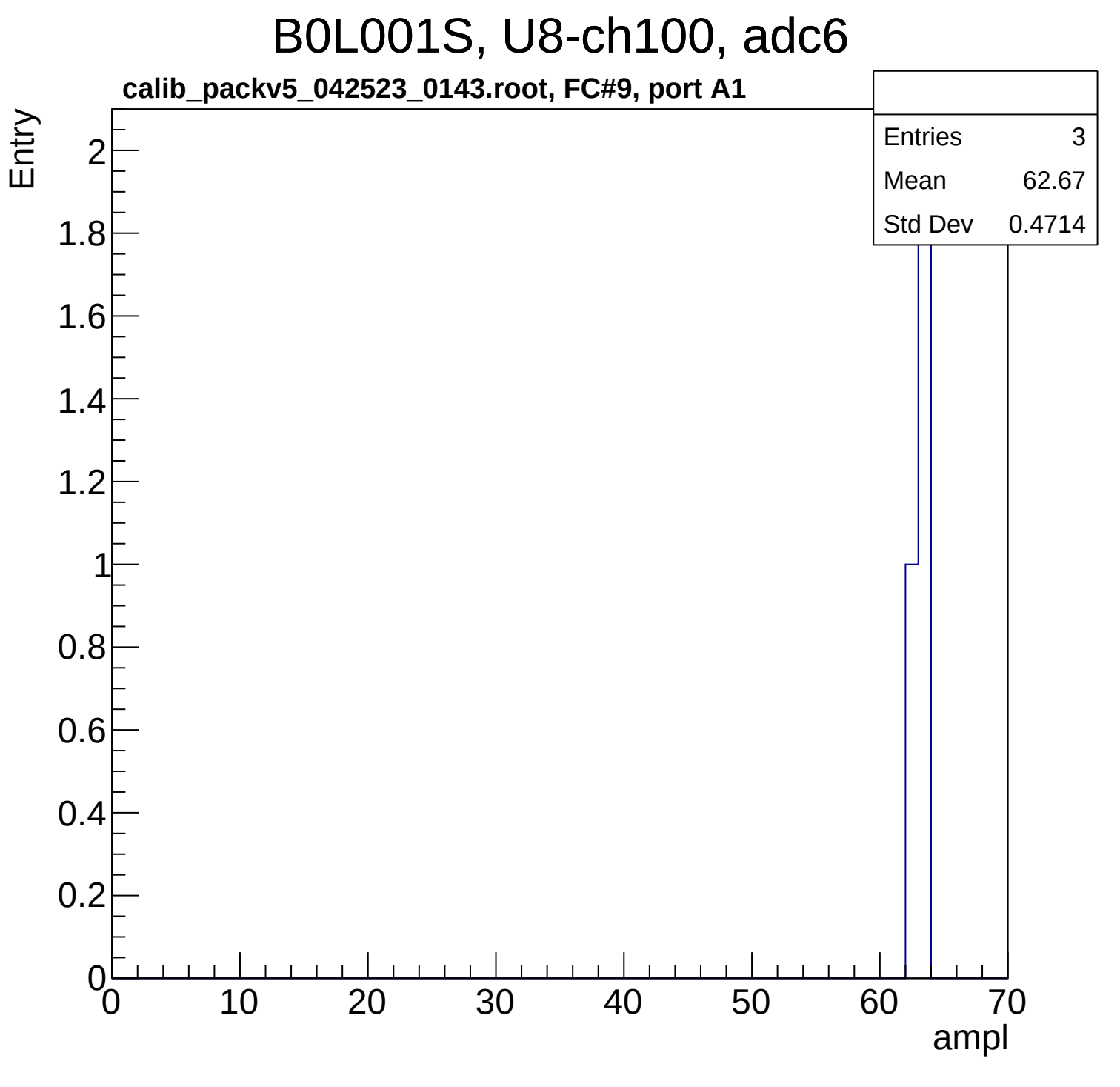
B0L001S, U8-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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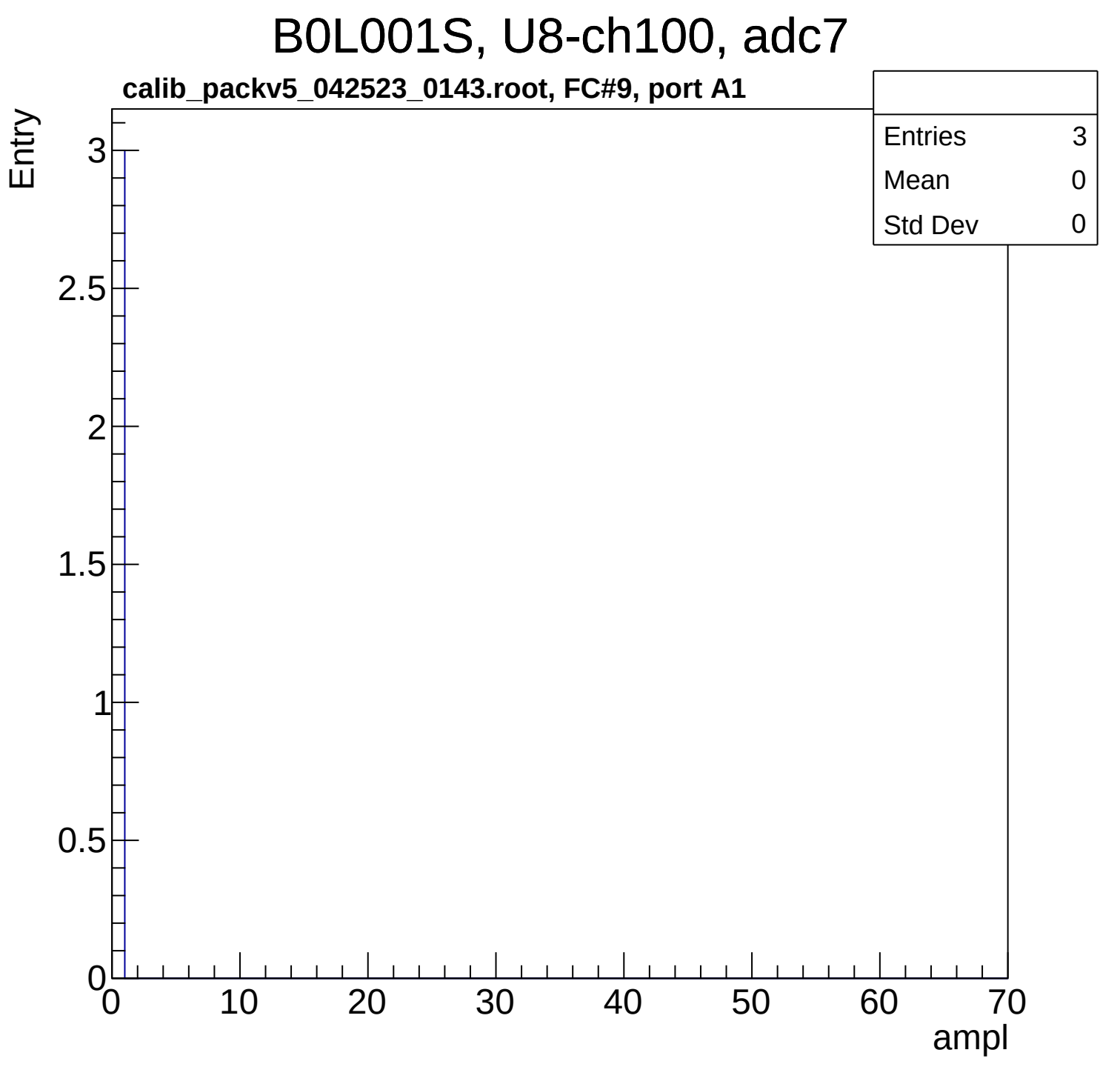
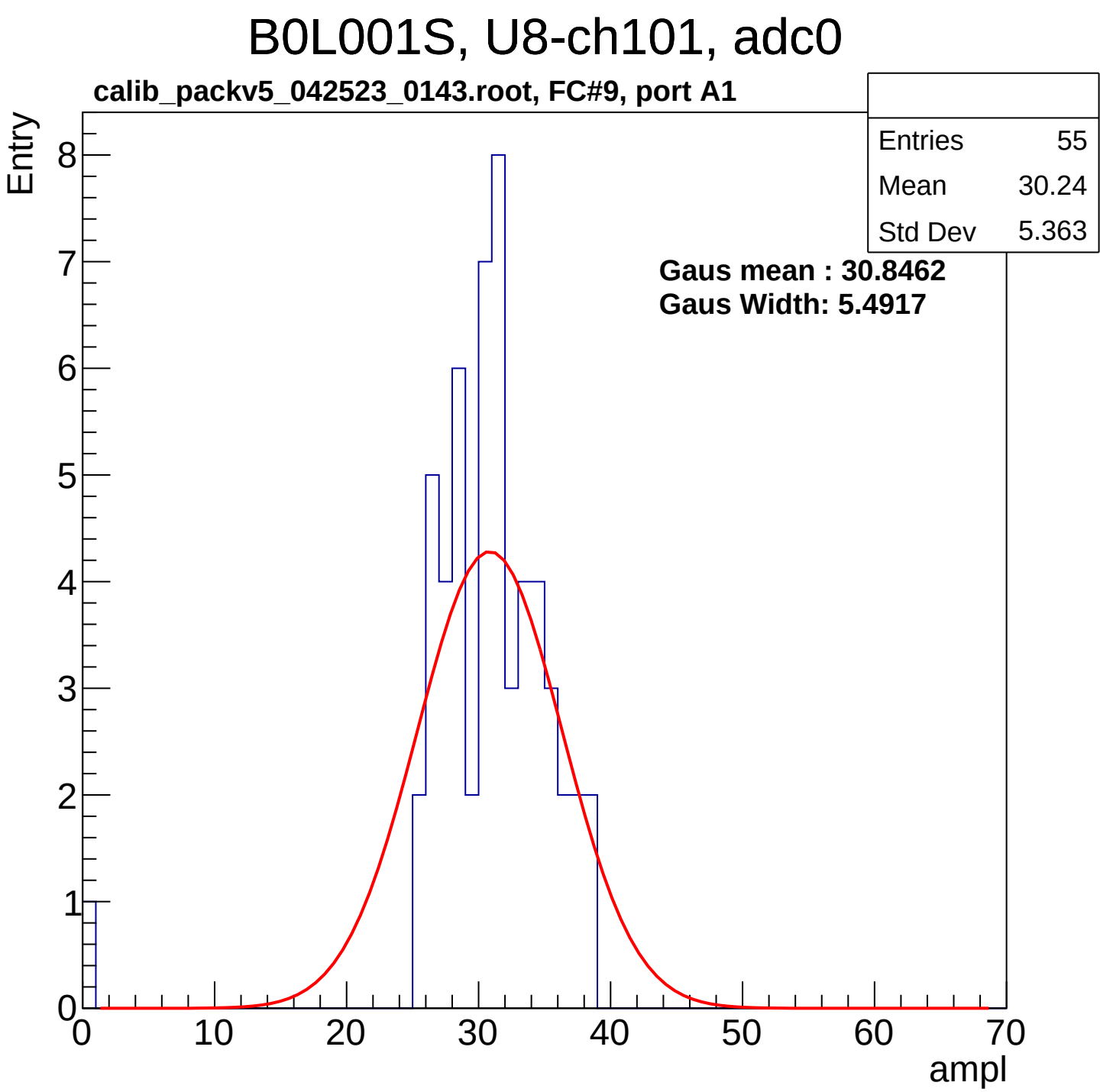

B0L001S, U8-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.24
Std Dev 5.363
Gaus mean : 30.8462
Gaus Width: 5.4917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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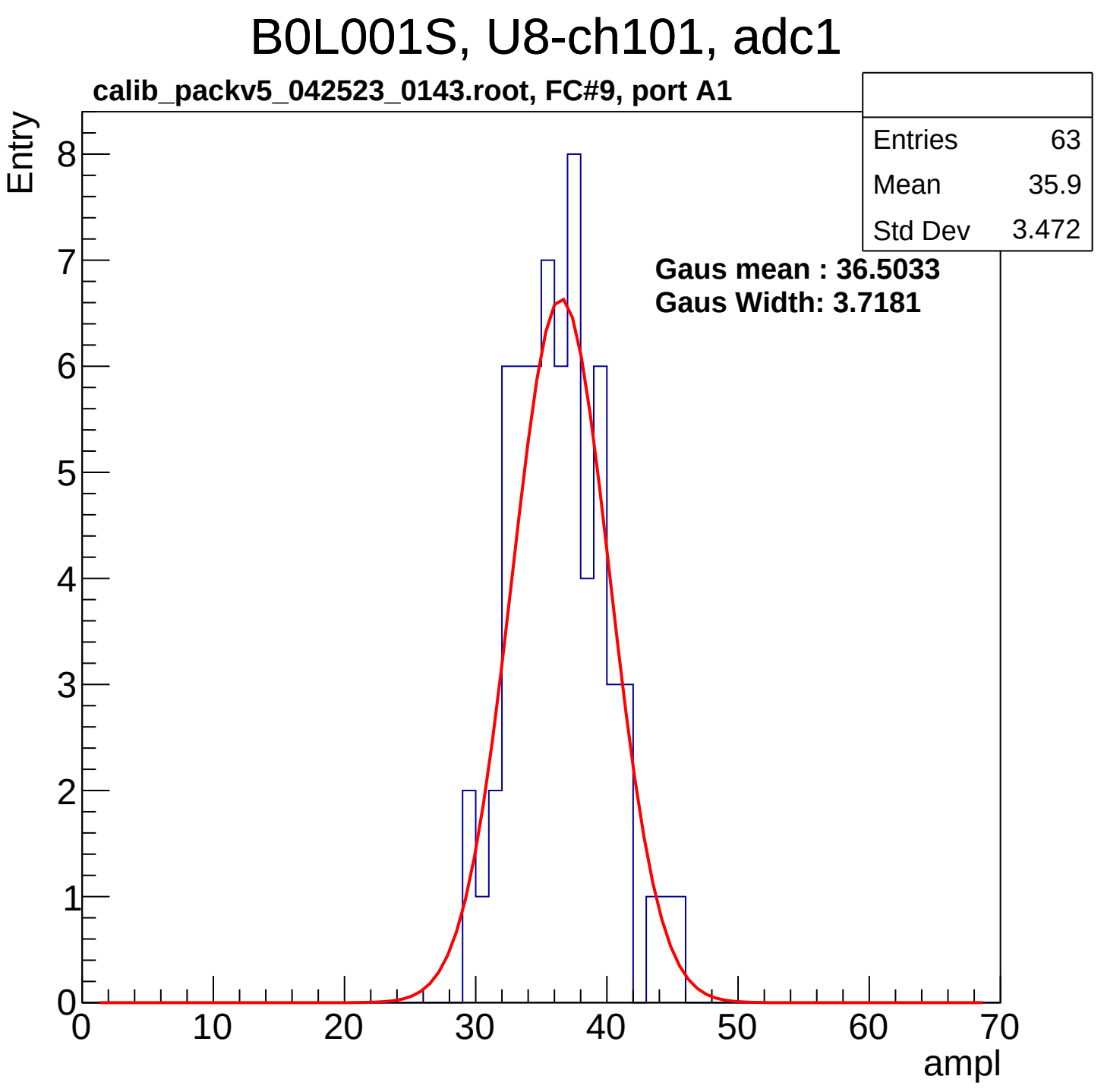
B0L001S, U8-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 35.9
Std Dev 3.472
Gaus mean : 36.5033
Gaus Width: 3.7181
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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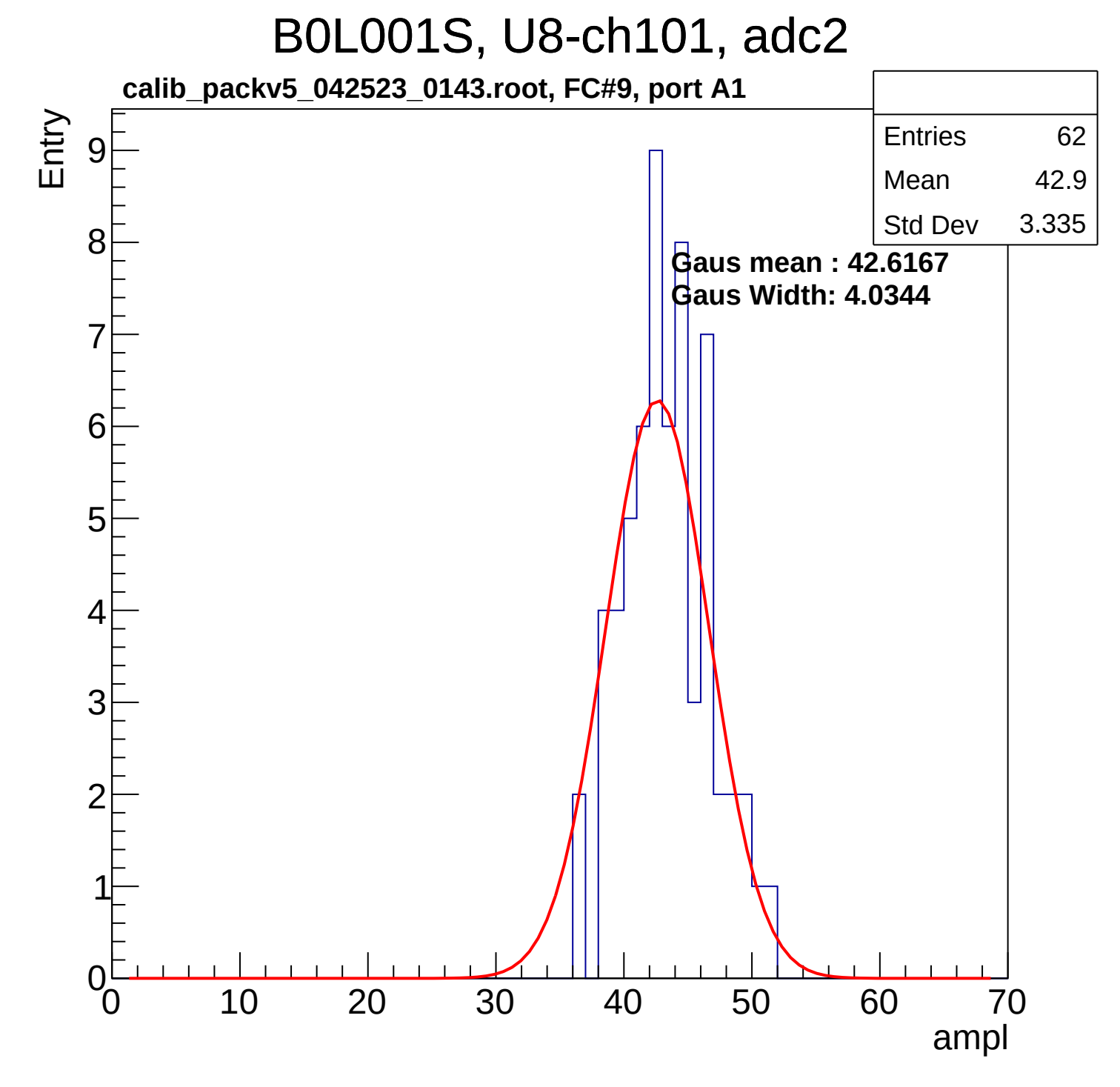
B0L001S, U8-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 42.9
Std Dev 3.335
Gaus mean : 42.6167
Gaus Width: 4.0344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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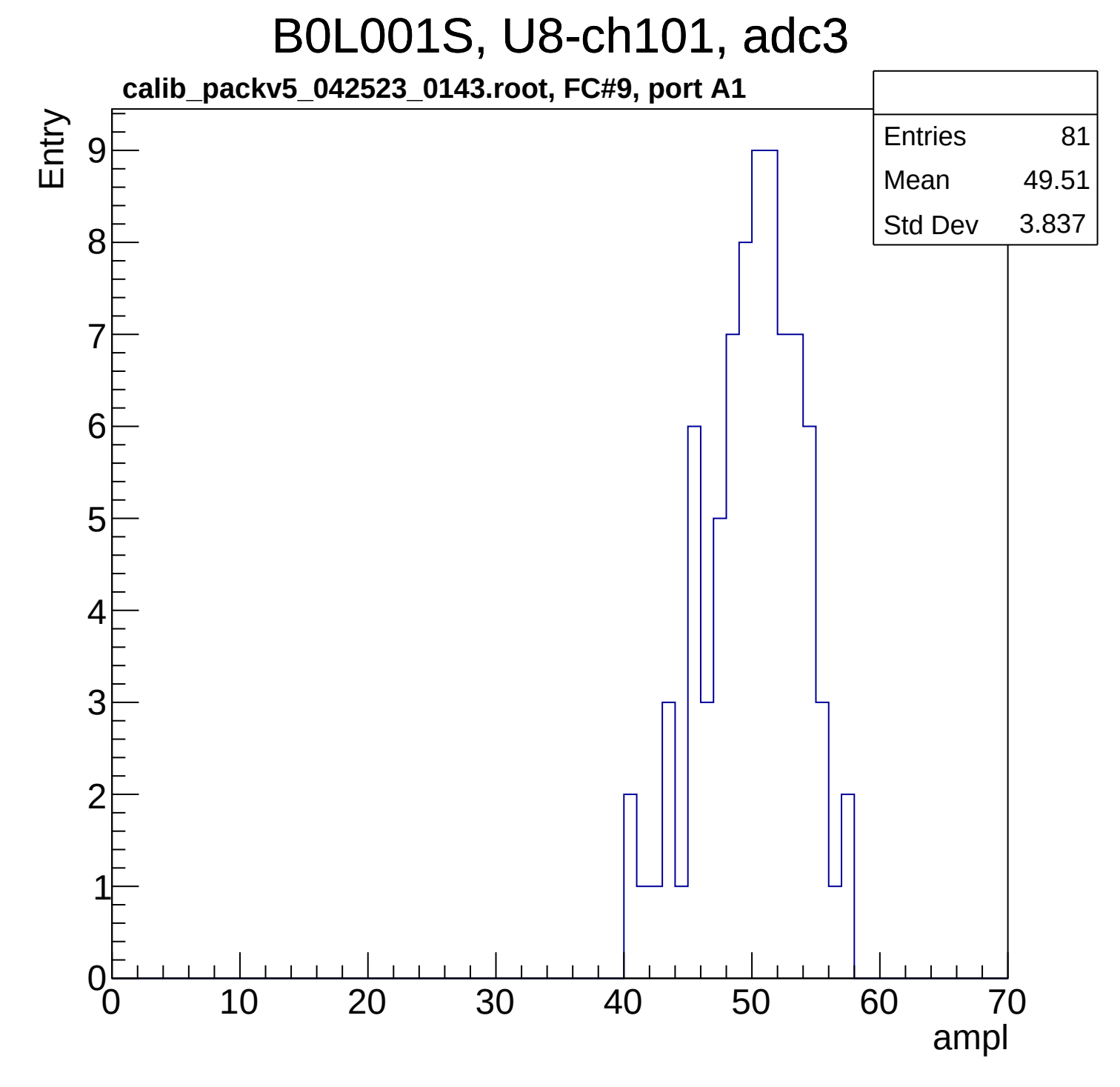
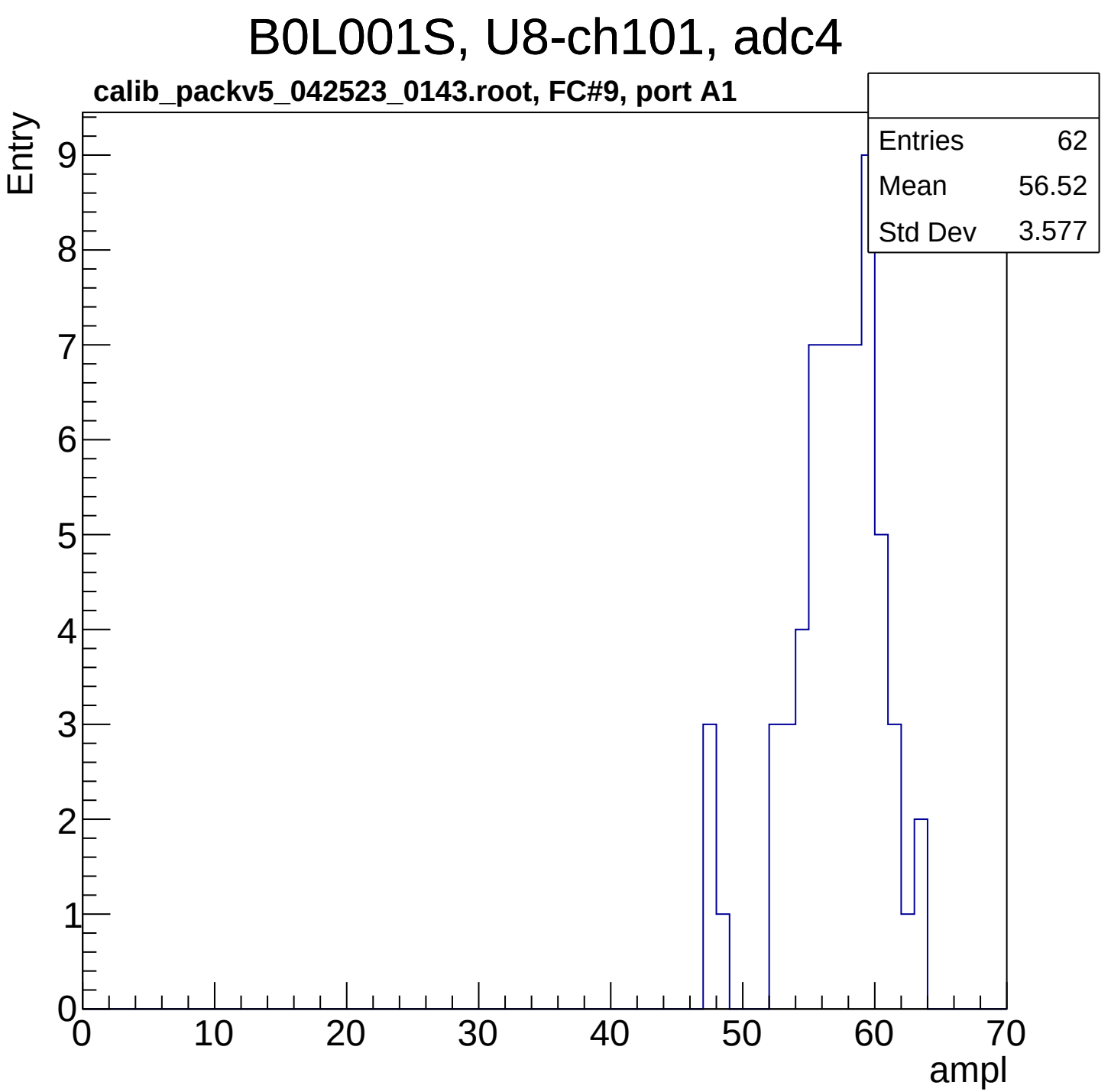

B0L001S, U8-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 49.51
Std Dev 3.837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.52
Std Dev 3.577
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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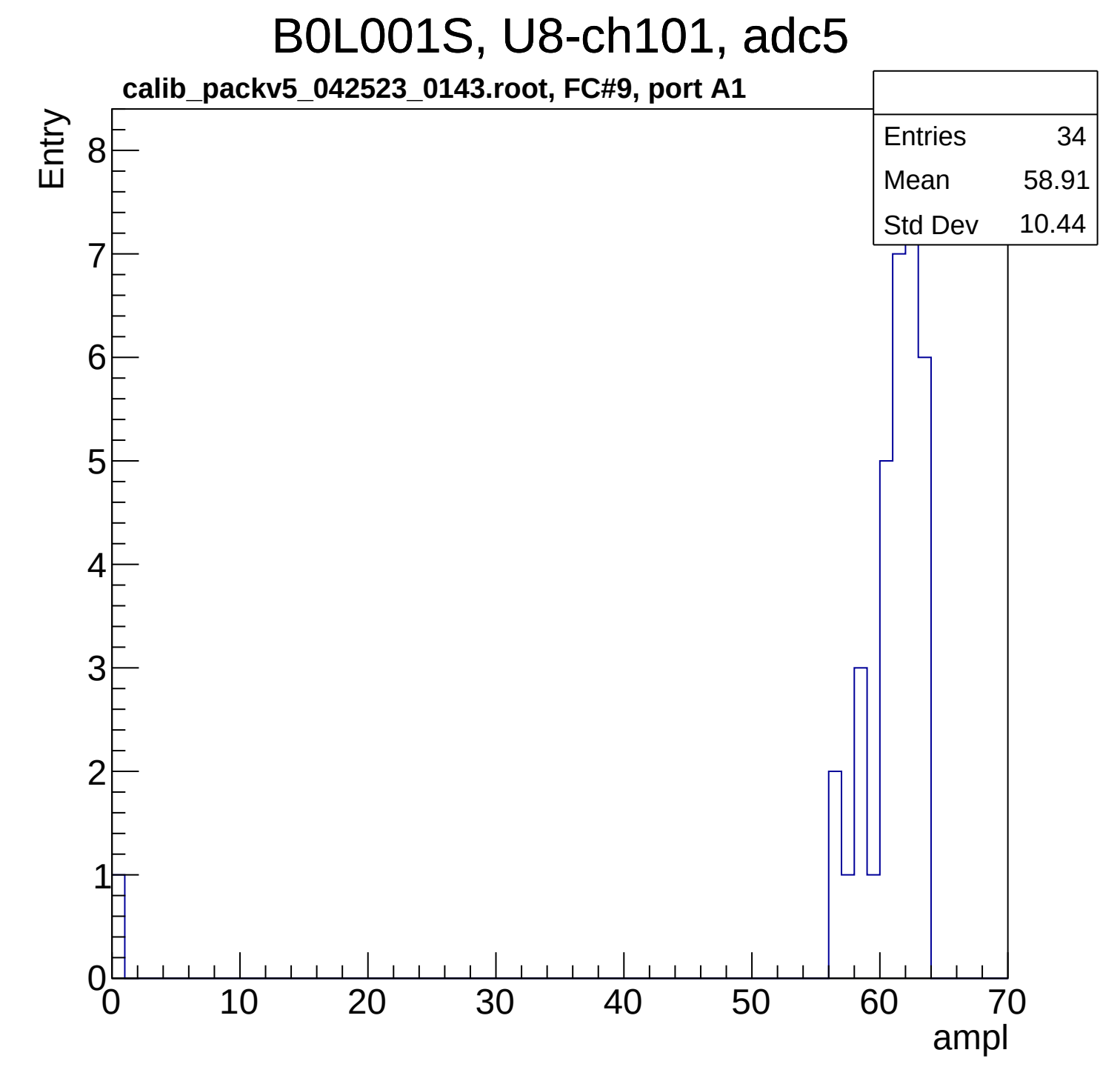
B0L001S, U8-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.91
Std Dev 10.44
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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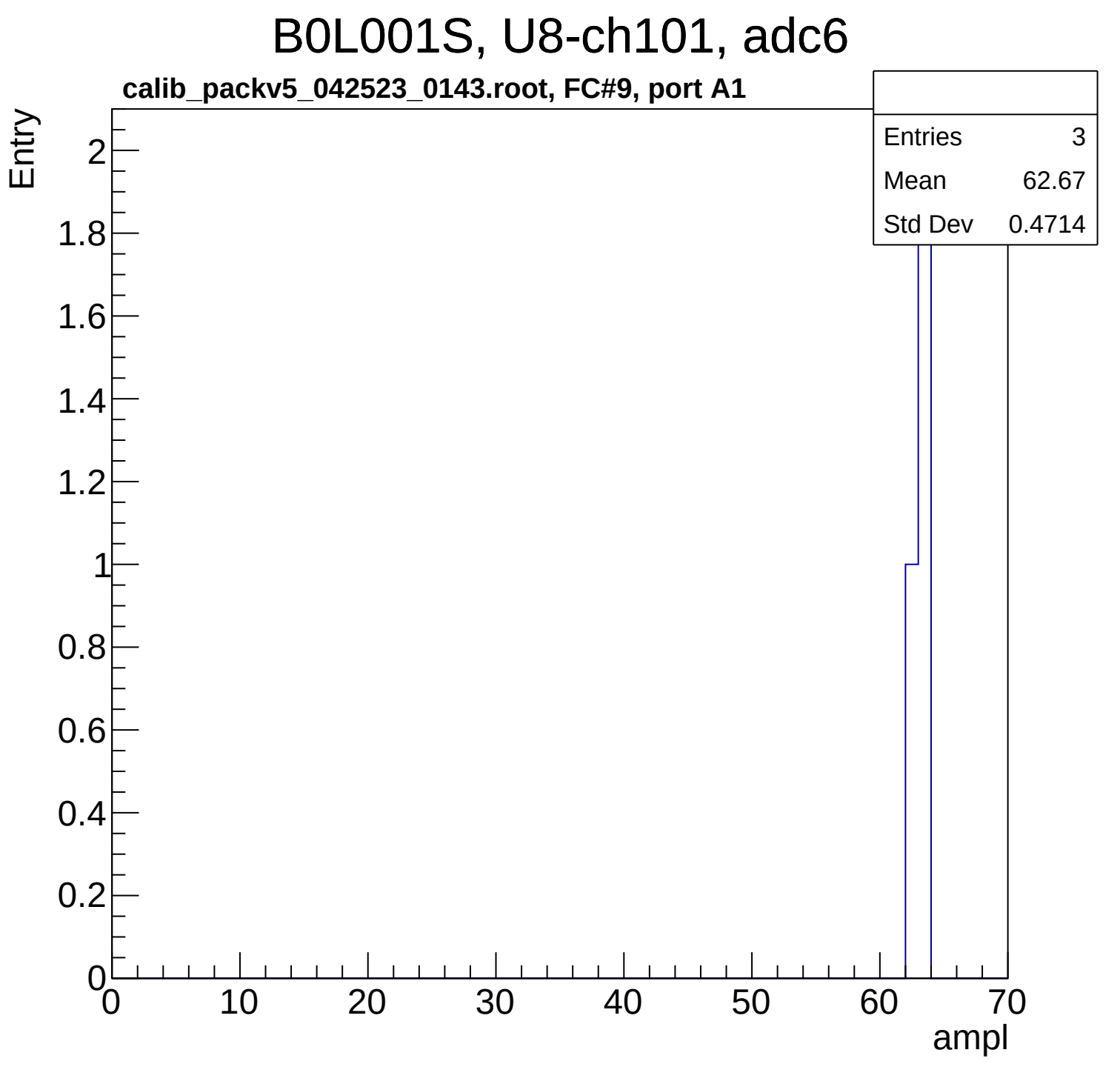
B0L001S, U8-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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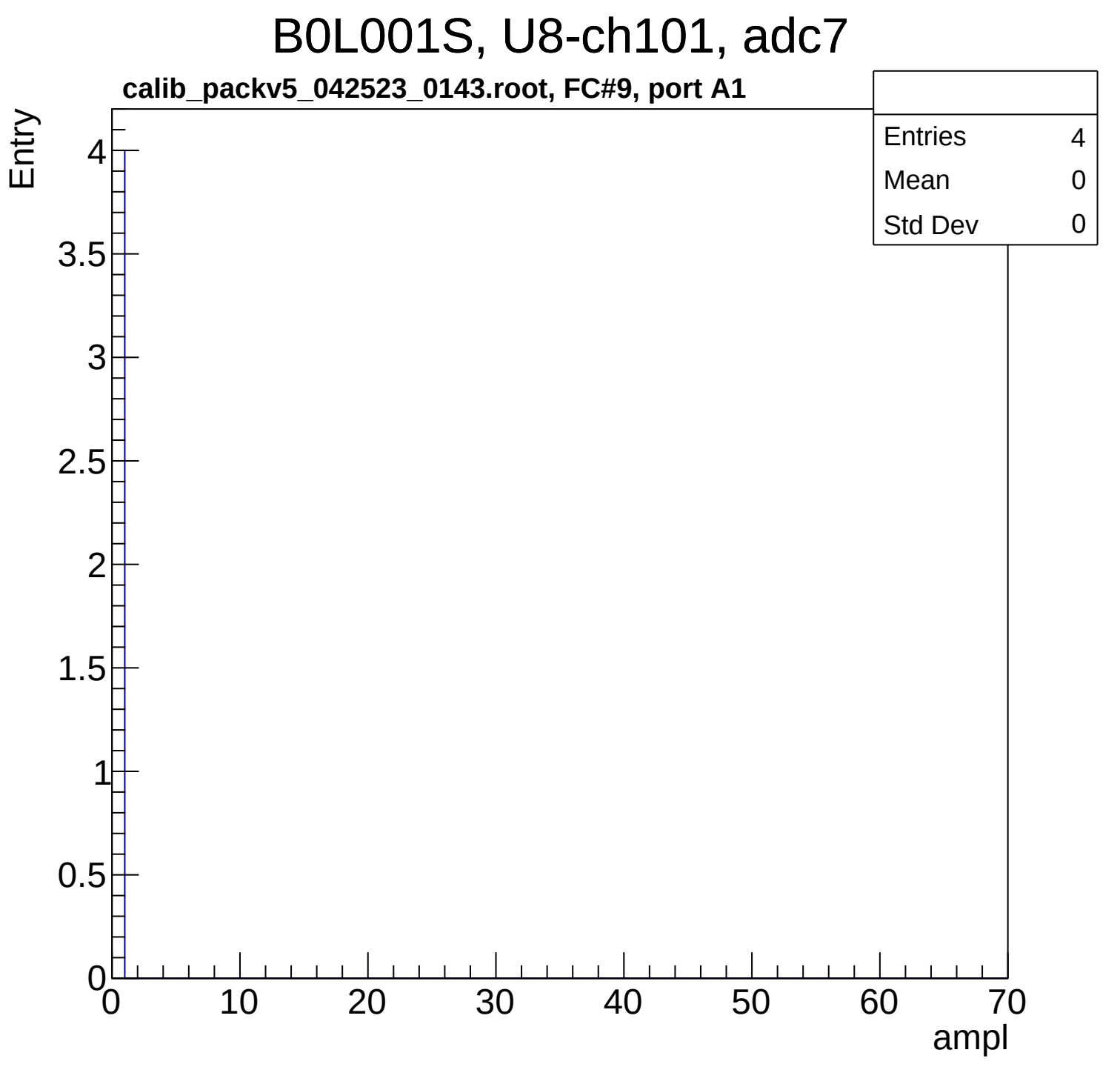
B0L001S, U8-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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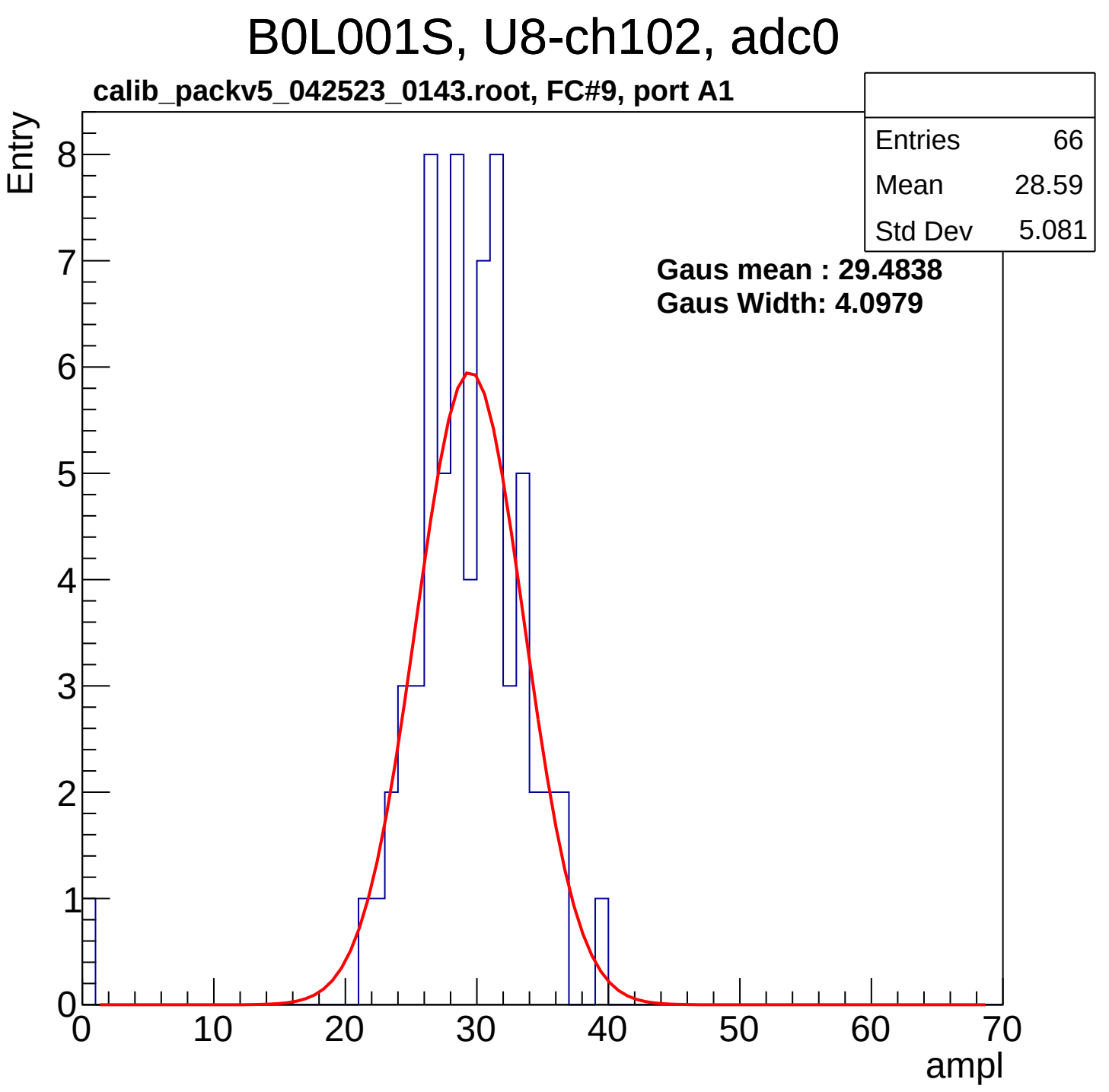
B0L001S, U8-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.59
Std Dev 5.081
Gaus mean : 29.4838
Gaus Width: 4.0979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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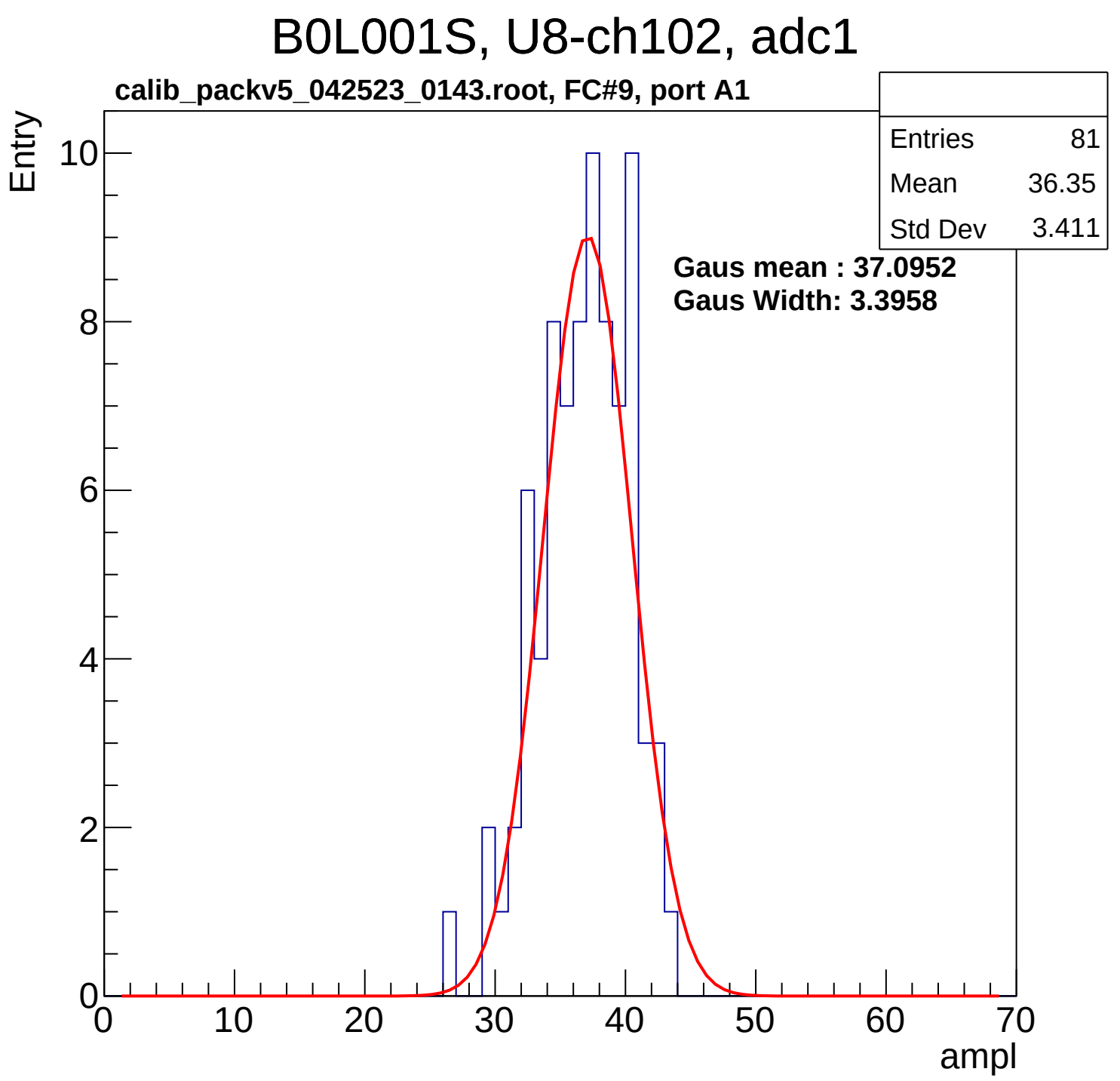
B0L001S, U8-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 36.35
Std Dev 3.411
Gaus mean : 37.0952
Gaus Width: 3.3958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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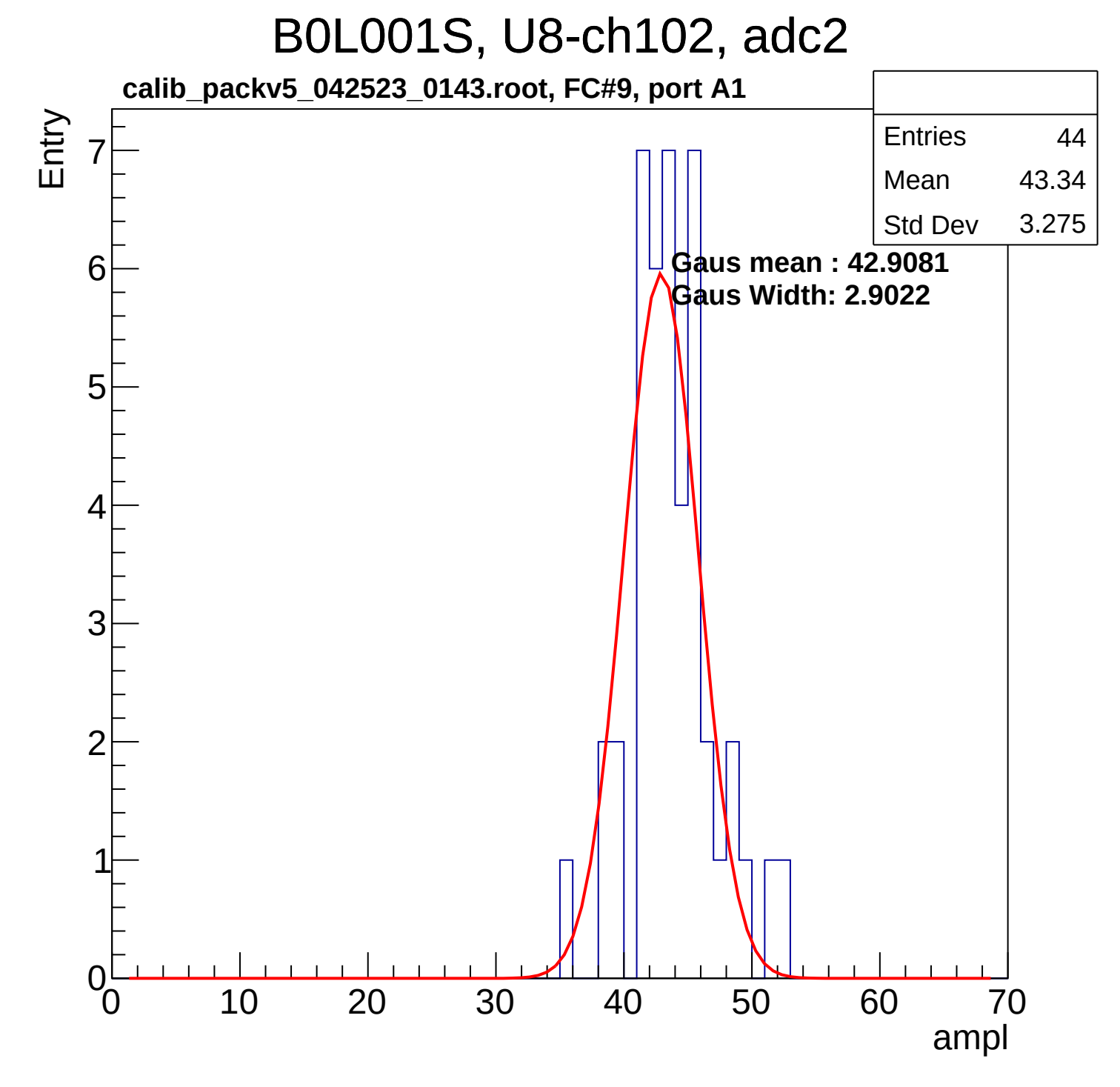
B0L001S, U8-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 43.34
Std Dev 3.275
Gaus mean : 42.9081
Gaus Width: 2.9022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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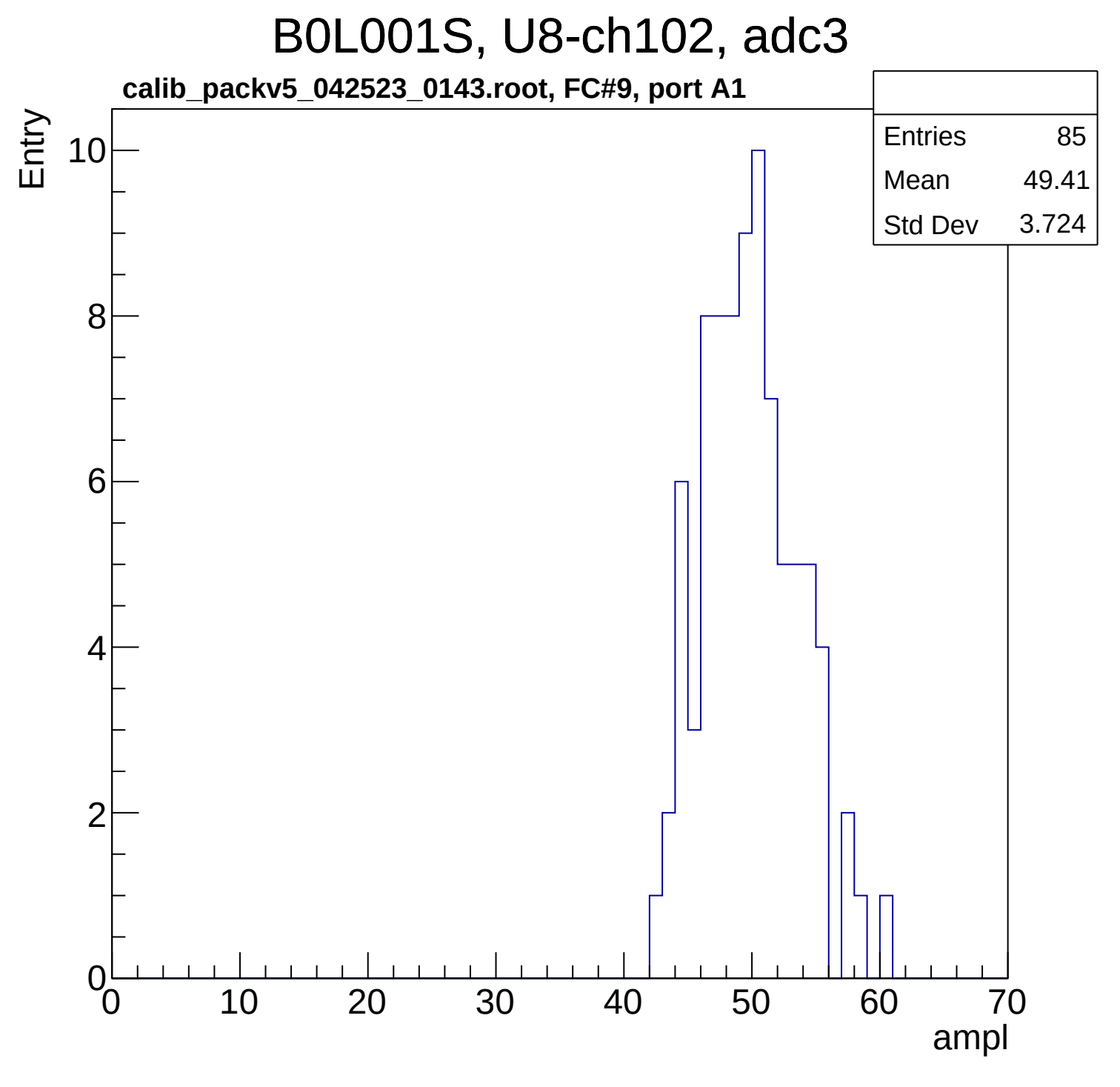
B0L001S, U8-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 49.41
Std Dev 3.724
Entry
ampl
0
2
4
6
8
10
0
10
20
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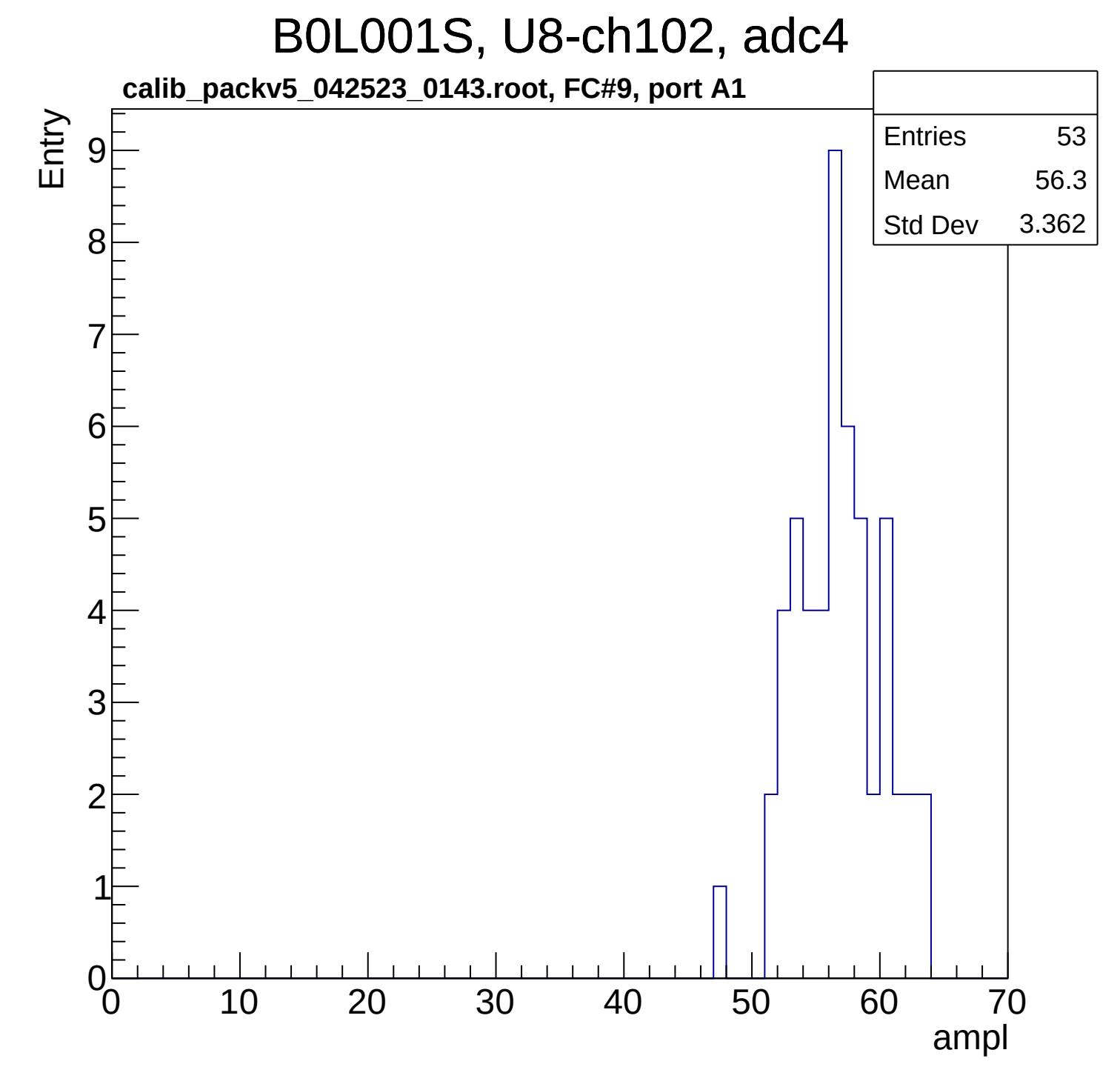
40
50
60
70


B0L001S, U8-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.3
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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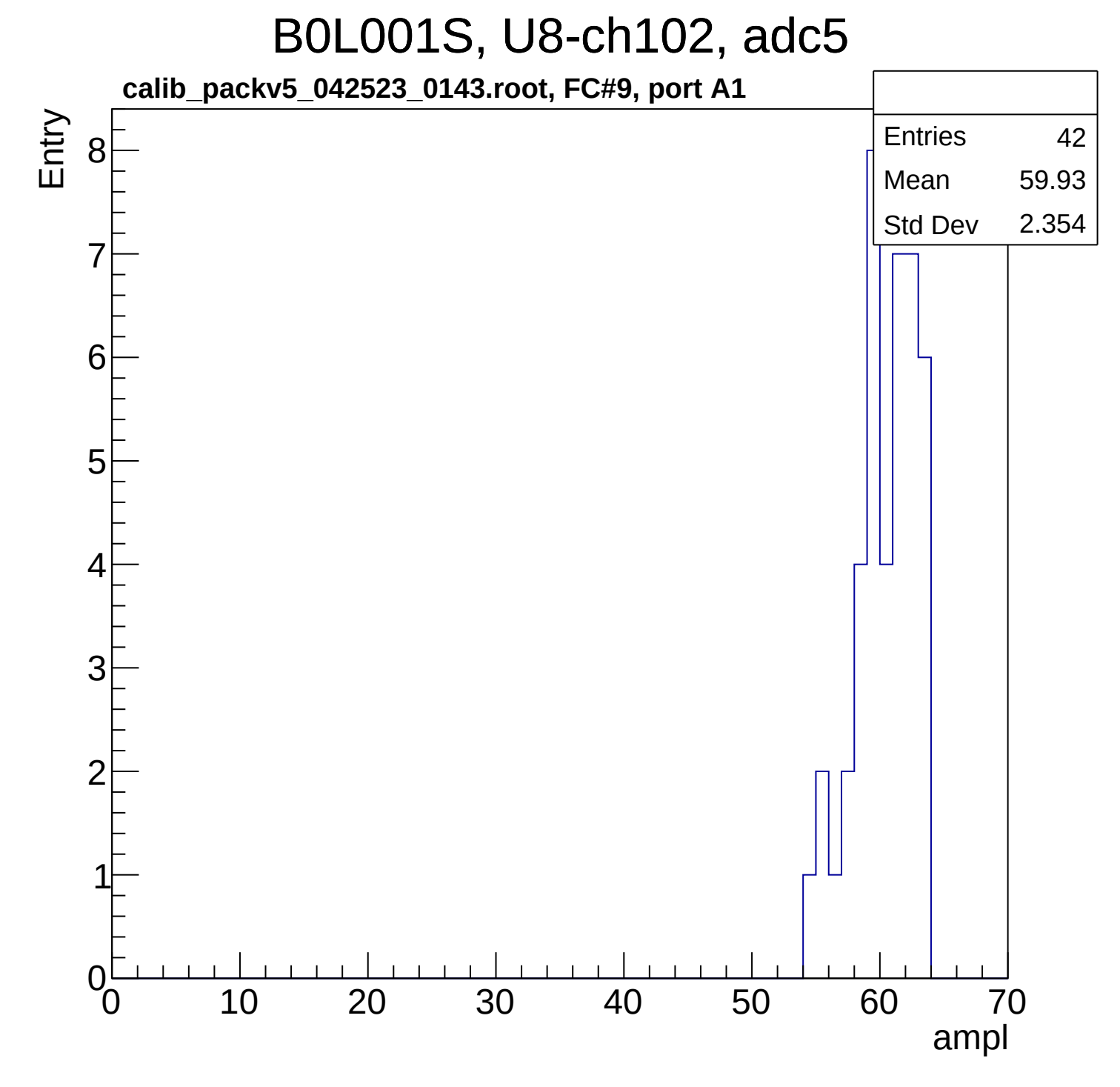
B0L001S, U8-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.93
Std Dev 2.354
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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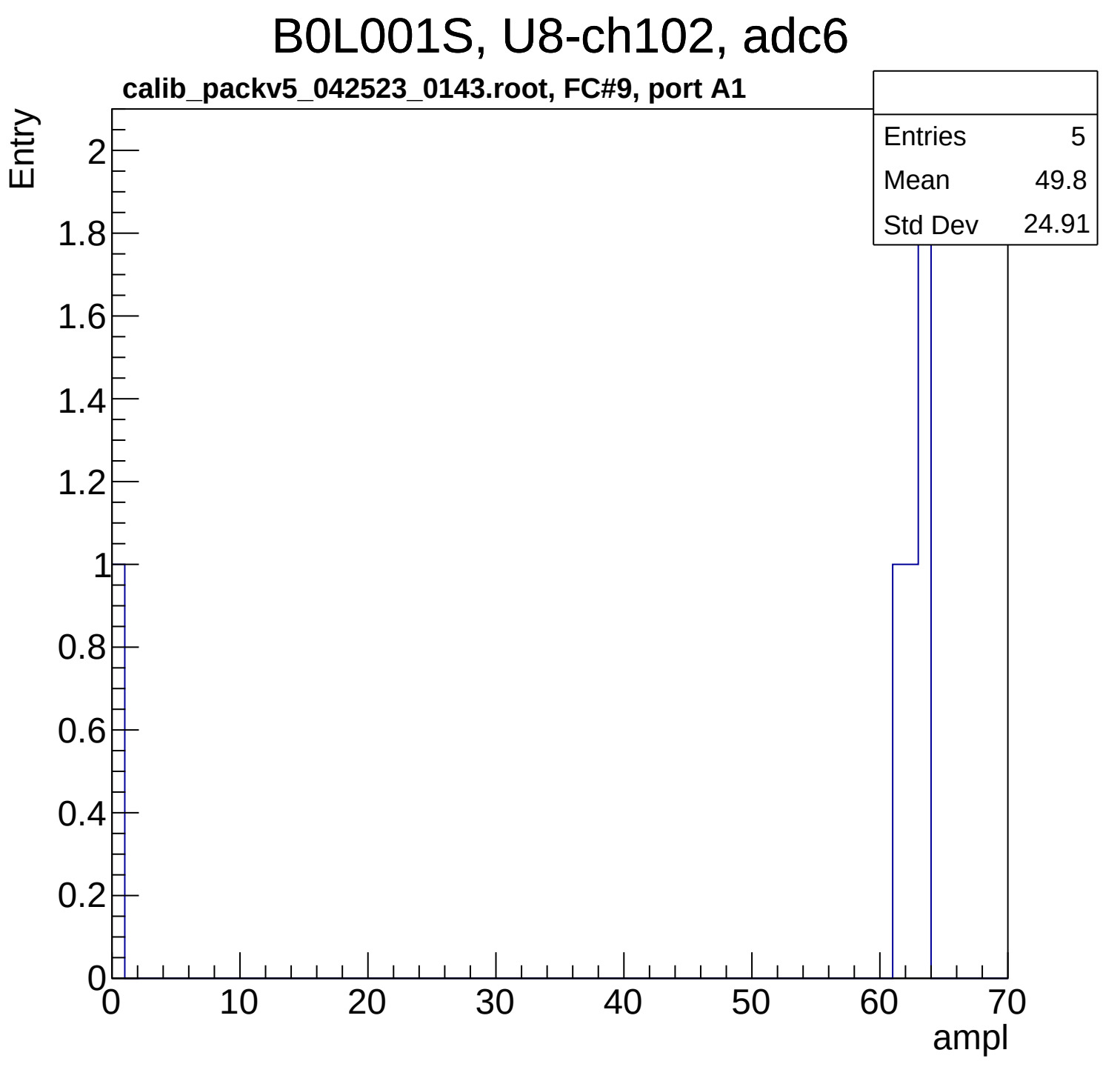
B0L001S, U8-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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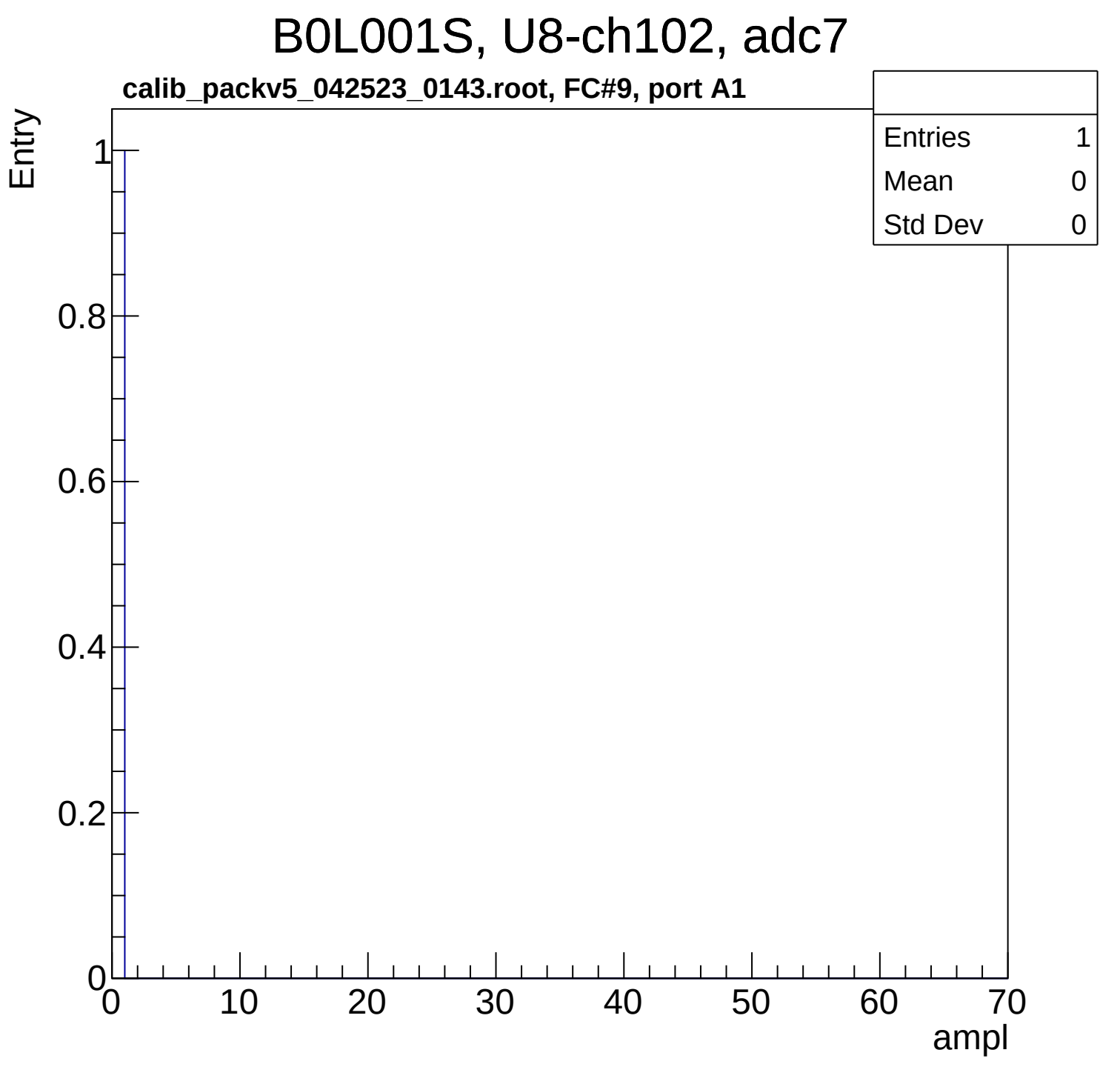
B0L001S, U8-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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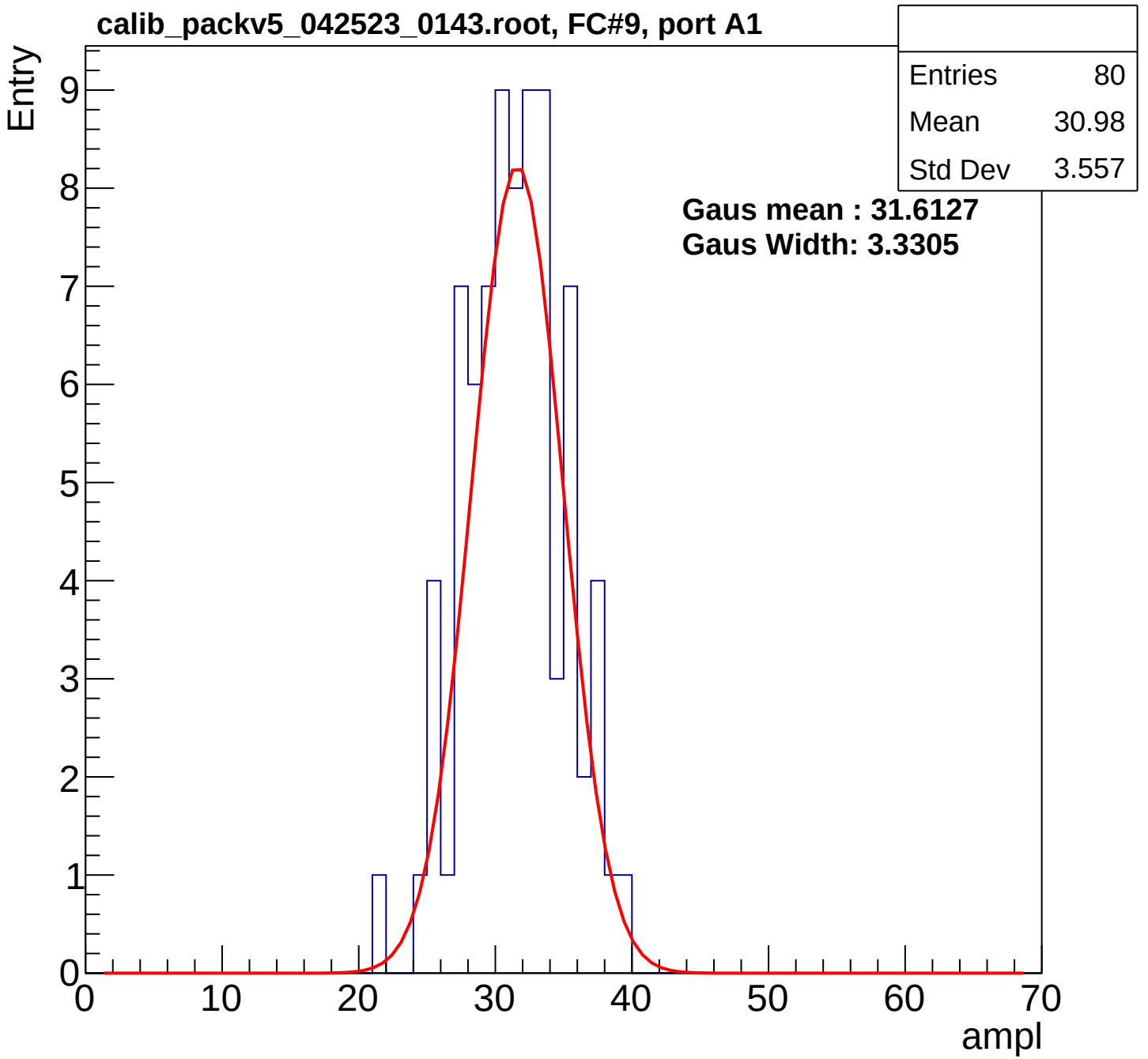
50
60
70
ampl


B0L001S, U8-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.98
Std Dev 3.557
Gaus mean : 31.6127
Gaus Width: 3.3305
Entry
ampl
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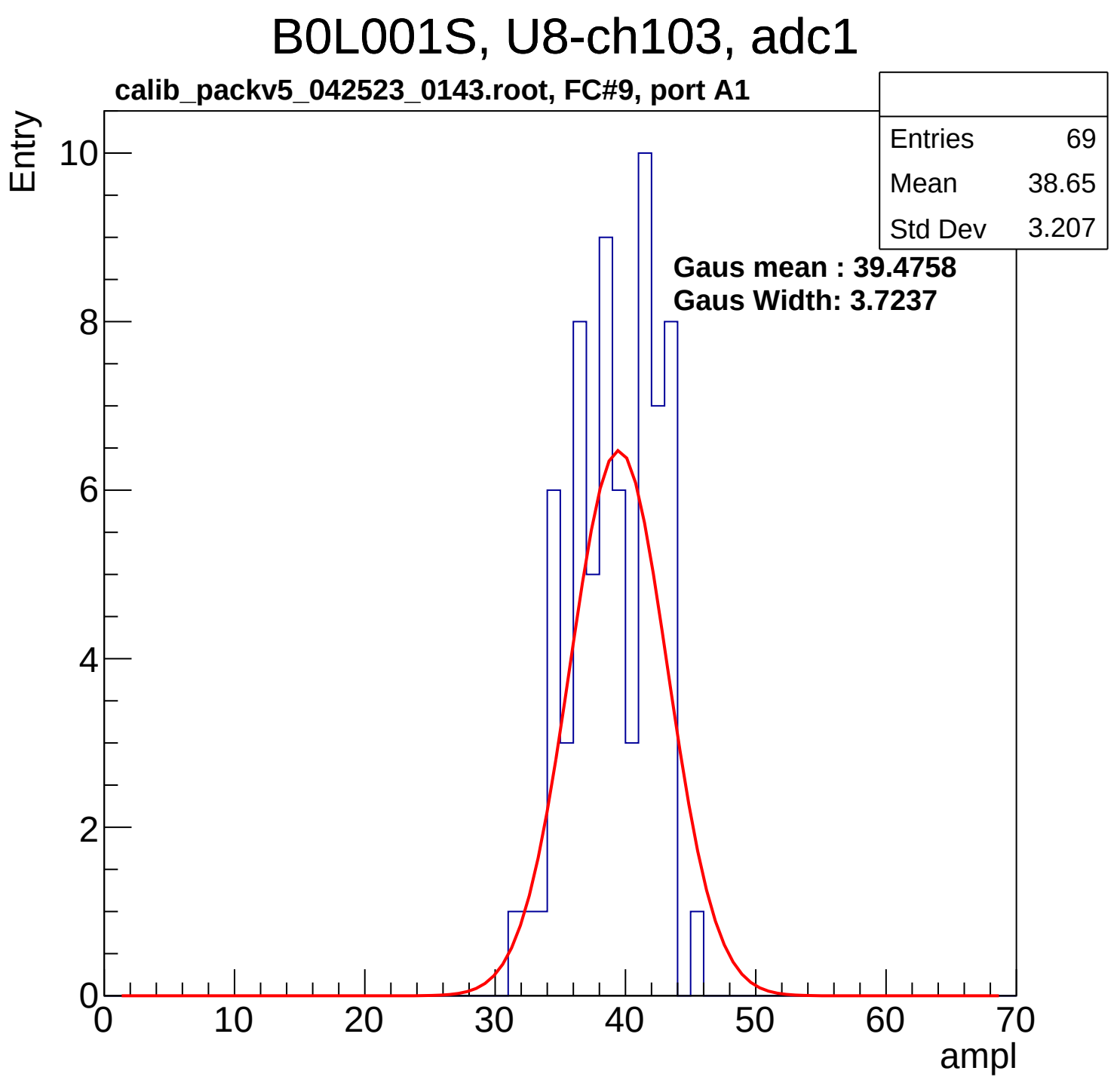
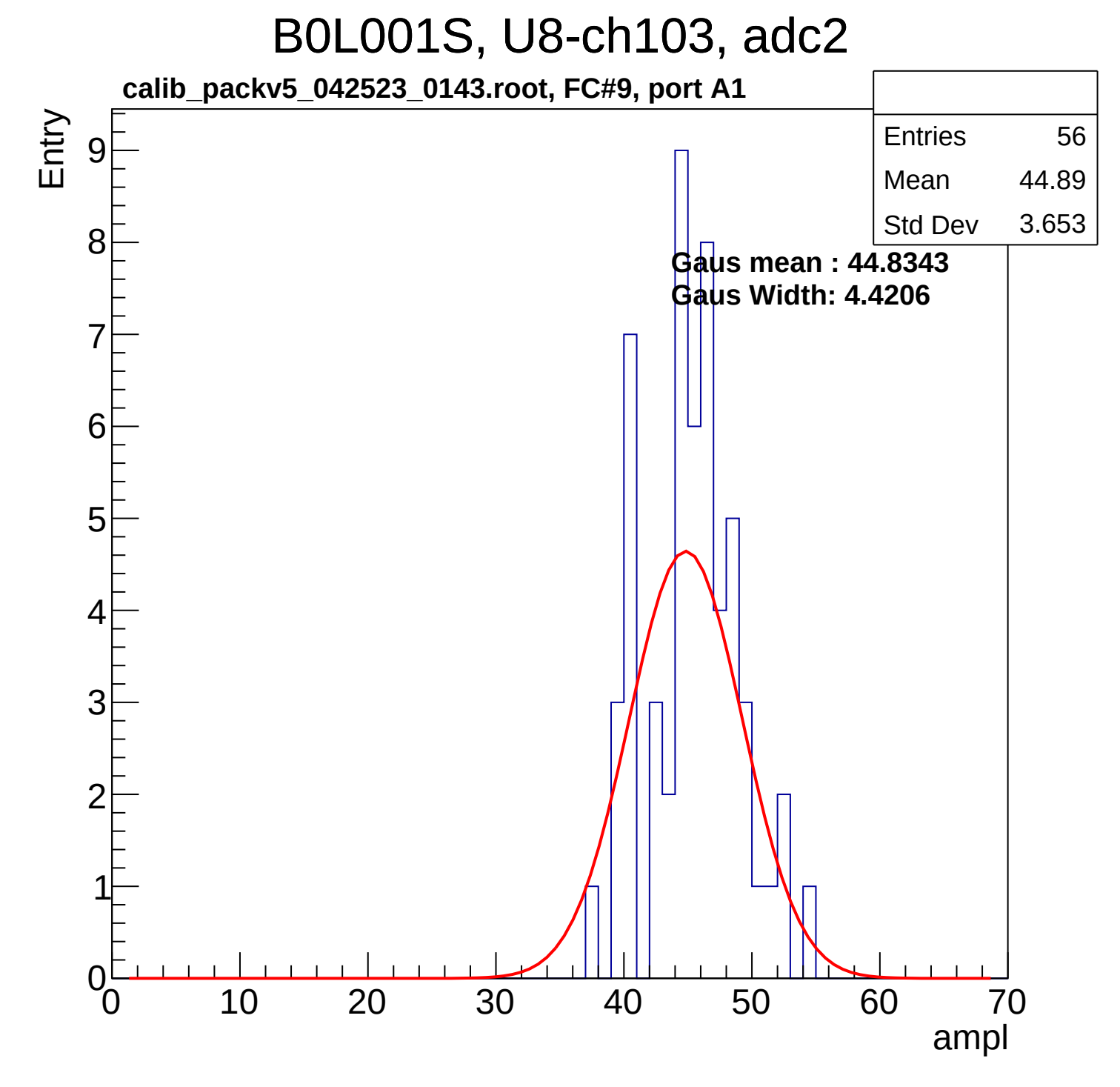
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.65
Std Dev 3.207
Gaus mean : 39.4758
Gaus Width: 3.7237
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.89
Std Dev 3.653
Gaus mean : 44.8343
Gaus Width: 4.4206
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
30
40
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60
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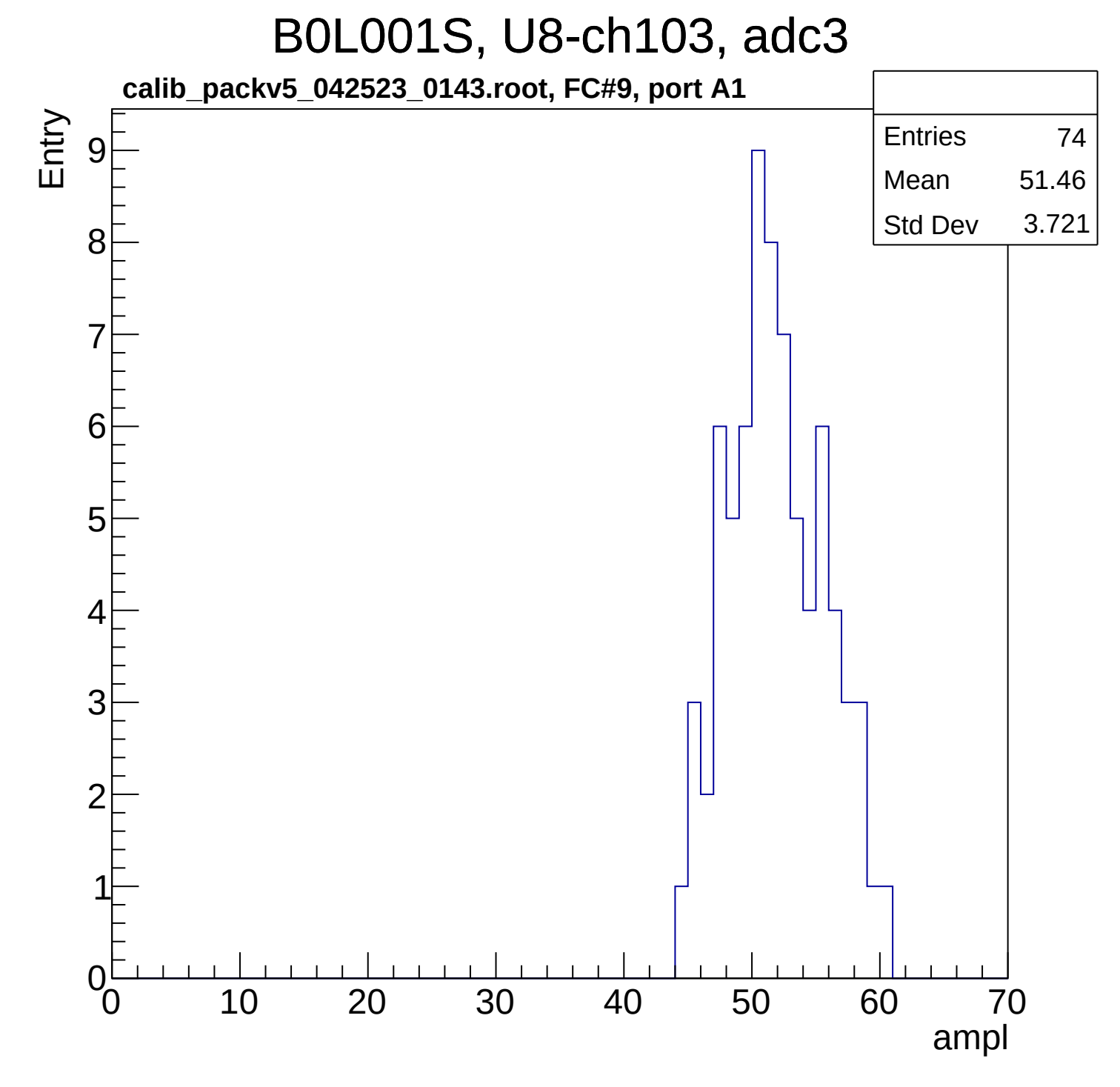
B0L001S, U8-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.46
Std Dev 3.721
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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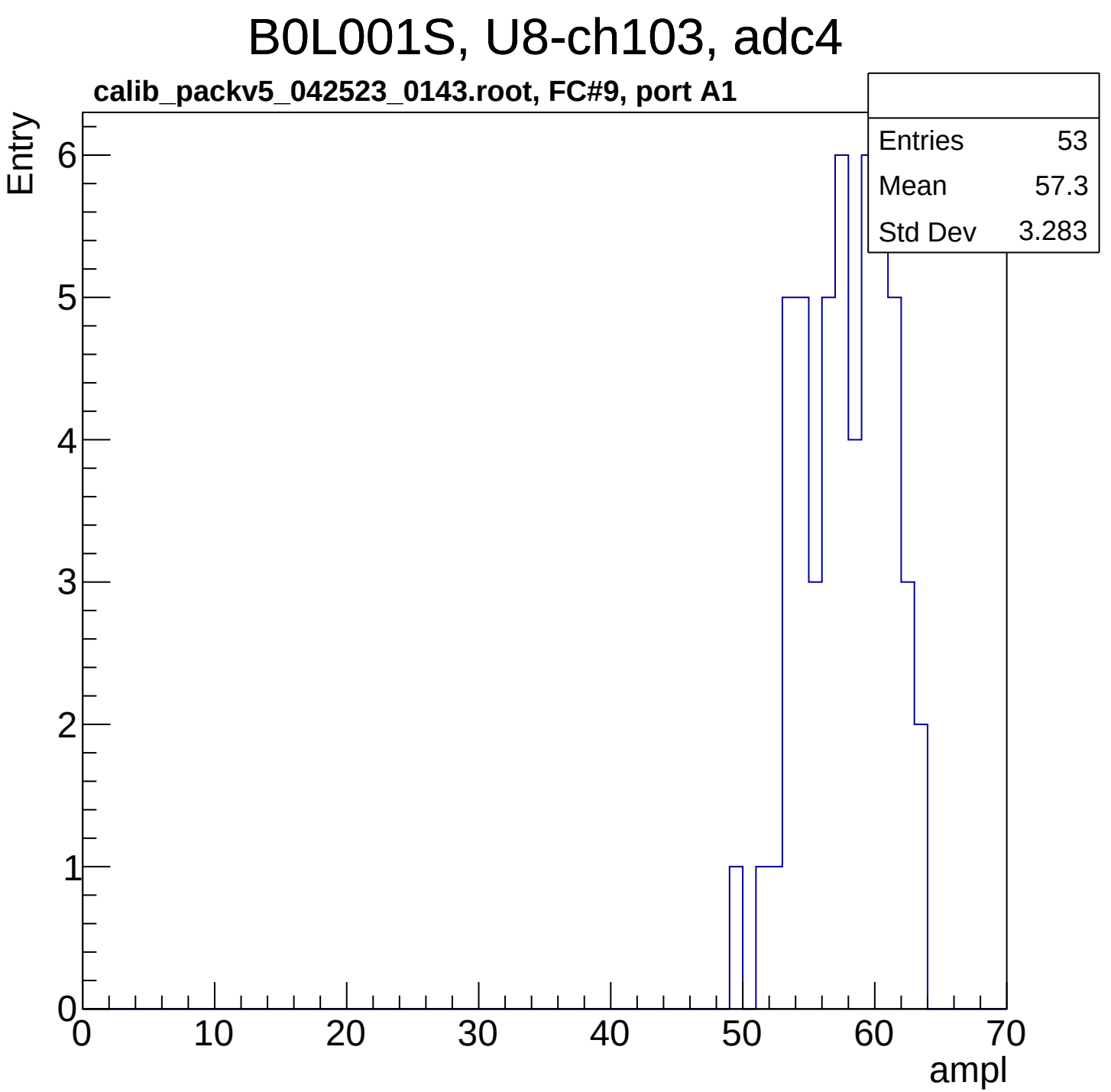
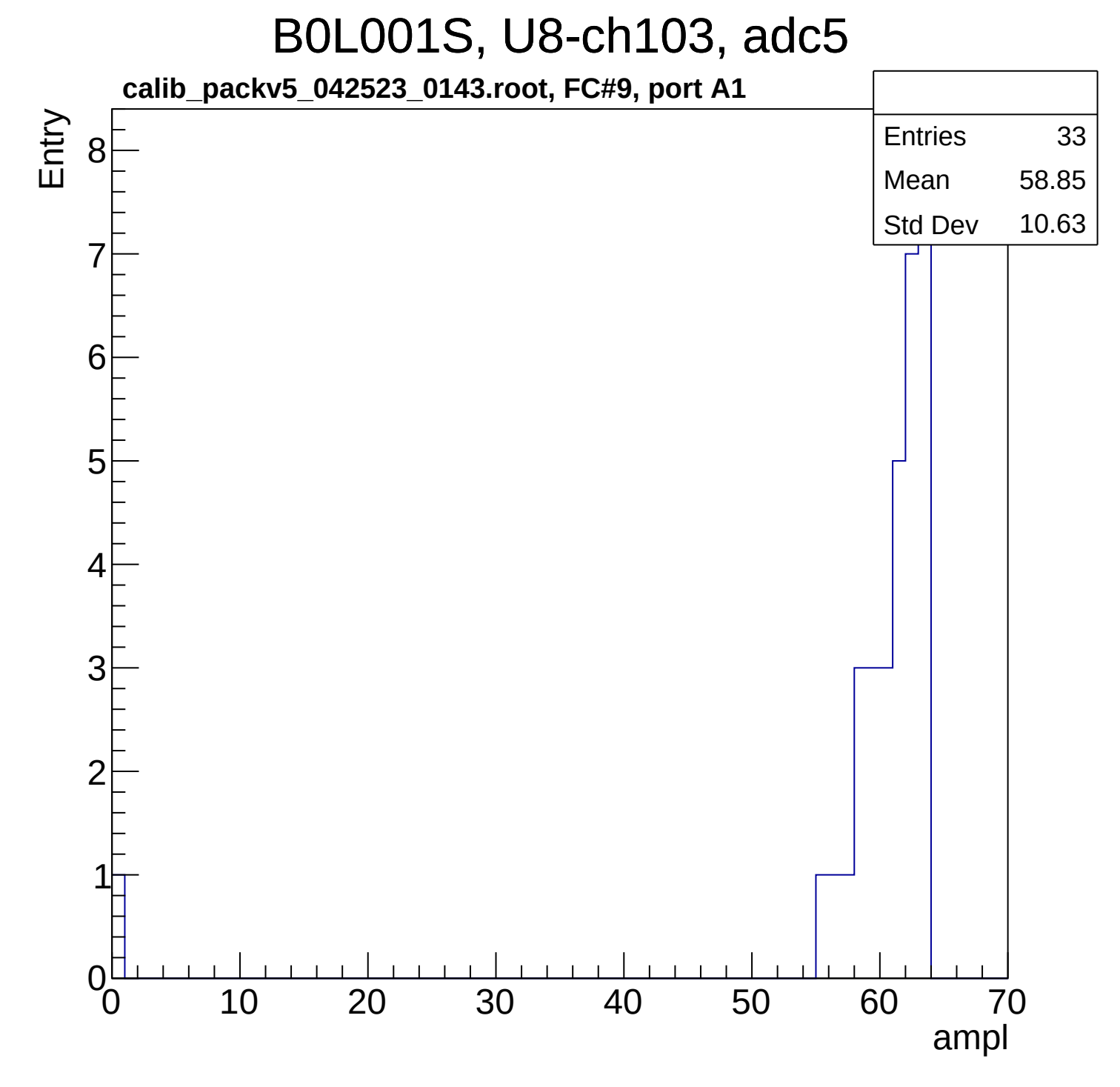
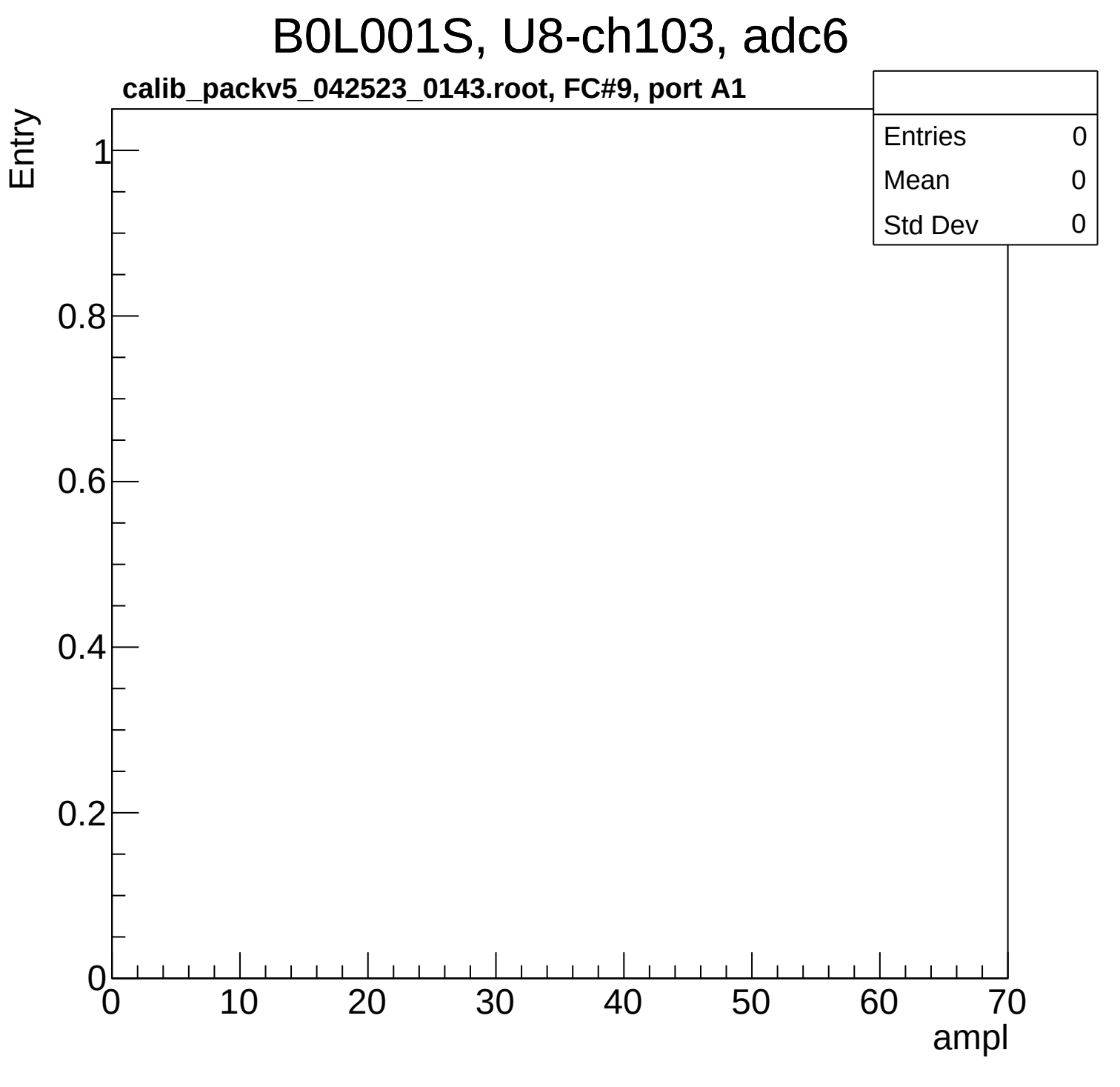
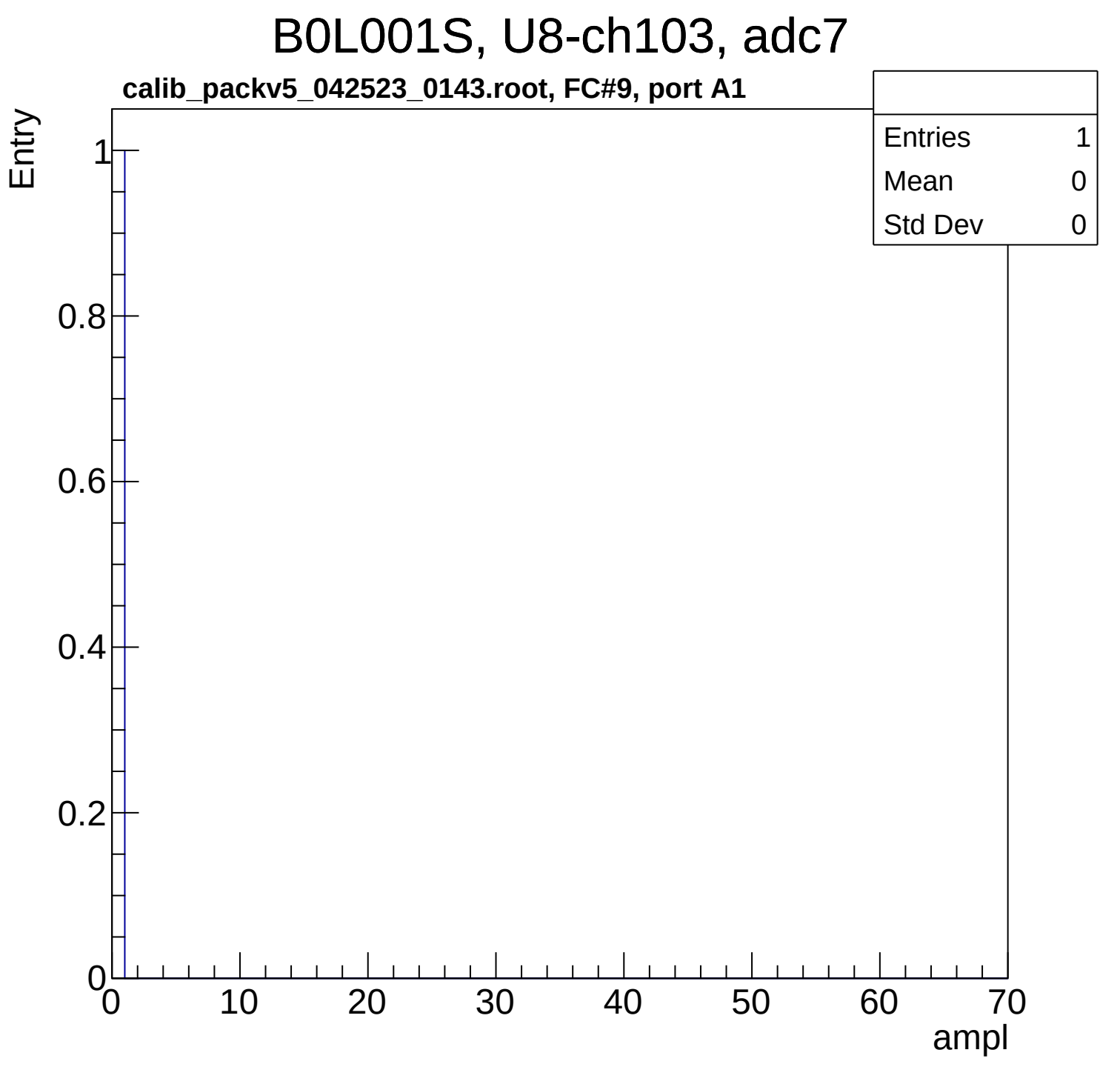

B0L001S, U8-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.3
Std Dev 3.283
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.85
Std Dev 10.63
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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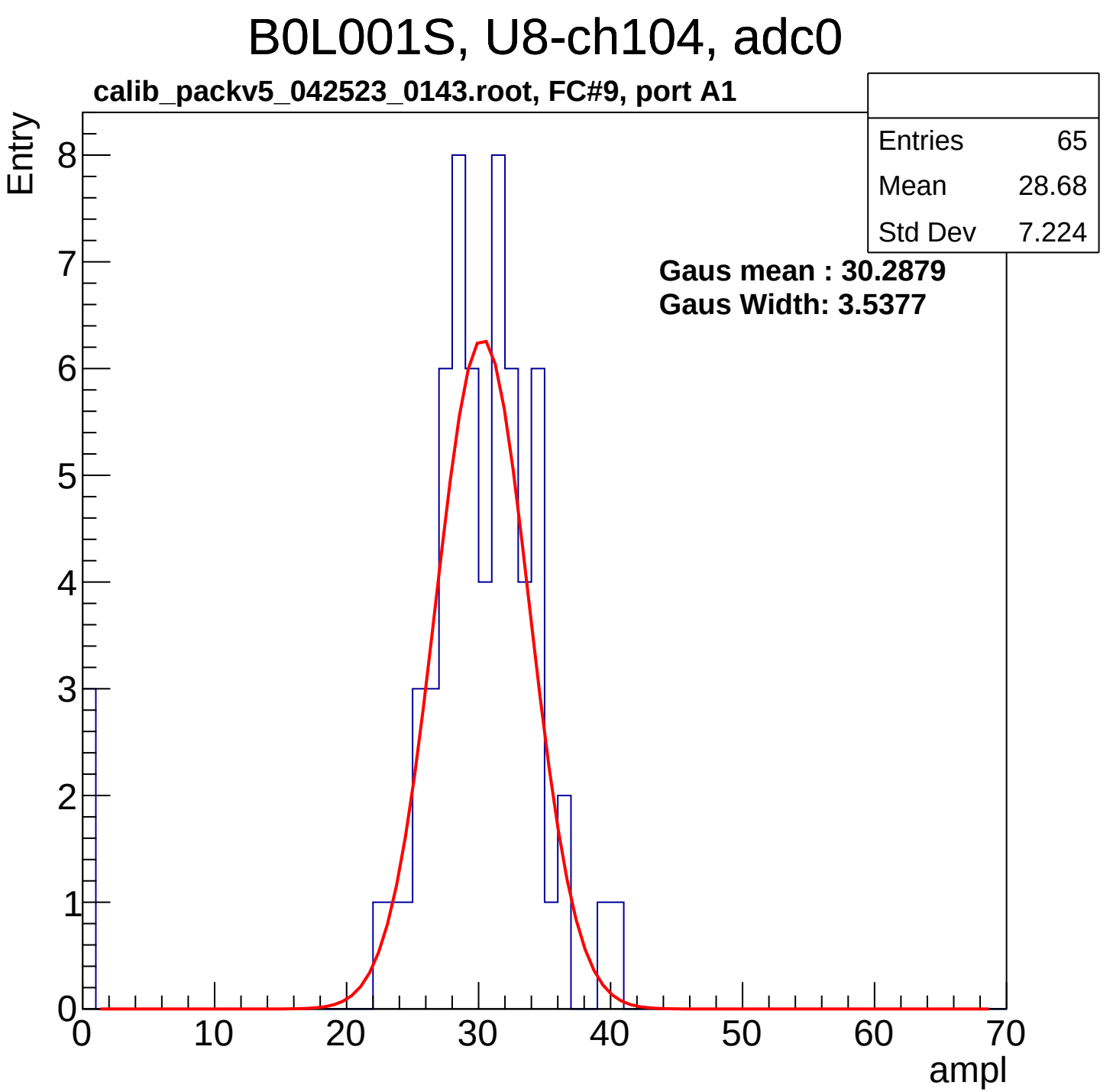
50
60
70
ampl


B0L001S, U8-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 28.68
Std Dev 7.224
Gaus mean : 30.2879
Gaus Width: 3.5377
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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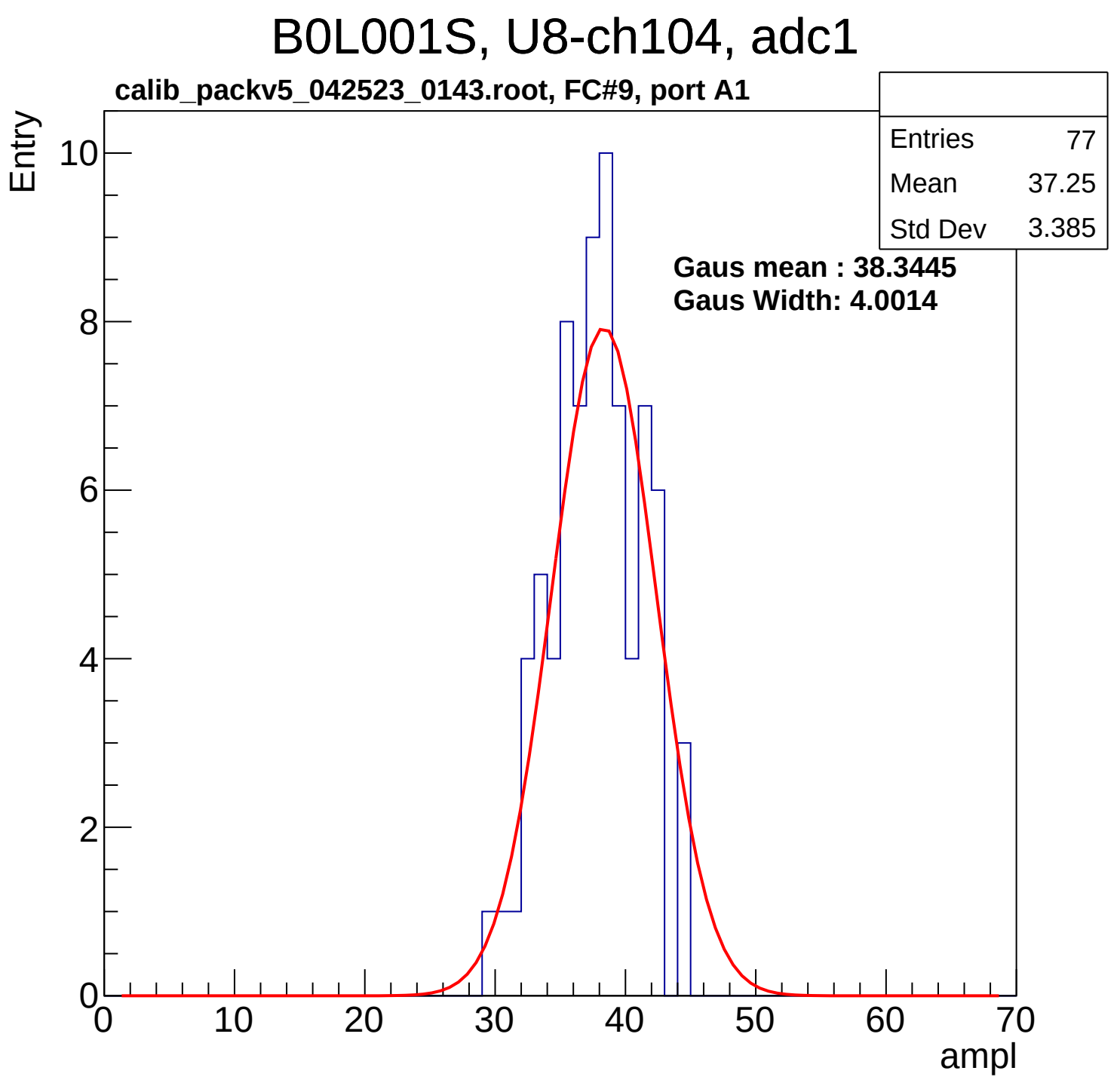
B0L001S, U8-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.25
Std Dev 3.385
Gaus mean : 38.3445
Gaus Width: 4.0014
Entry
ampl
0
2
4
6
8
10
0
10
20
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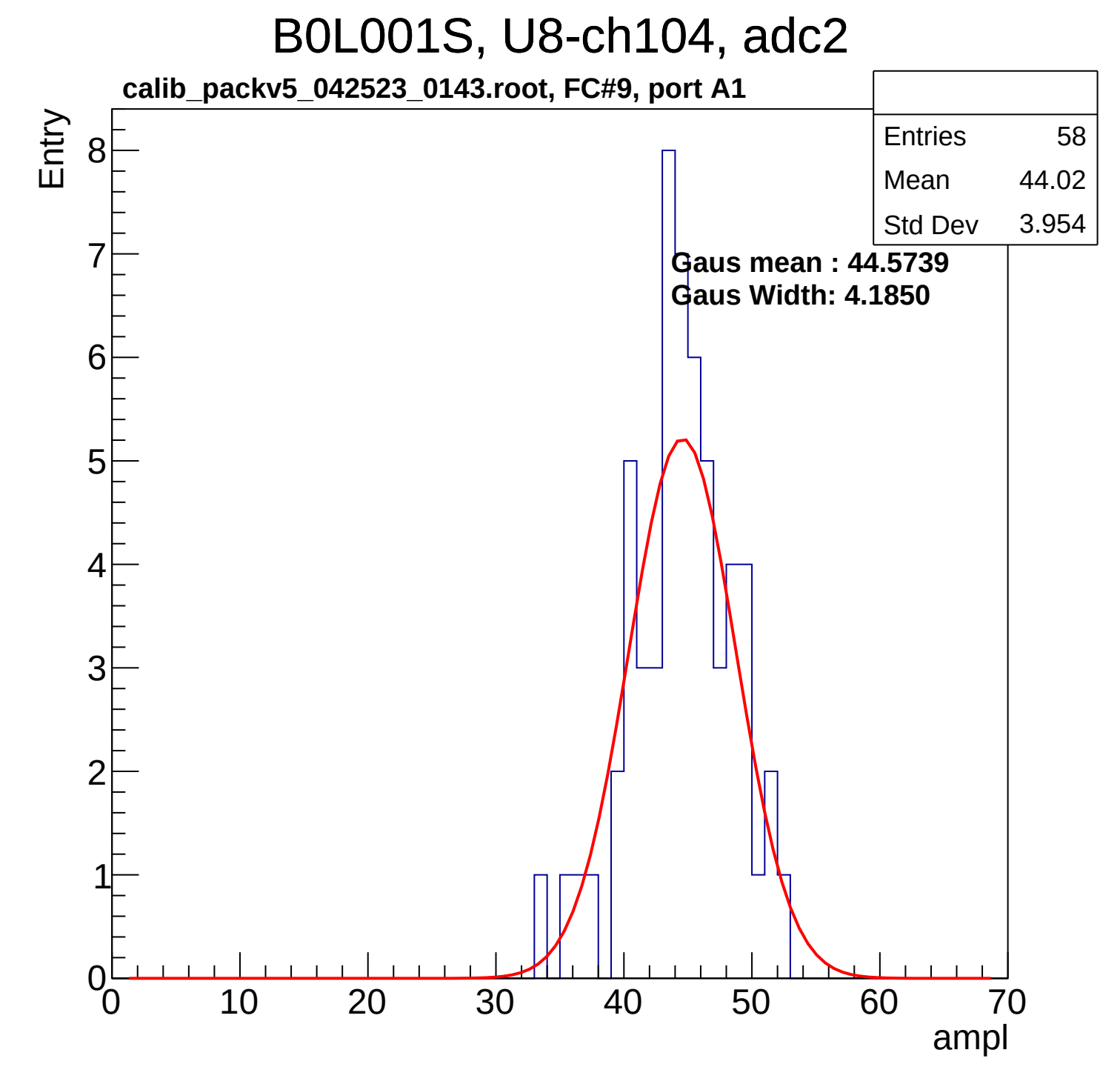
40
50
60
70


B0L001S, U8-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.02
Std Dev 3.954
Gaus mean : 44.5739
Gaus Width: 4.1850
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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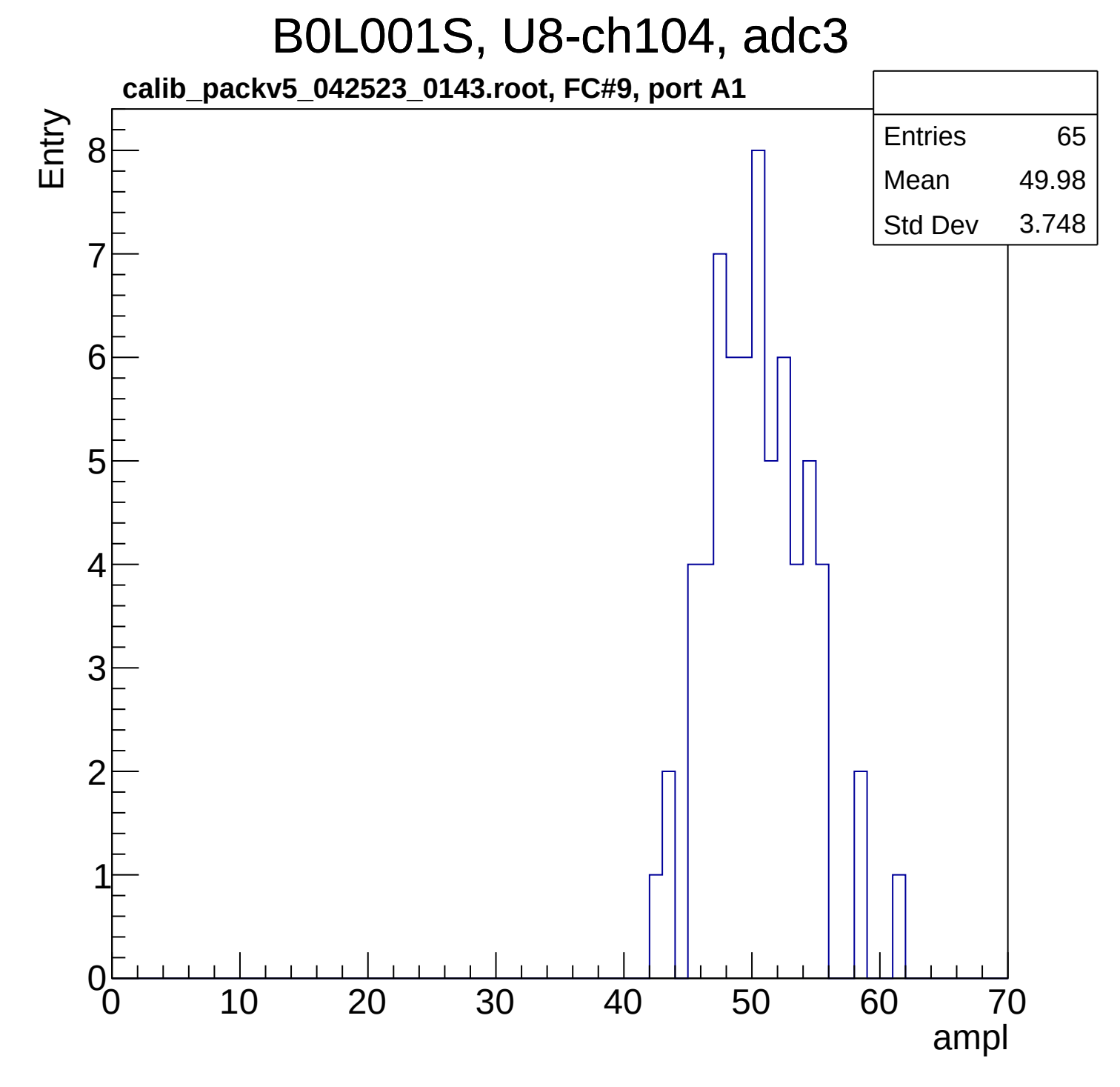
B0L001S, U8-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.98
Std Dev 3.748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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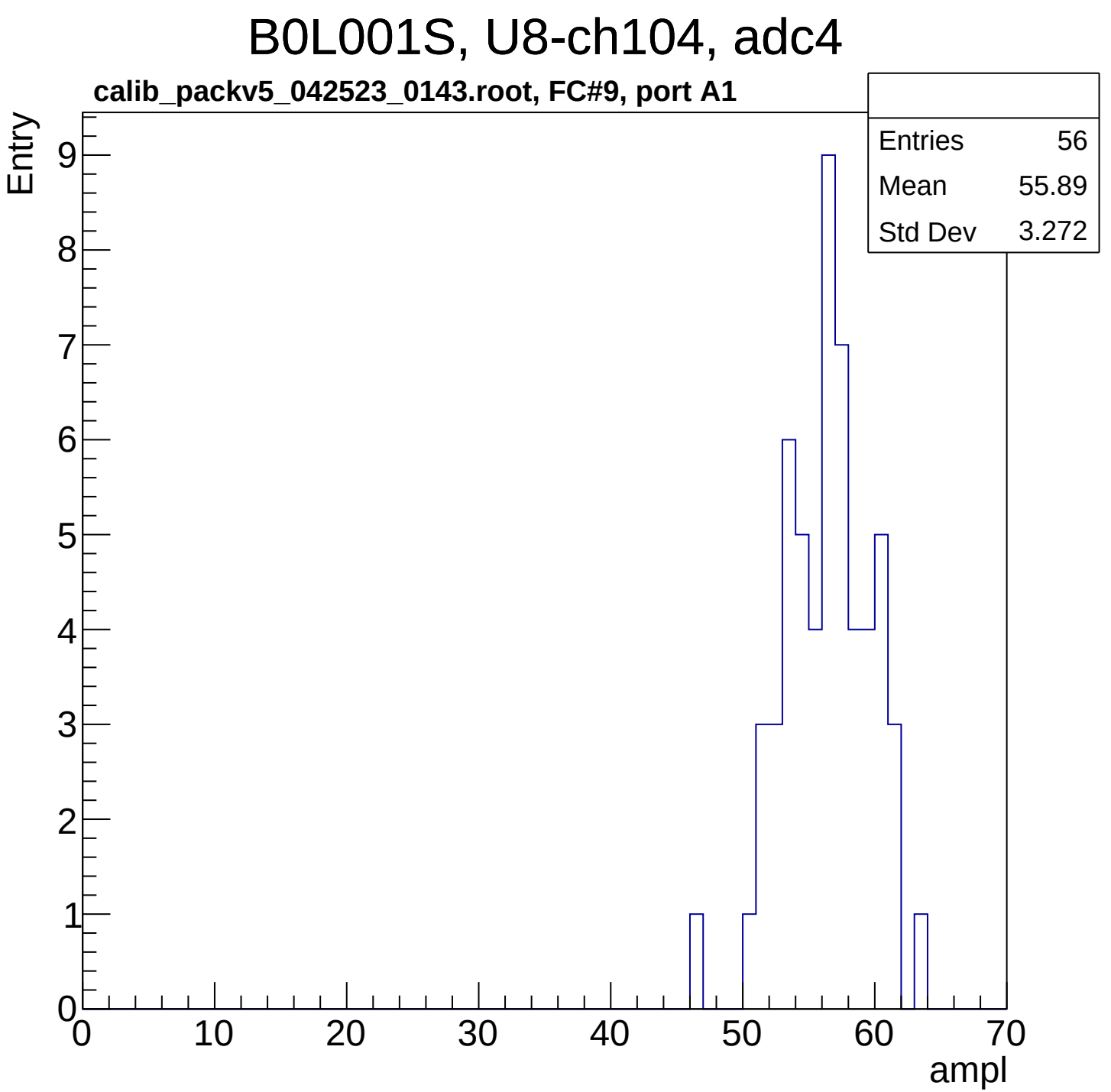
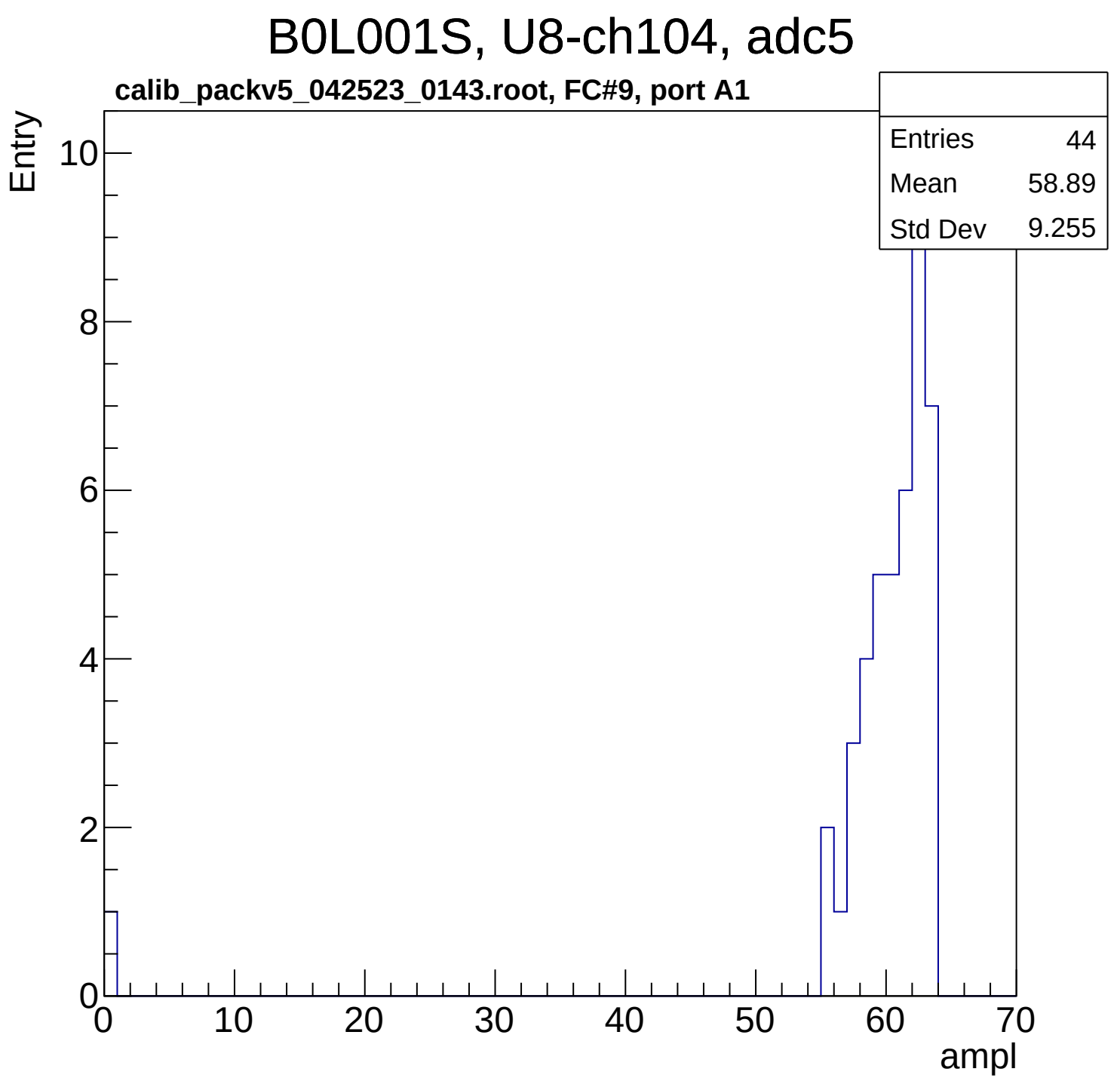
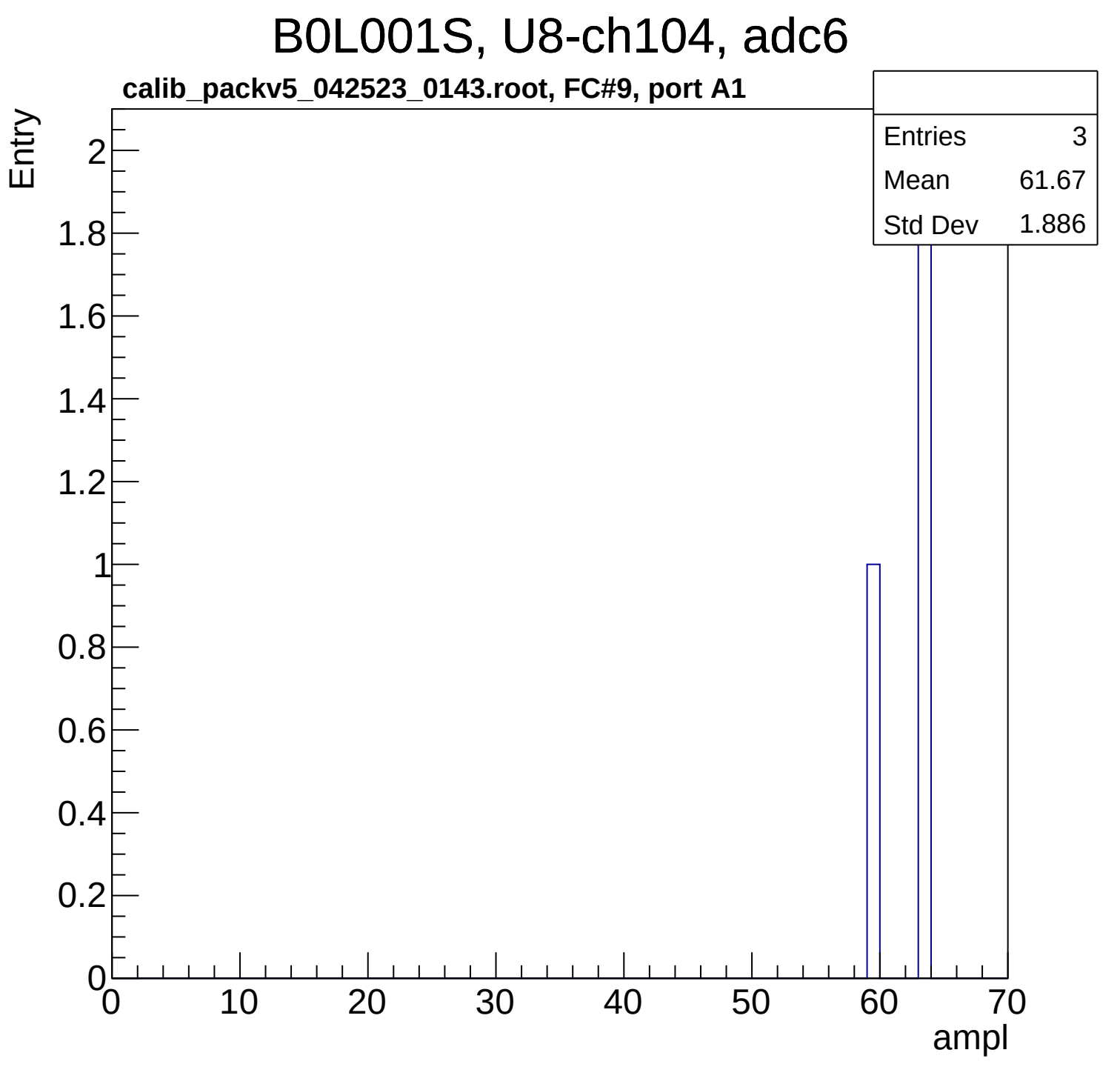
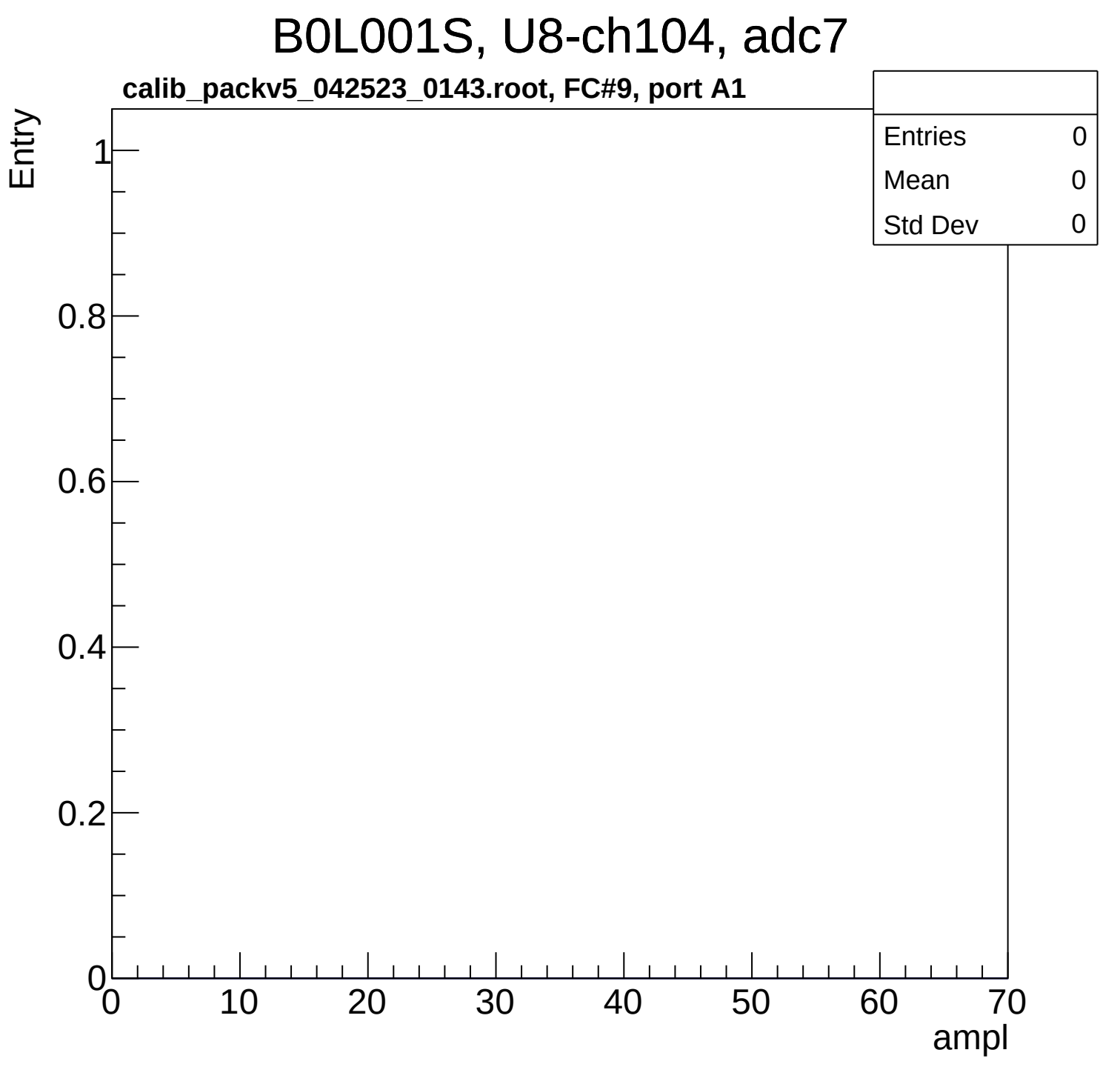
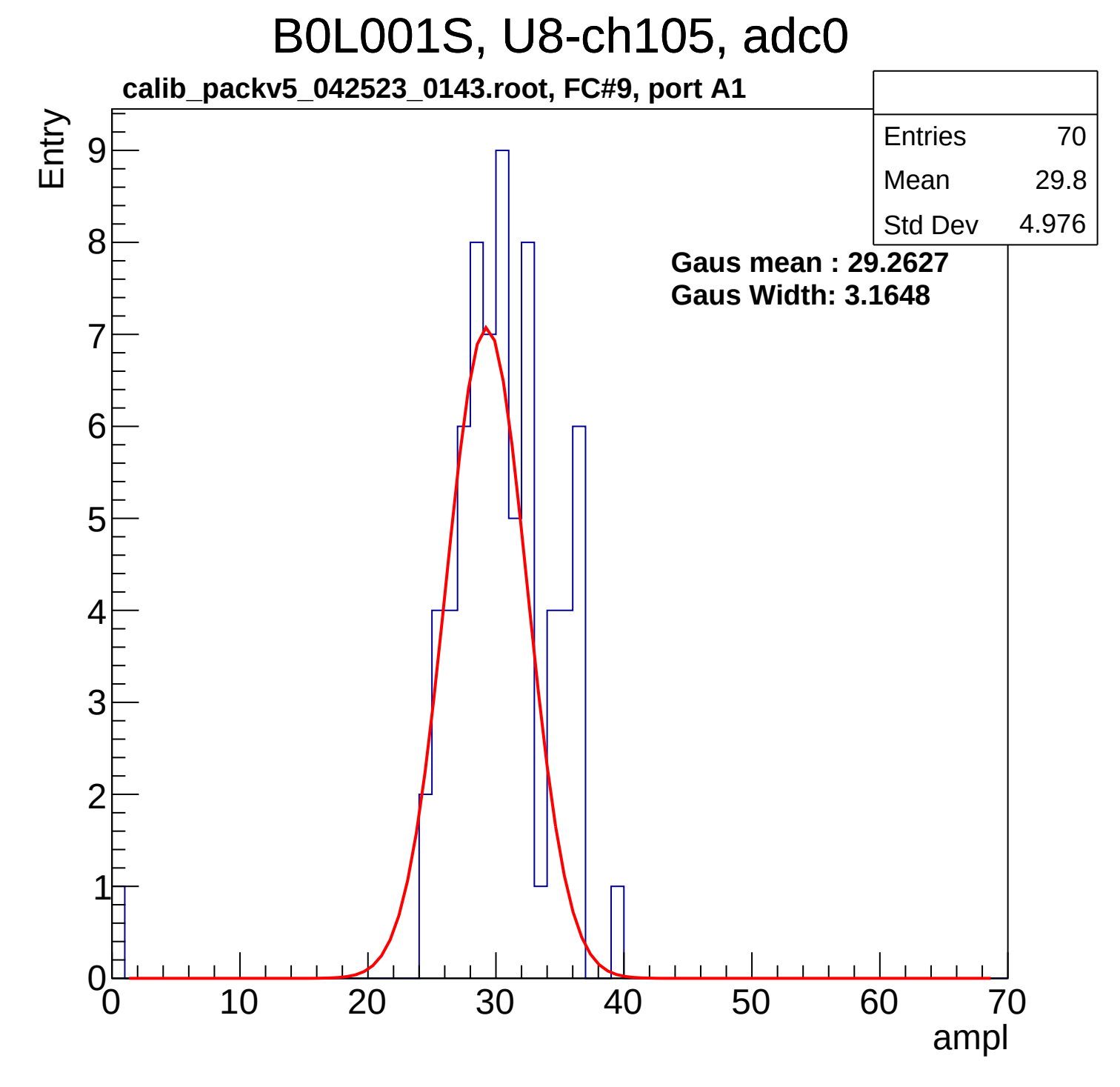
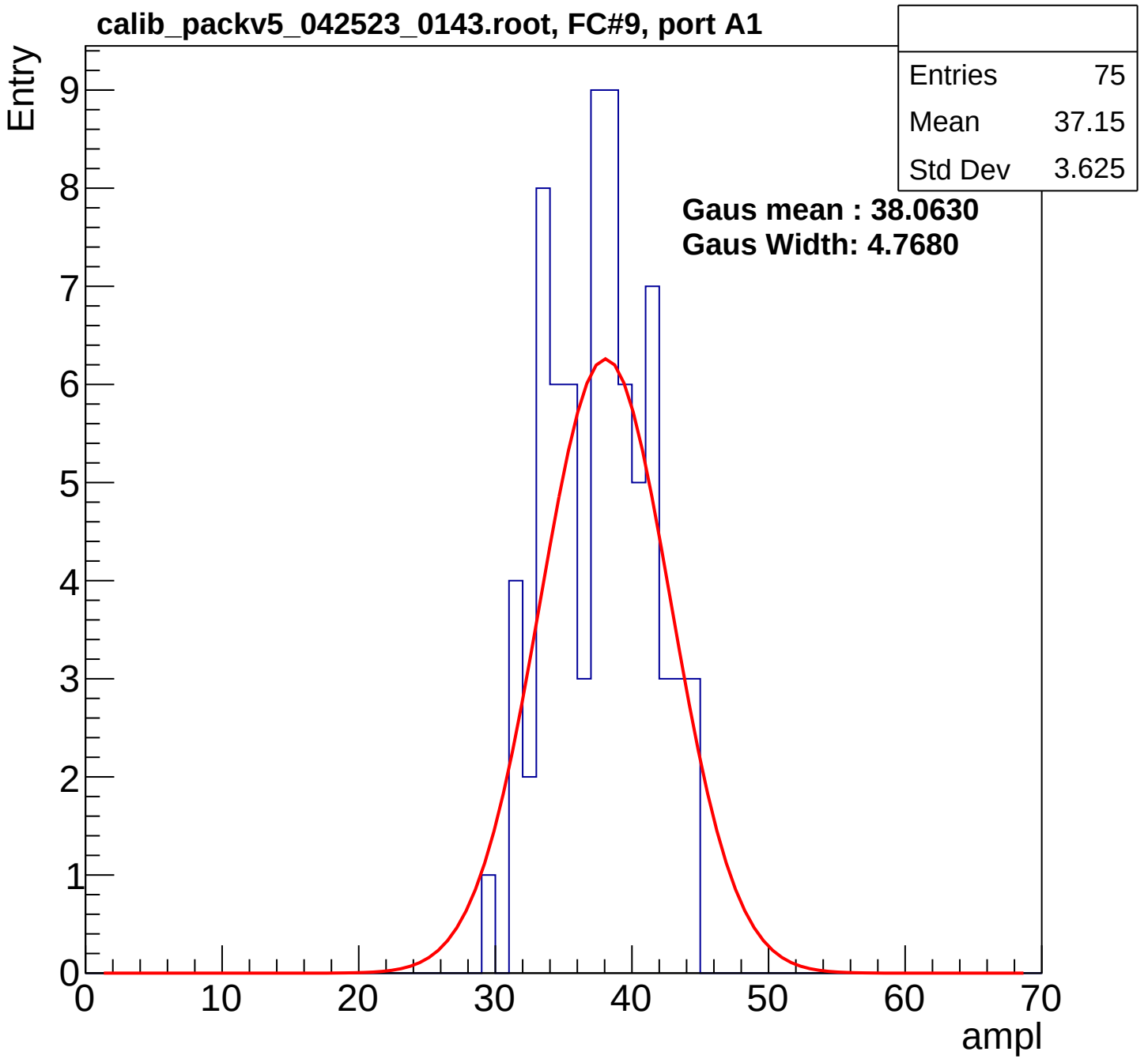

B0L001S, U8-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.89
Std Dev 3.272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

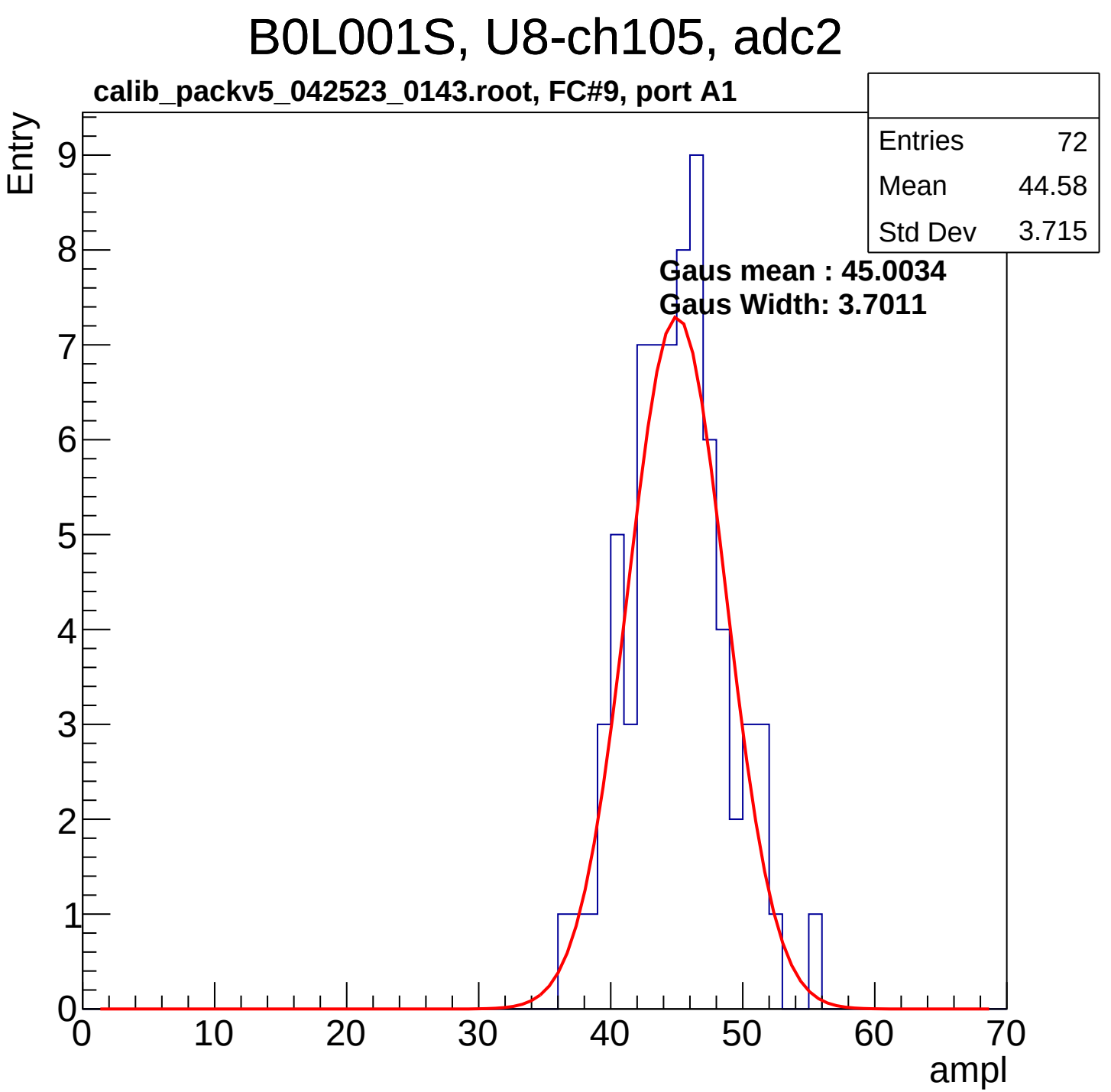

B0L001S, U8-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.89
Std Dev 9.255
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

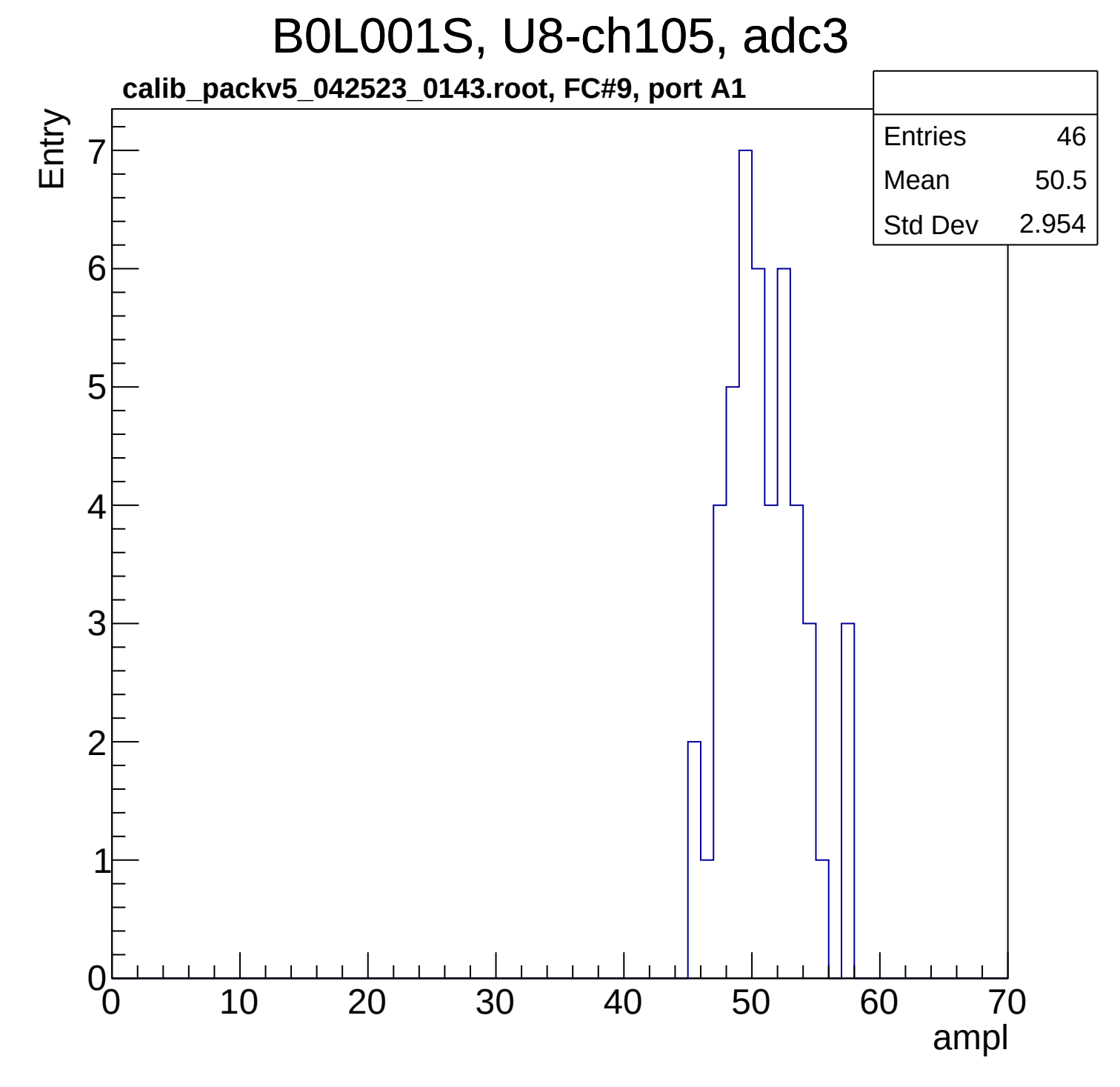

B0L001S, U8-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

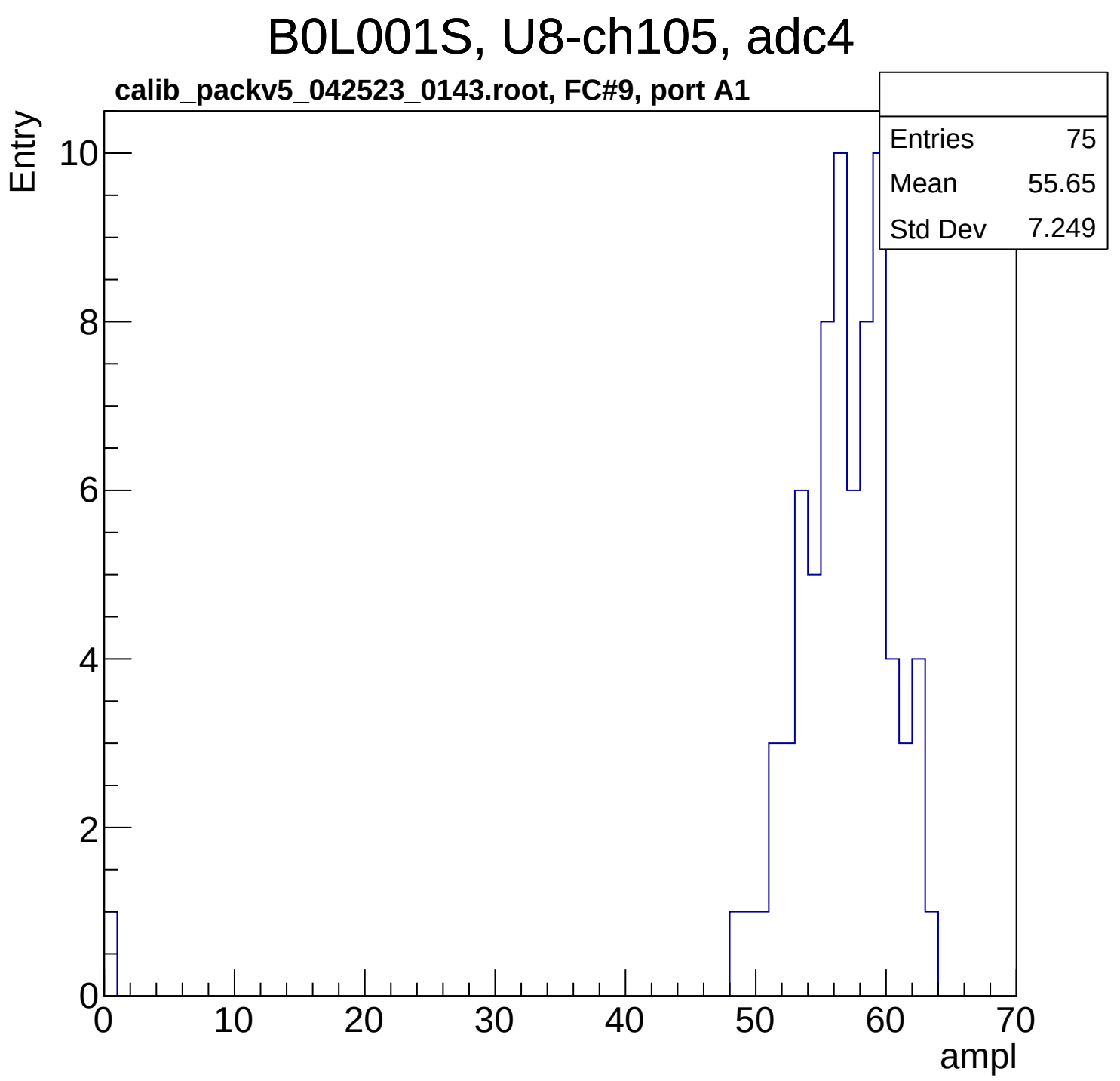

B0L001S, U8-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

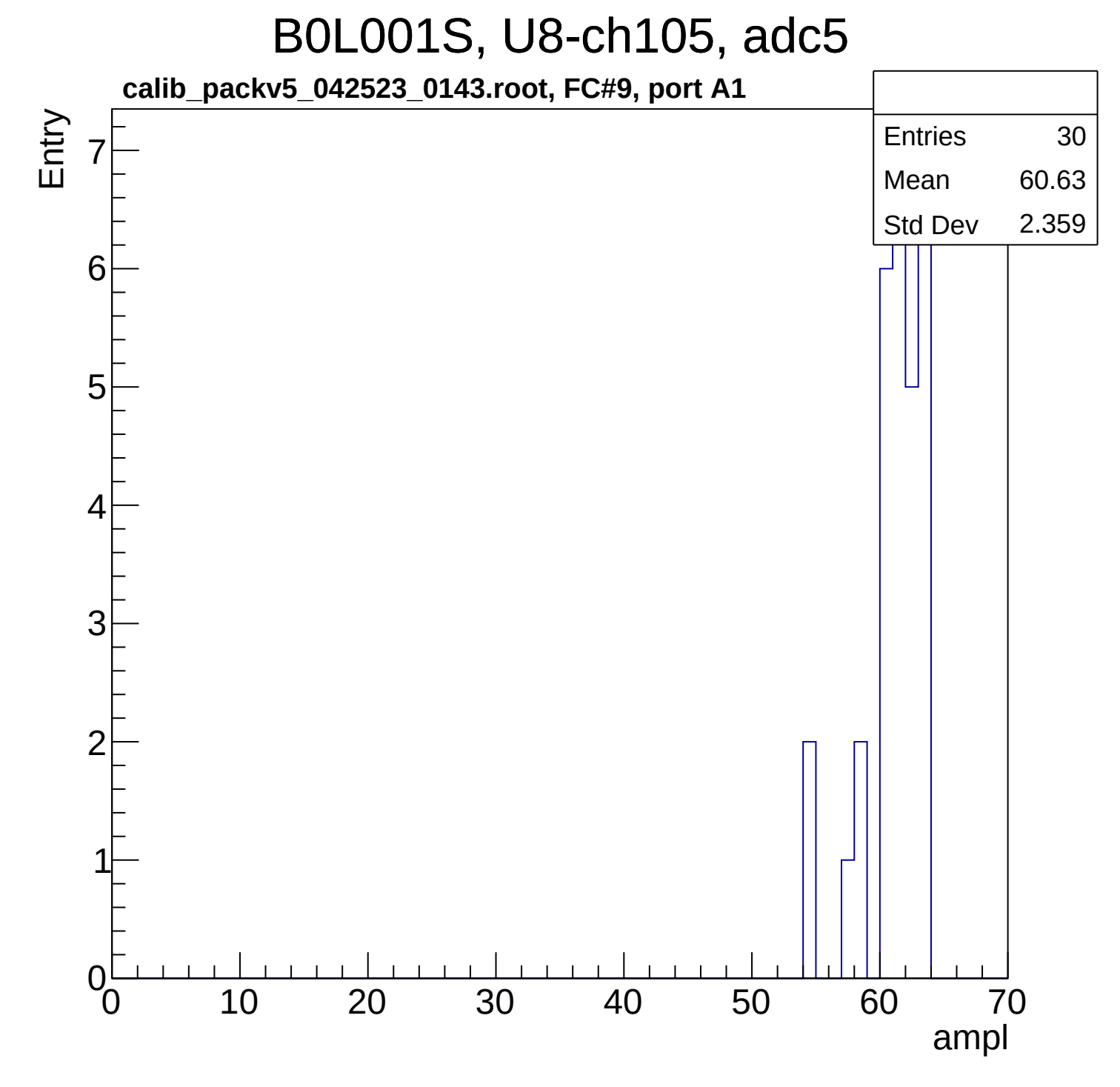

B0L001S, U8-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.8
Std Dev 4.976
Gaus mean : 29.2627
Gaus Width: 3.1648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

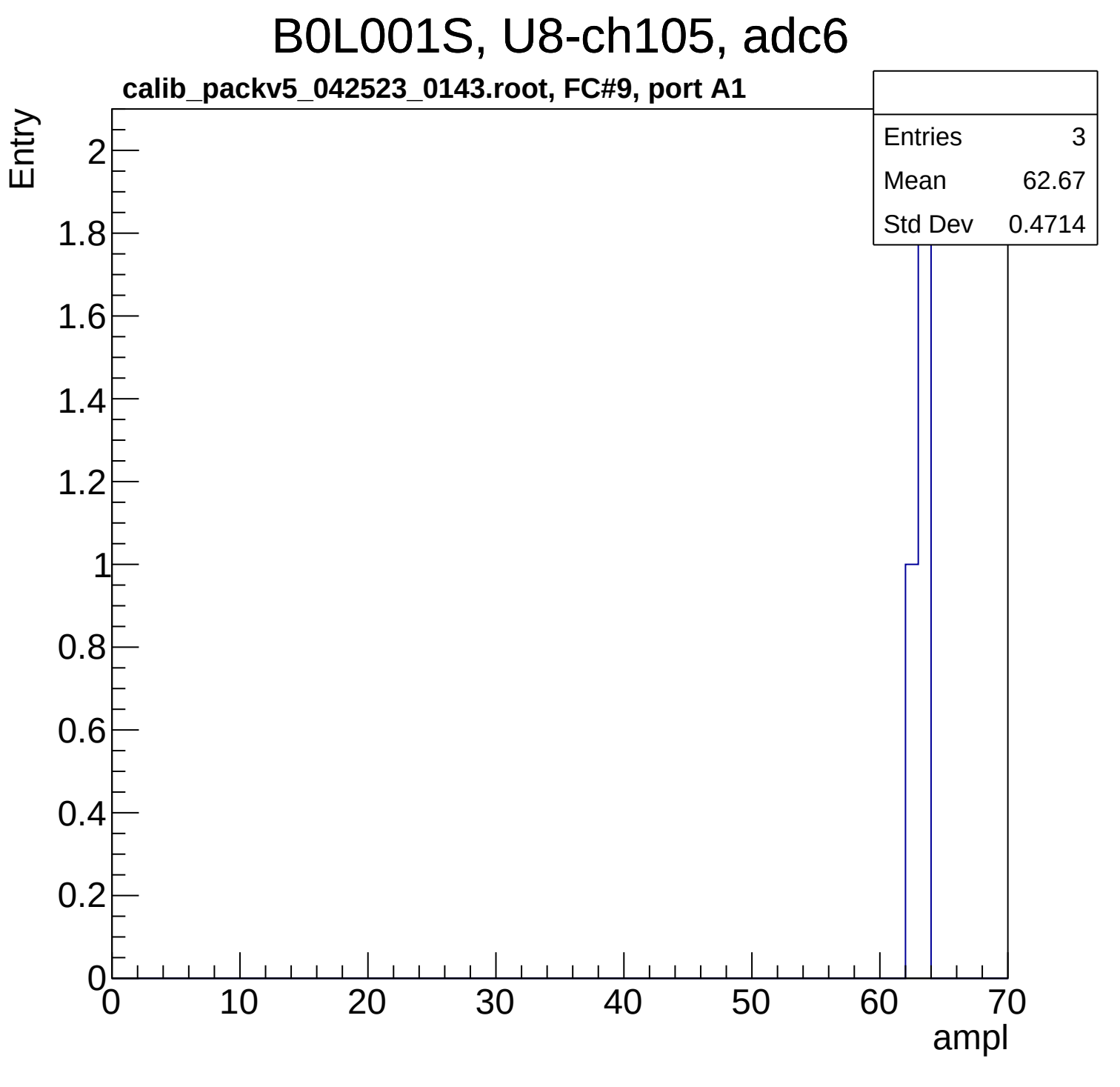

B0L001S, U8-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.15
Std Dev 3.625
Gaus mean : 38.0630
Gaus Width: 4.7680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

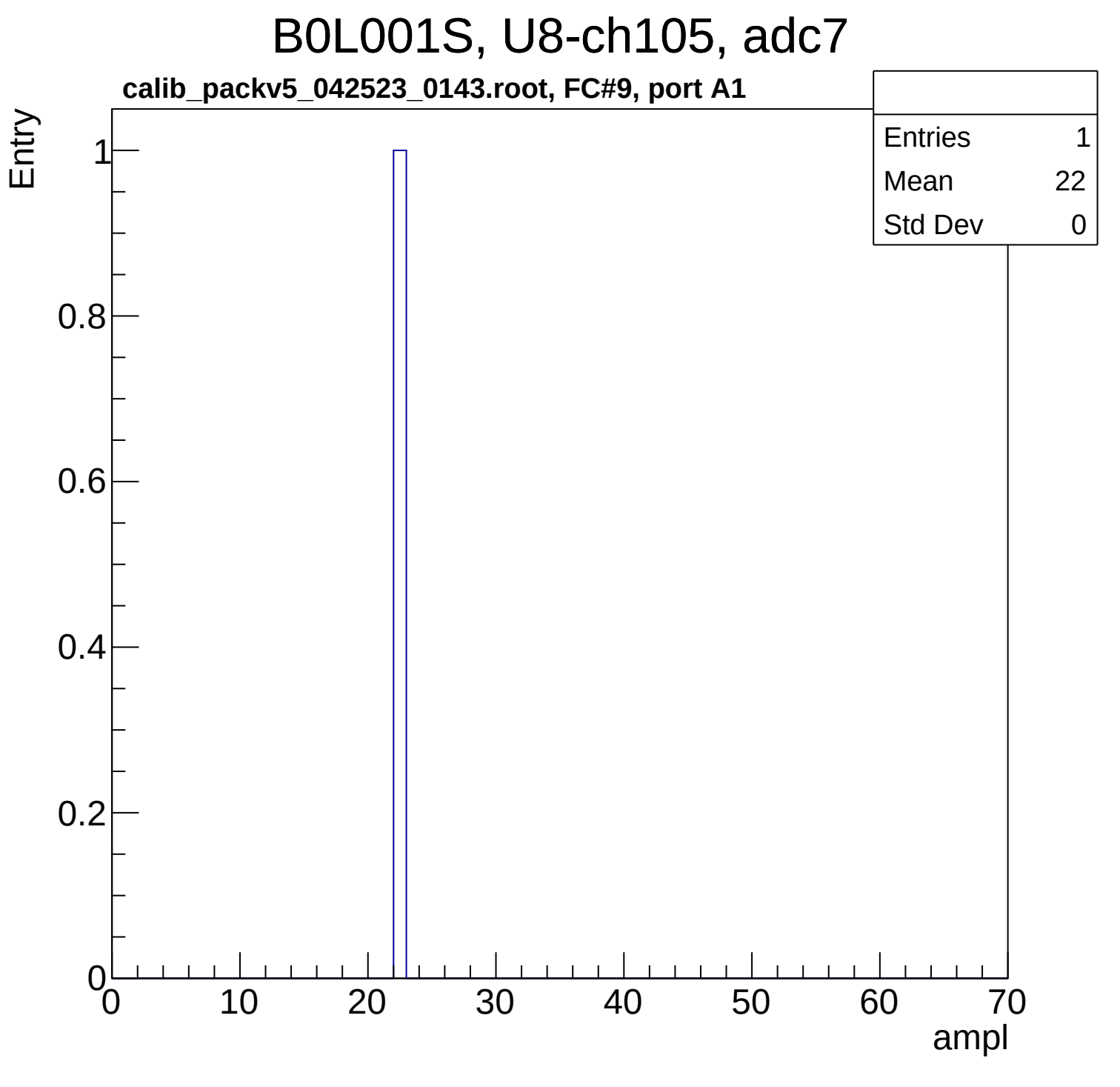

B0L001S, U8-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.58
Std Dev 3.715
Gaus mean : 45.0034
Gaus Width: 3.7011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

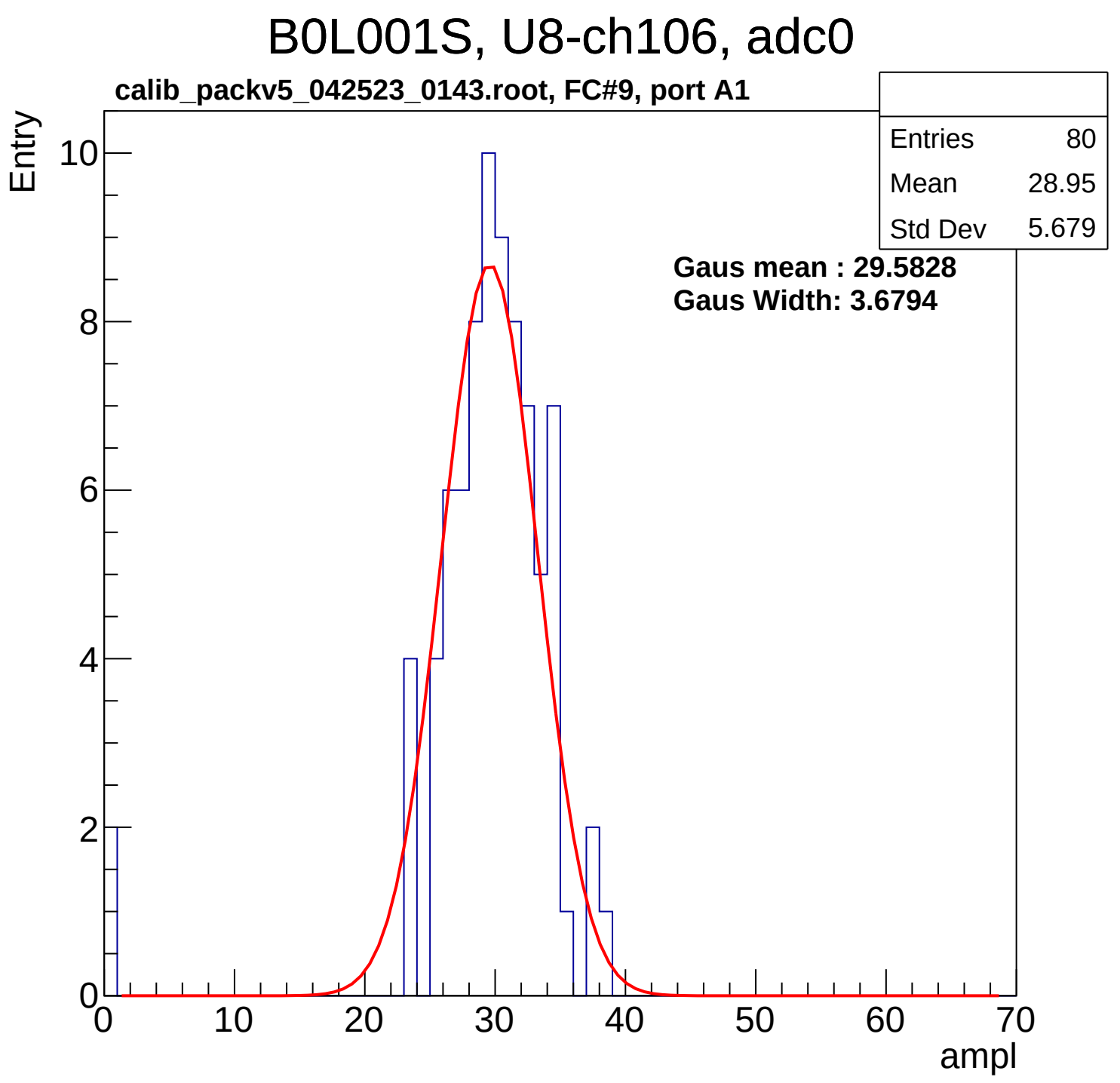

B0L001S, U8-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 50.5
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

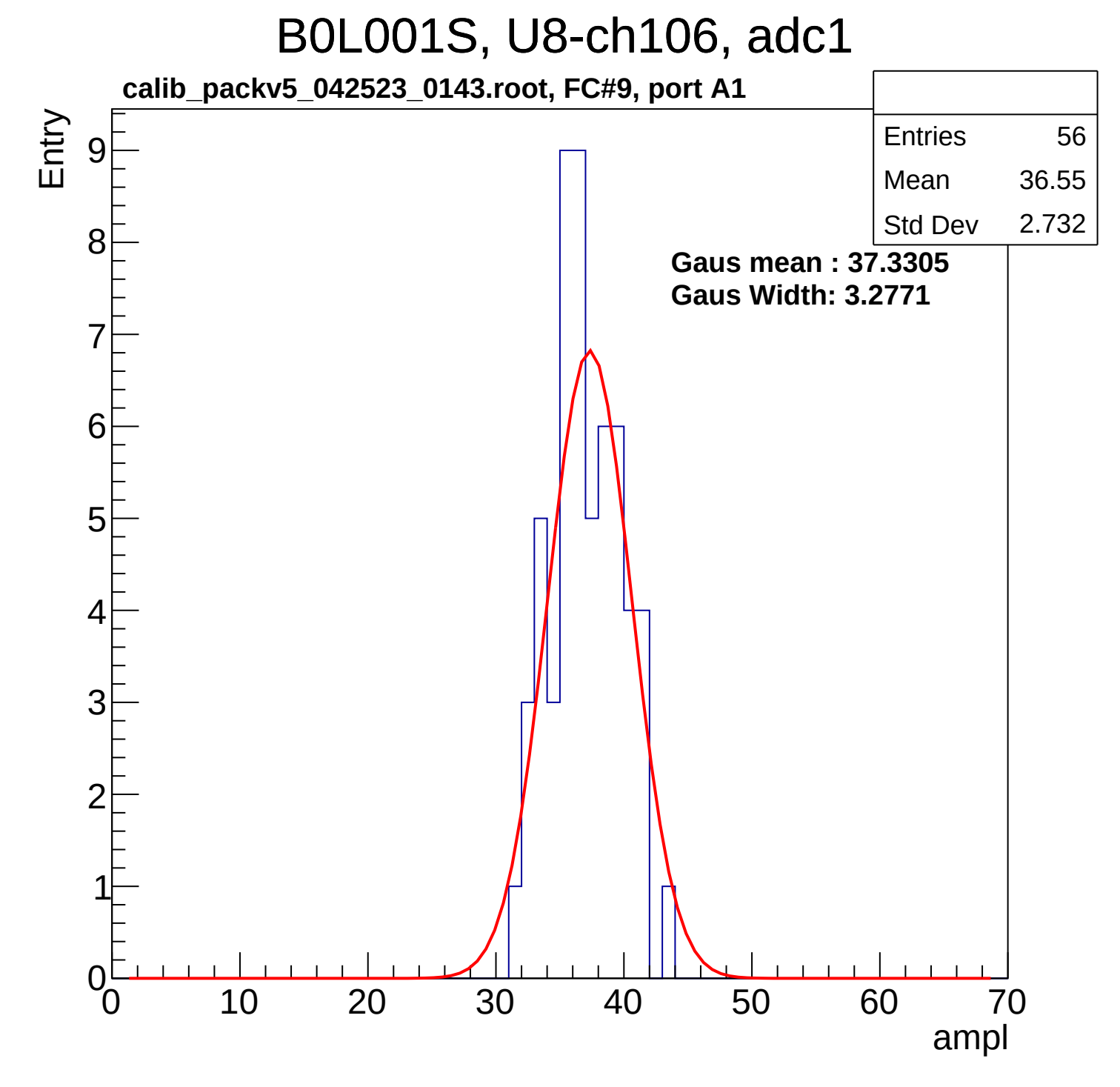

B0L001S, U8-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 55.65
Std Dev 7.249
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

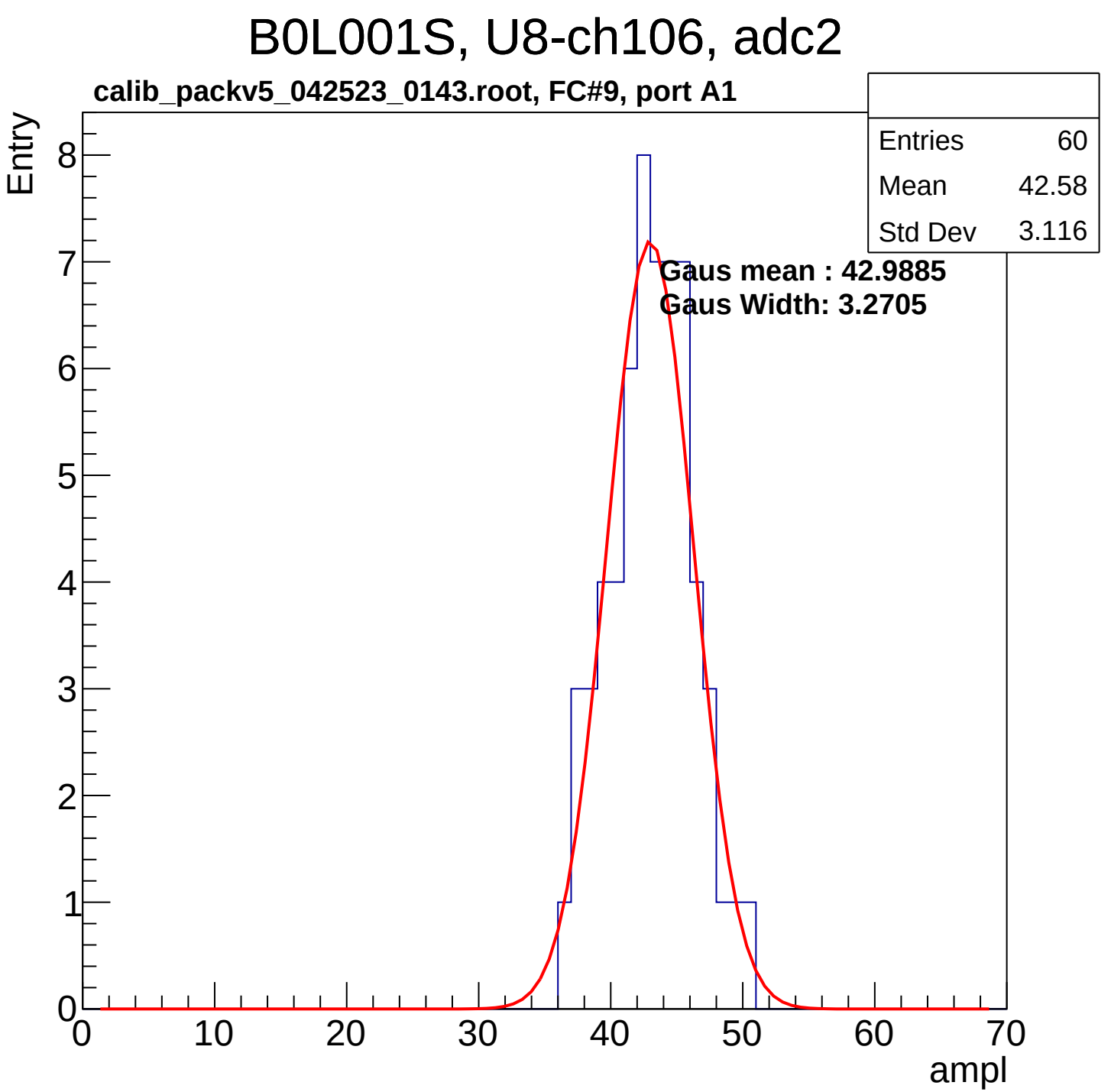

B0L001S, U8-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.63
Std Dev 2.359
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

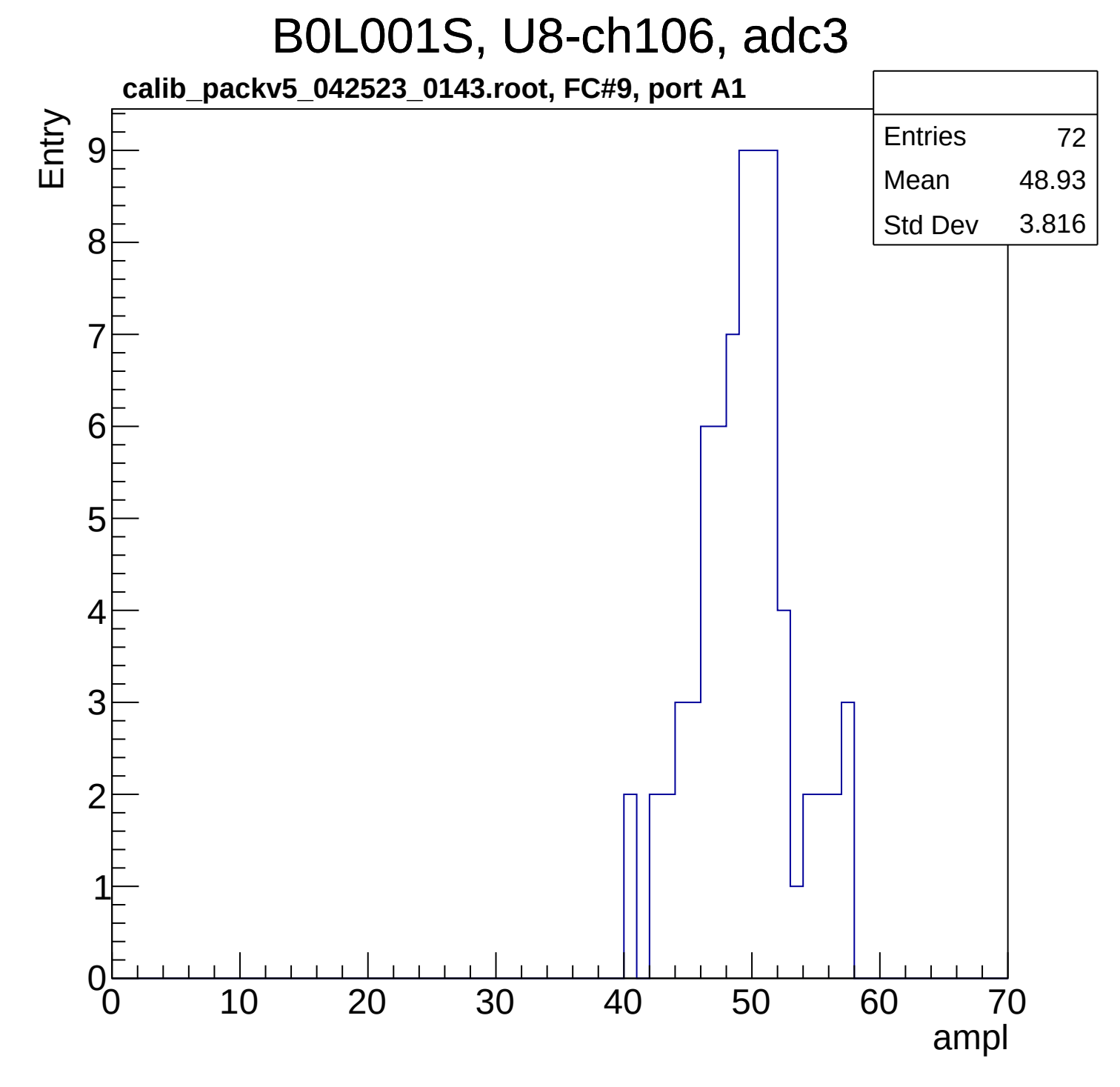

B0L001S, U8-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 28.95
Std Dev 5.679
Gaus mean : 29.5828
Gaus Width: 3.6794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 36.55
Std Dev 2.732
Gaus mean : 37.3305
Gaus Width: 3.2771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 42.58
Std Dev 3.116
Gaus mean : 42.9885
Gaus Width: 3.2705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 48.93
Std Dev 3.816
Entry
ampl
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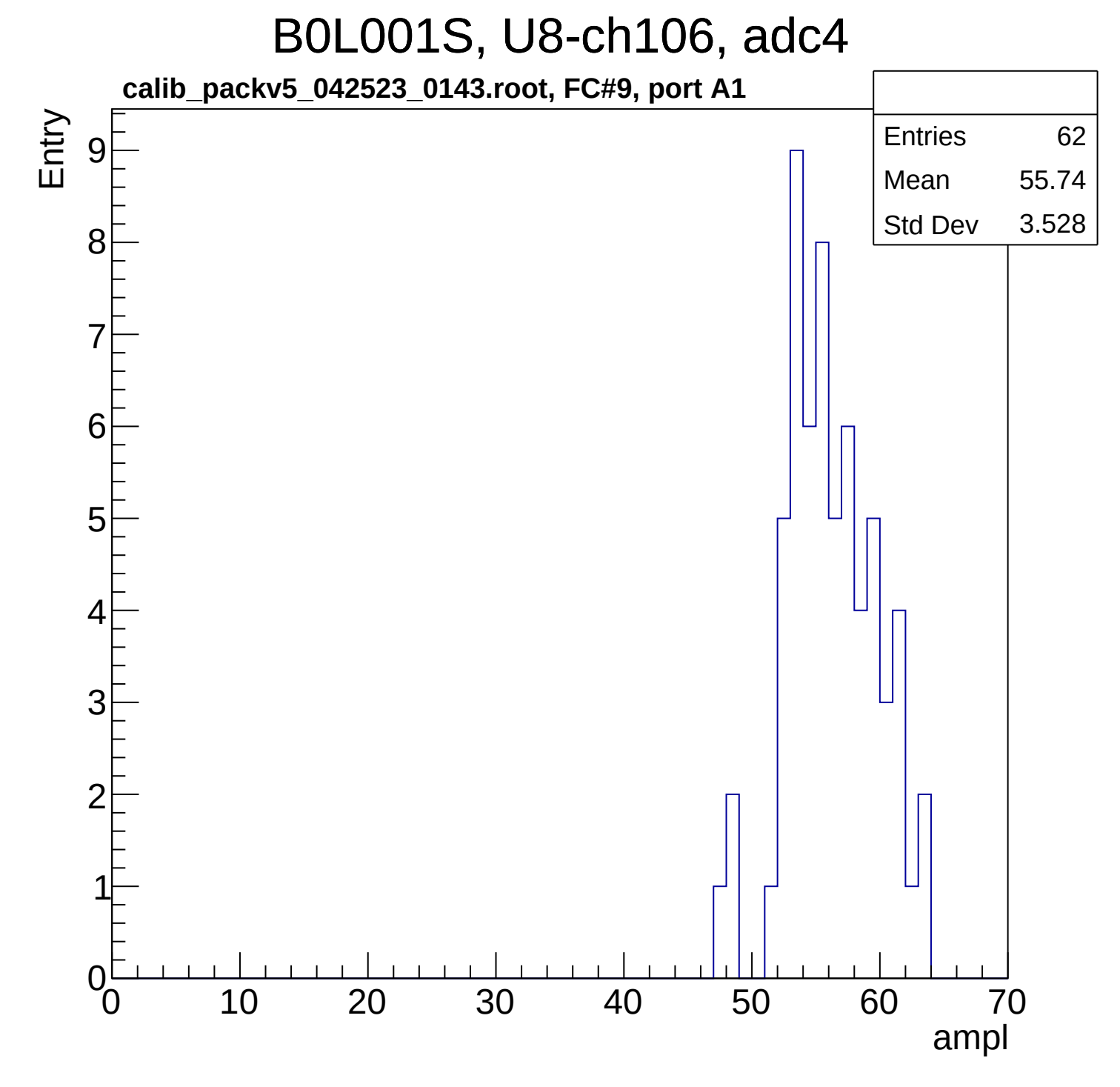
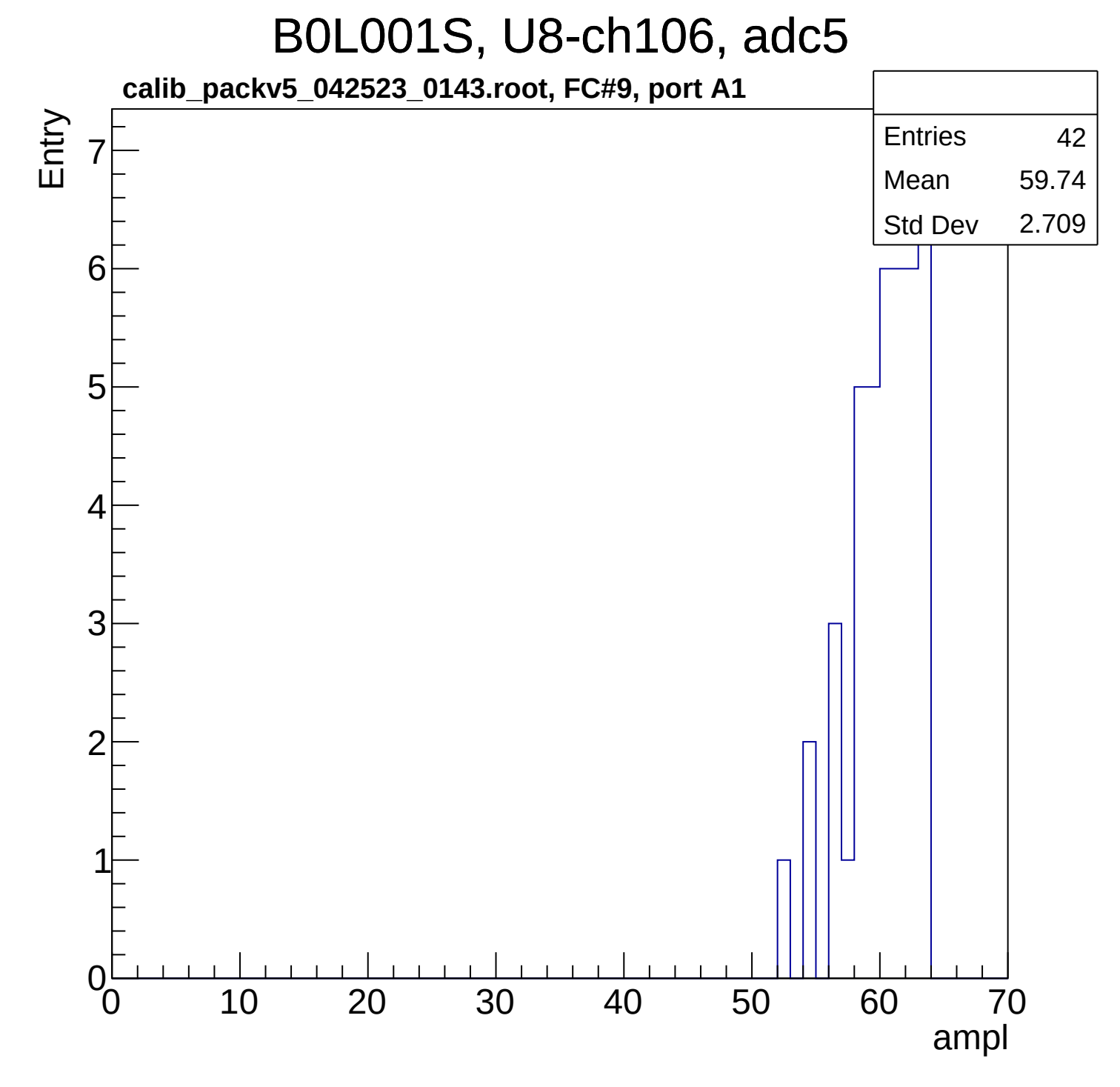
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.74
Std Dev 3.528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.74
Std Dev 2.709
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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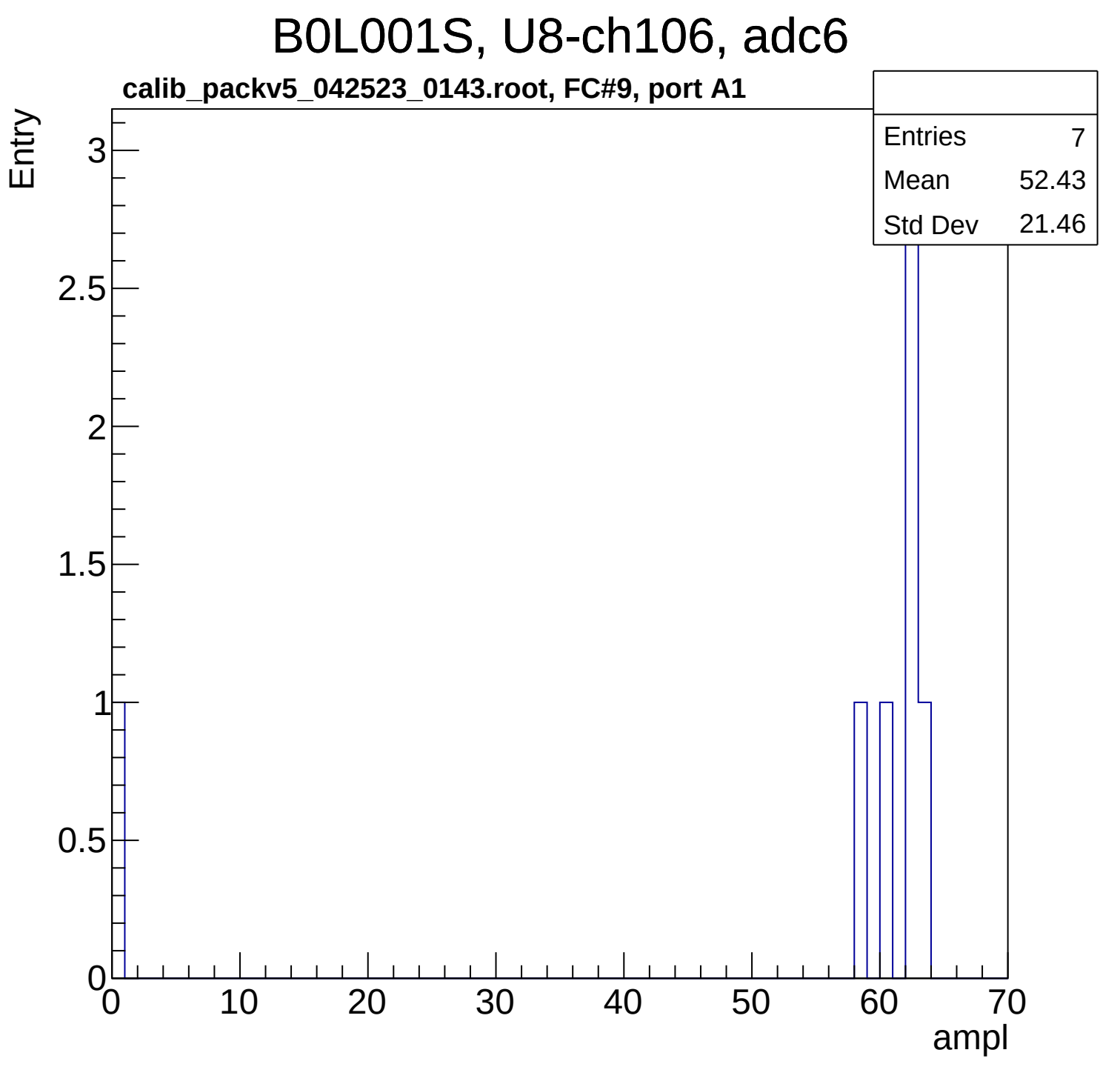
40
50
60
70


B0L001S, U8-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 52.43
Std Dev 21.46
Entry
ampl
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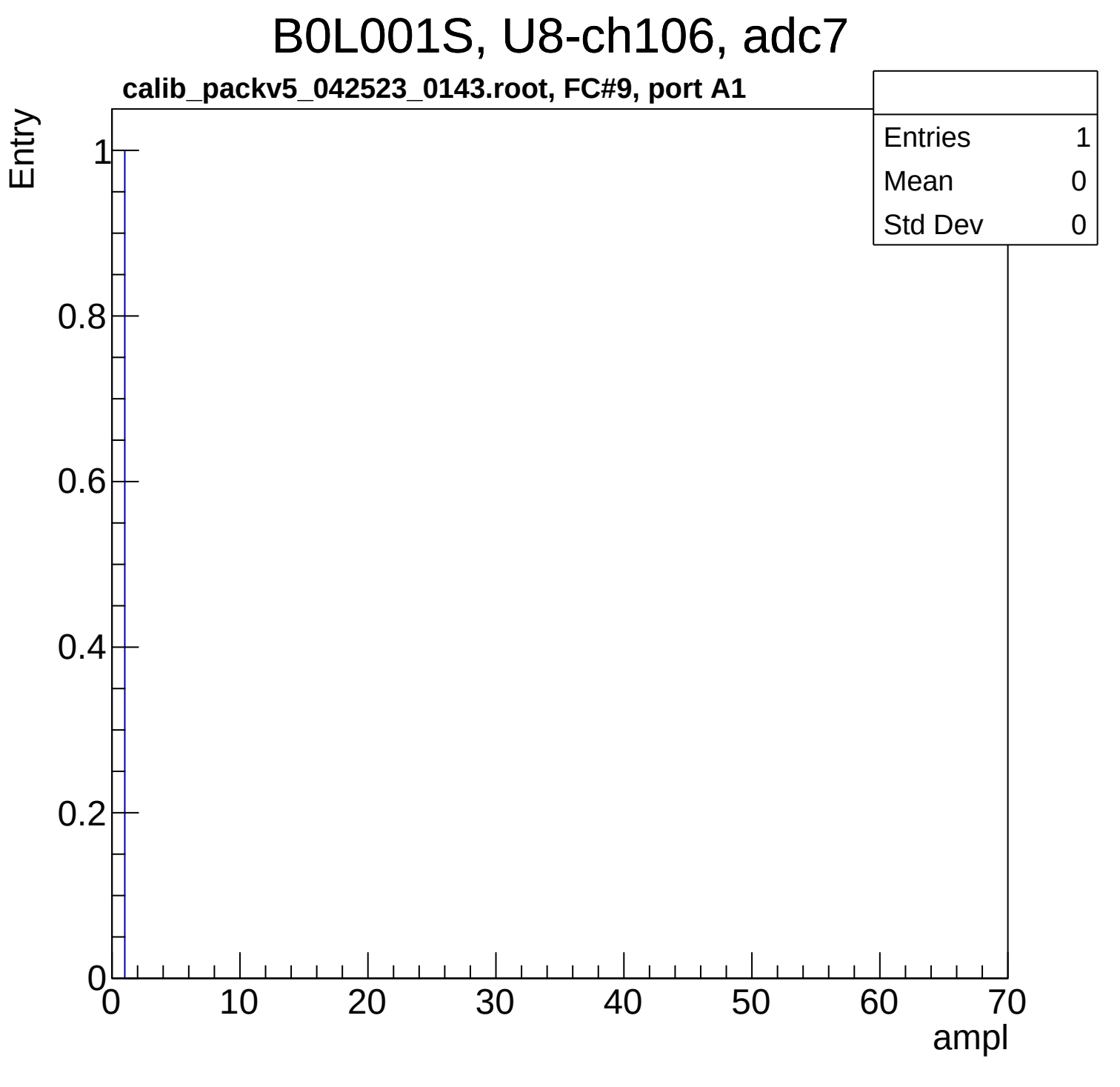
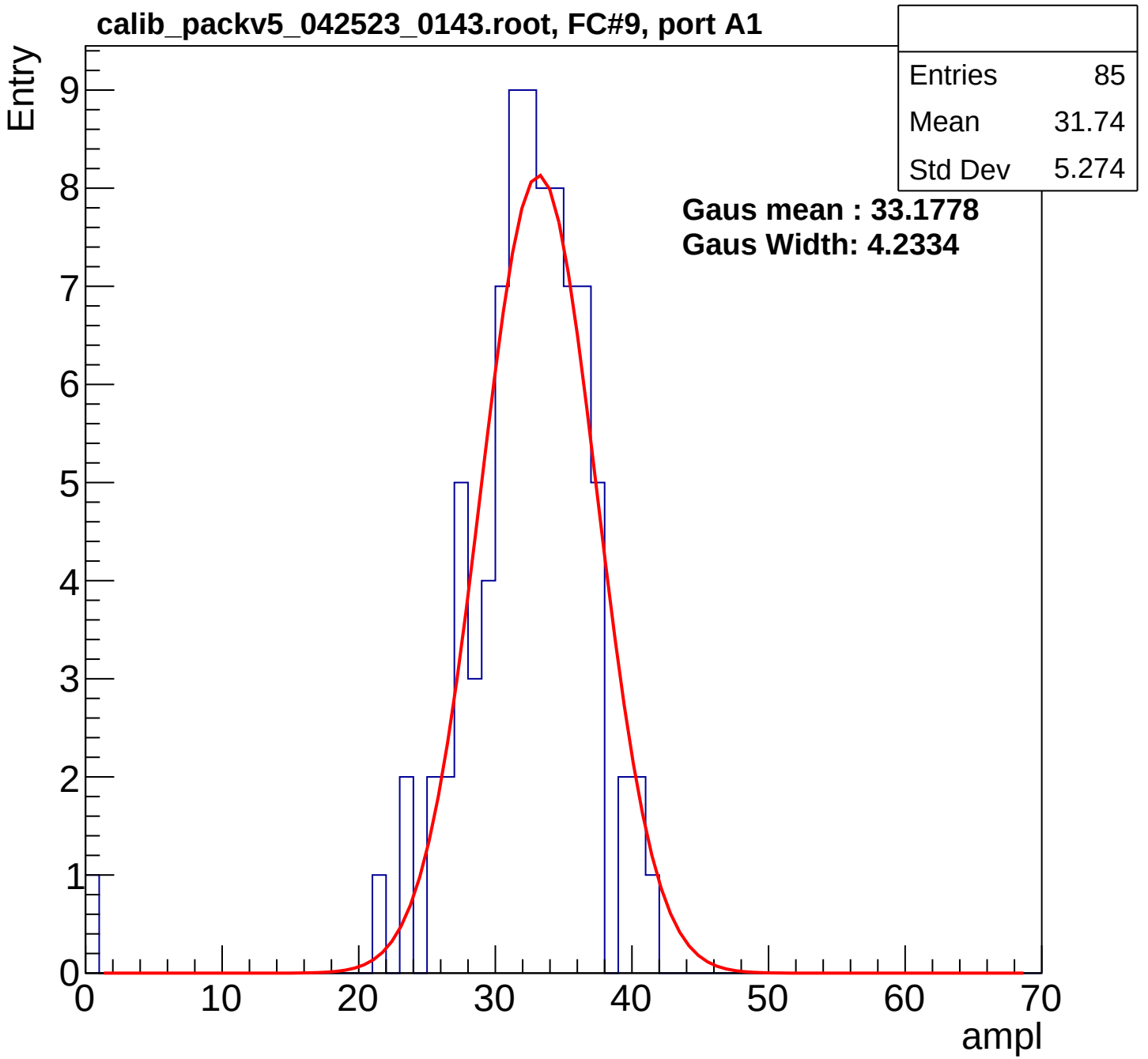
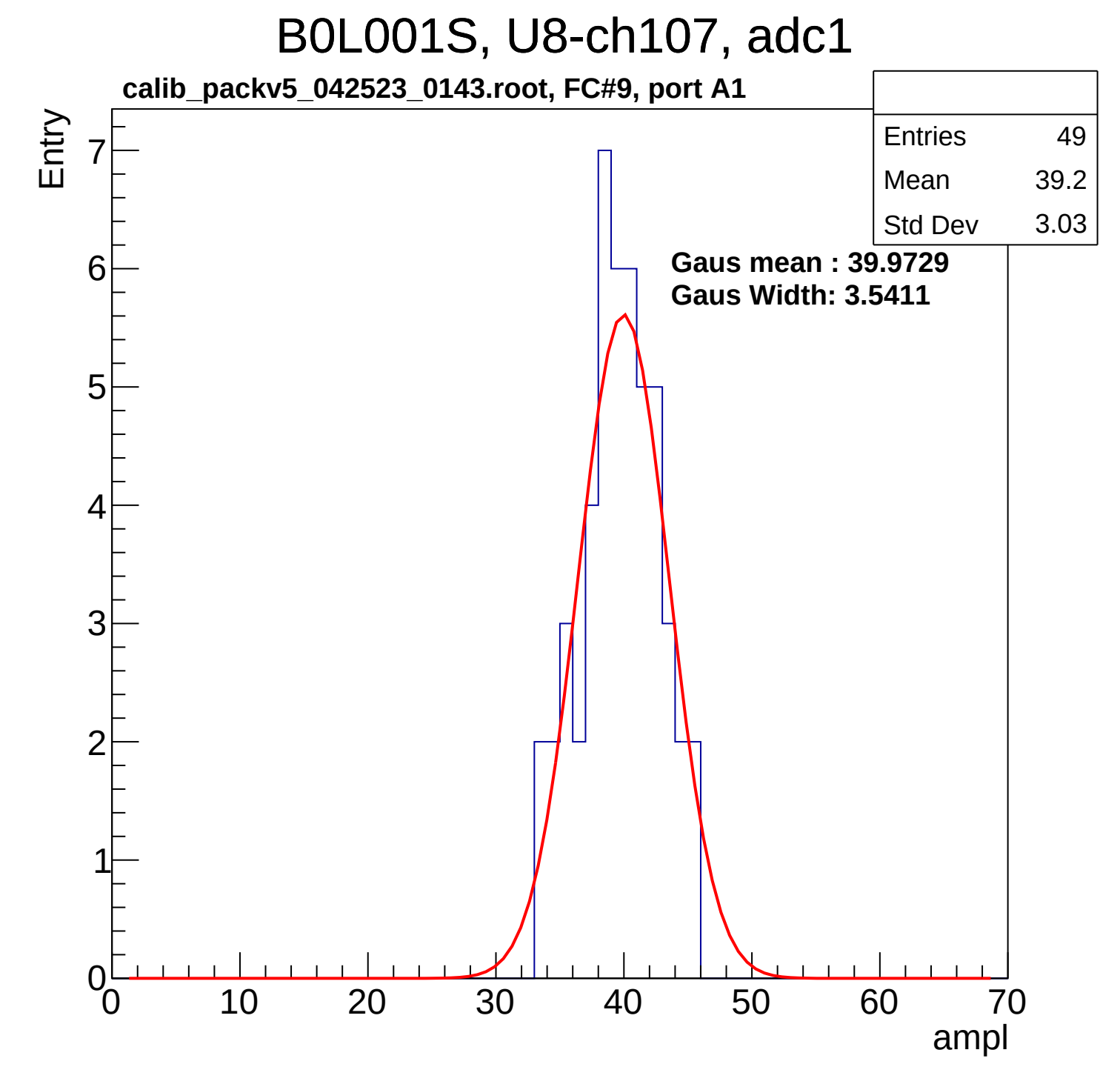
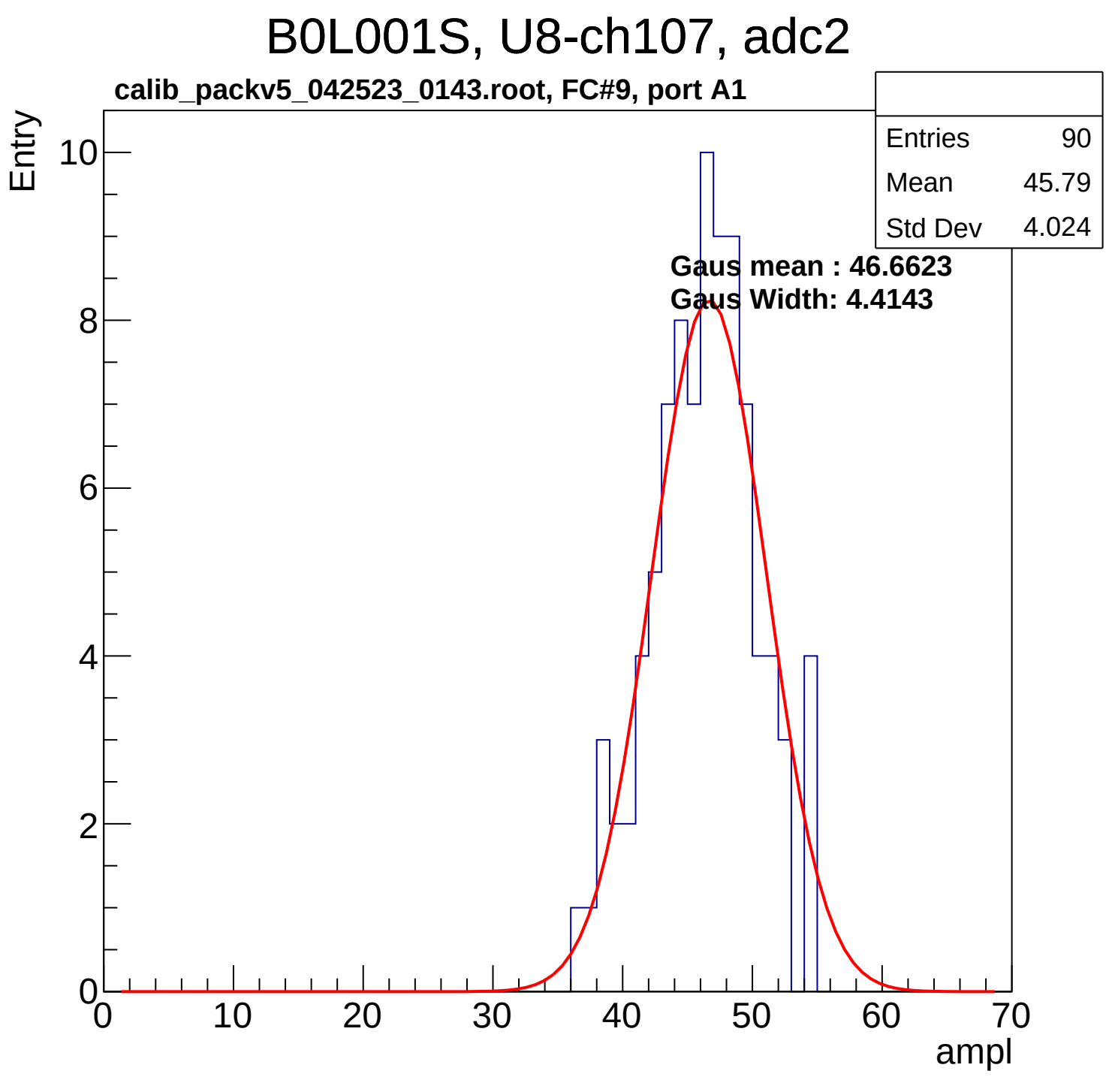
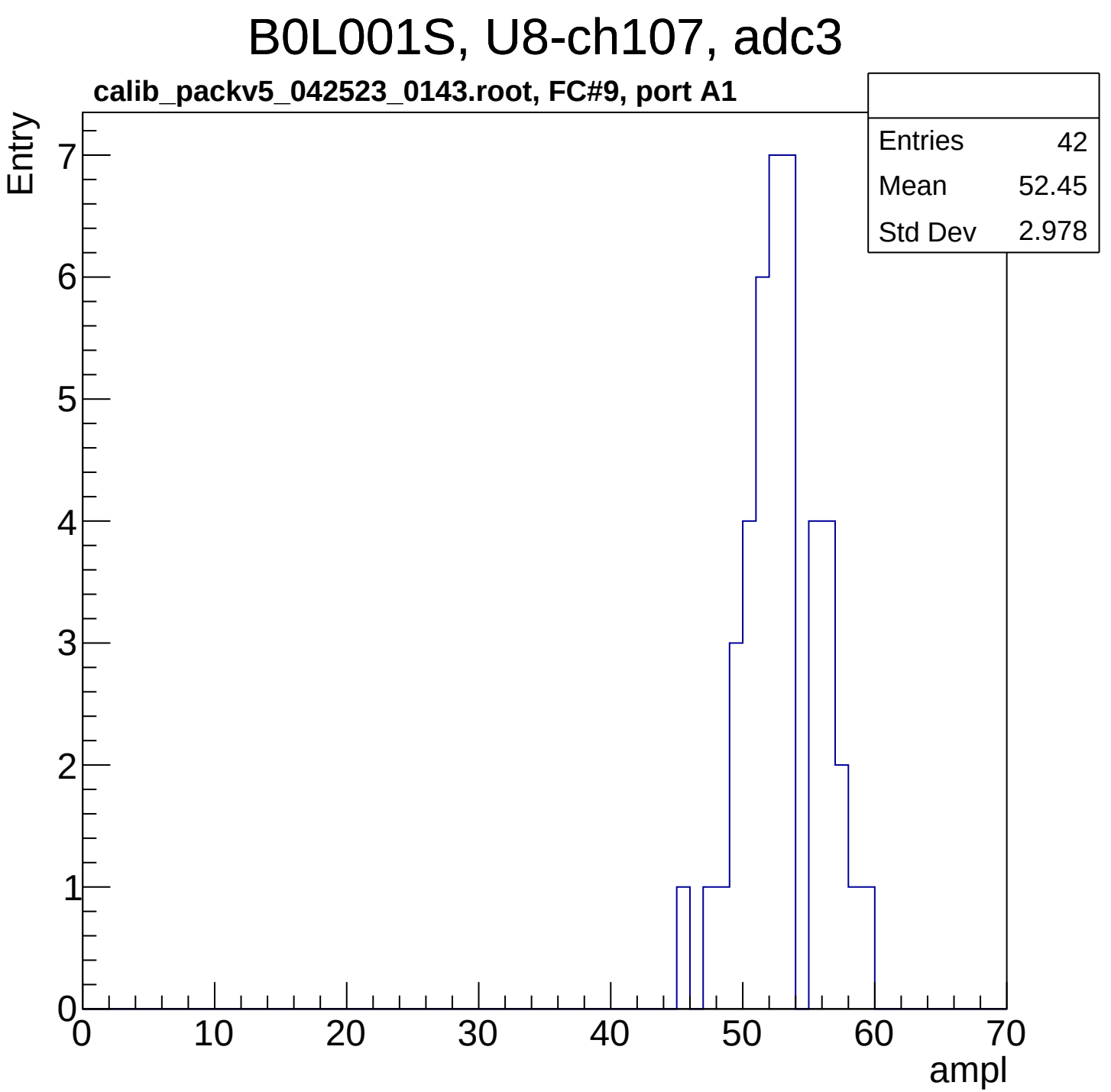
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 31.74
Std Dev 5.274
Gaus mean : 33.1778
Gaus Width: 4.2334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 39.2
Std Dev 3.03
Gaus mean : 39.9729
Gaus Width: 3.5411
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 45.79
Std Dev 4.024
Gaus mean : 46.6623
Gaus Width: 4.4143
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 52.45
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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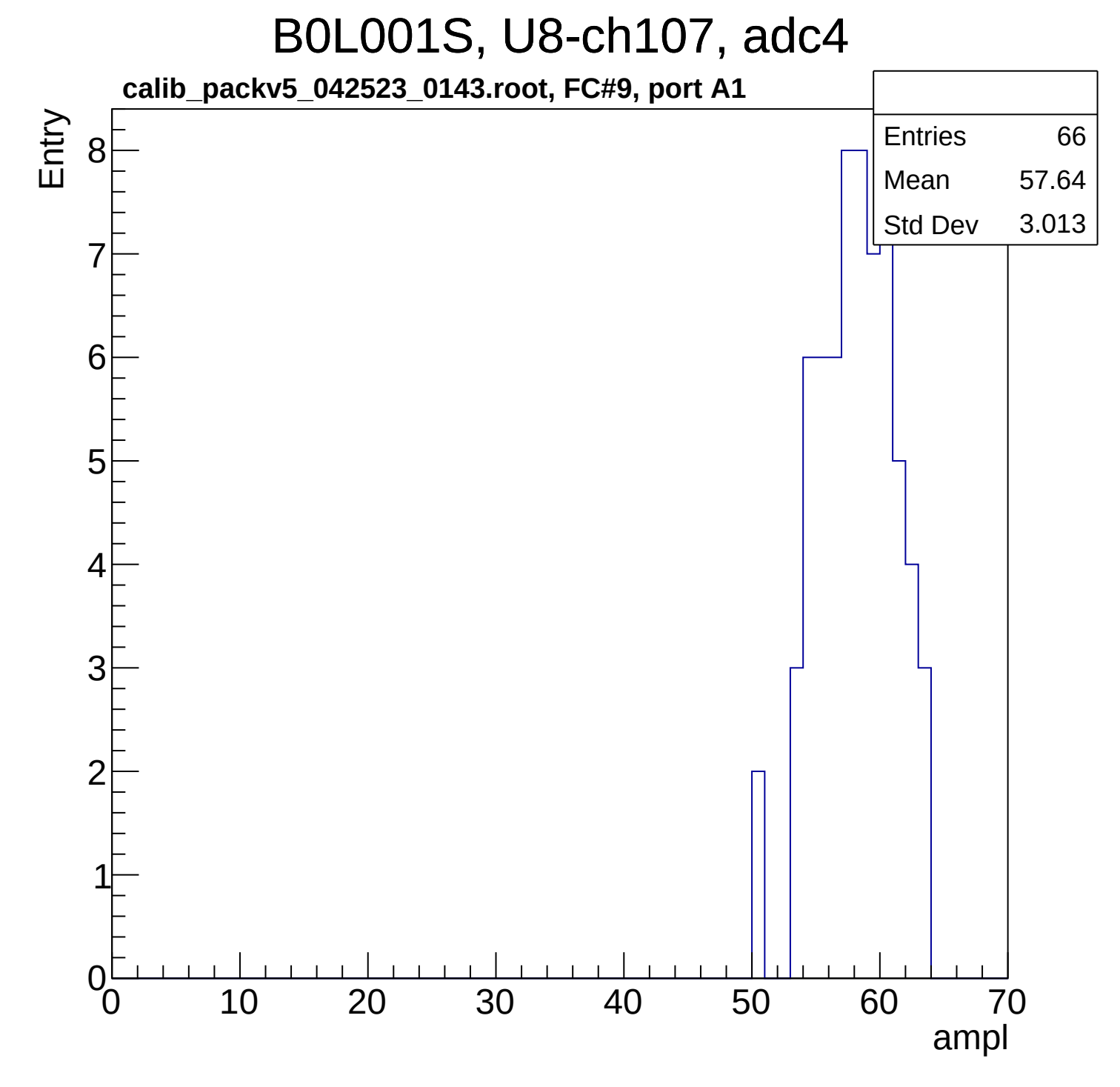
B0L001S, U8-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.64
Std Dev 3.013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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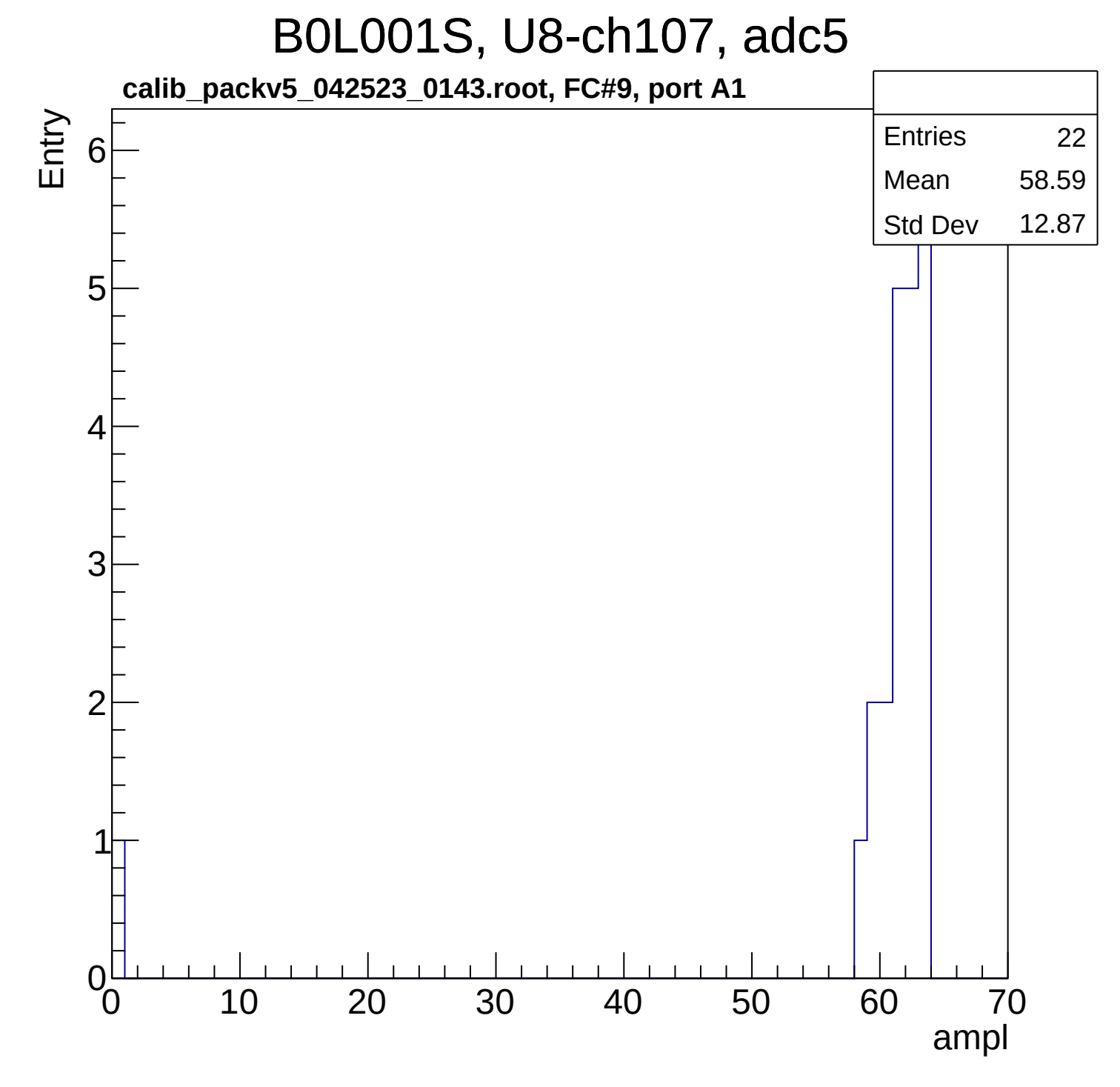
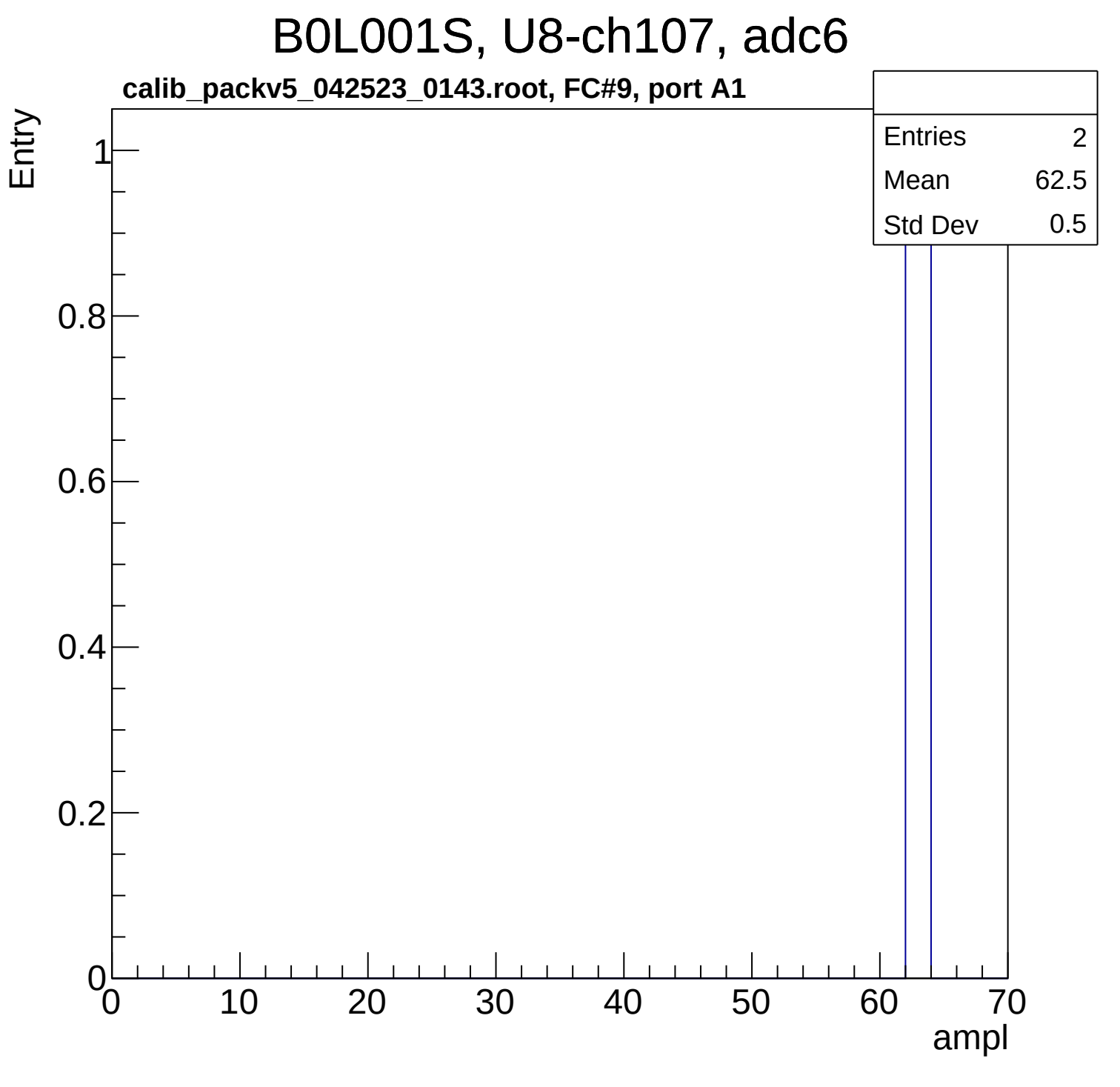
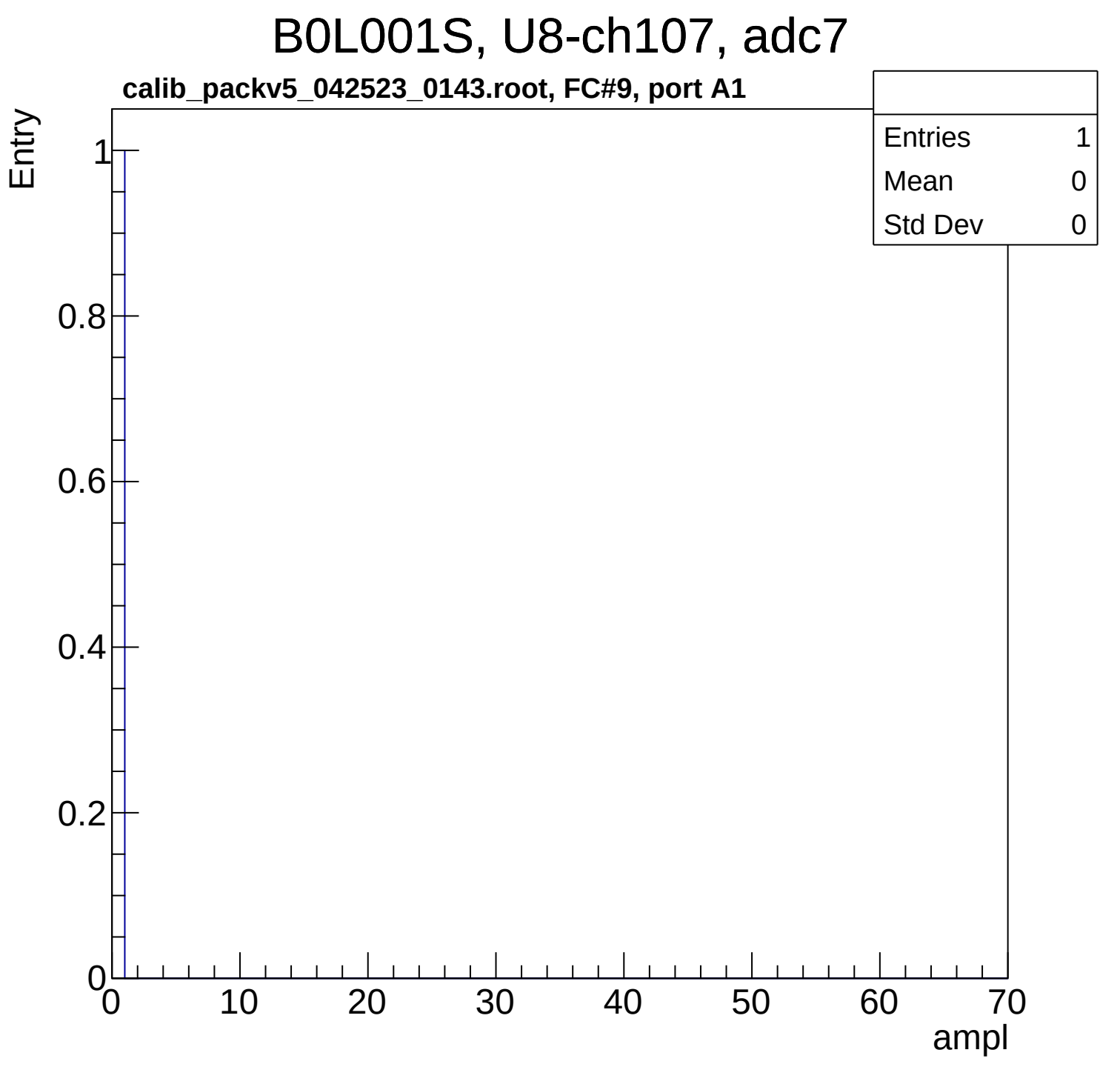
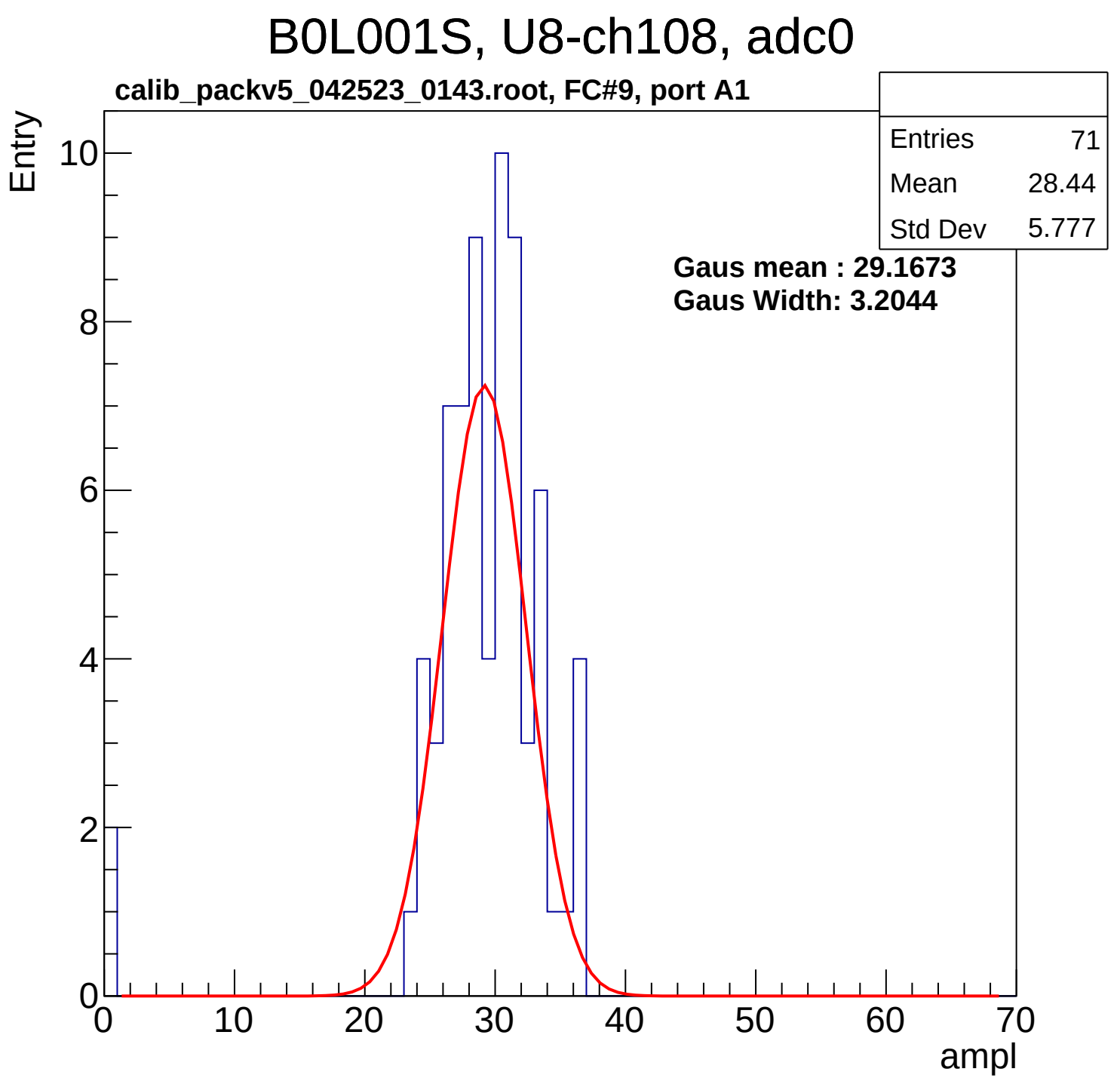

B0L001S, U8-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.59
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.44
Std Dev 5.777
Gaus mean : 29.1673
Gaus Width: 3.2044
Entry
ampl
0
2
4
6
8
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0
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20
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60
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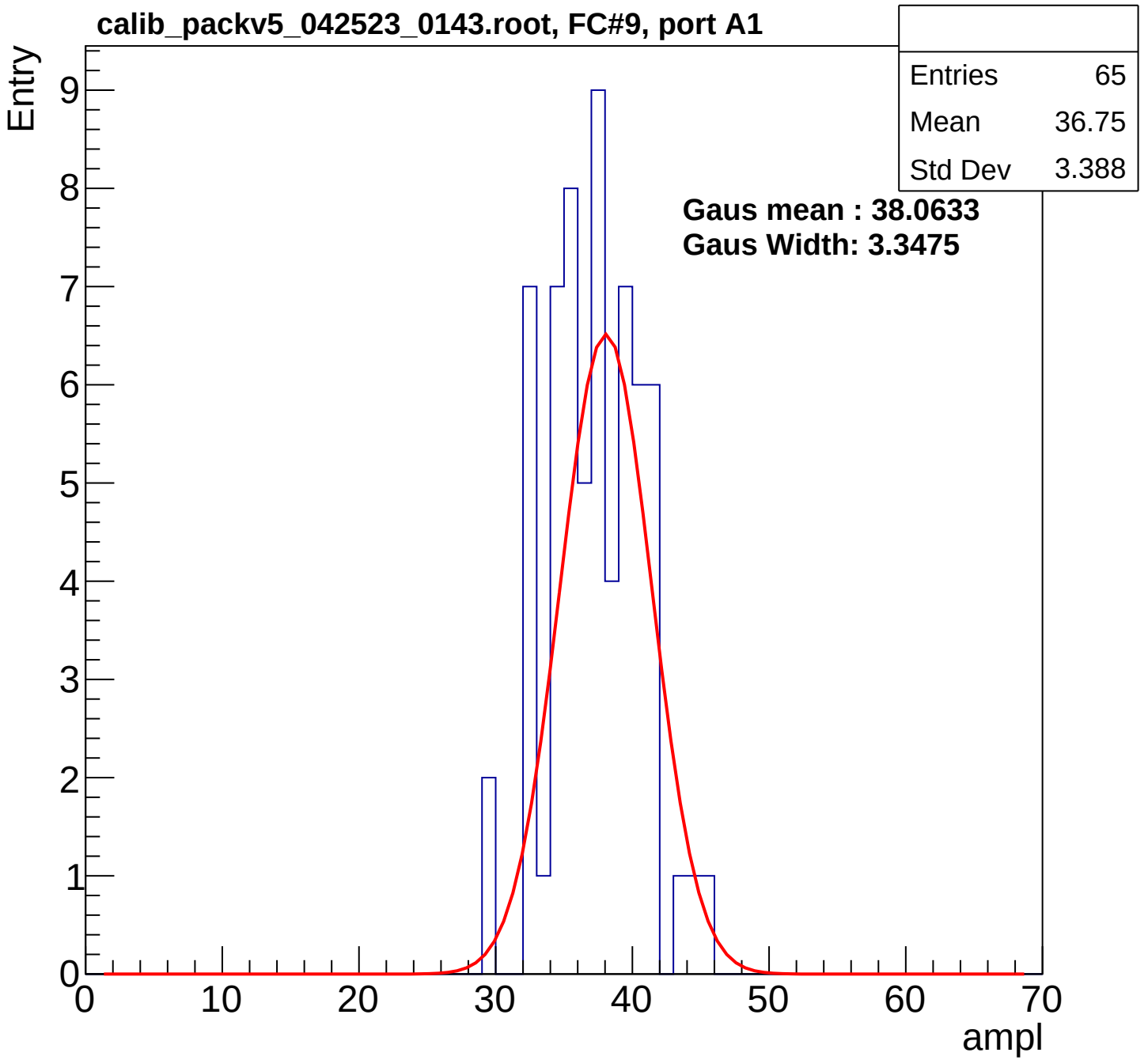
B0L001S, U8-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.75
Std Dev 3.388
Gaus mean : 38.0633
Gaus Width: 3.3475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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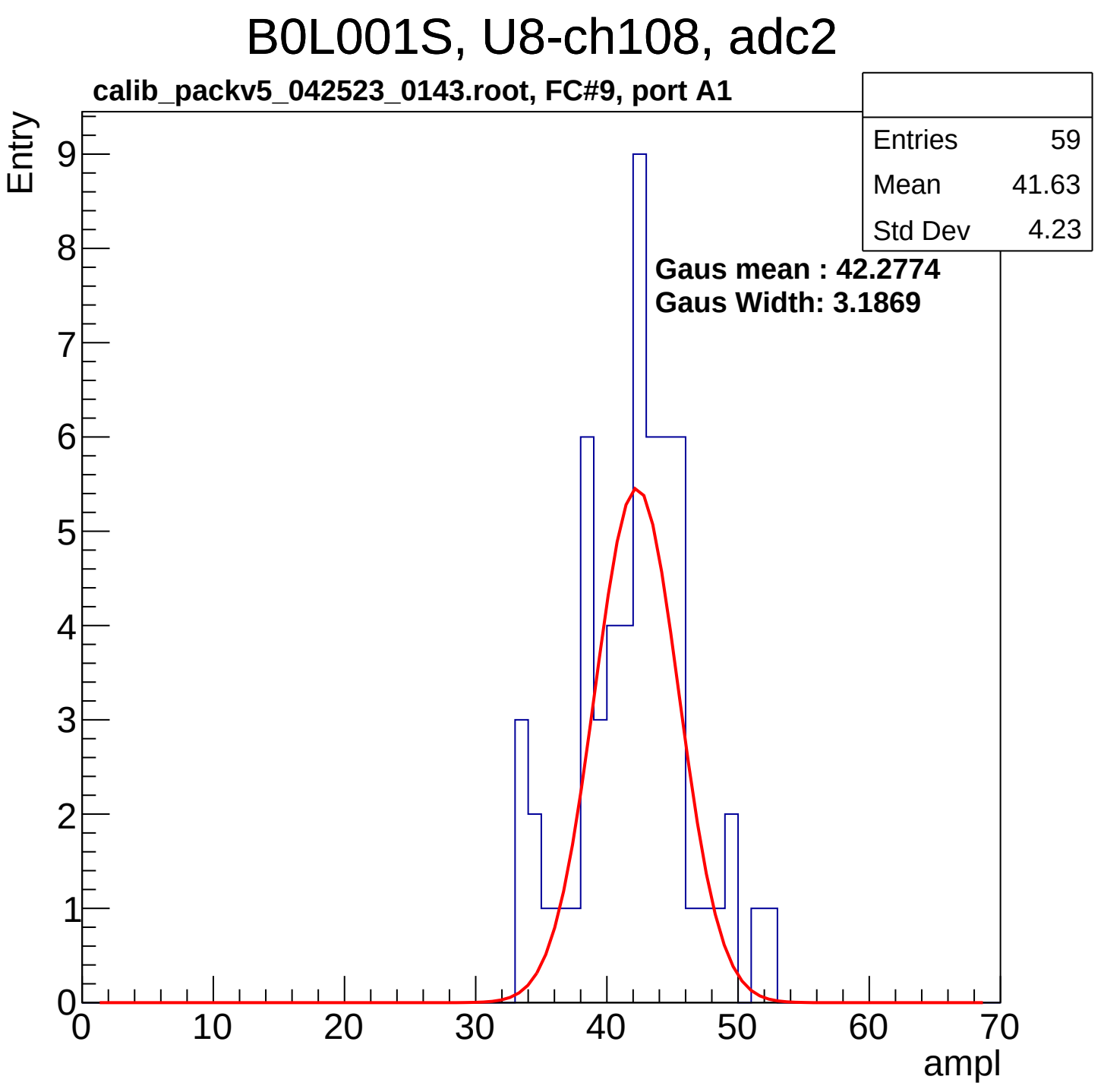
B0L001S, U8-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 41.63
Std Dev 4.23
Gaus mean : 42.2774
Gaus Width: 3.1869
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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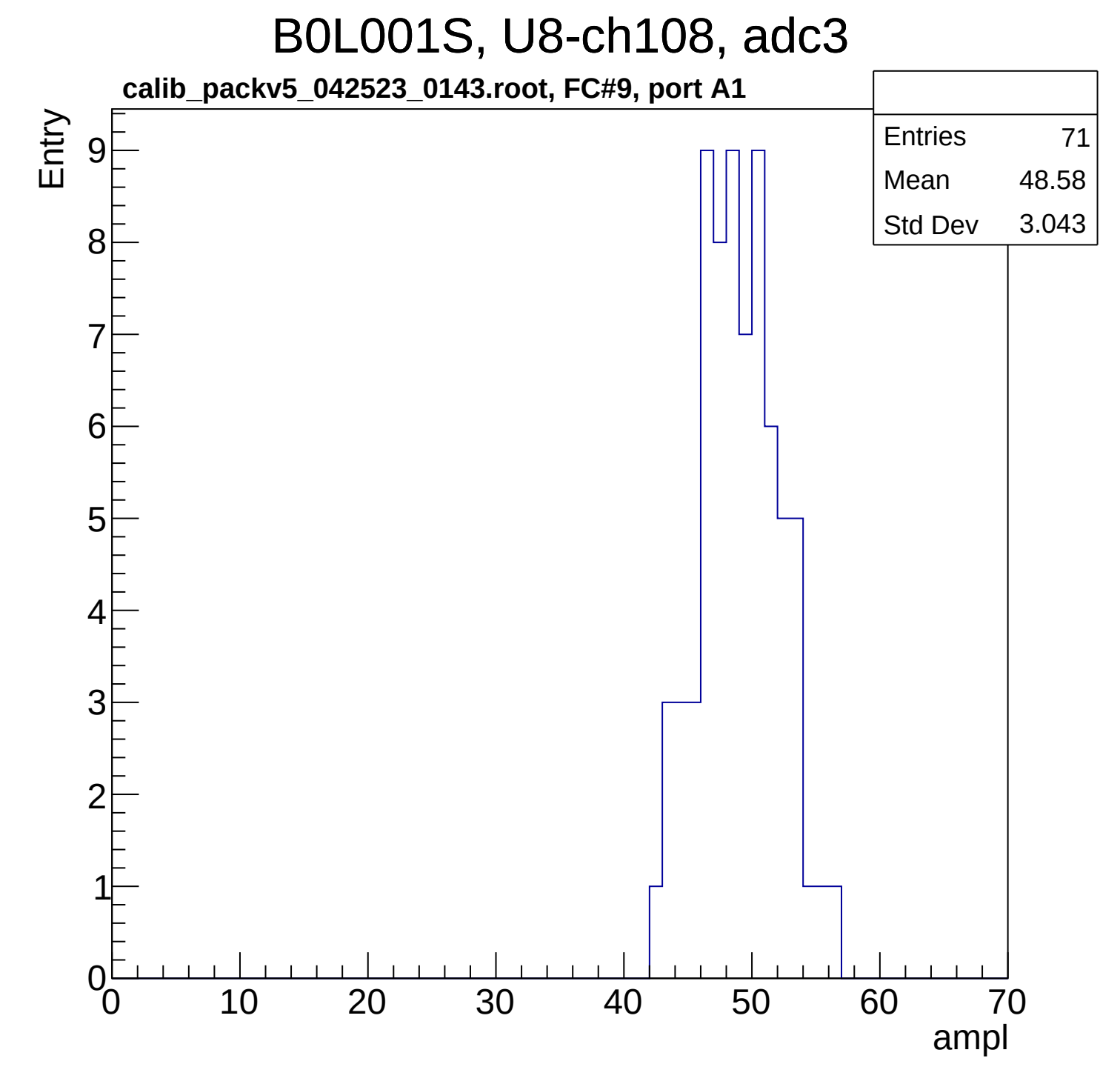
B0L001S, U8-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 48.58
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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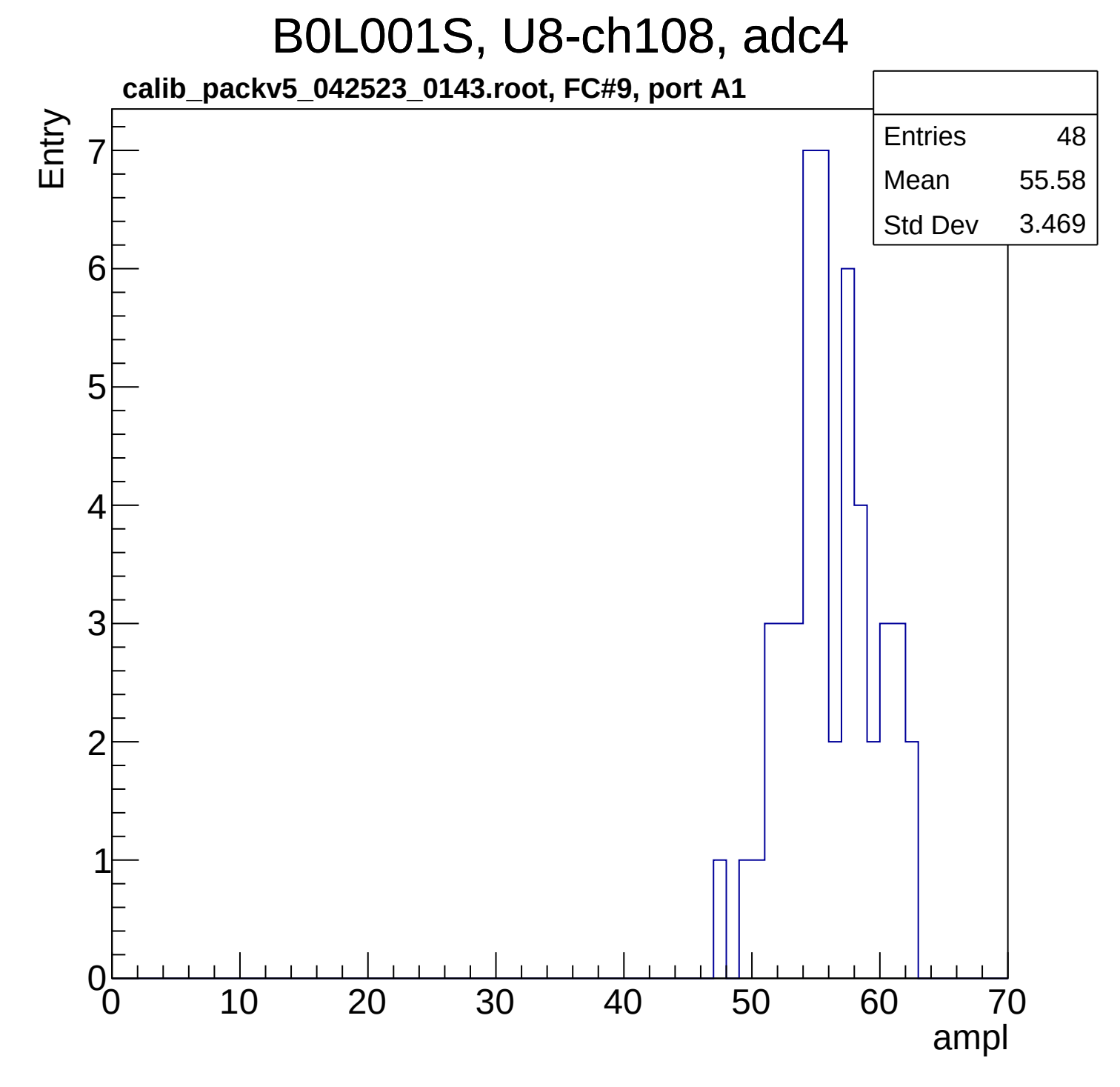
B0L001S, U8-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.58
Std Dev 3.469
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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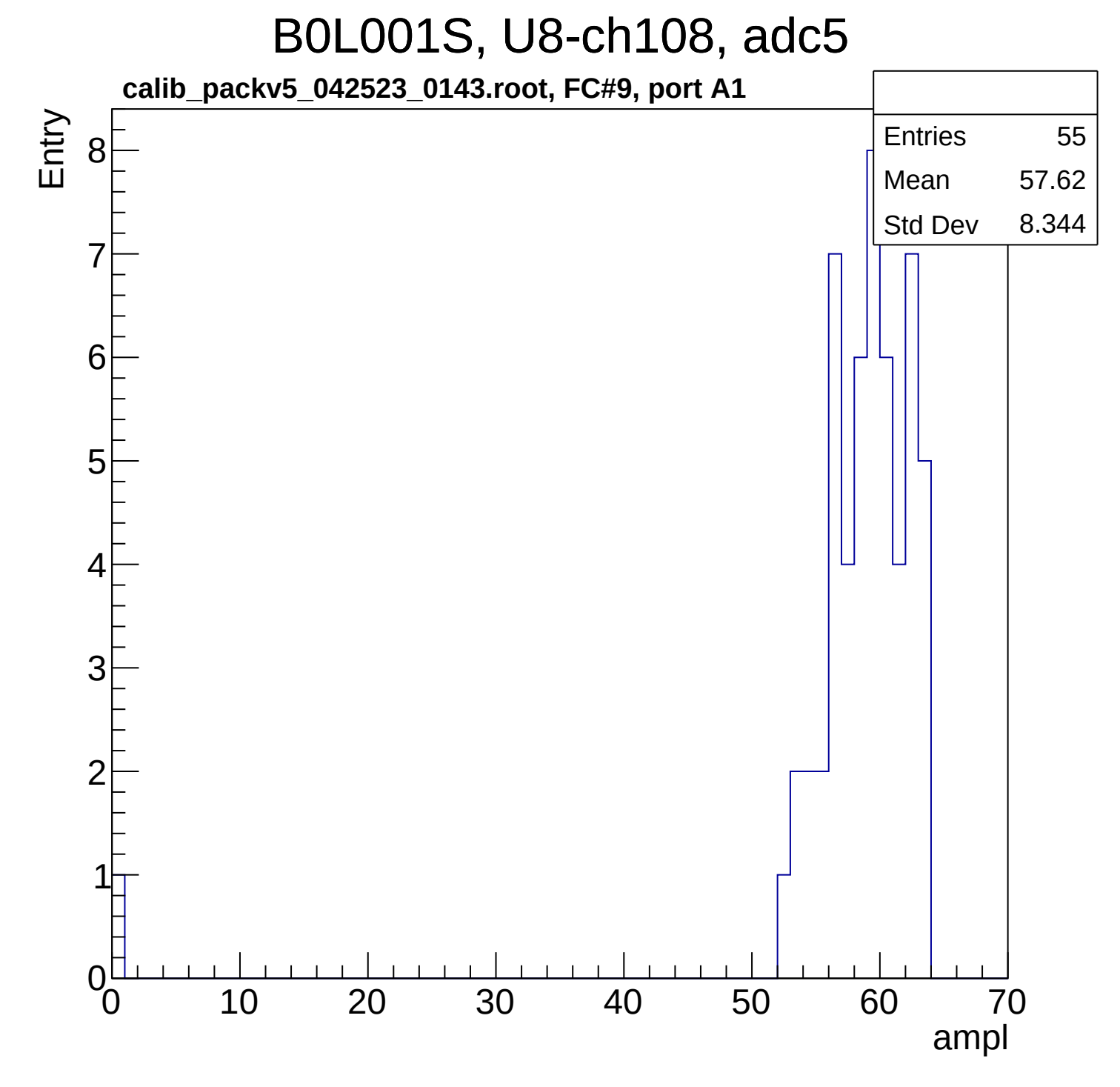
B0L001S, U8-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.62
Std Dev 8.344
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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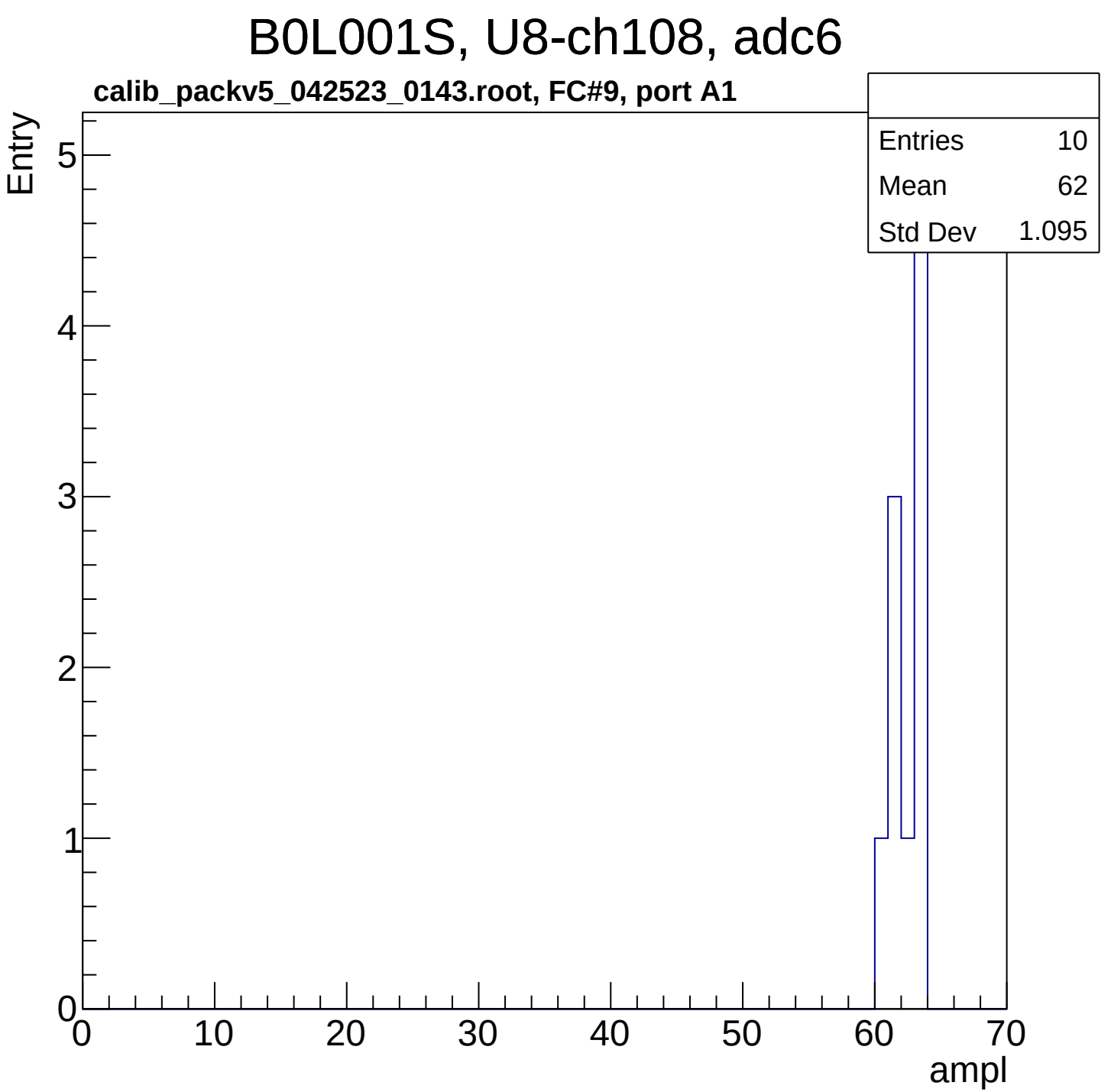
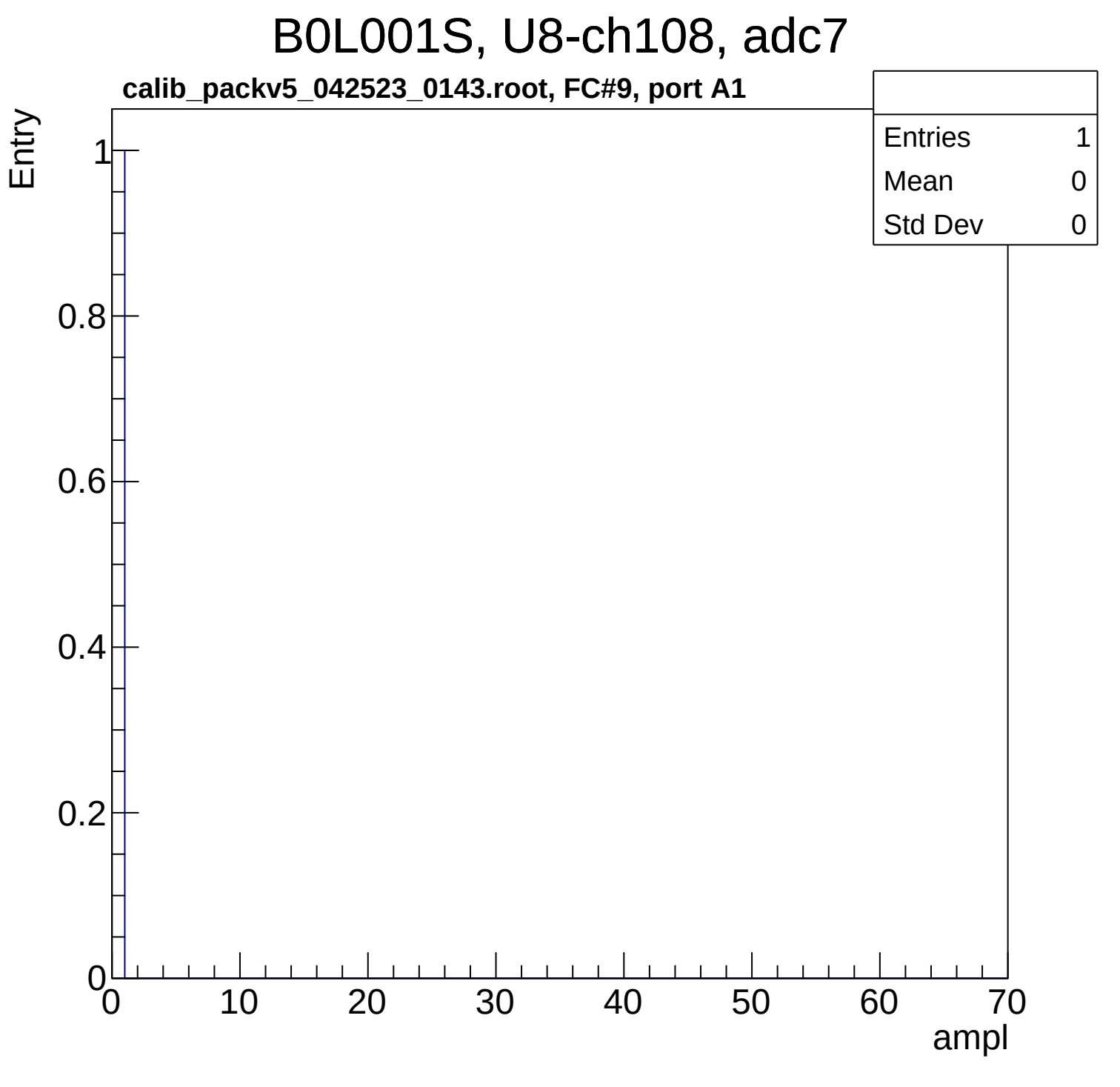
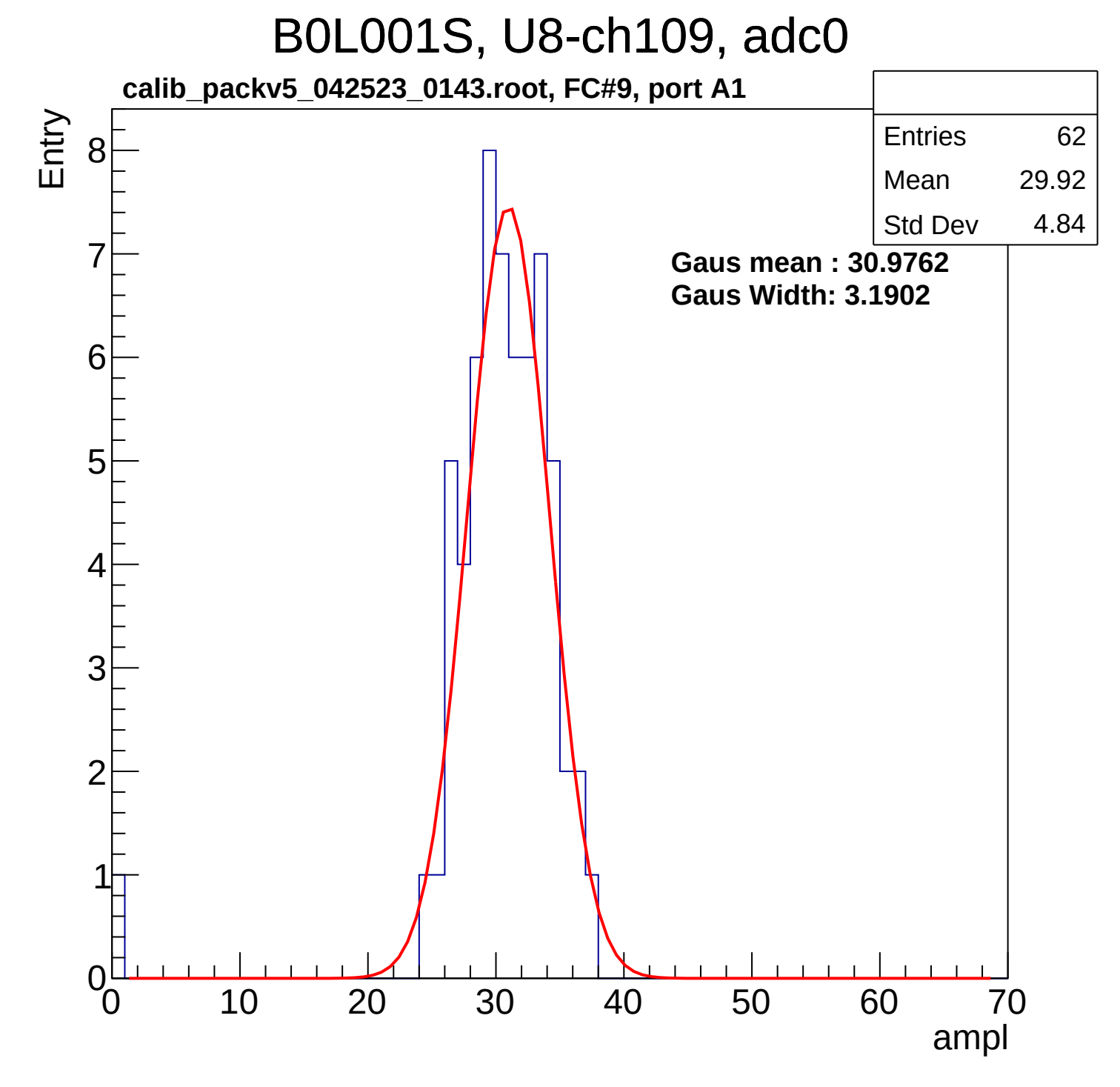
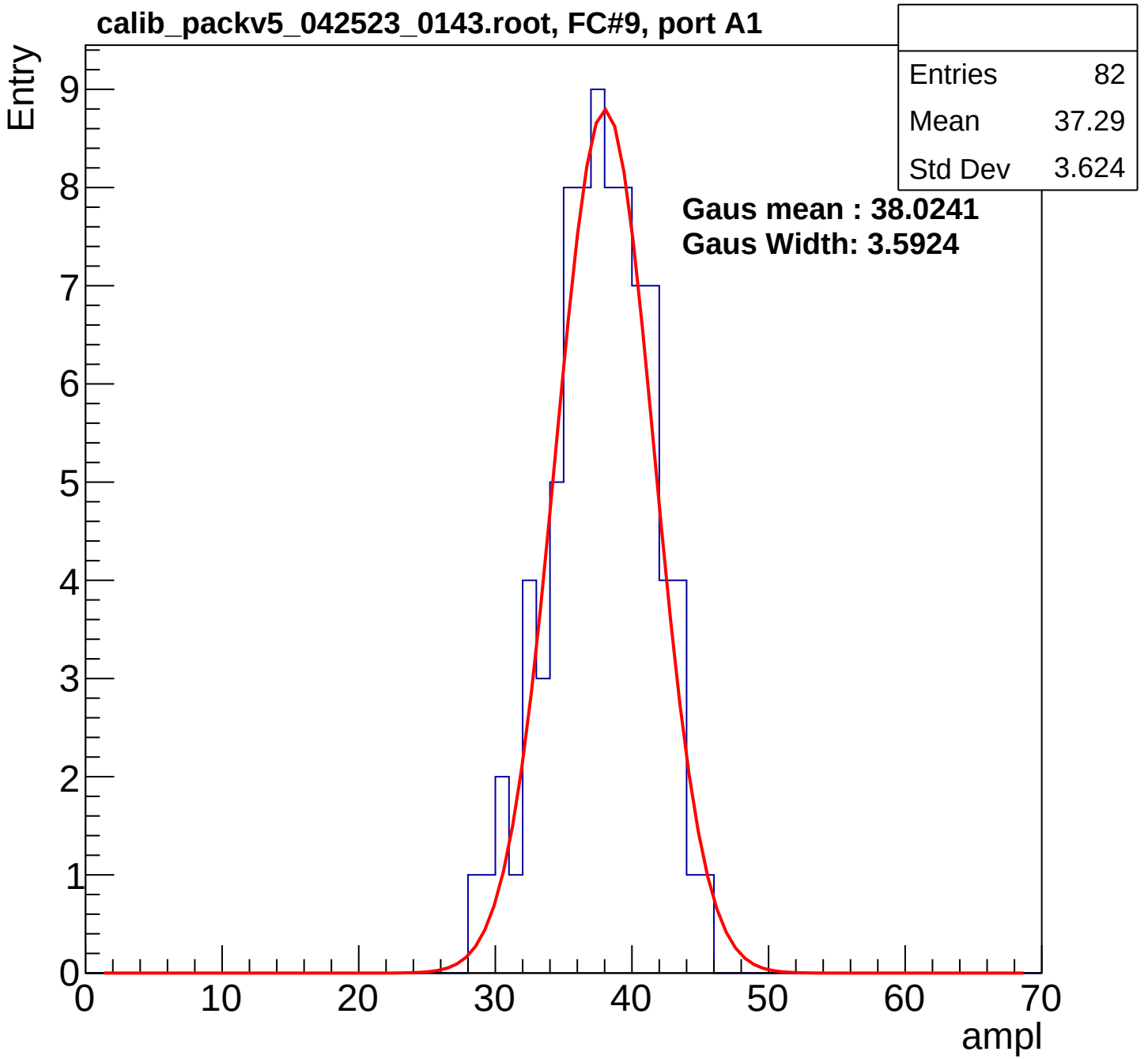
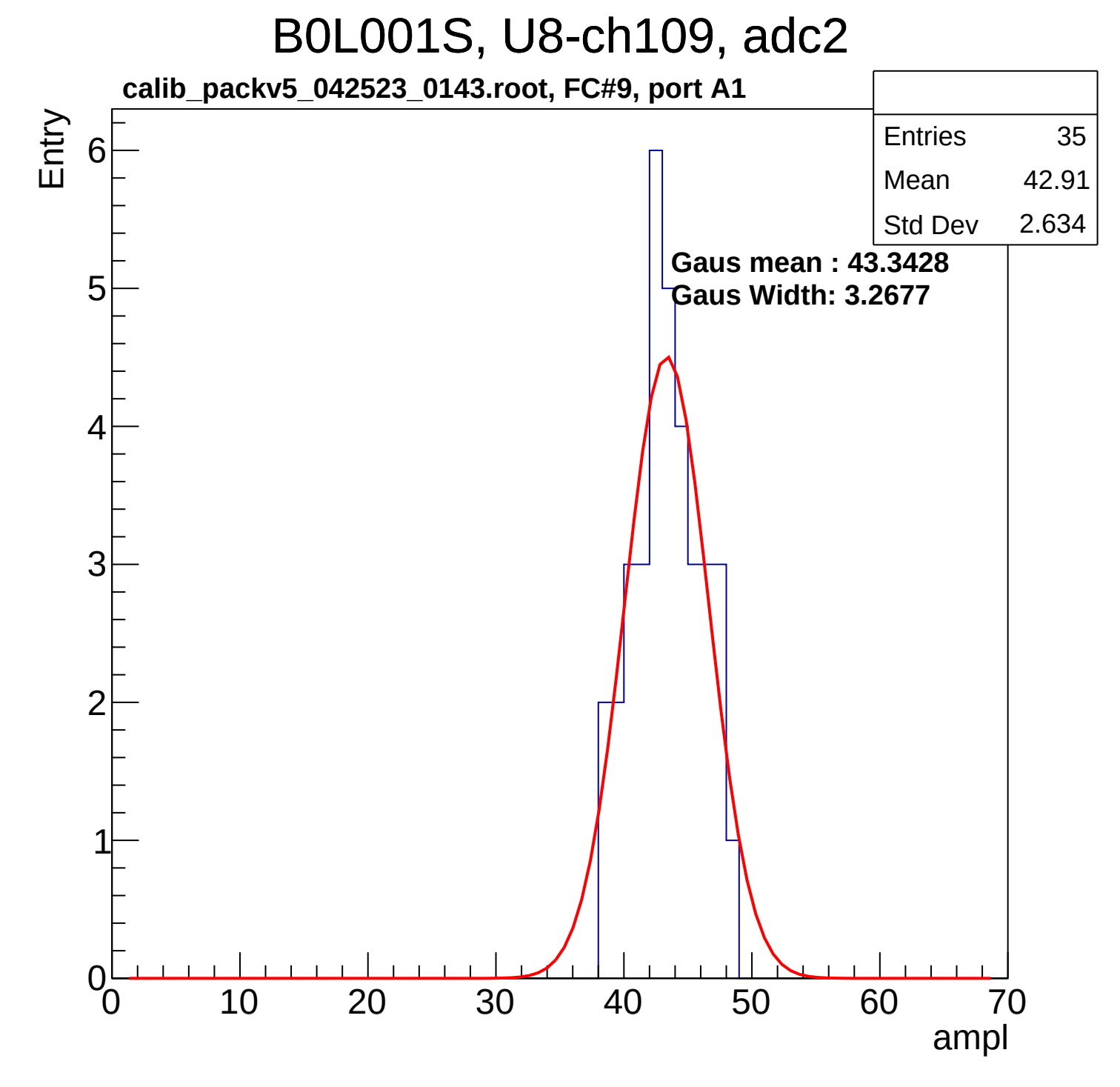
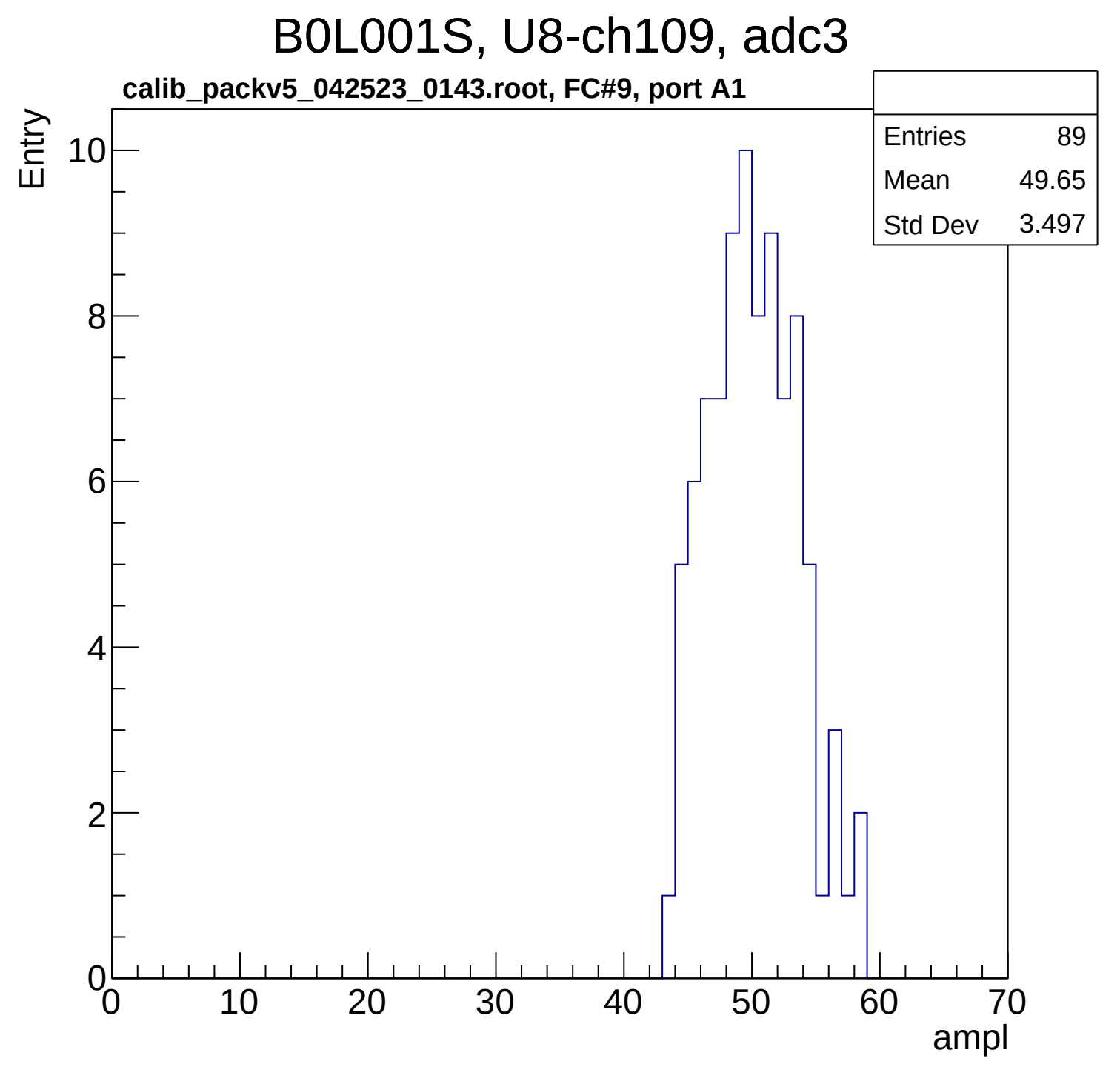

B0L001S, U8-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 62
Std Dev 1.095
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

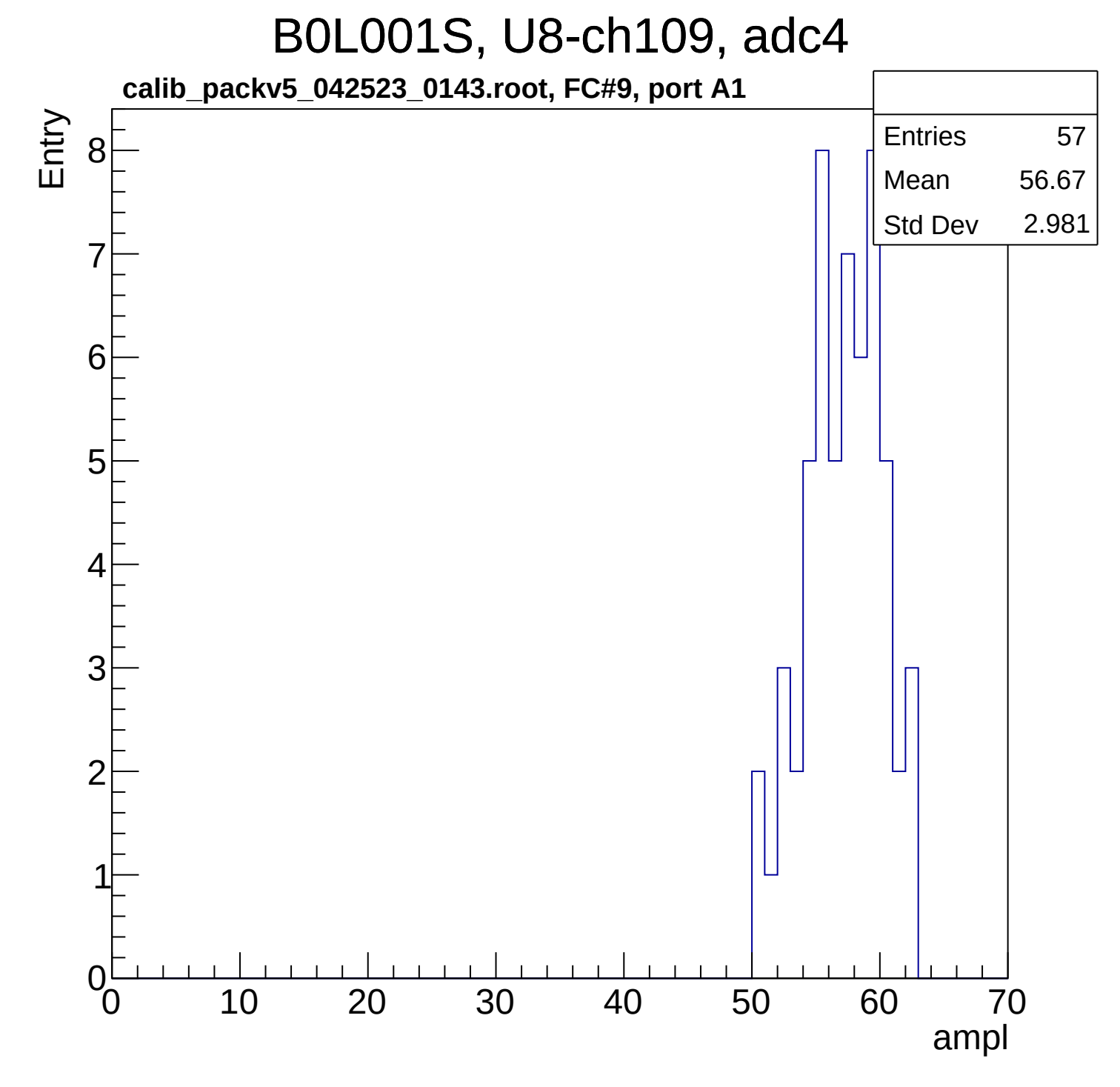

B0L001S, U8-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 29.92
Std Dev 4.84
Gaus mean : 30.9762
Gaus Width: 3.1902
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 37.29
Std Dev 3.624
Gaus mean : 38.0241
Gaus Width: 3.5924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 42.91
Std Dev 2.634
Gaus mean : 43.3428
Gaus Width: 3.2677
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U8-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 49.65
Std Dev 3.497
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.67
Std Dev 2.981
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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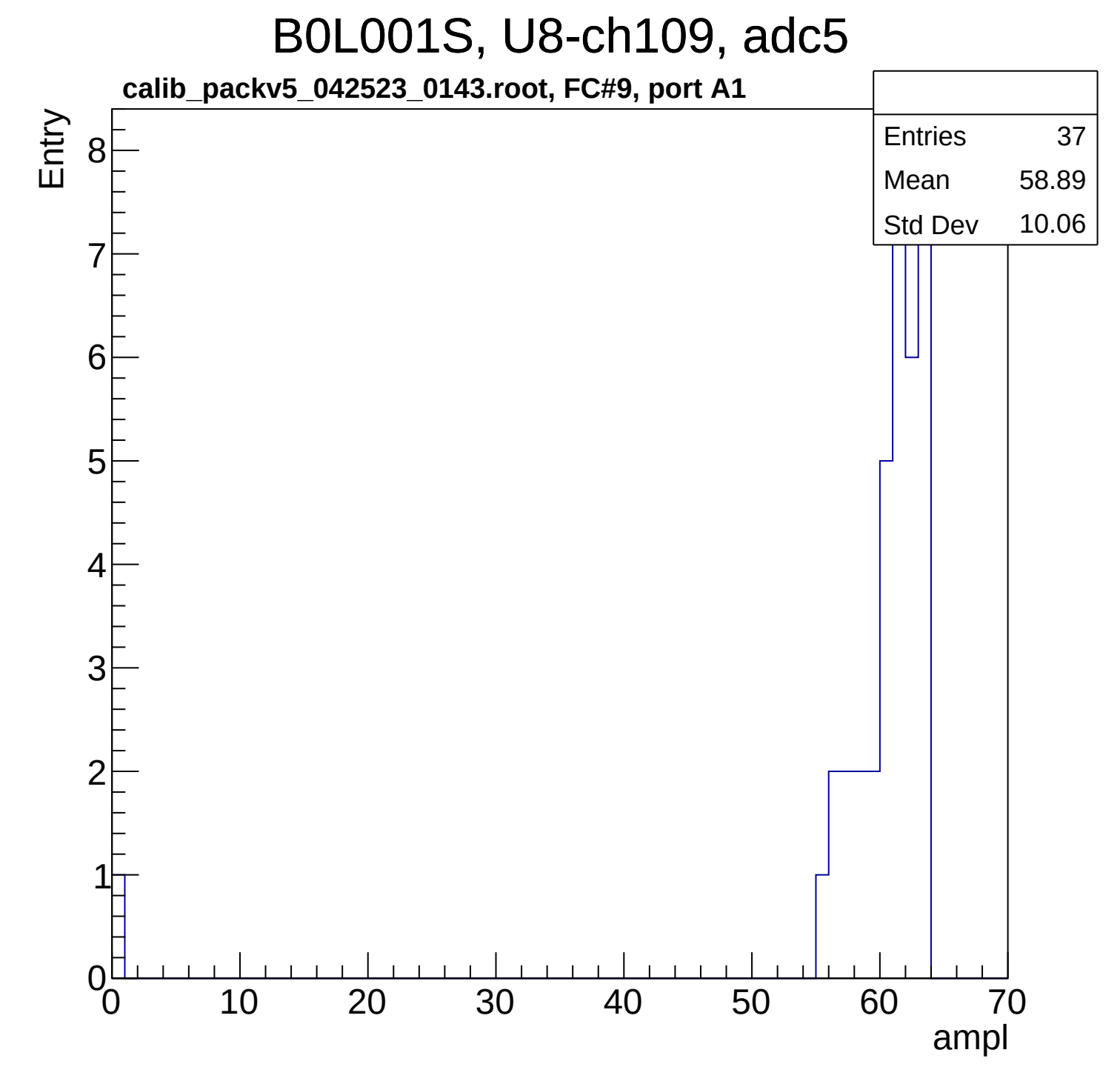
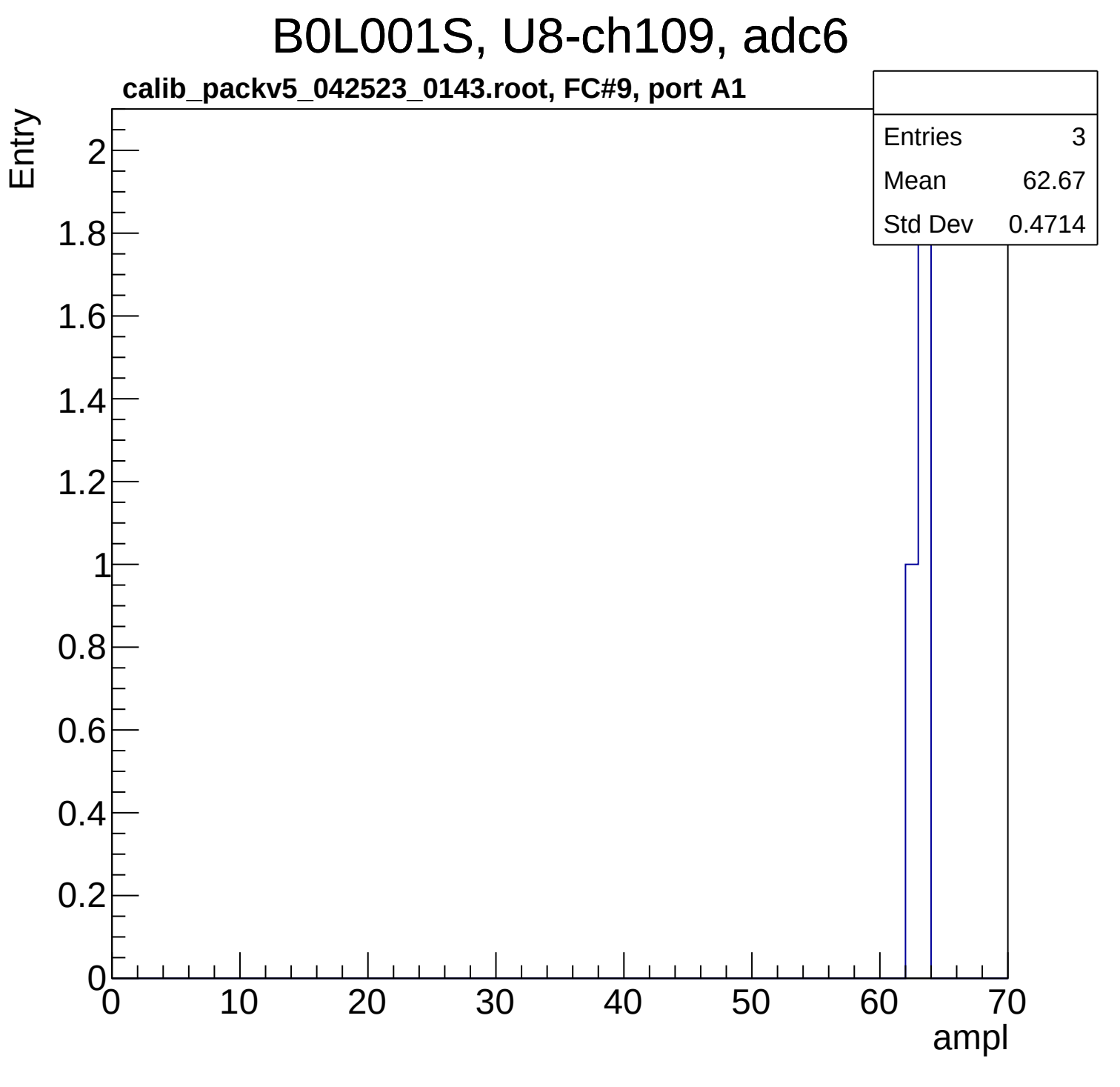
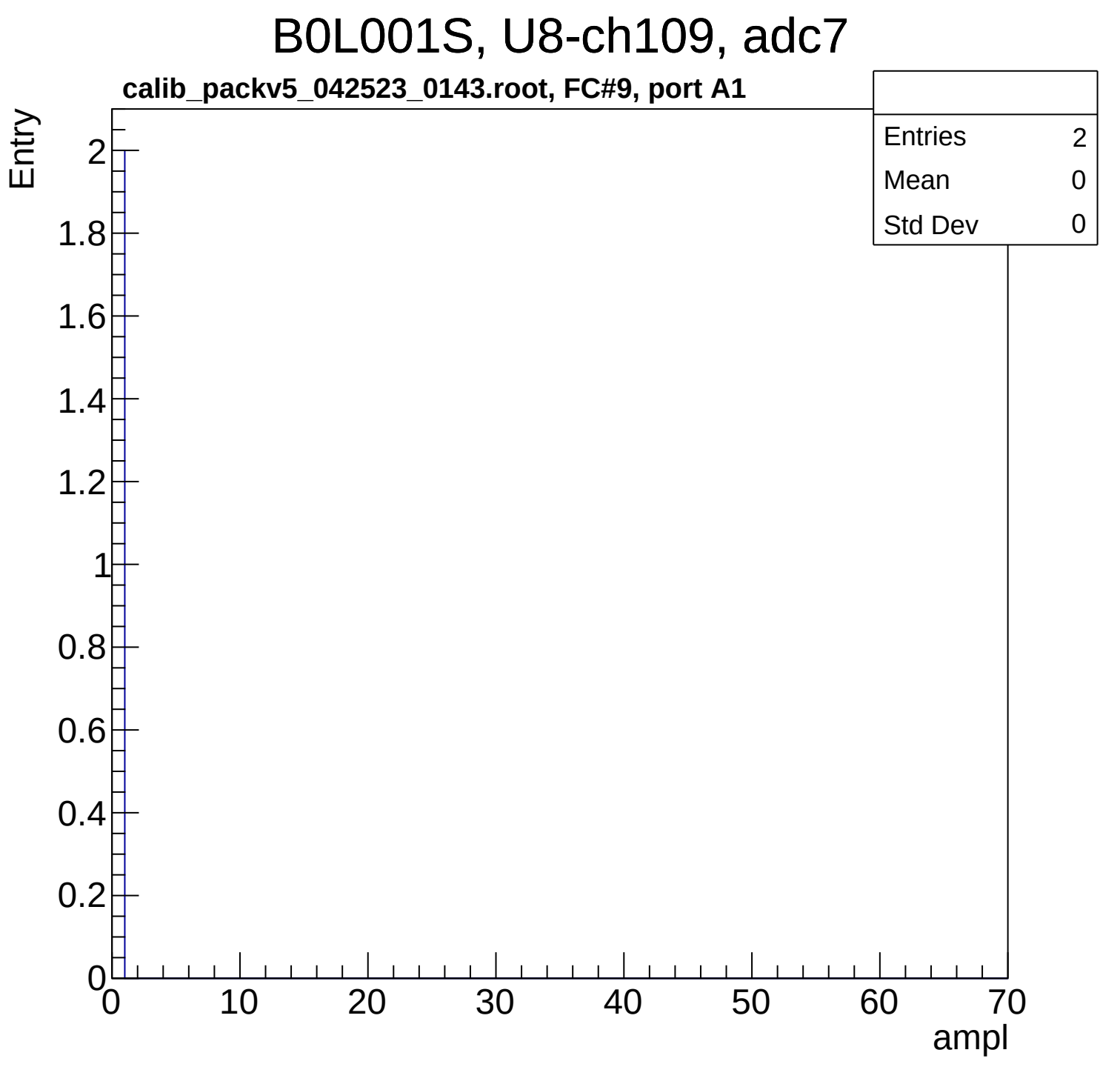
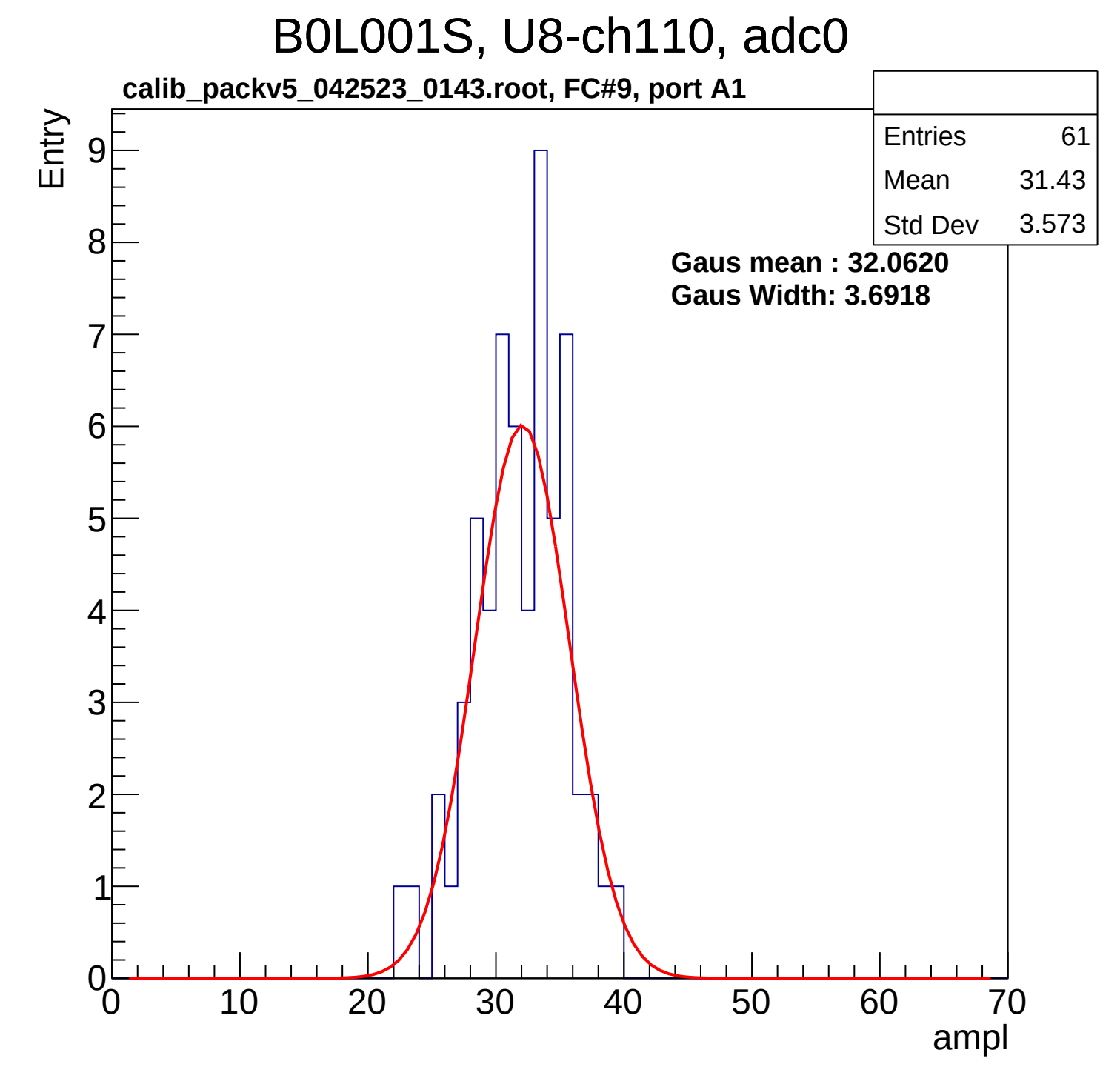
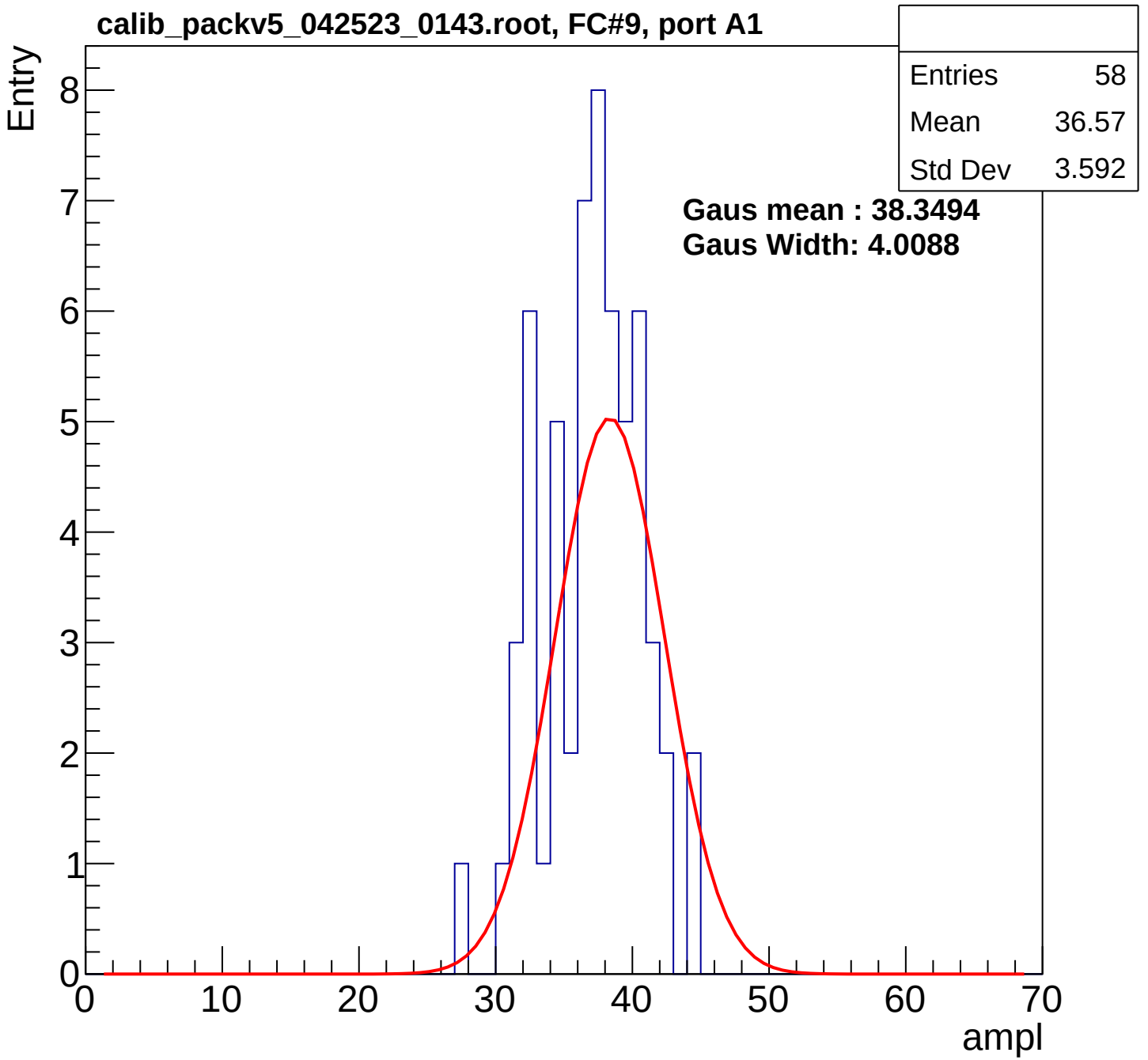
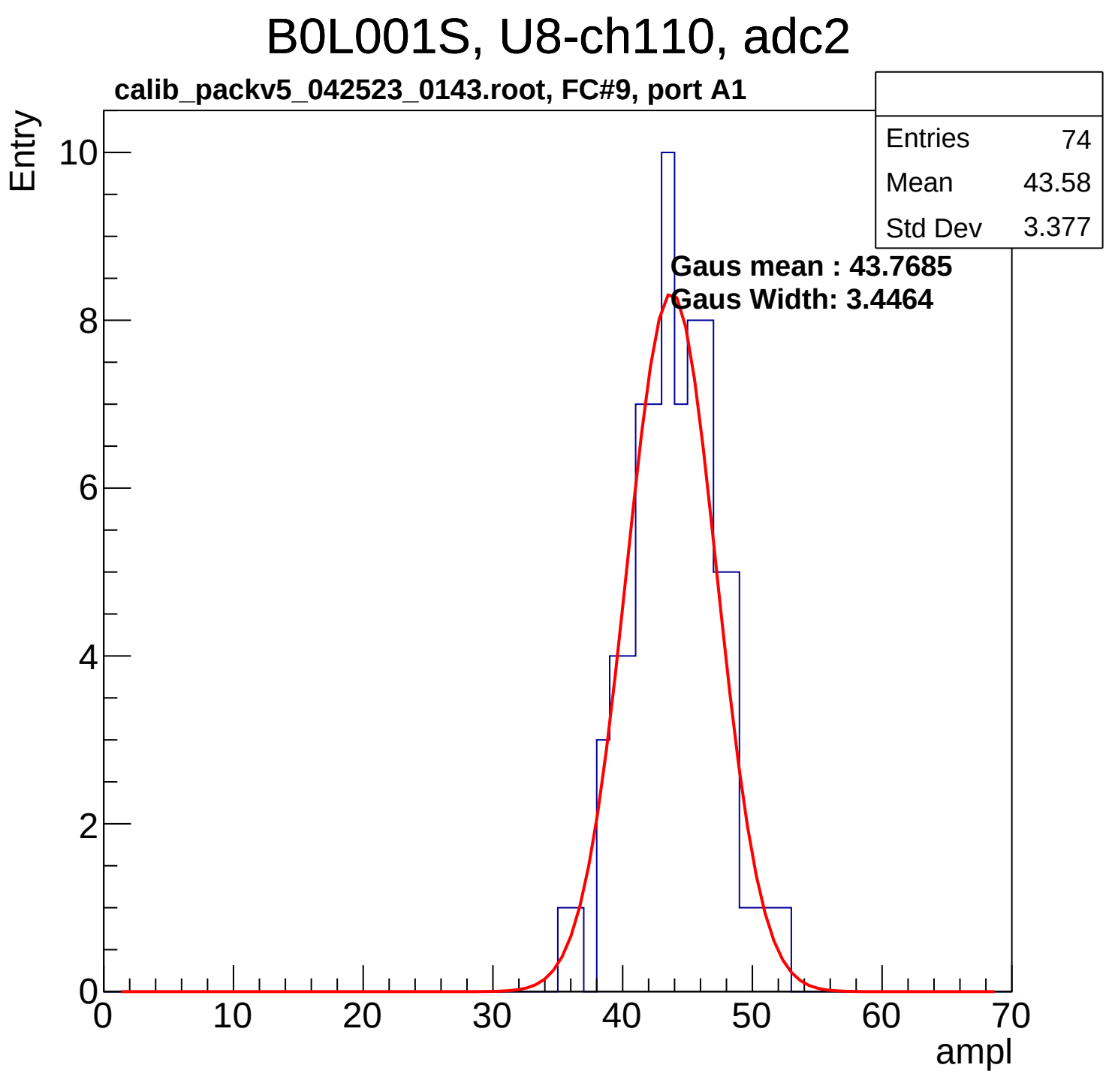

B0L001S, U8-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.89
Std Dev 10.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

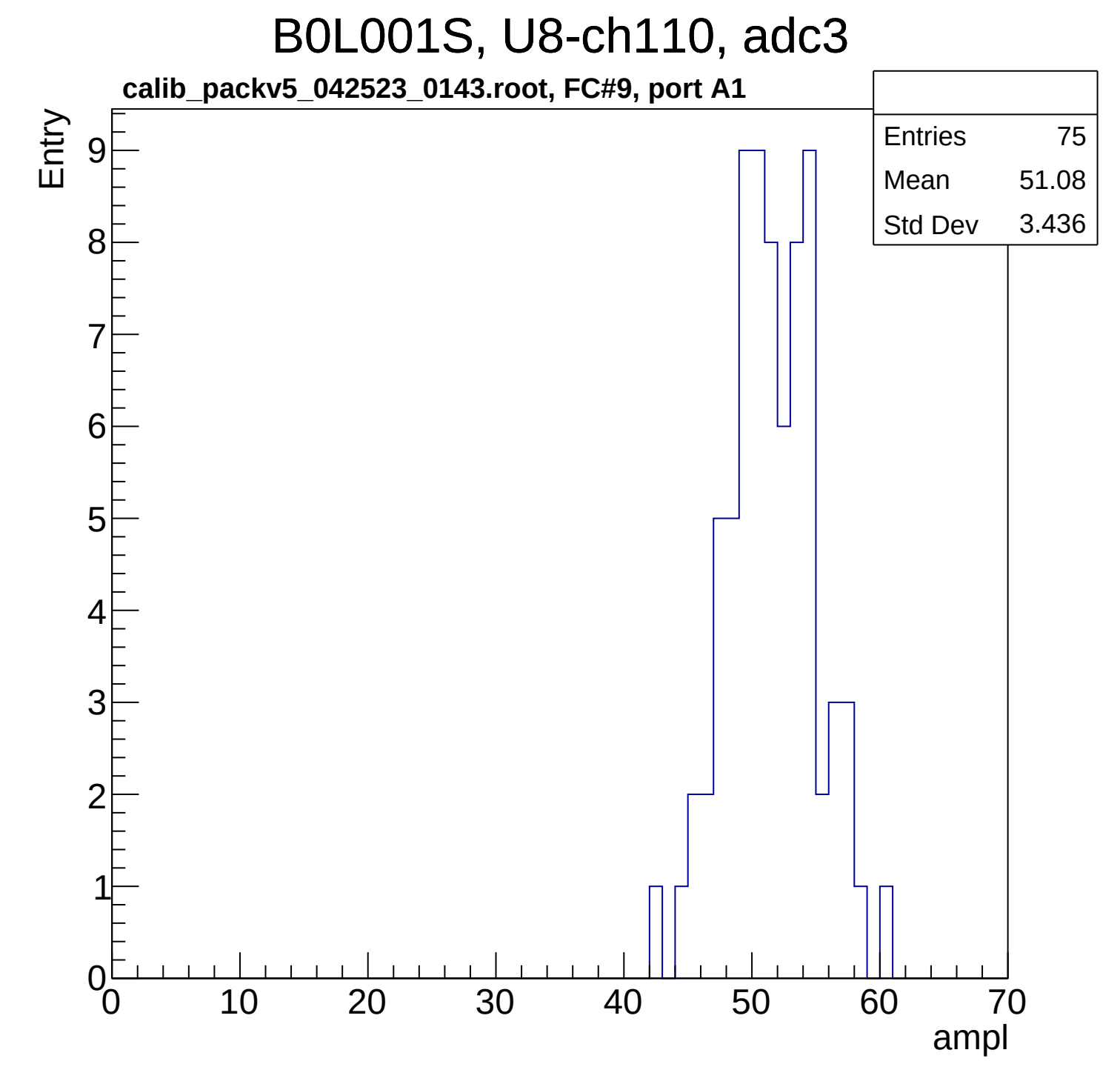

B0L001S, U8-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.43
Std Dev 3.573
Gaus mean : 32.0620
Gaus Width: 3.6918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.57
Std Dev 3.592
Gaus mean : 38.3494
Gaus Width: 4.0088
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.58
Std Dev 3.377
Gaus mean : 43.7685
Gaus Width: 3.4464
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.08
Std Dev 3.436
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
30
40
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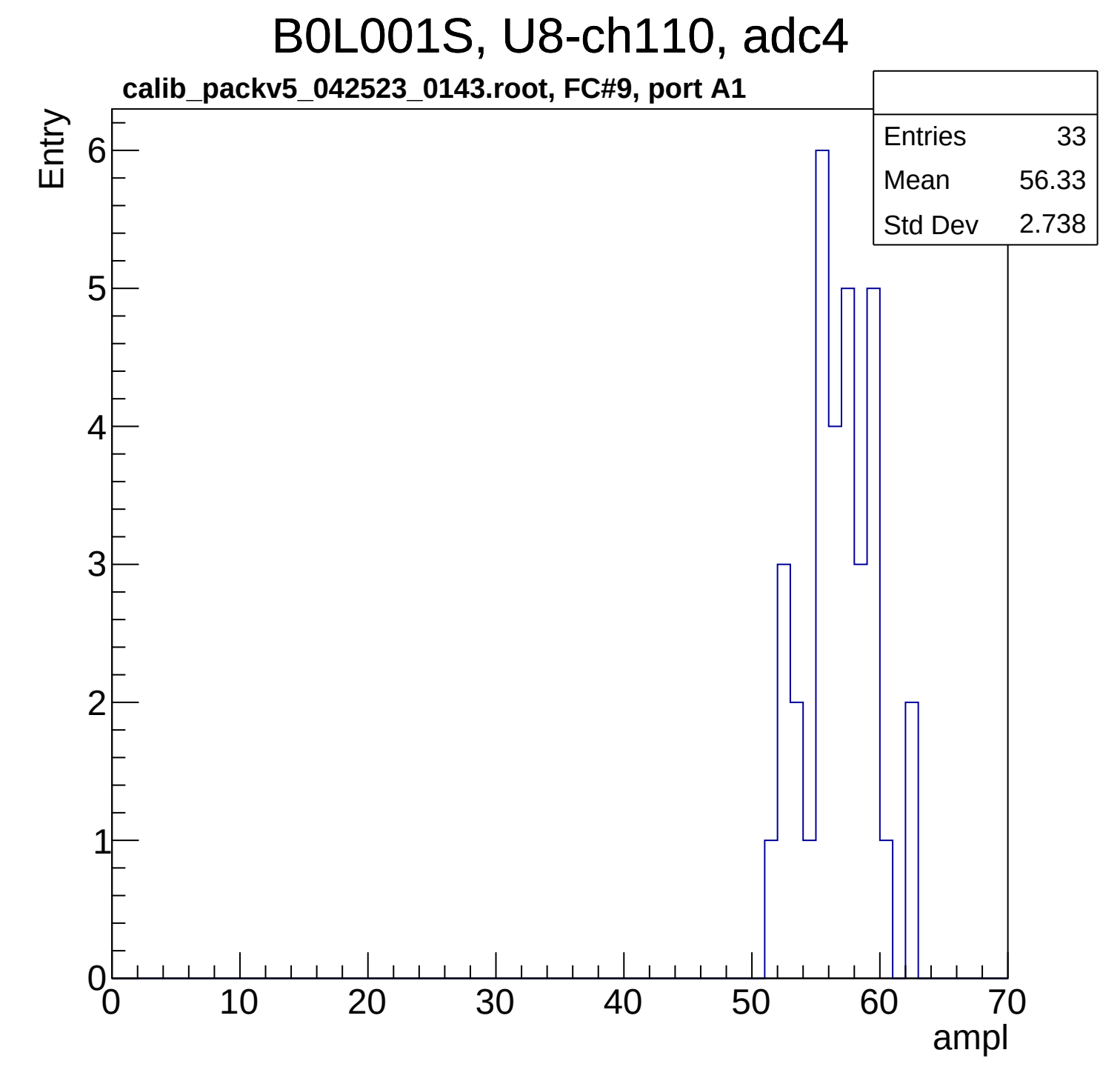
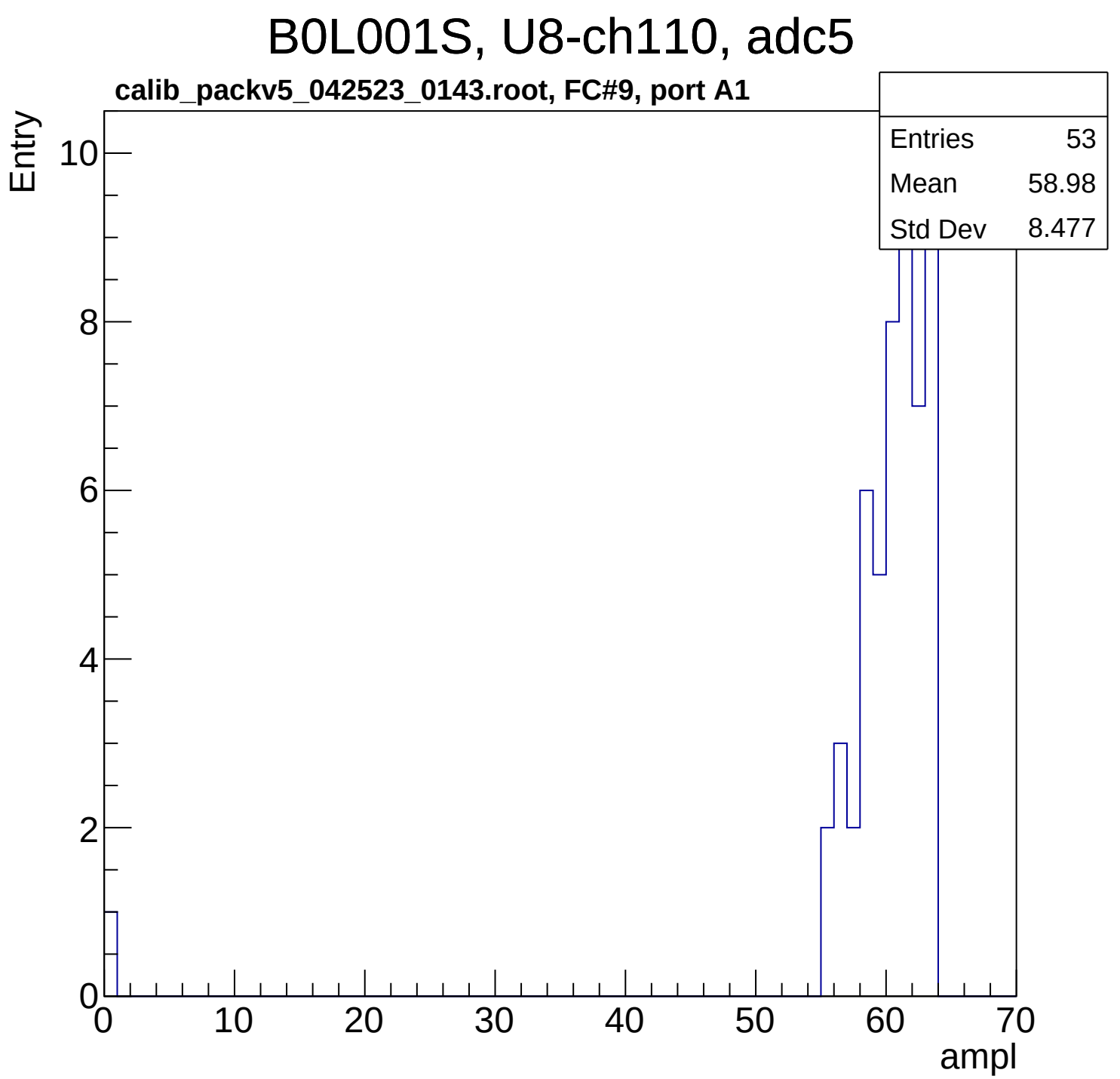
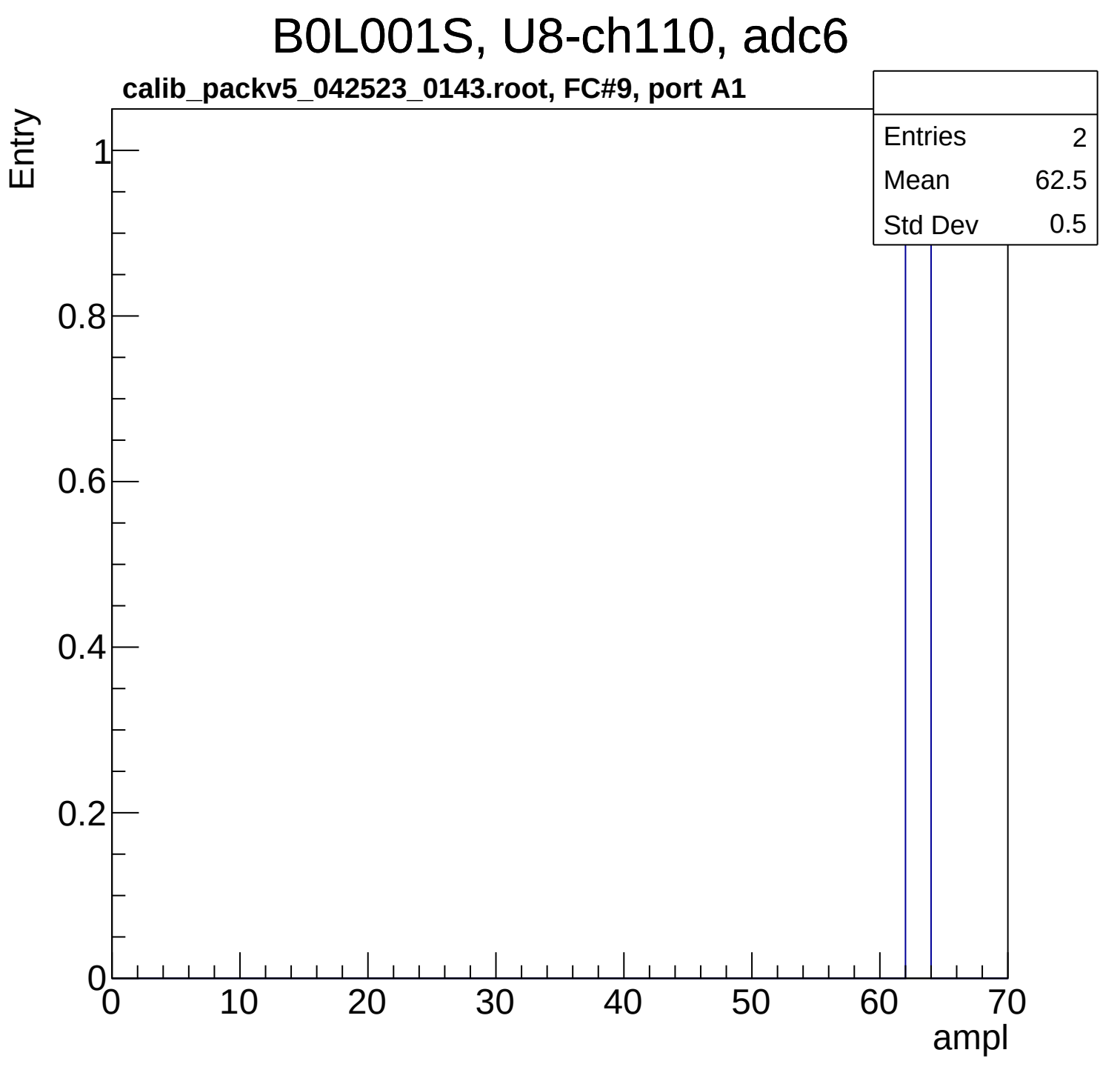
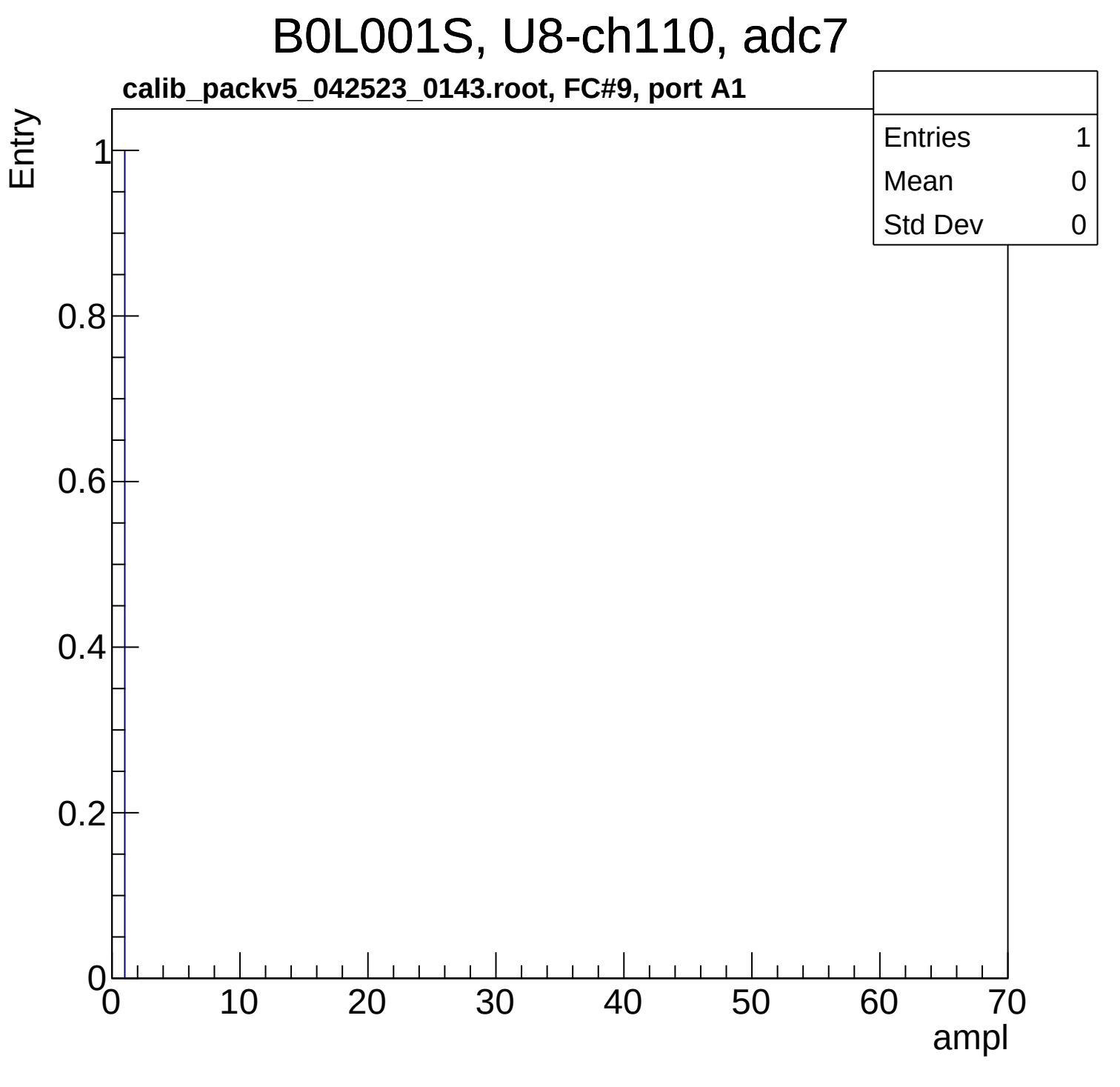
60
70


B0L001S, U8-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 56.33
Std Dev 2.738
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.98
Std Dev 8.477
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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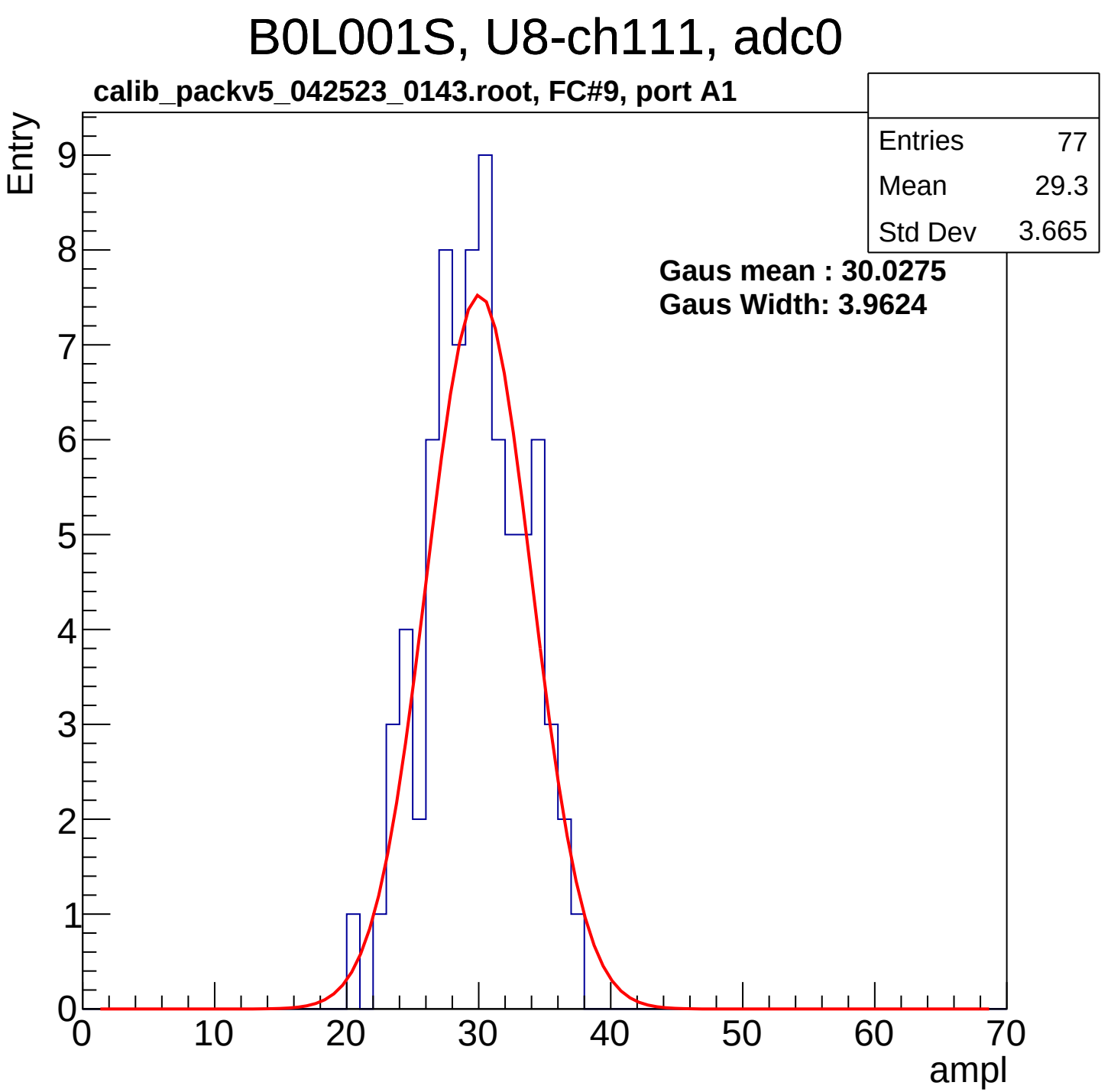
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.3
Std Dev 3.665
Gaus mean : 30.0275
Gaus Width: 3.9624
Entry
ampl
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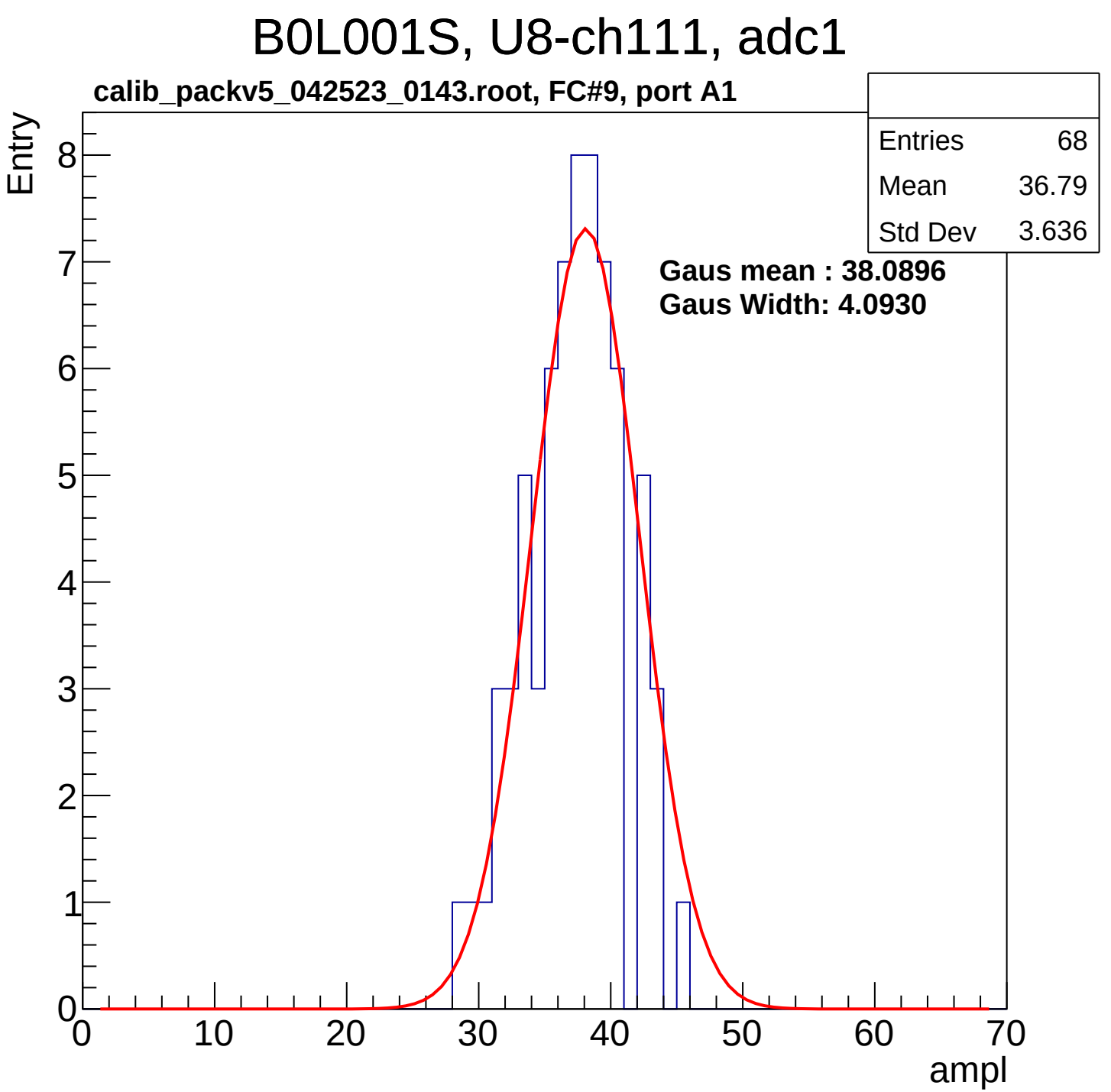
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.79
Std Dev 3.636
Gaus mean : 38.0896
Gaus Width: 4.0930
Entry
ampl
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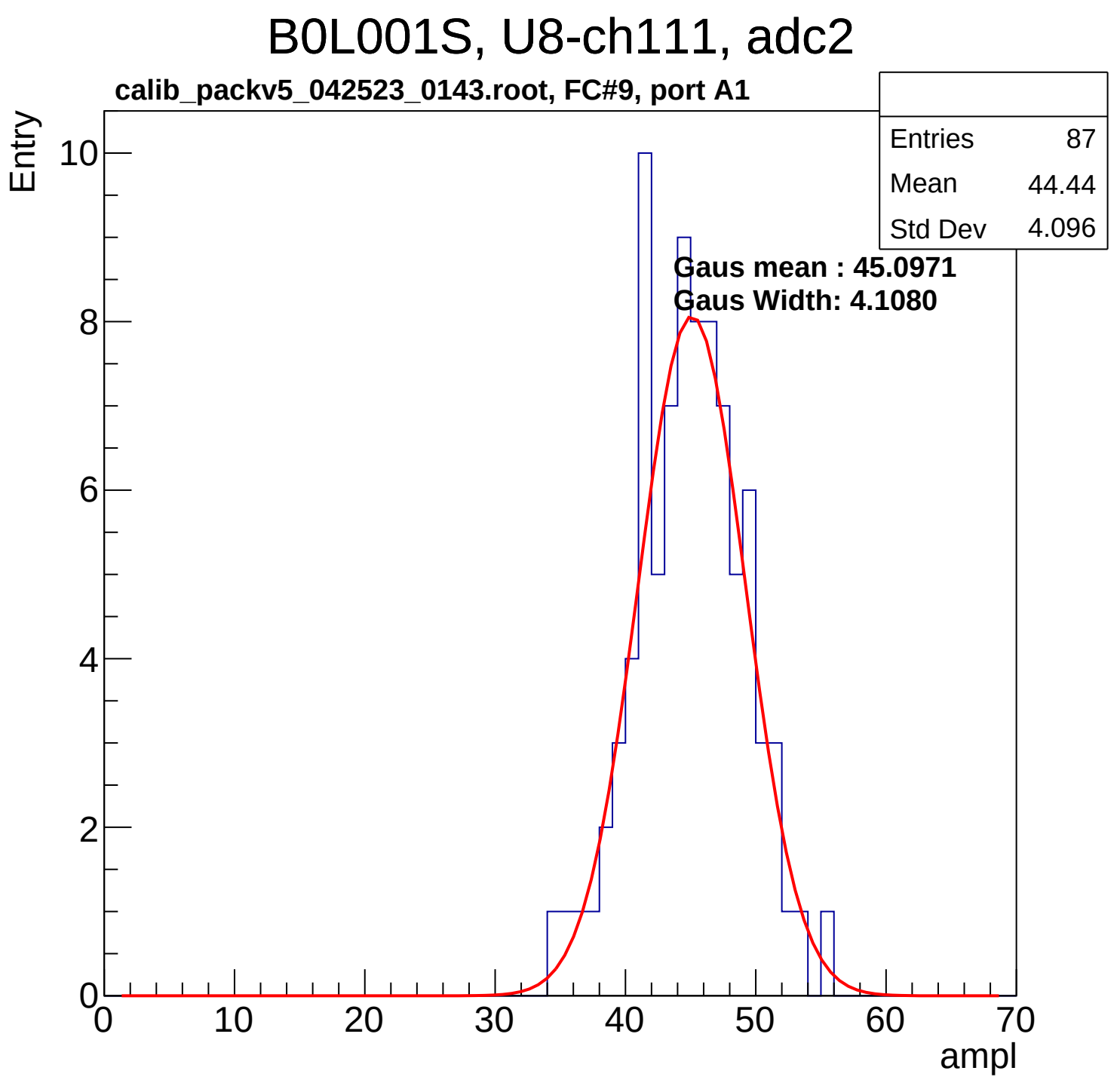
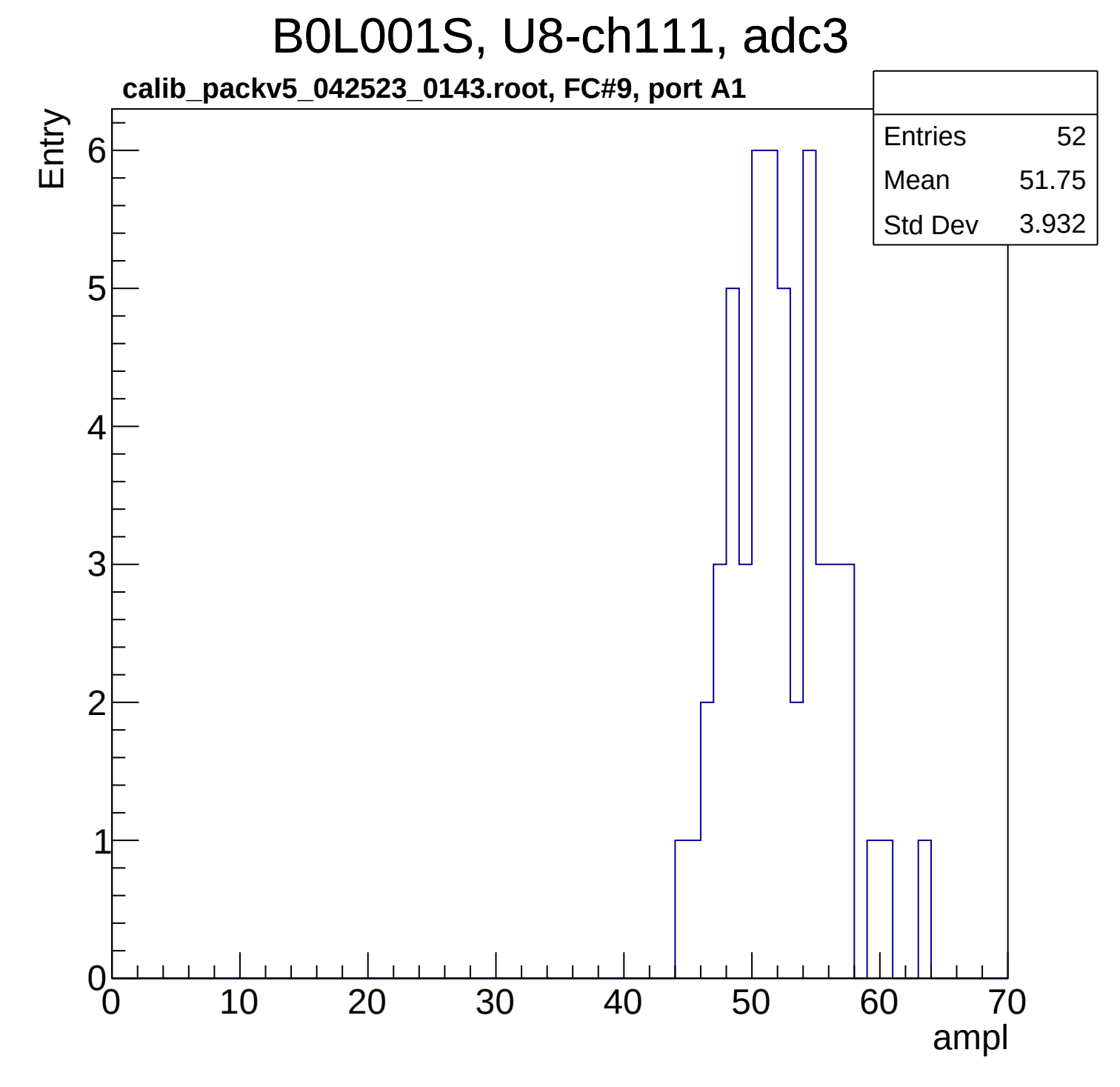
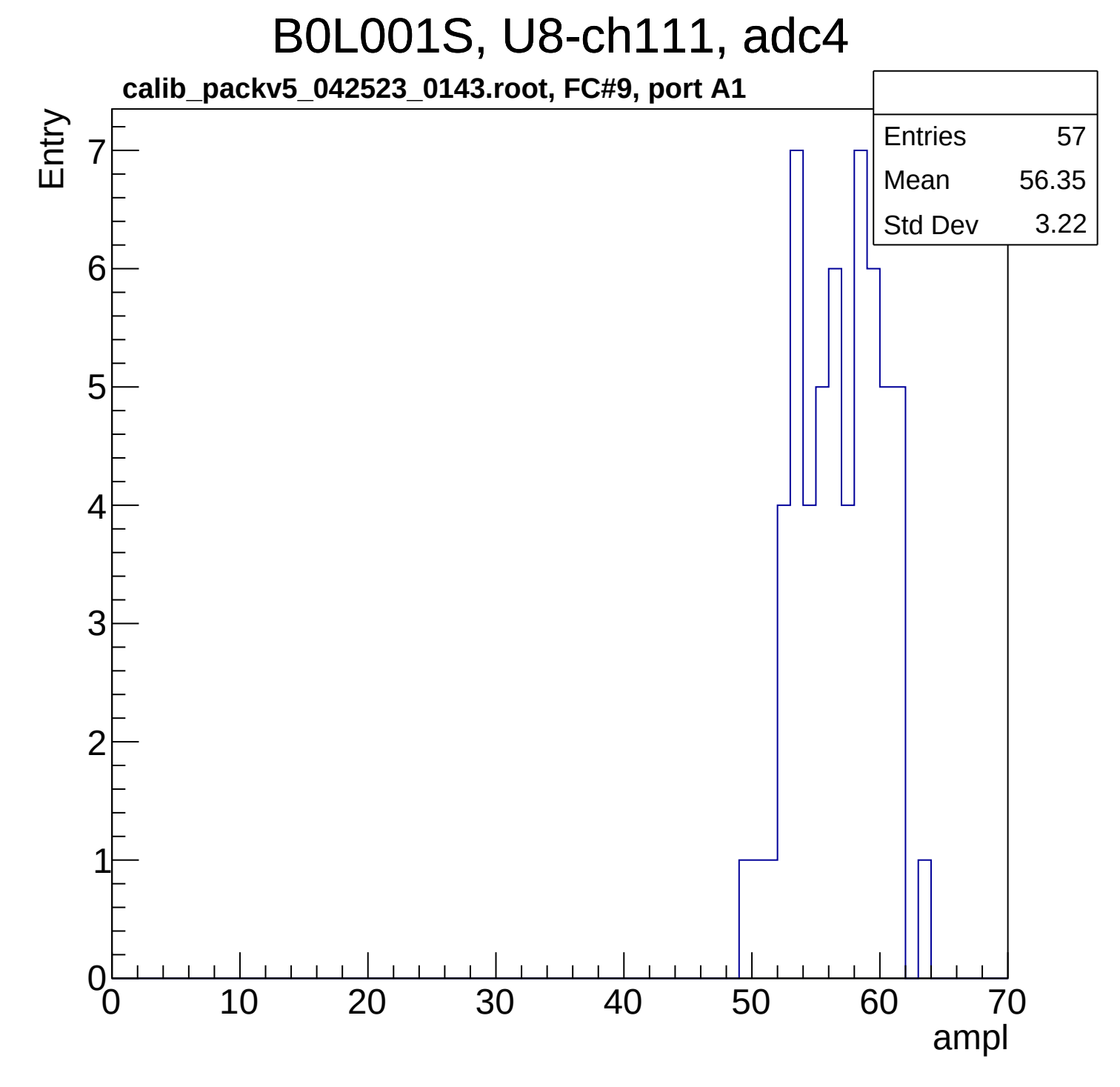
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 44.44
Std Dev 4.096
Gaus mean : 45.0971
Gaus Width: 4.1080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 51.75
Std Dev 3.932
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.35
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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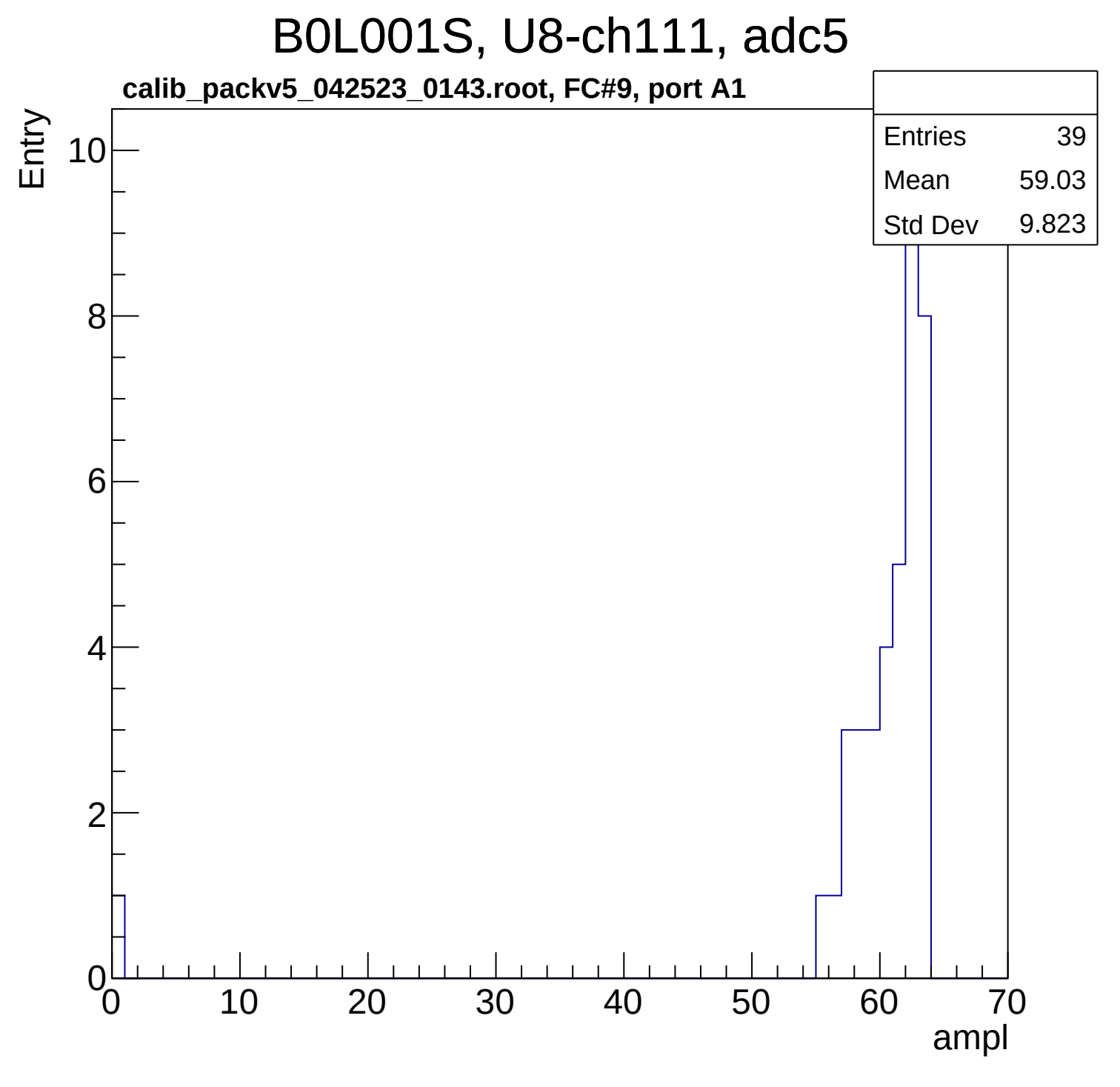
B0L001S, U8-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.03
Std Dev 9.823
Entry
ampl
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2
4
6
8
10
0
10
20
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60
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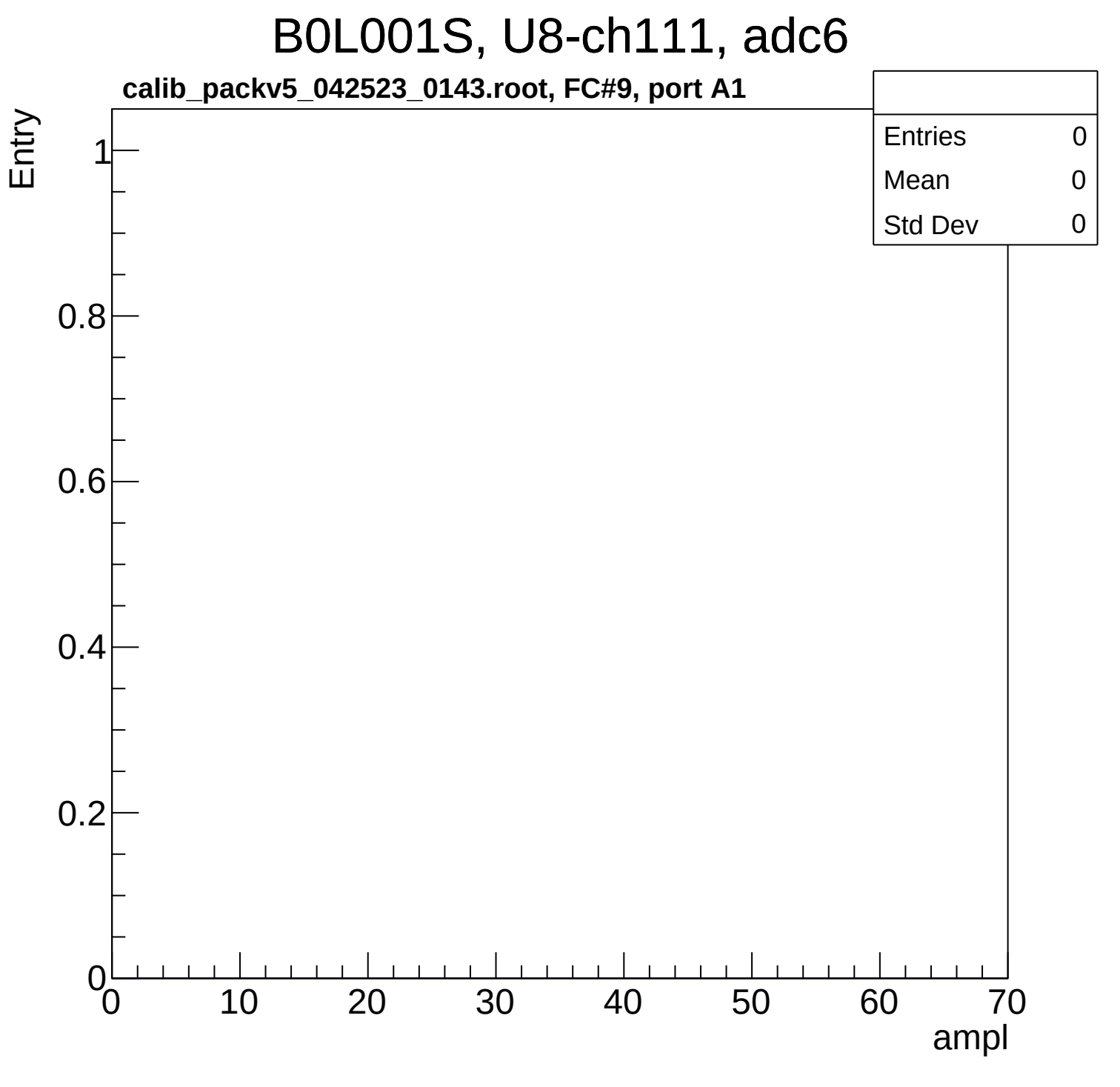
B0L001S, U8-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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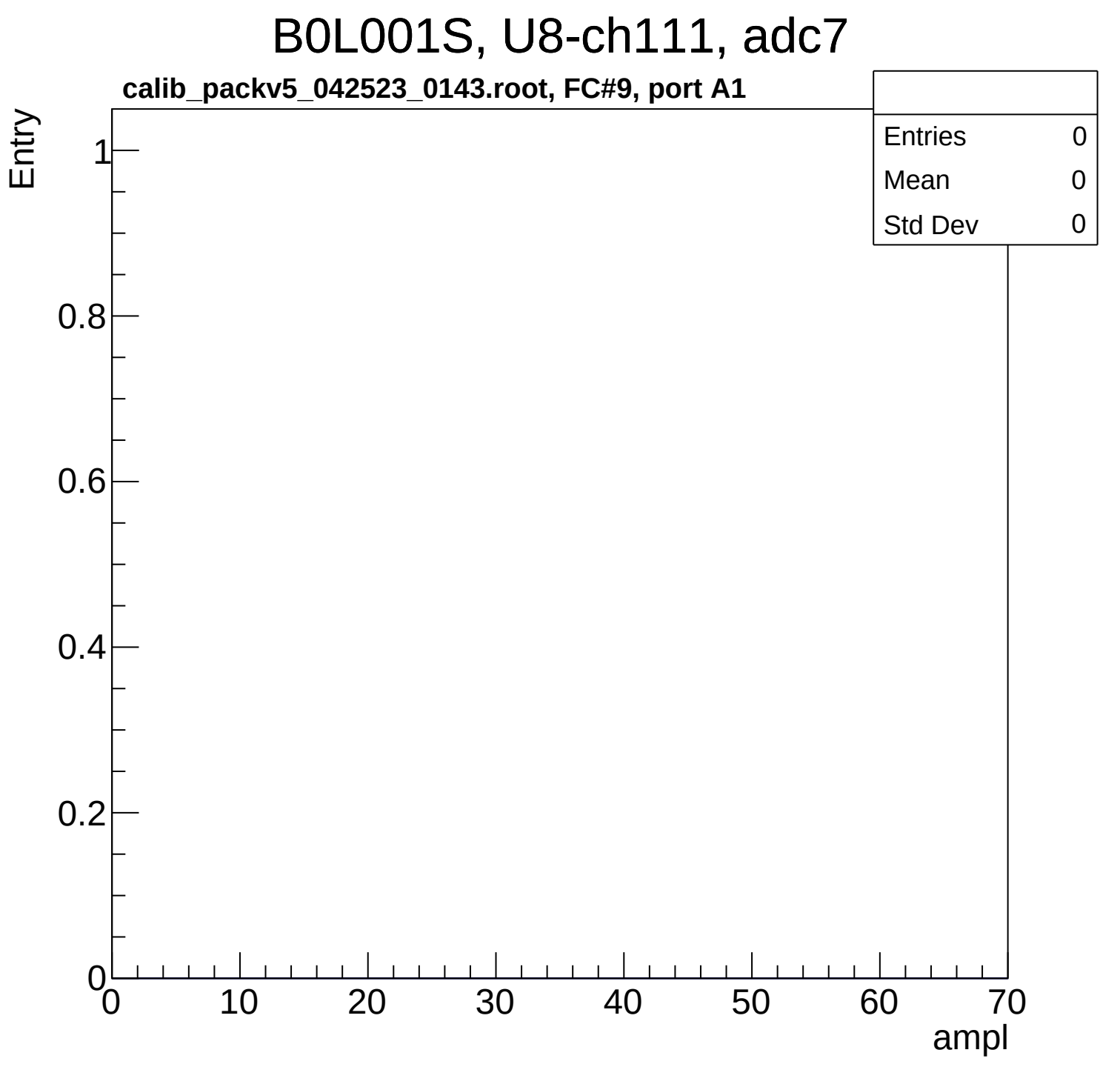
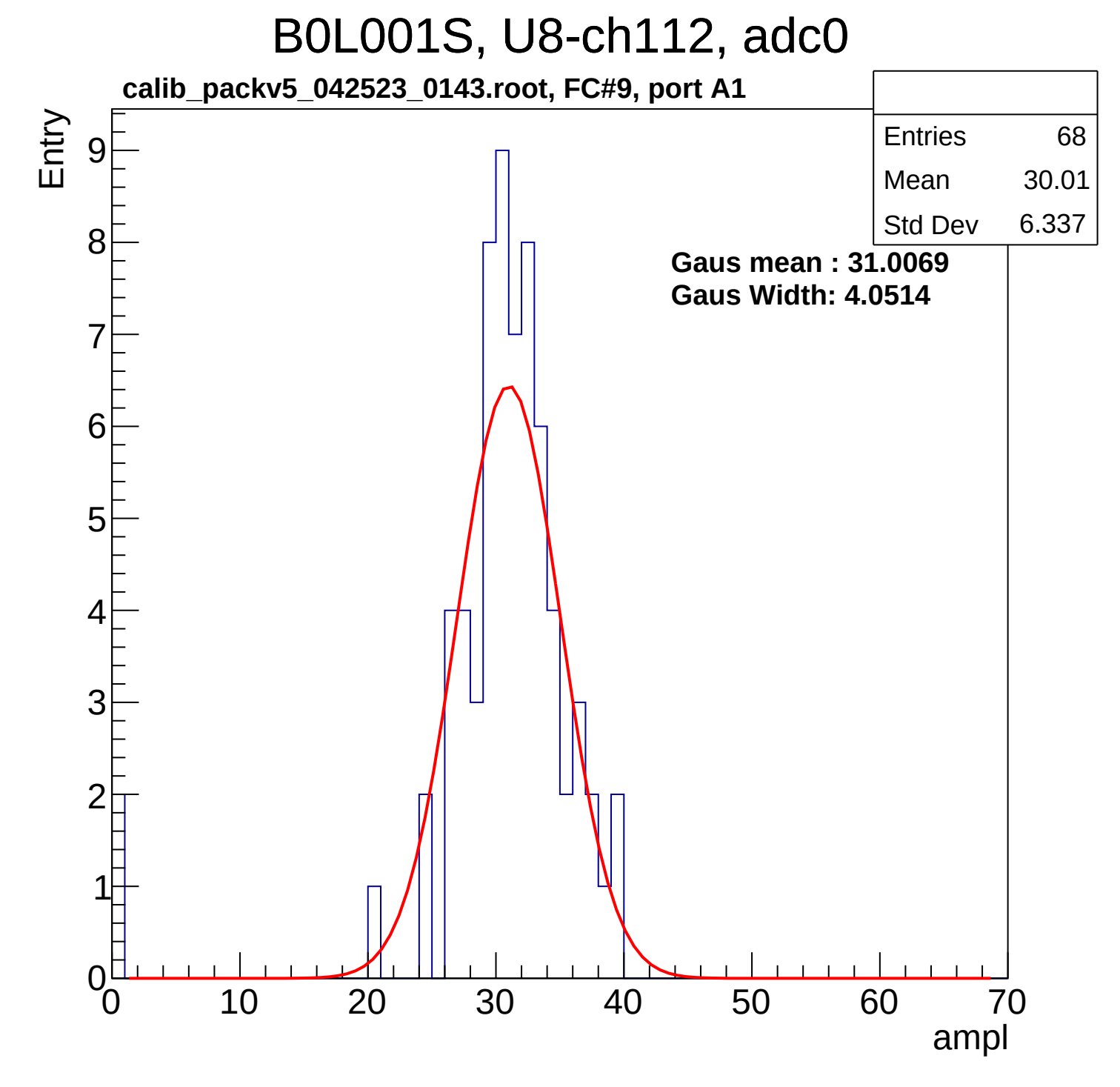
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.01
Std Dev 6.337
Gaus mean : 31.0069
Gaus Width: 4.0514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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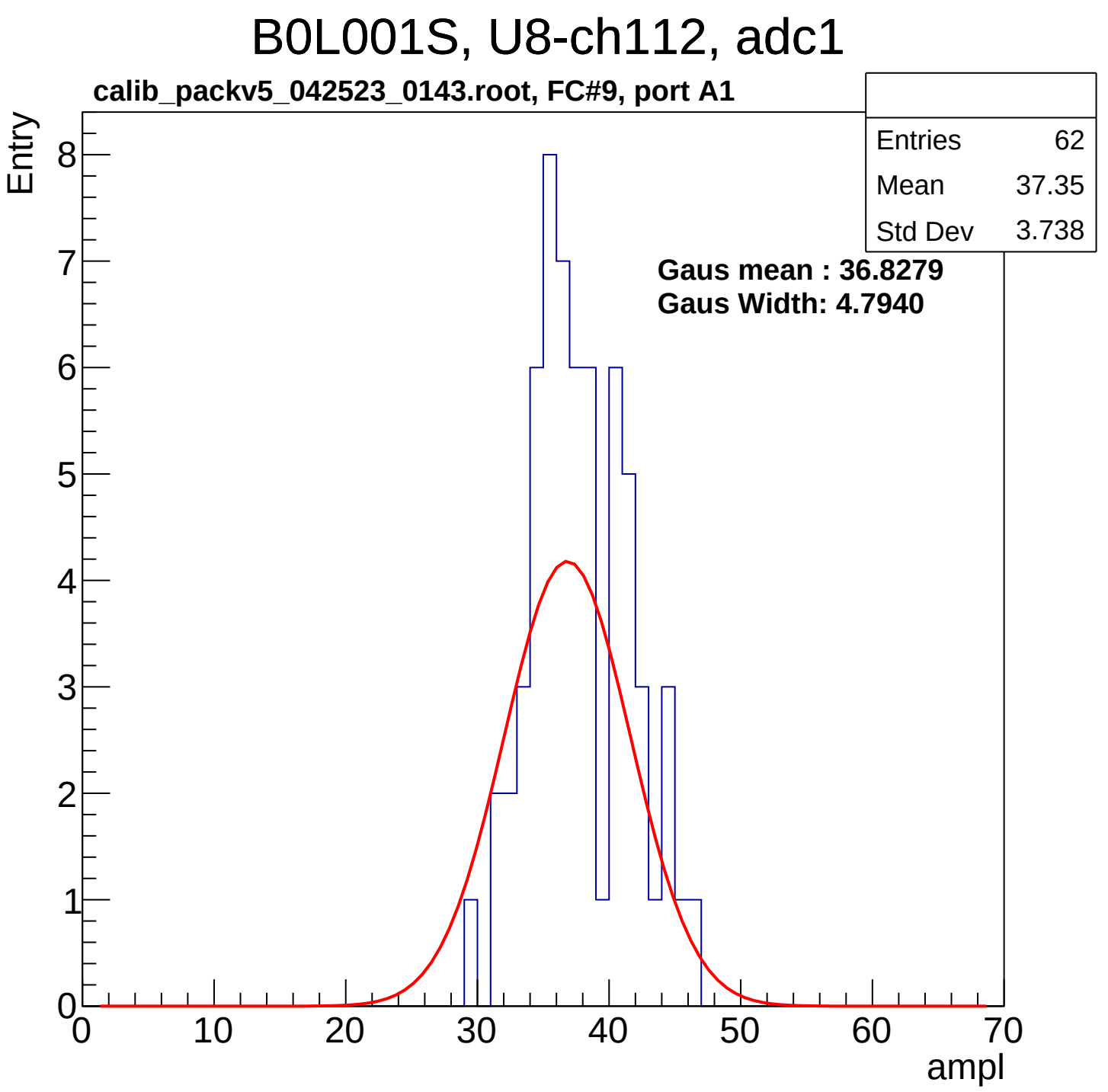
B0L001S, U8-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.35
Std Dev 3.738
Gaus mean : 36.8279
Gaus Width: 4.7940
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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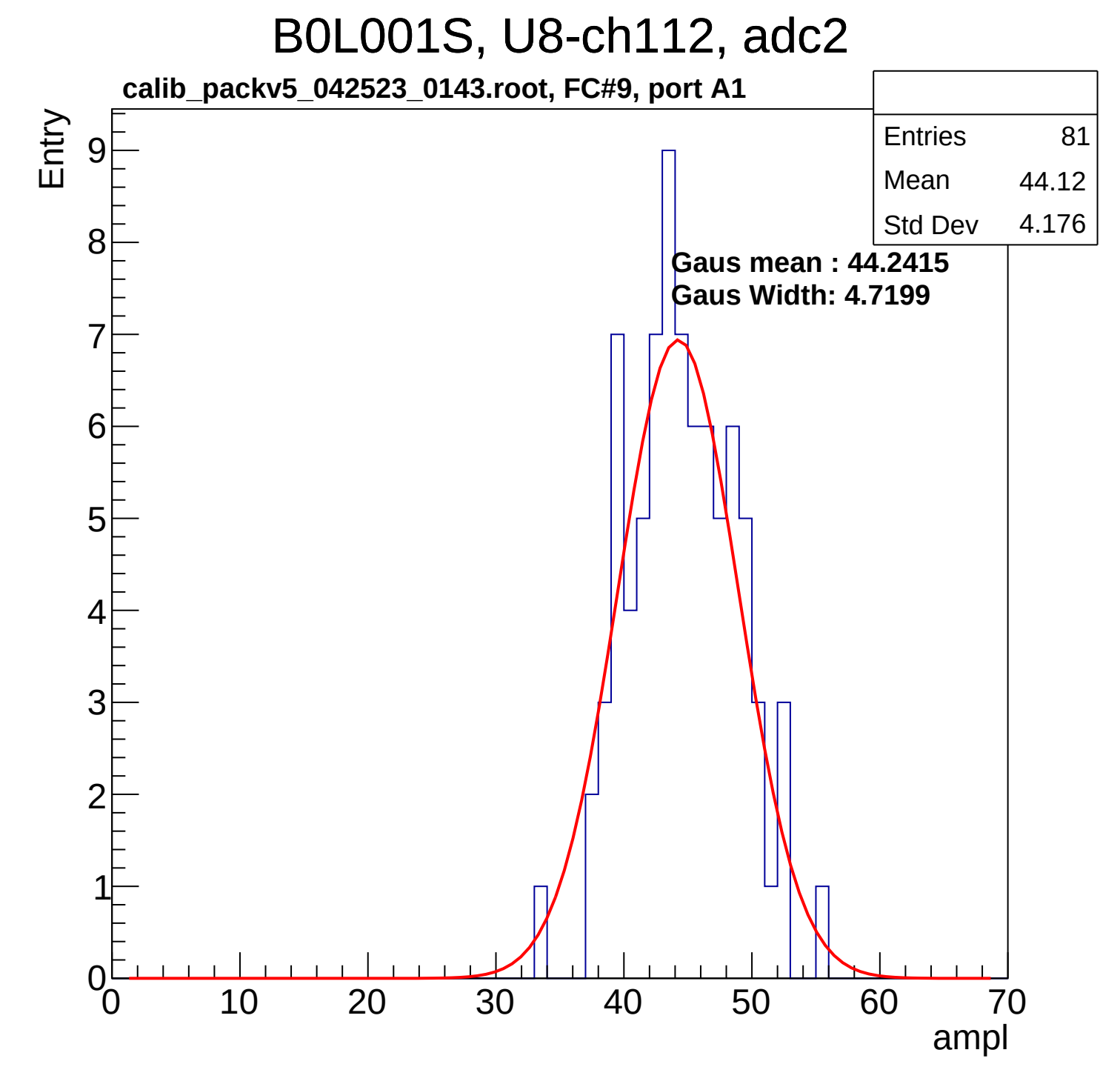
B0L001S, U8-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 44.12
Std Dev 4.176
Gaus mean : 44.2415
Gaus Width: 4.7199
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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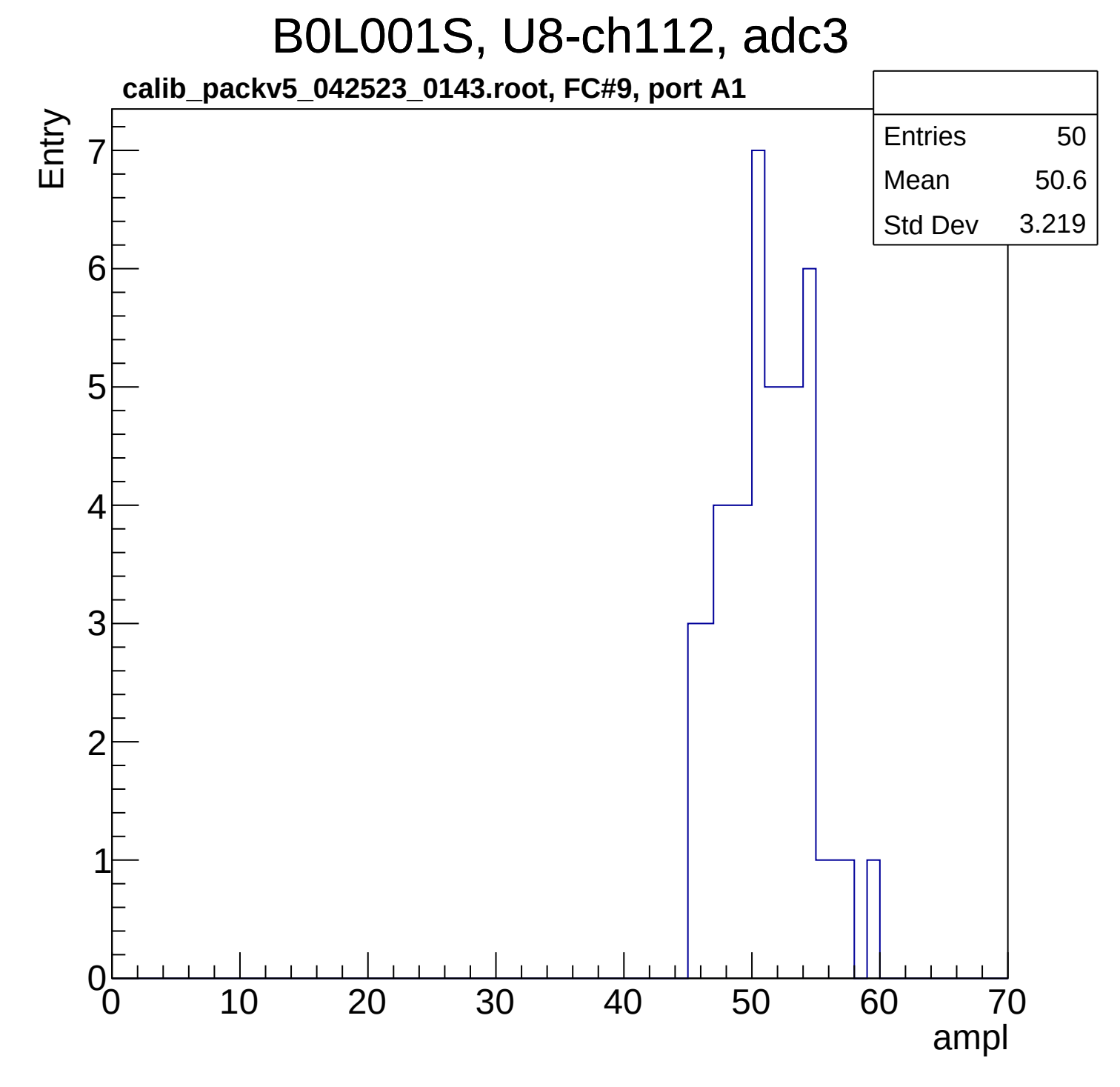
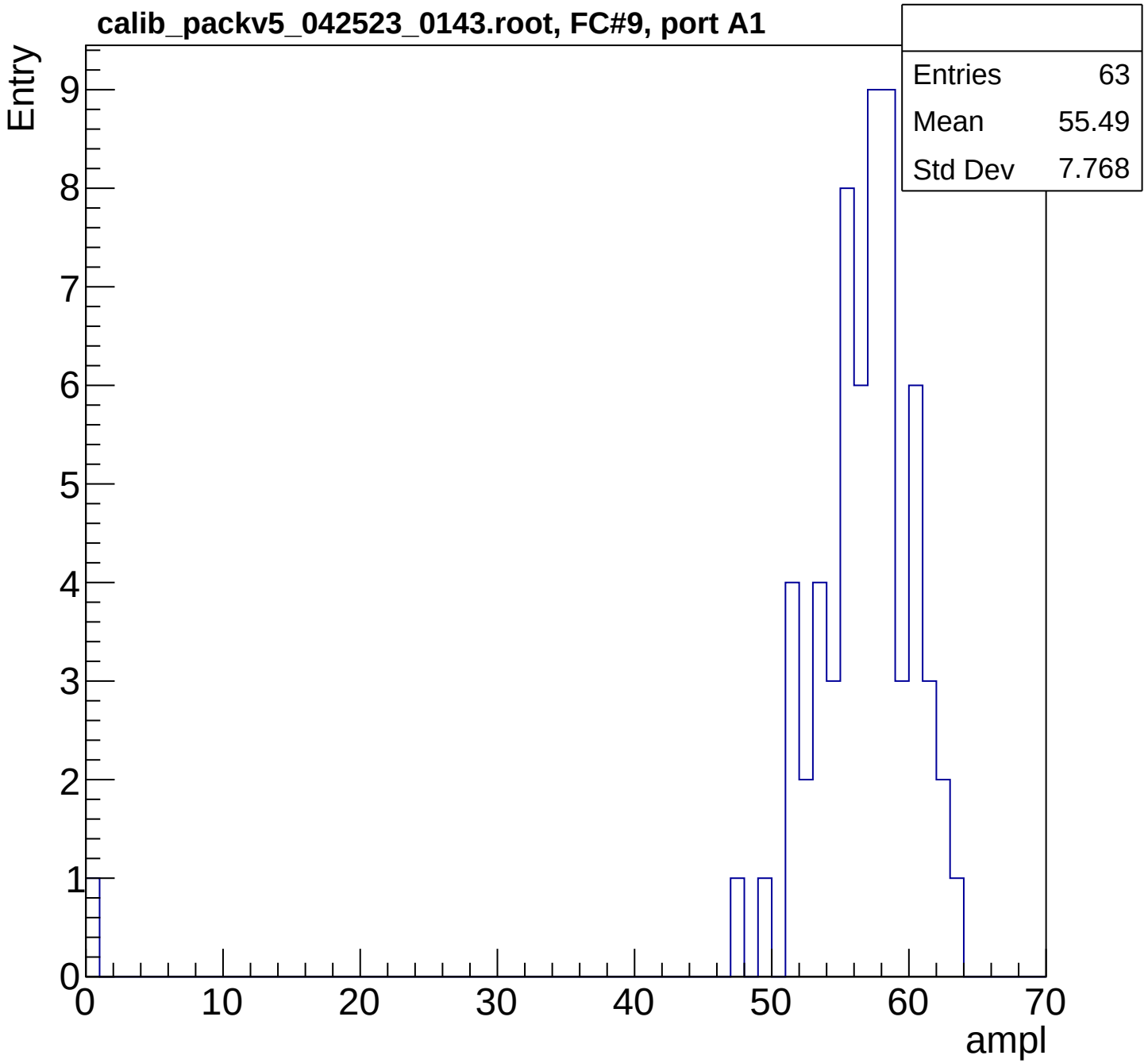
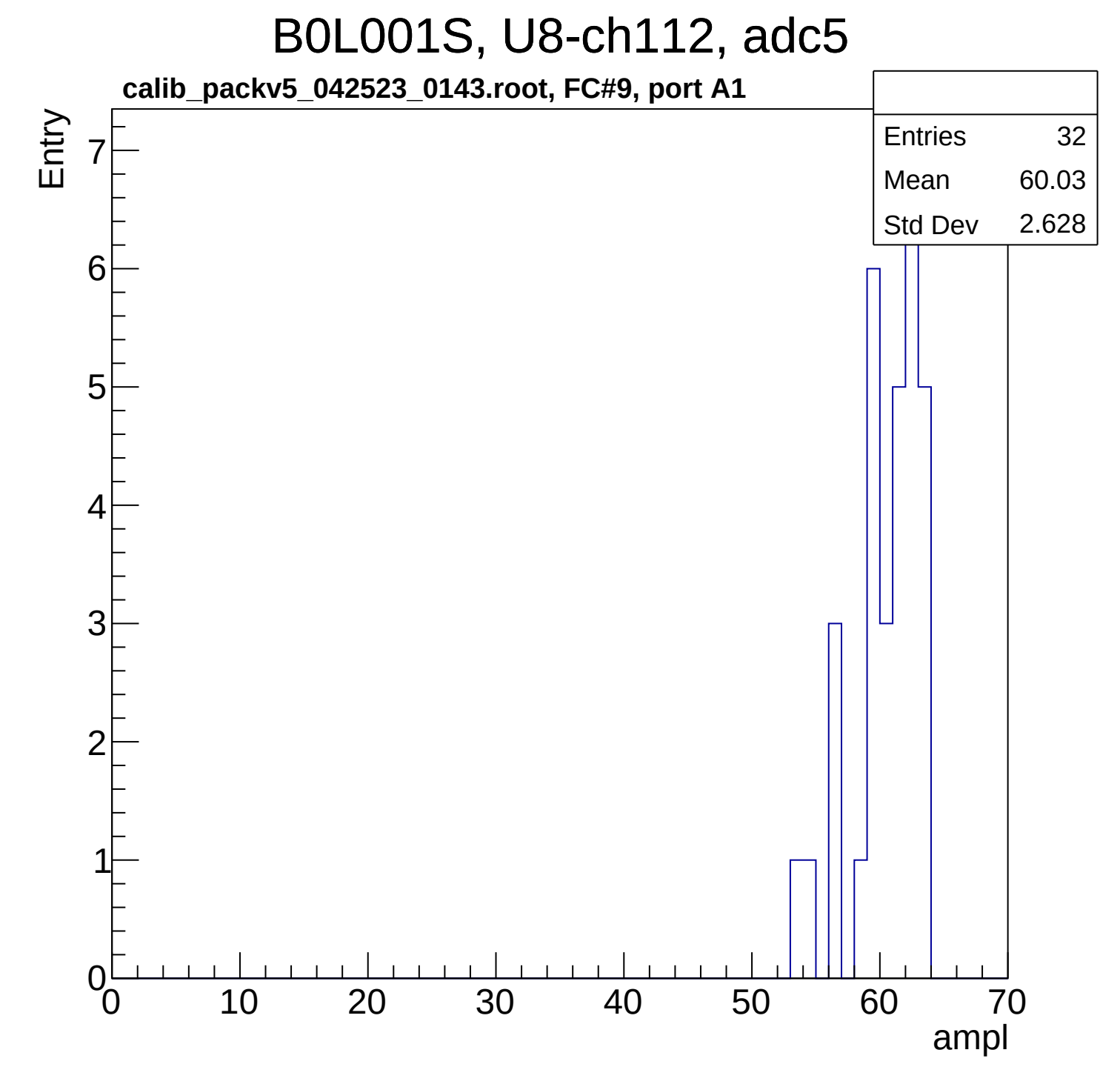

B0L001S, U8-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 50.6
Std Dev 3.219
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.49
Std Dev 7.768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.03
Std Dev 2.628
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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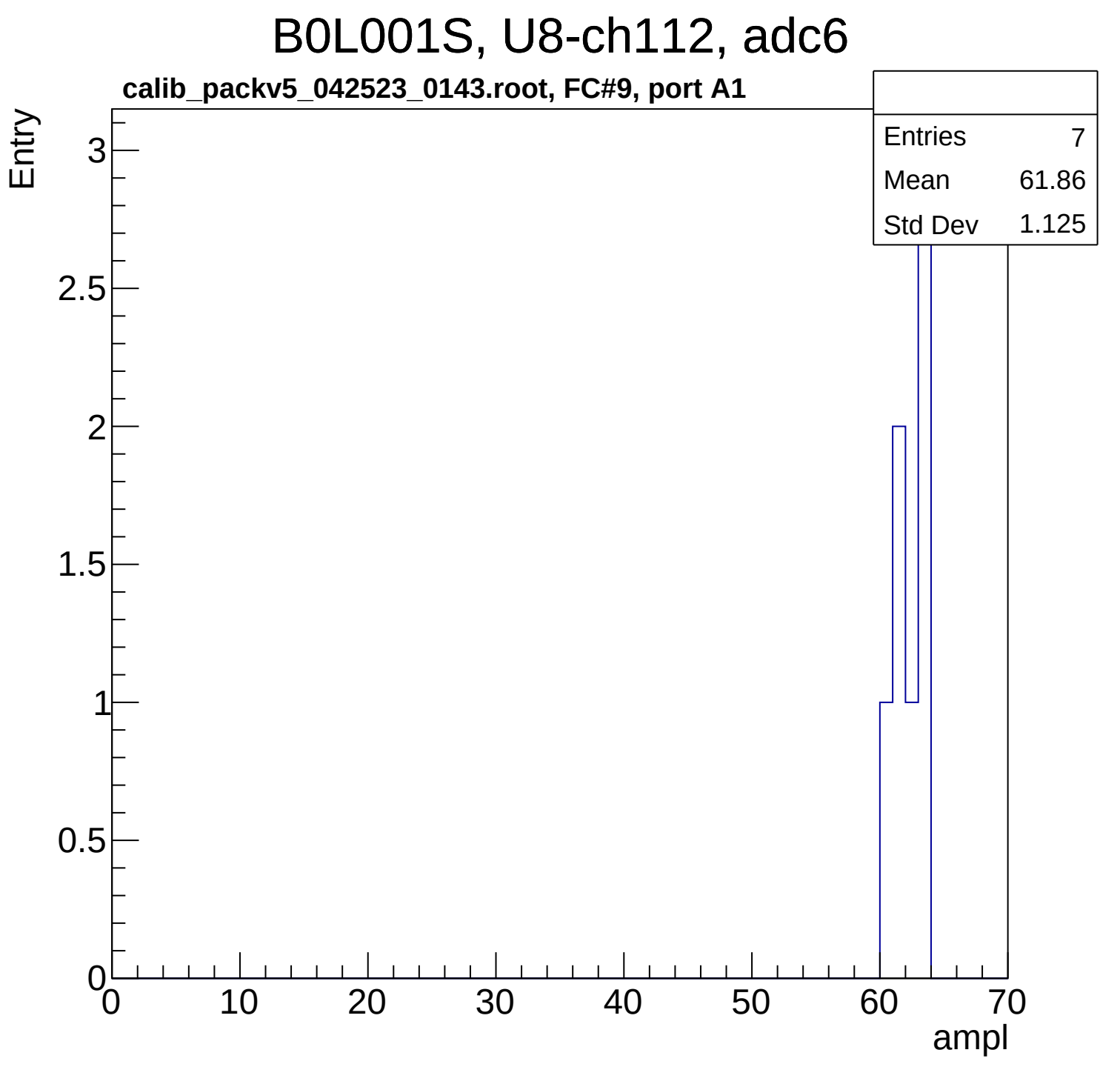
B0L001S, U8-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.86
Std Dev 1.125
Entry
ampl
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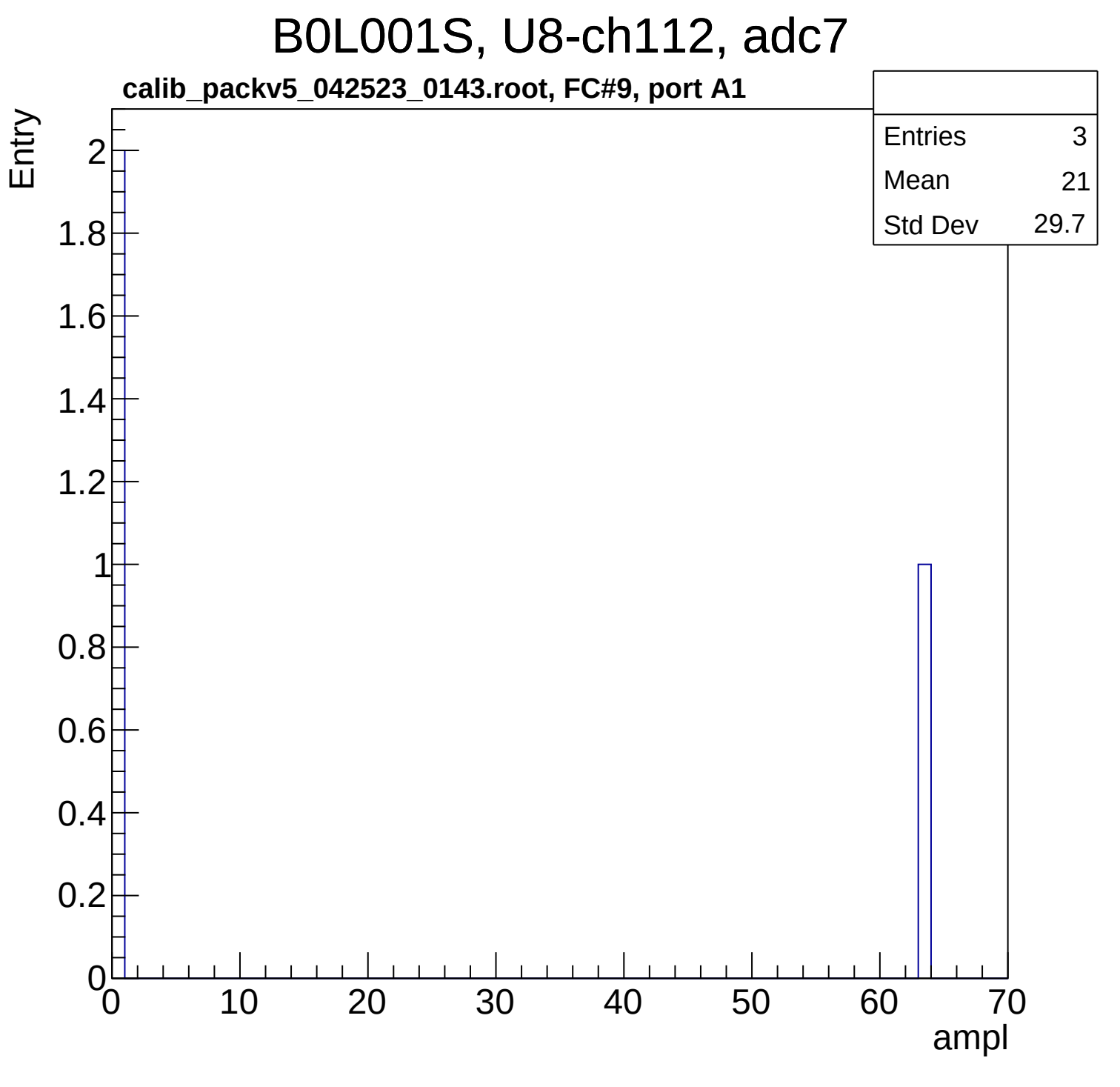
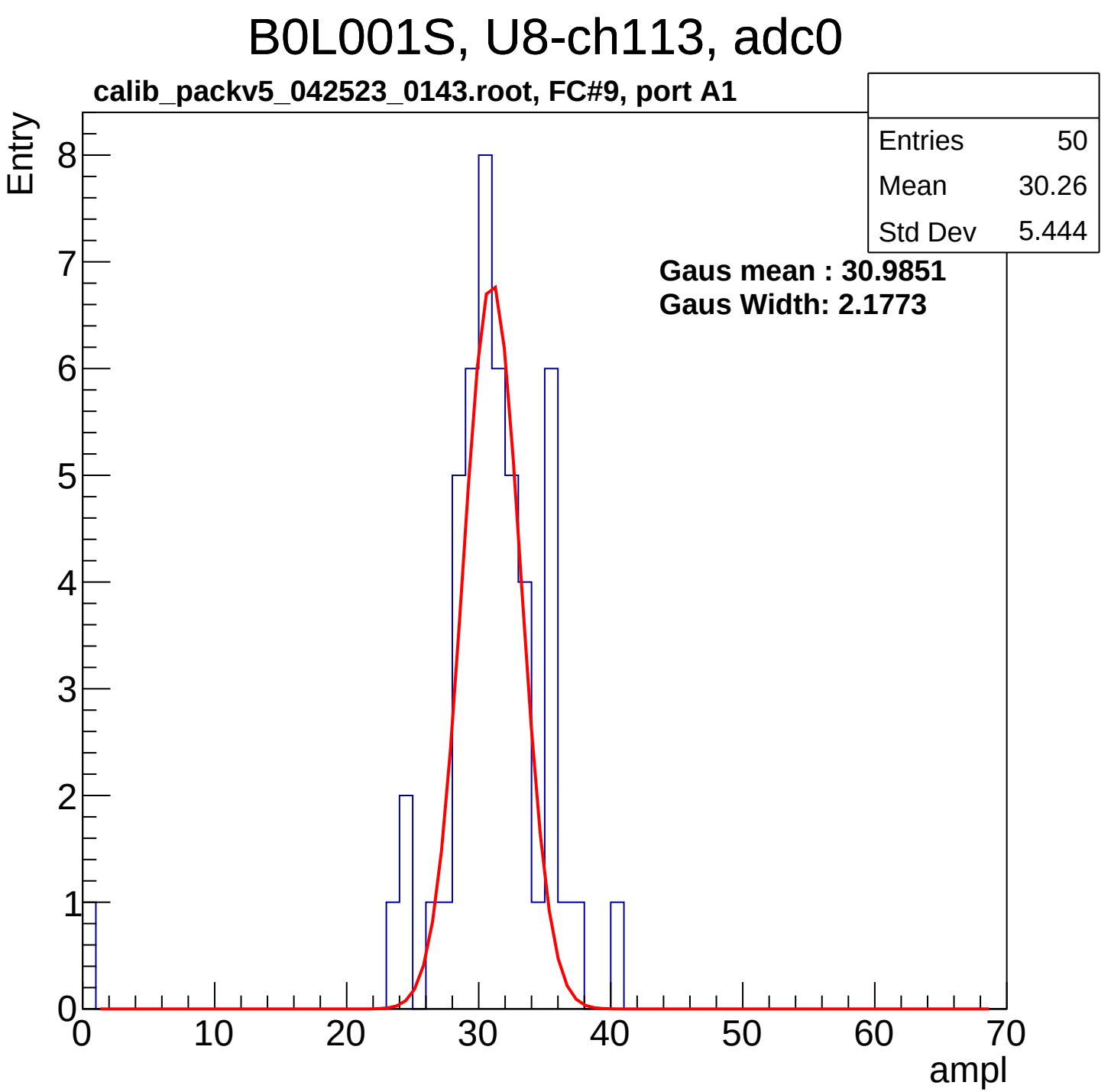
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 30.26
Std Dev 5.444
Gaus mean : 30.9851
Gaus Width: 2.1773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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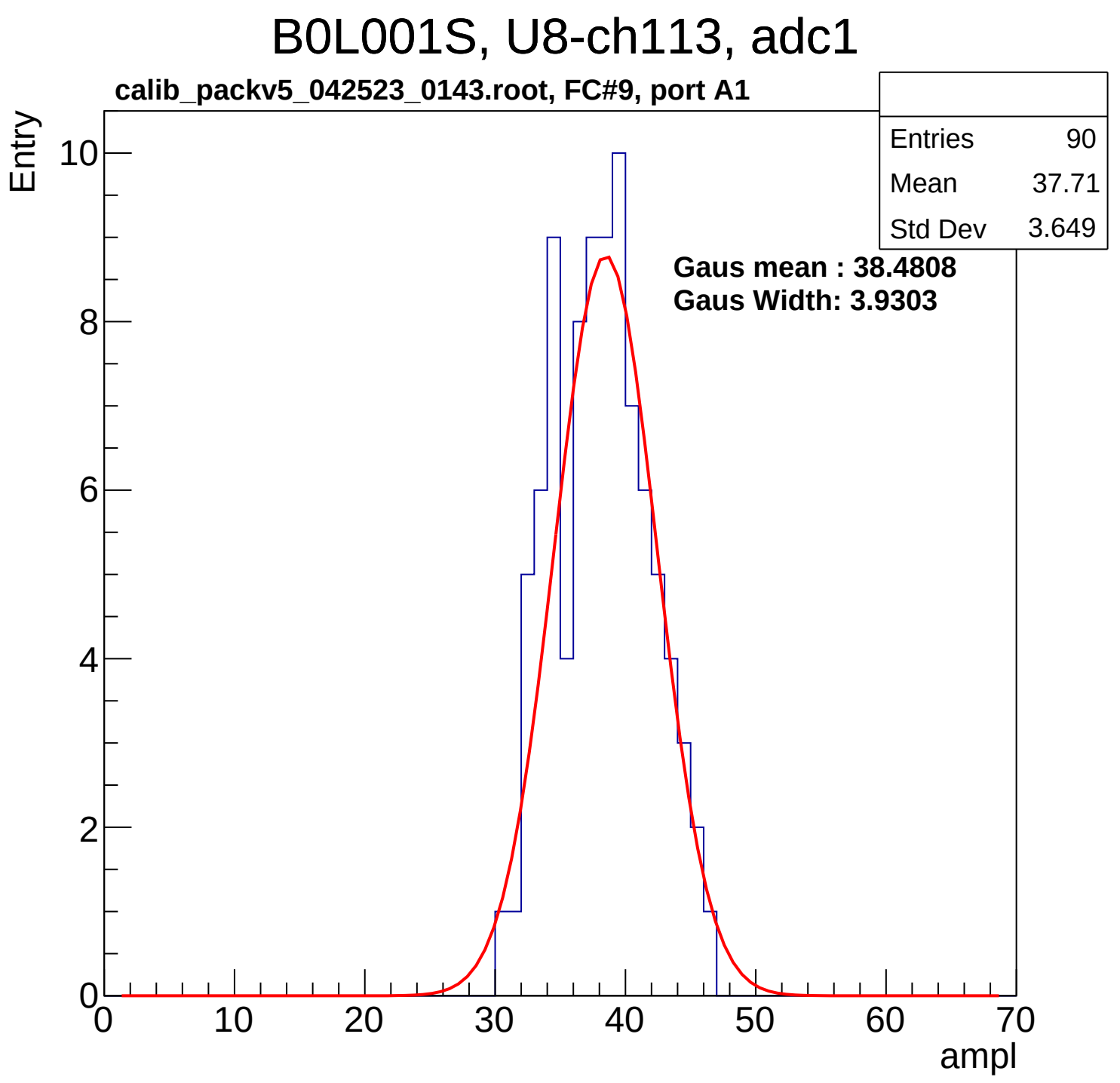
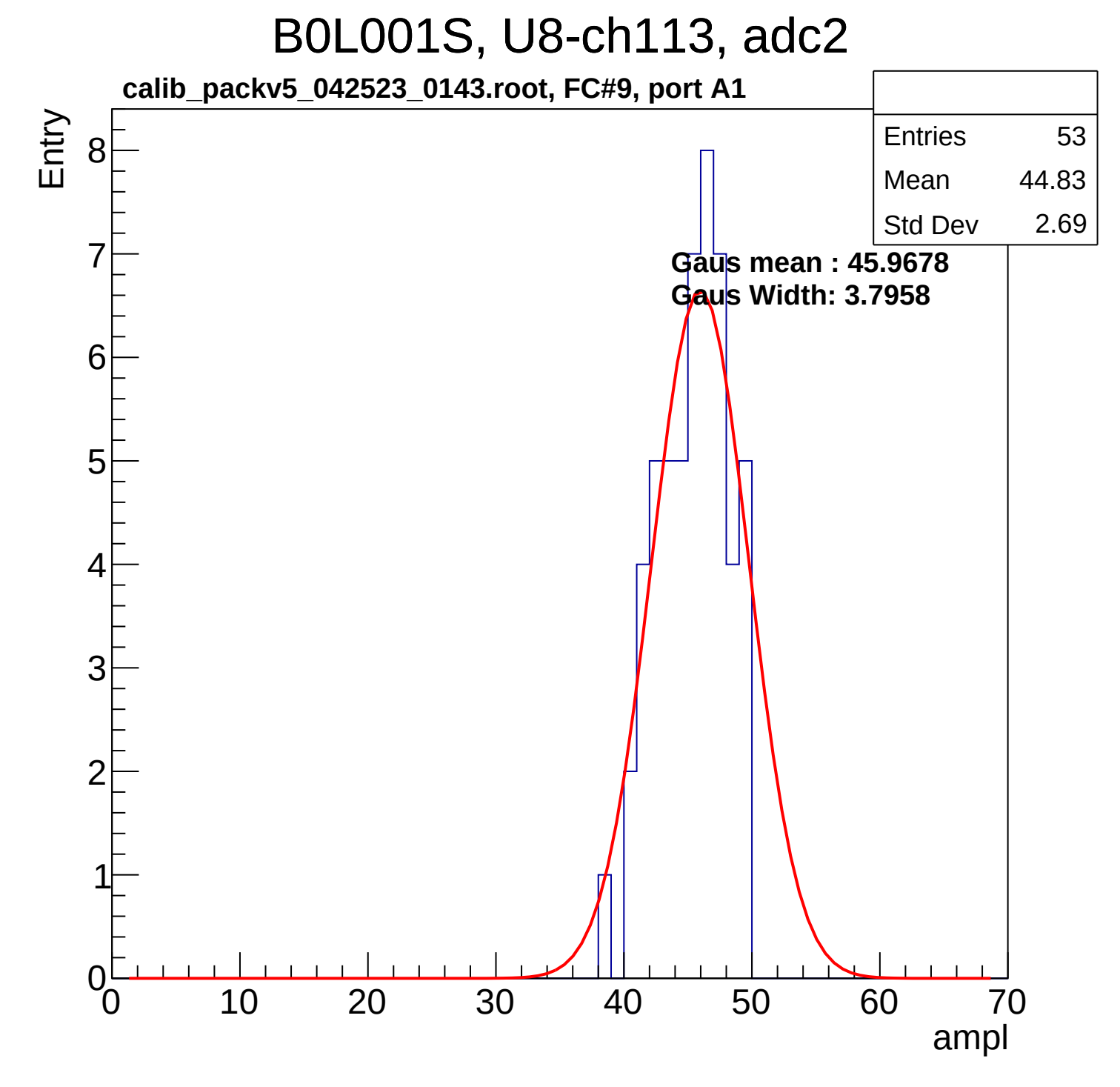

B0L001S, U8-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 37.71
Std Dev 3.649
Gaus mean : 38.4808
Gaus Width: 3.9303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.83
Std Dev 2.69
Gaus mean : 45.9678
Gaus Width: 3.7958
Entry
ampl
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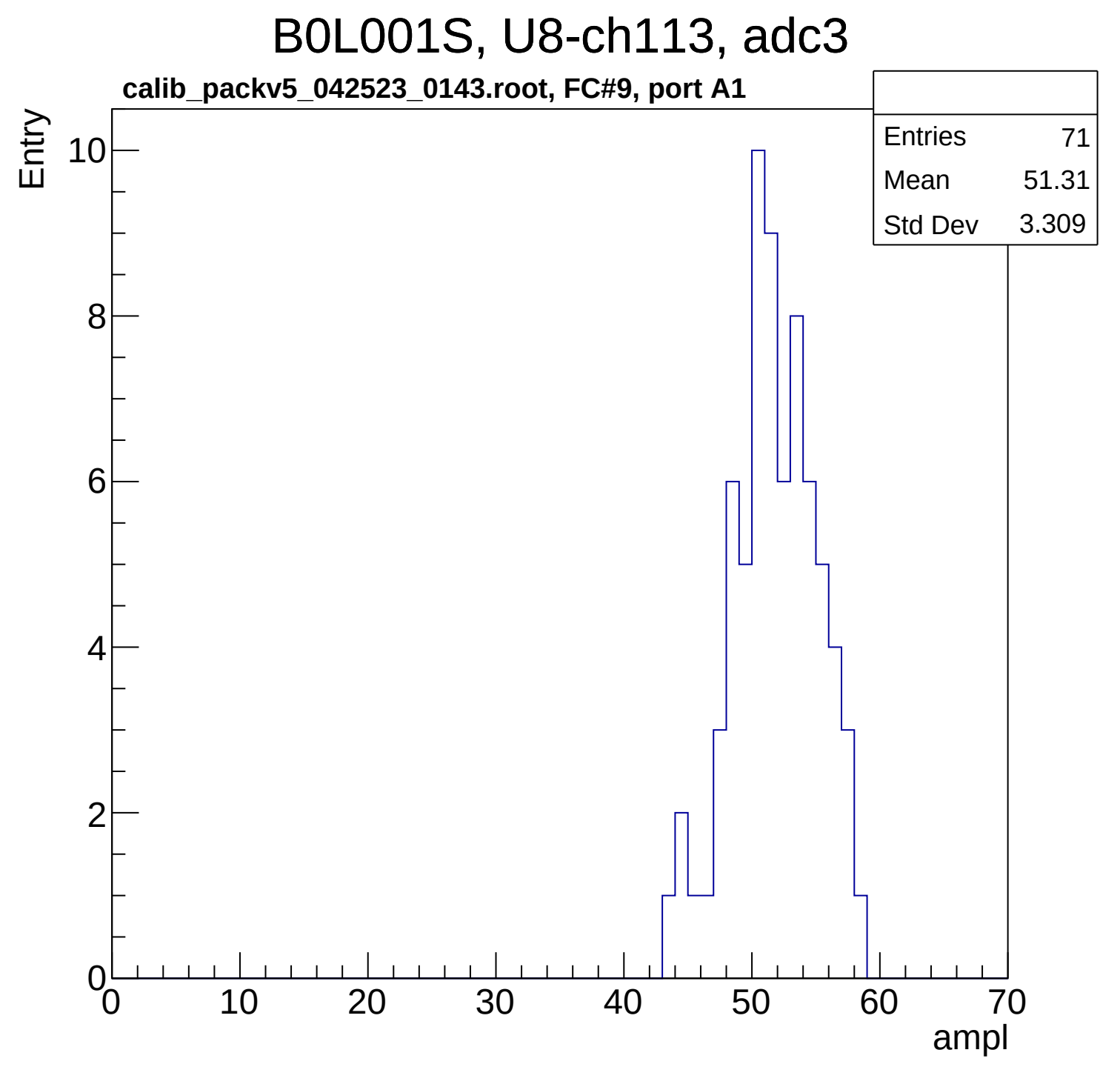
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.31
Std Dev 3.309
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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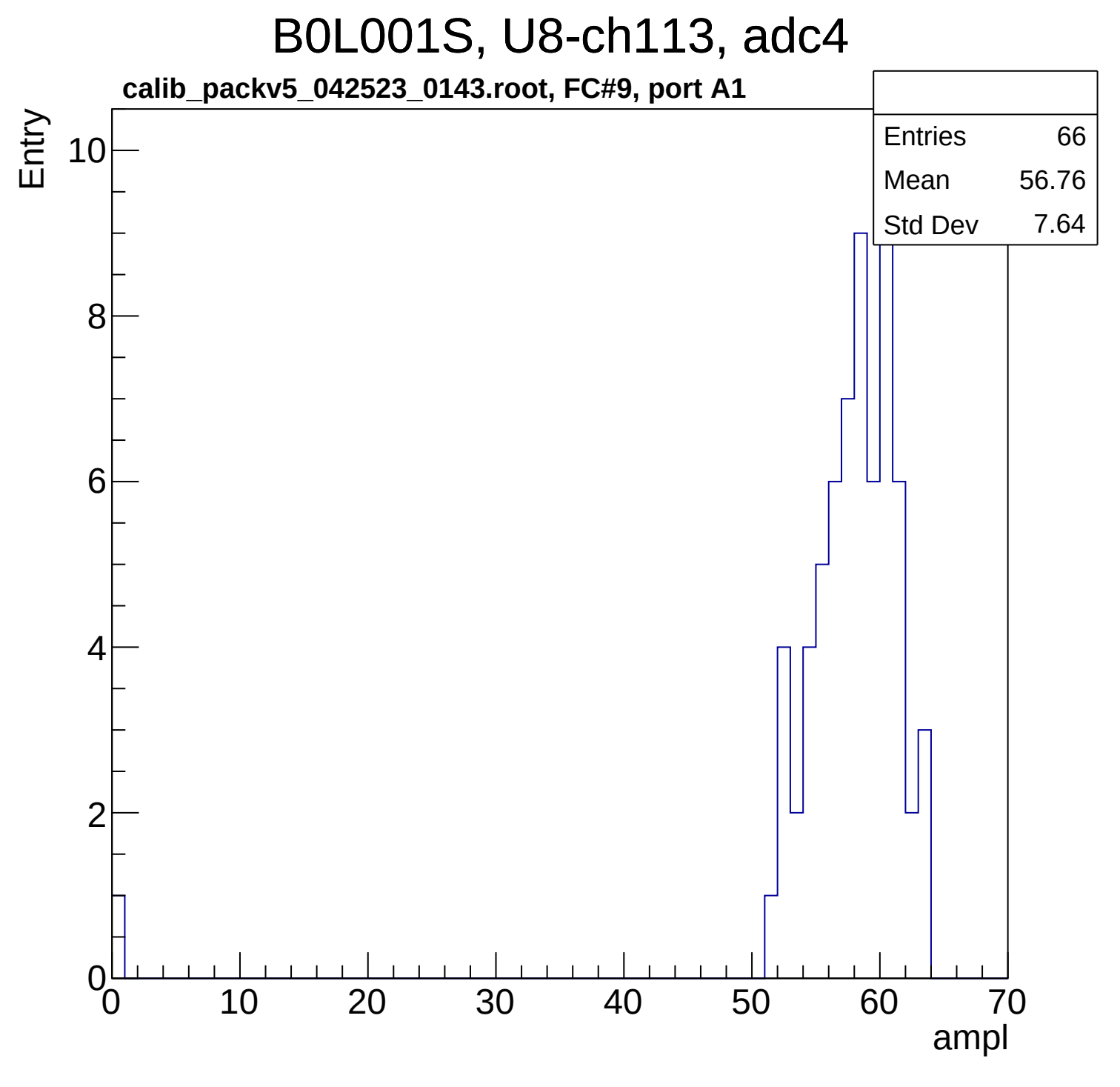
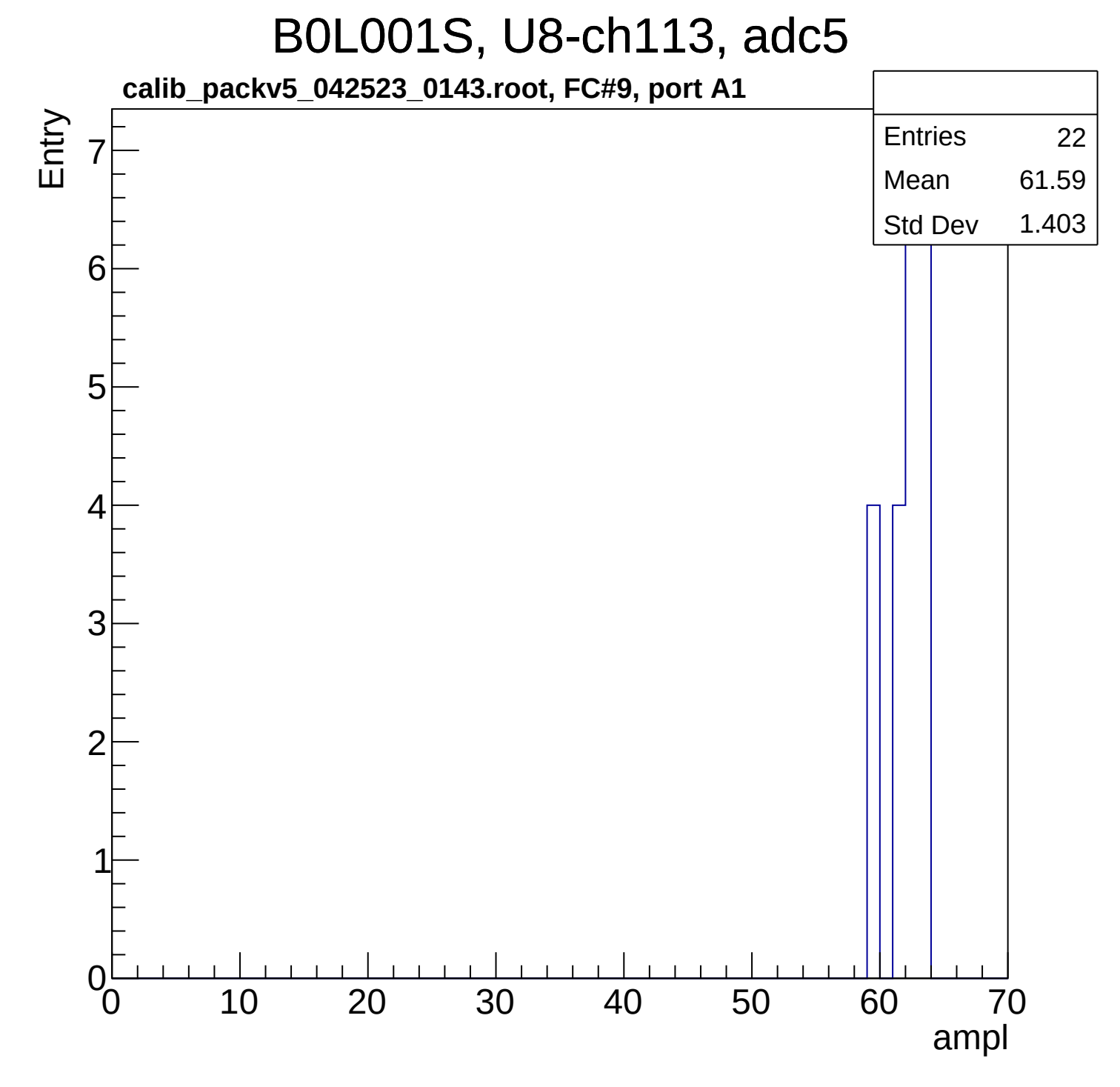
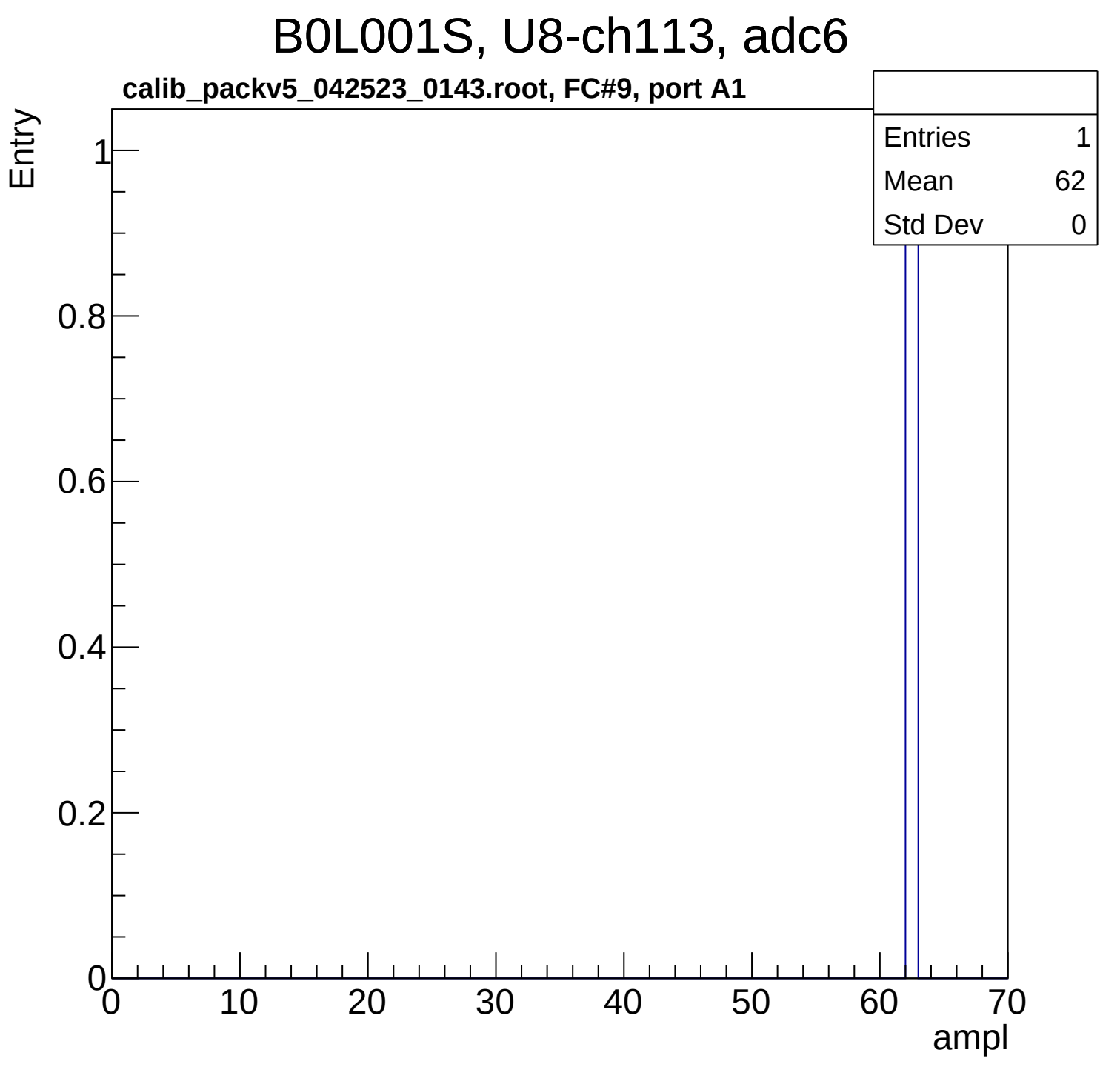

B0L001S, U8-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.76
Std Dev 7.64
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 61.59
Std Dev 1.403
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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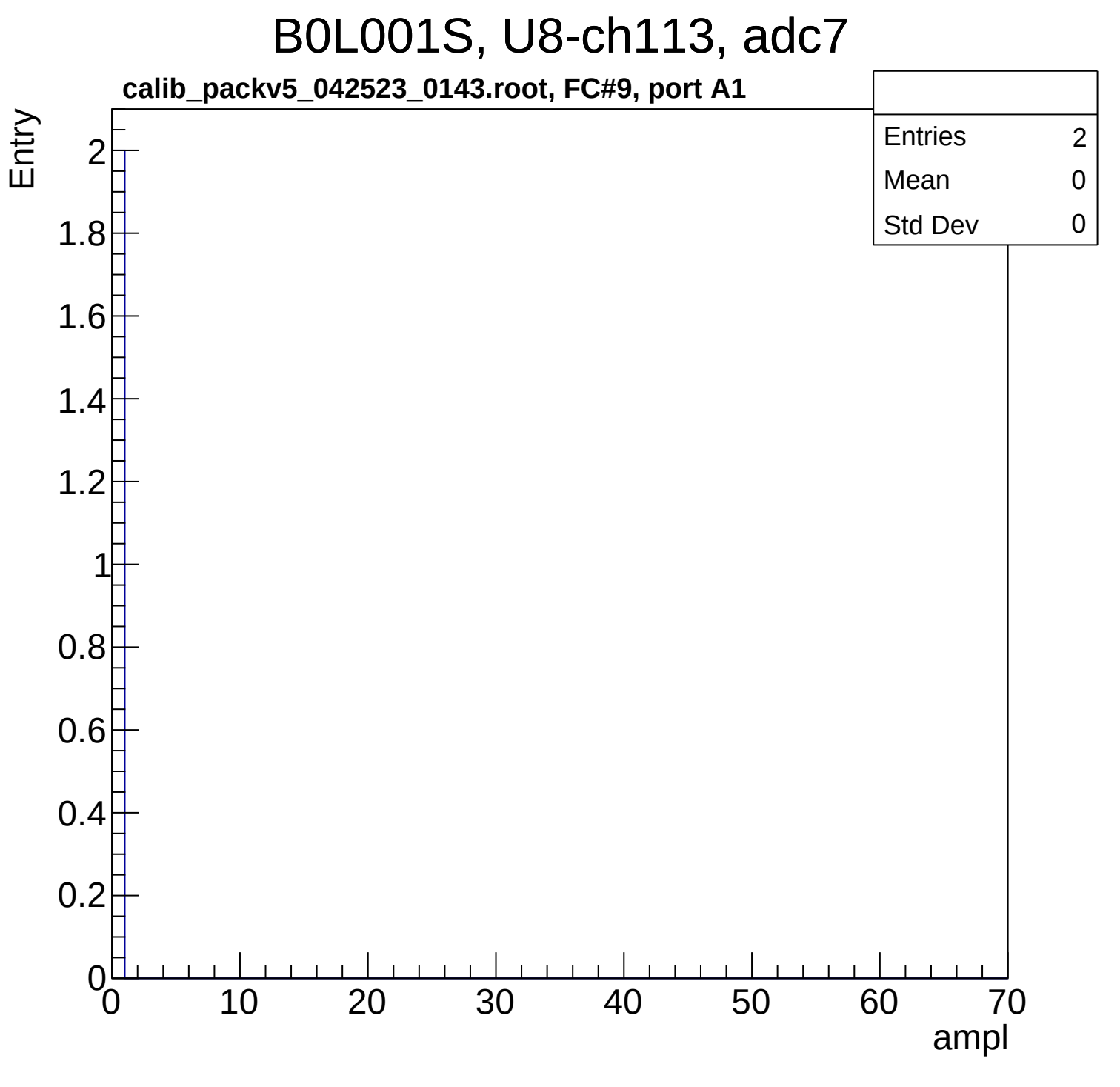
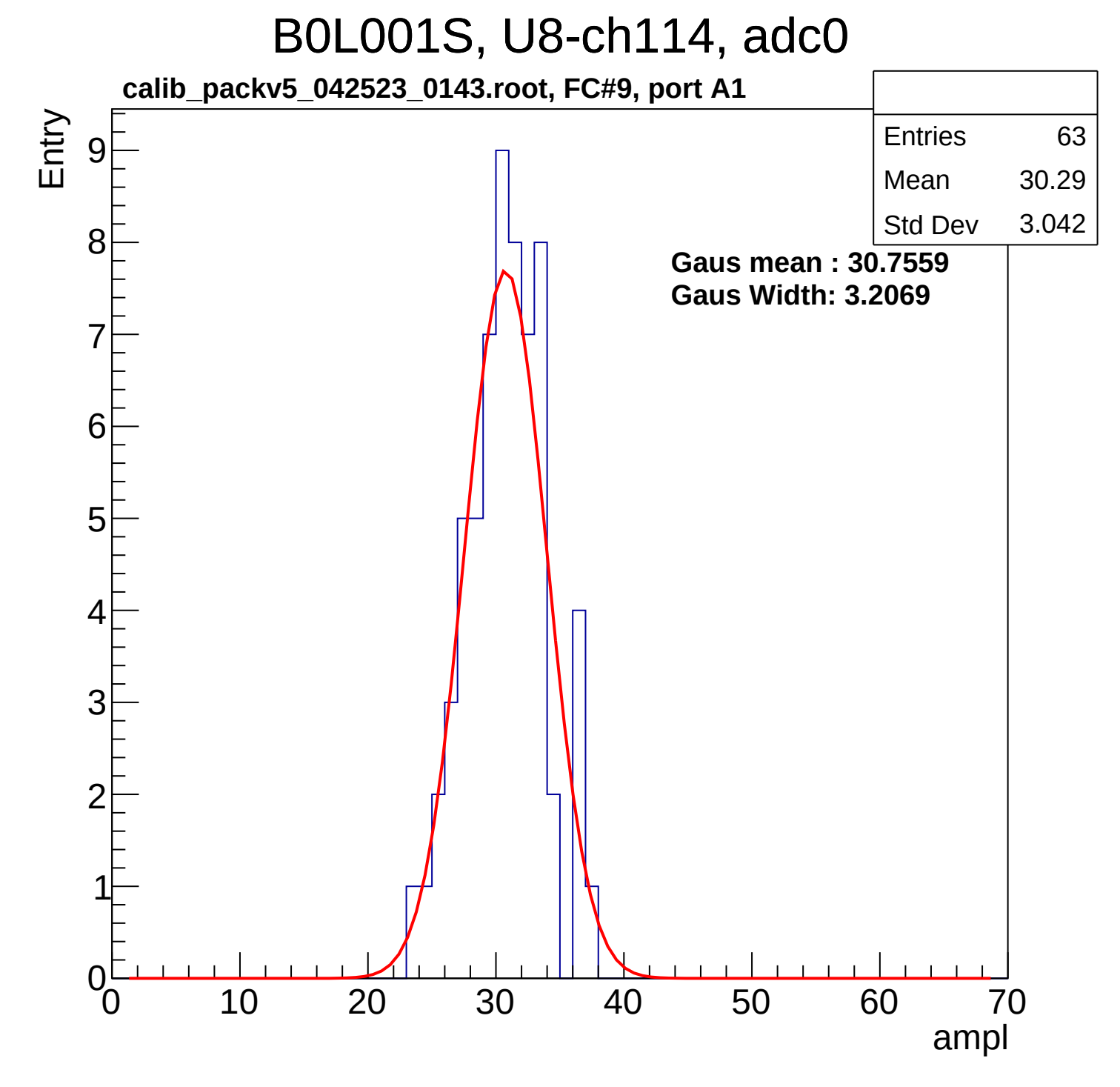
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.29
Std Dev 3.042
Gaus mean : 30.7559
Gaus Width: 3.2069
Entry
ampl
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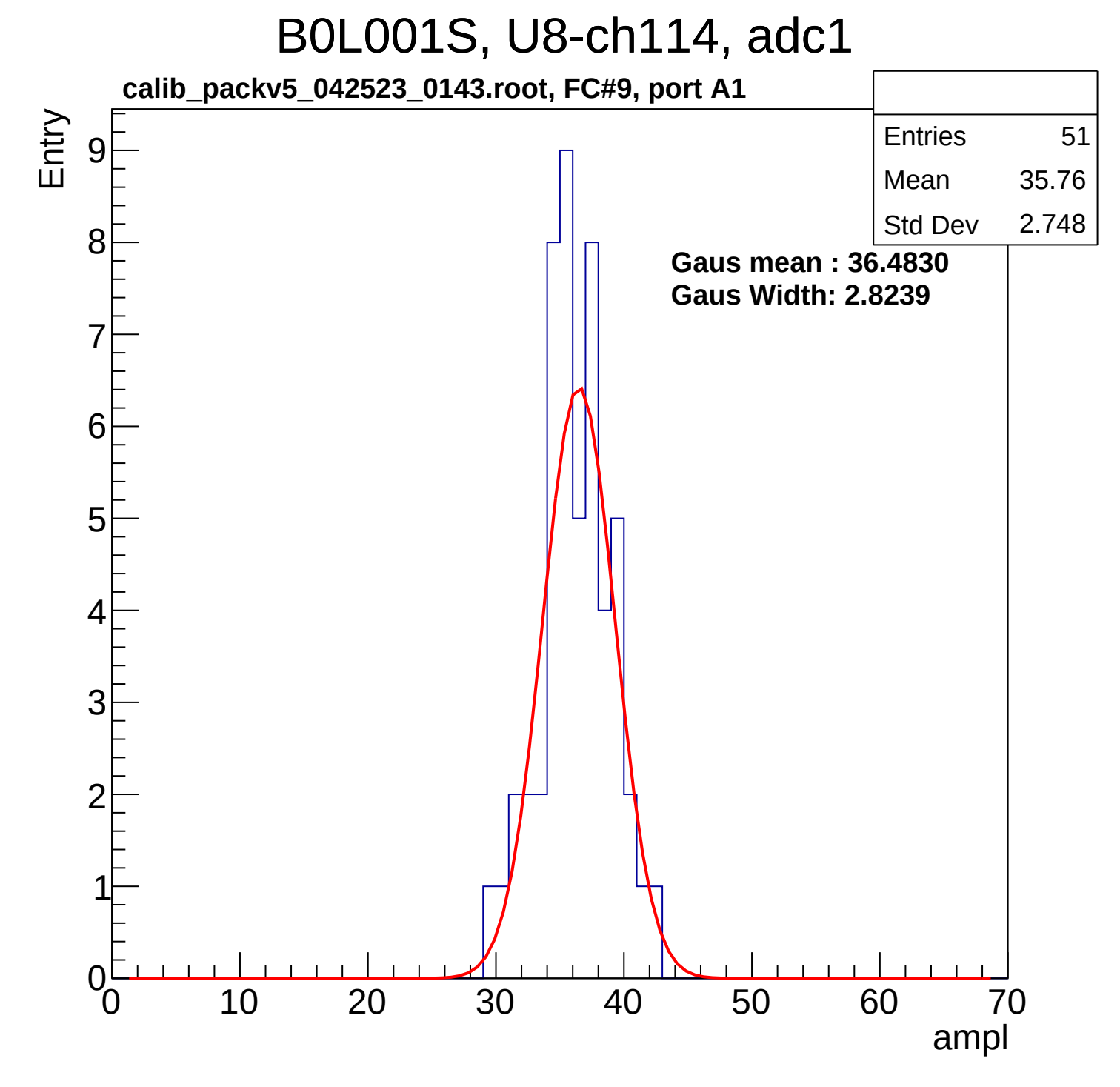
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 35.76
Std Dev 2.748
Gaus mean : 36.4830
Gaus Width: 2.8239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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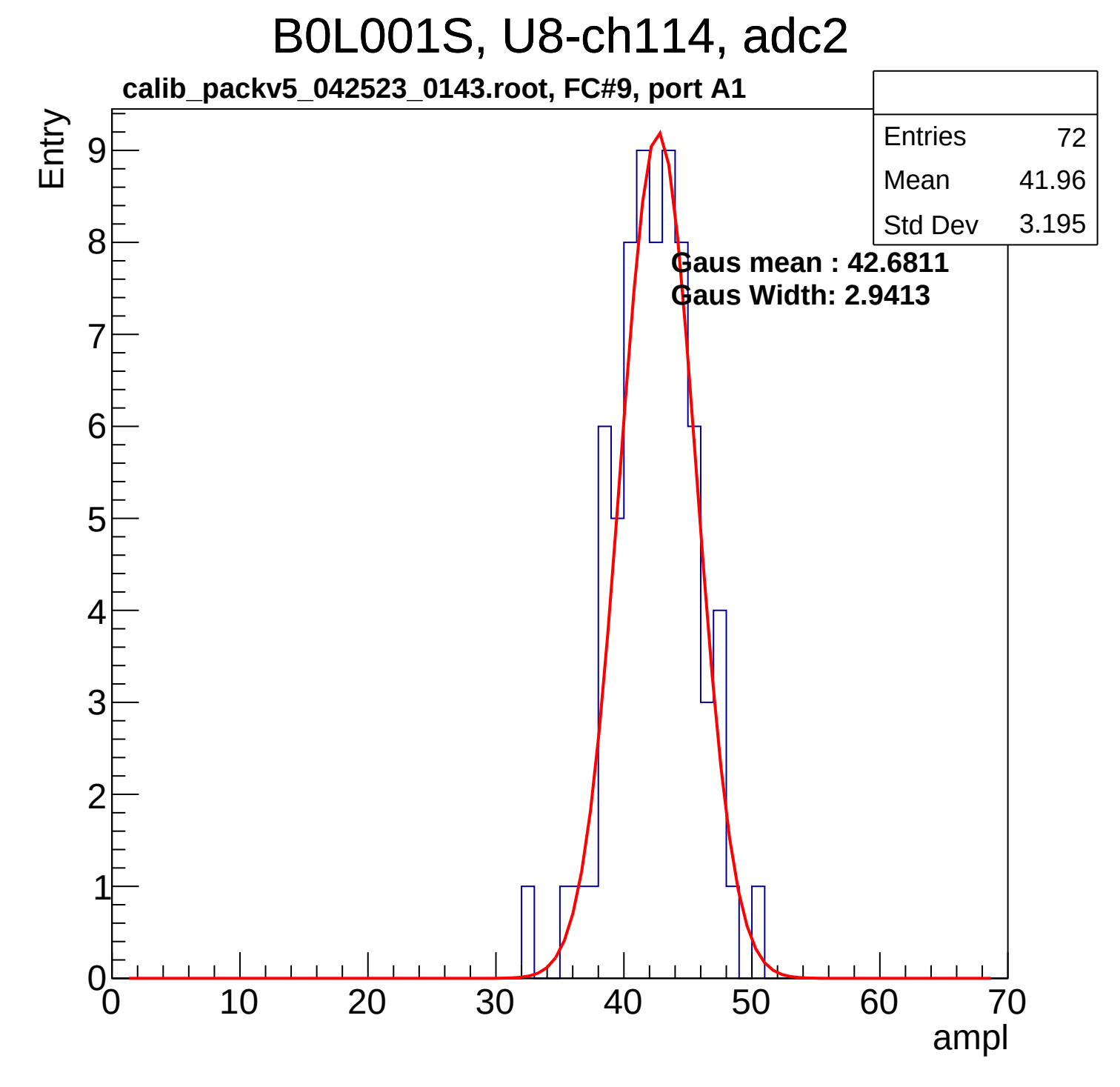
B0L001S, U8-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 41.96
Std Dev 3.195
Gaus mean : 42.6811
Gaus Width: 2.9413
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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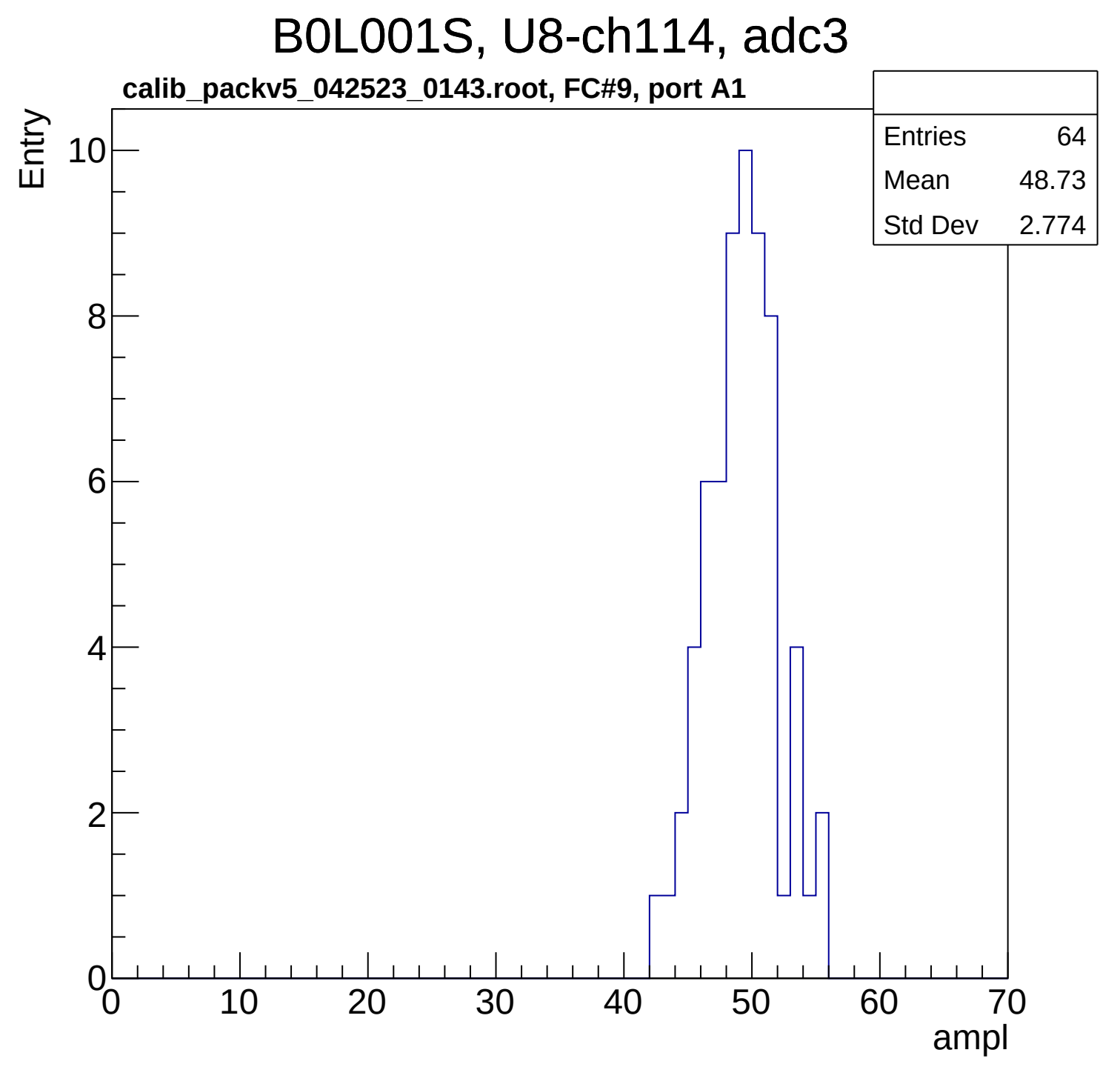
B0L001S, U8-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.73
Std Dev 2.774
Entry
ampl
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10
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40
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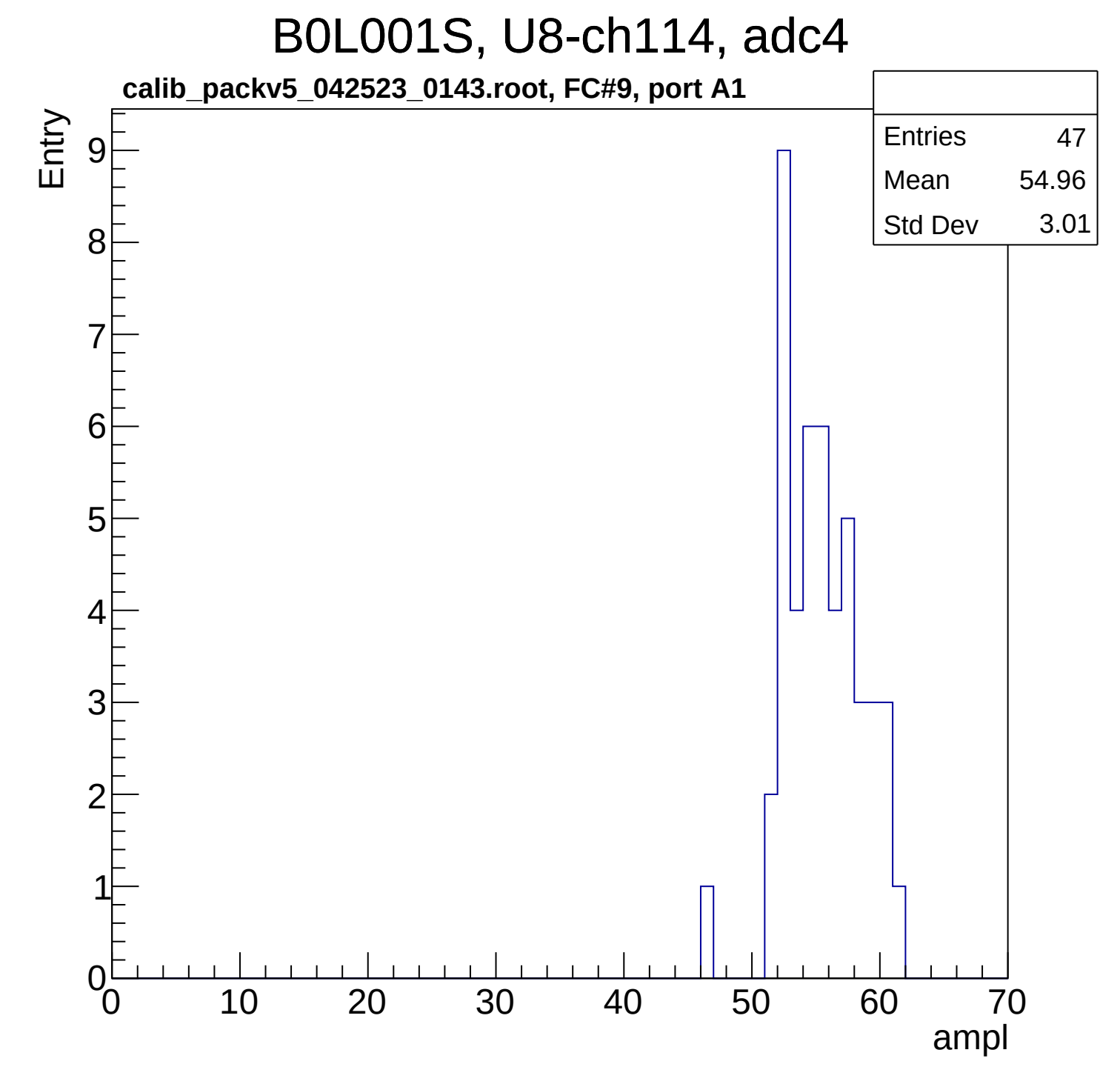
60
70


B0L001S, U8-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 54.96
Std Dev 3.01
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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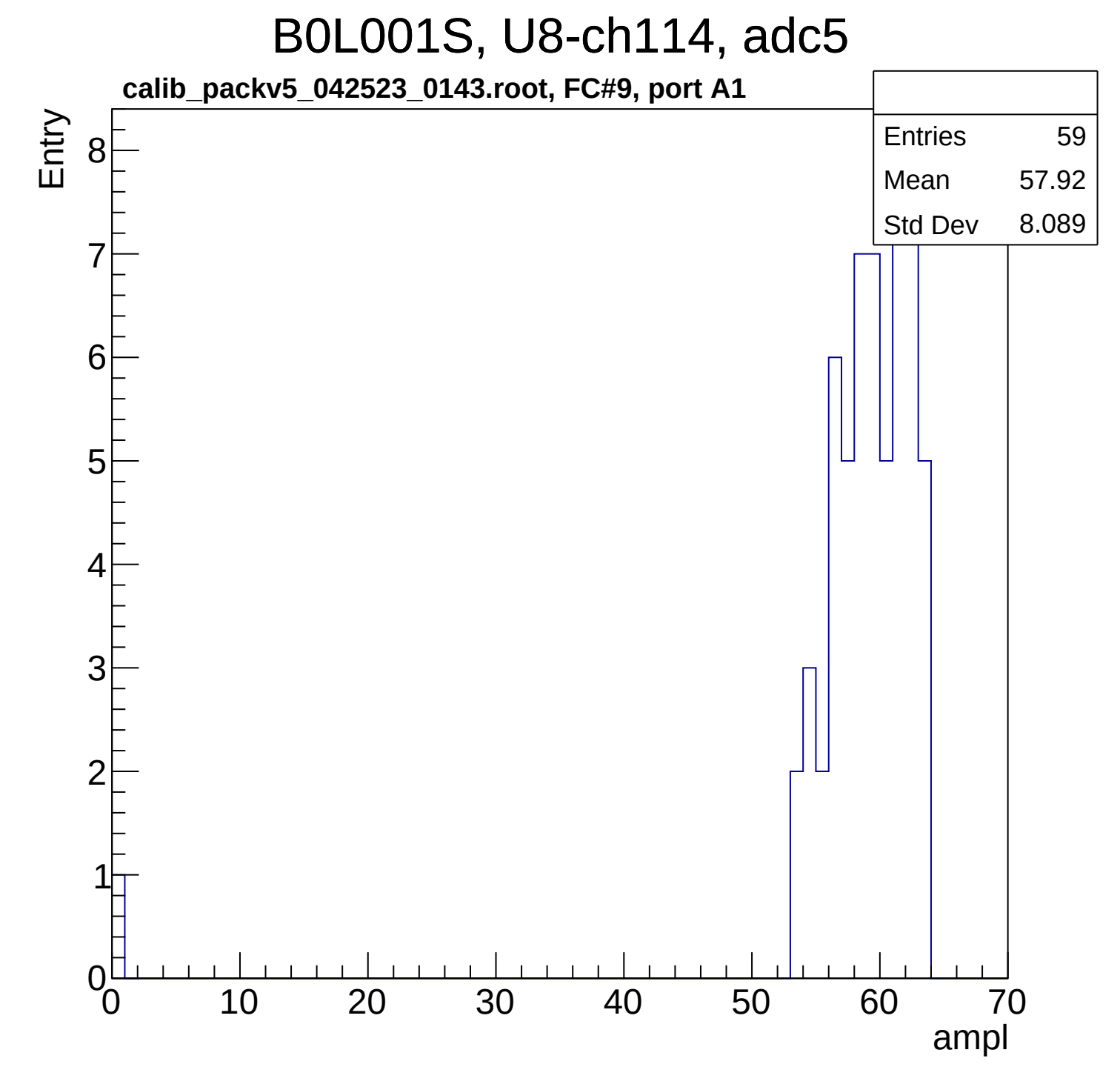
B0L001S, U8-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.92
Std Dev 8.089
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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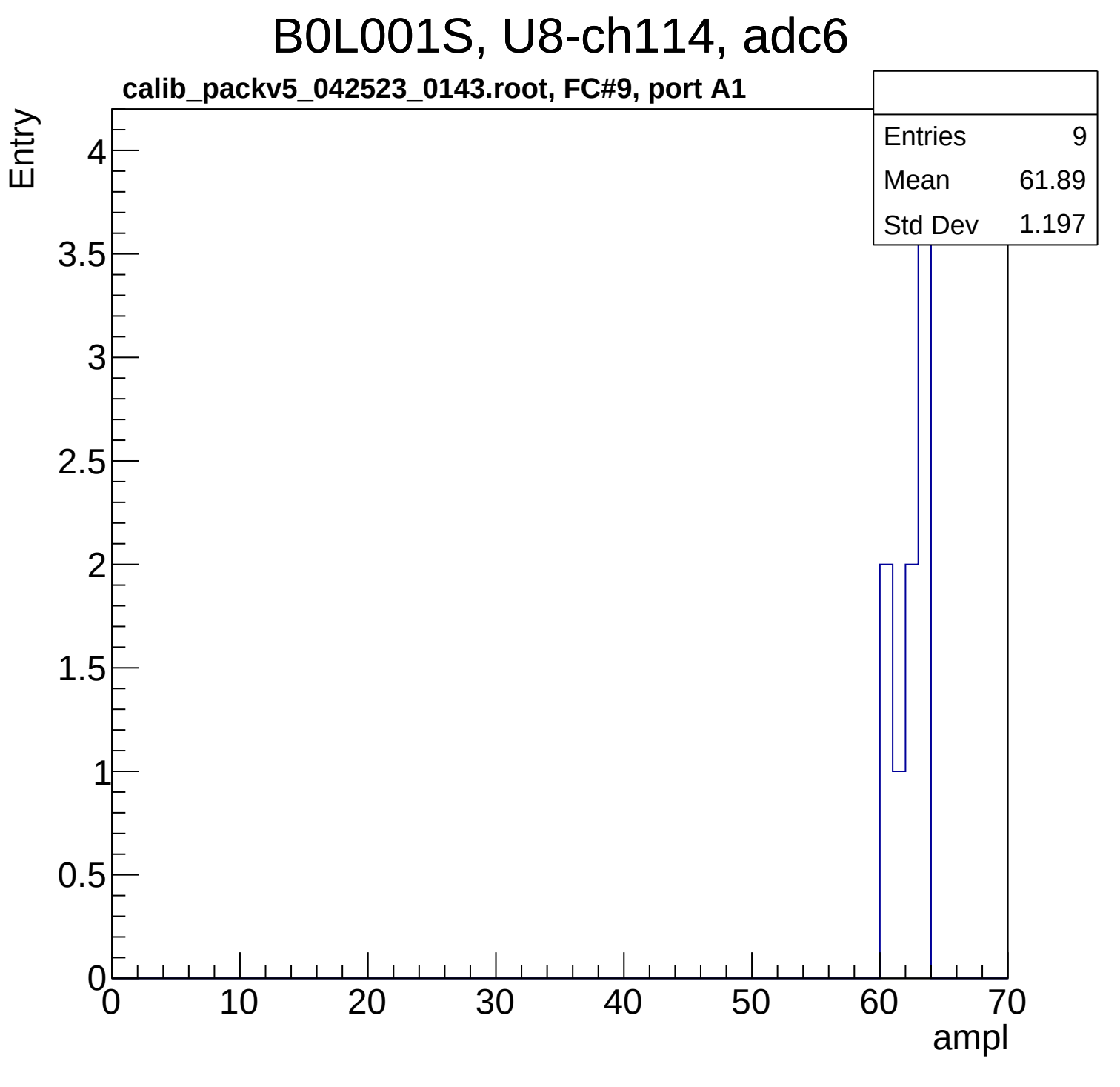
B0L001S, U8-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.89
Std Dev 1.197
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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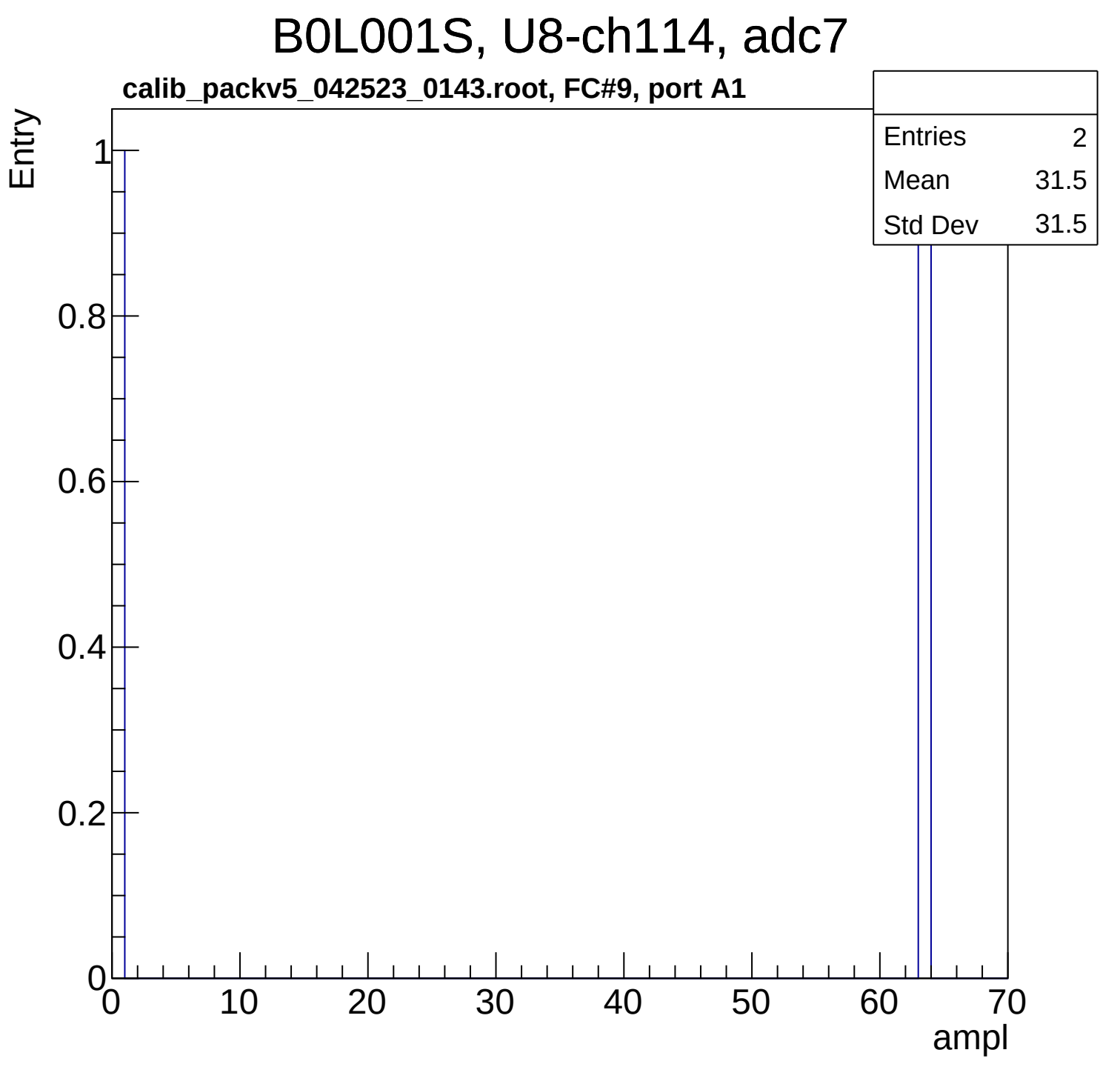
20
30
40
50
60
70


B0L001S, U8-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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0
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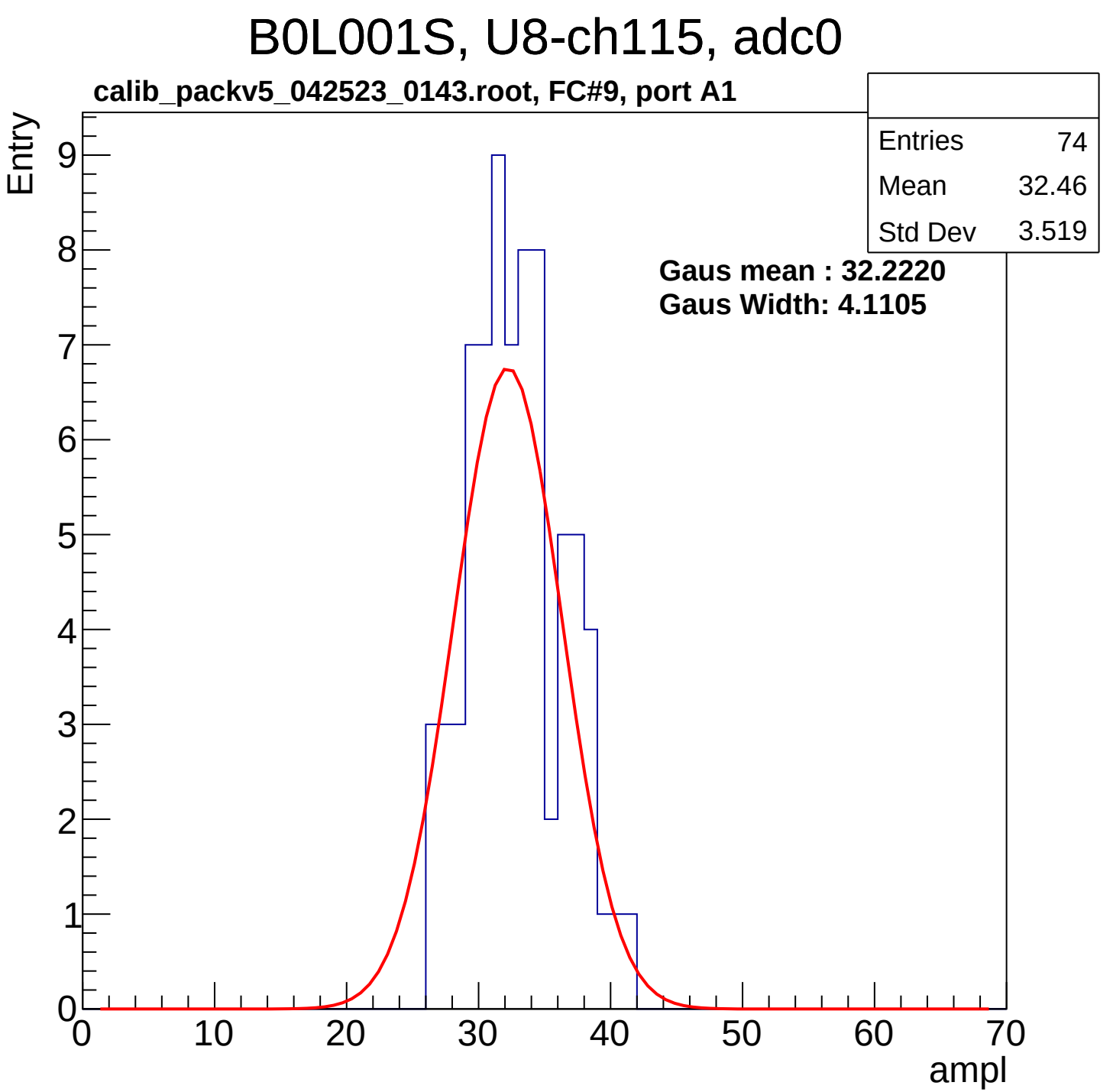
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 32.46
Std Dev 3.519
Gaus mean : 32.2220
Gaus Width: 4.1105
Entry
ampl
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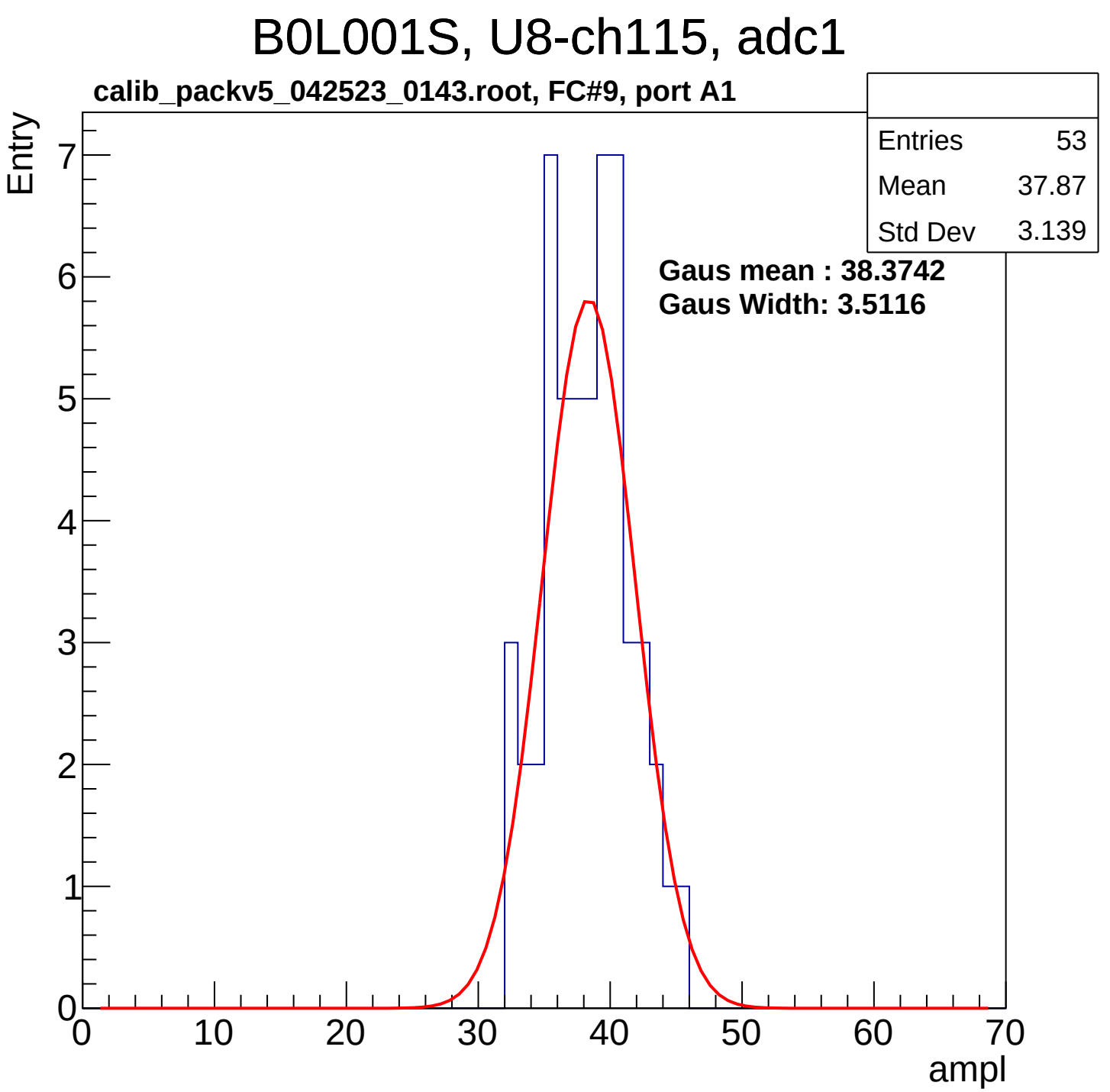
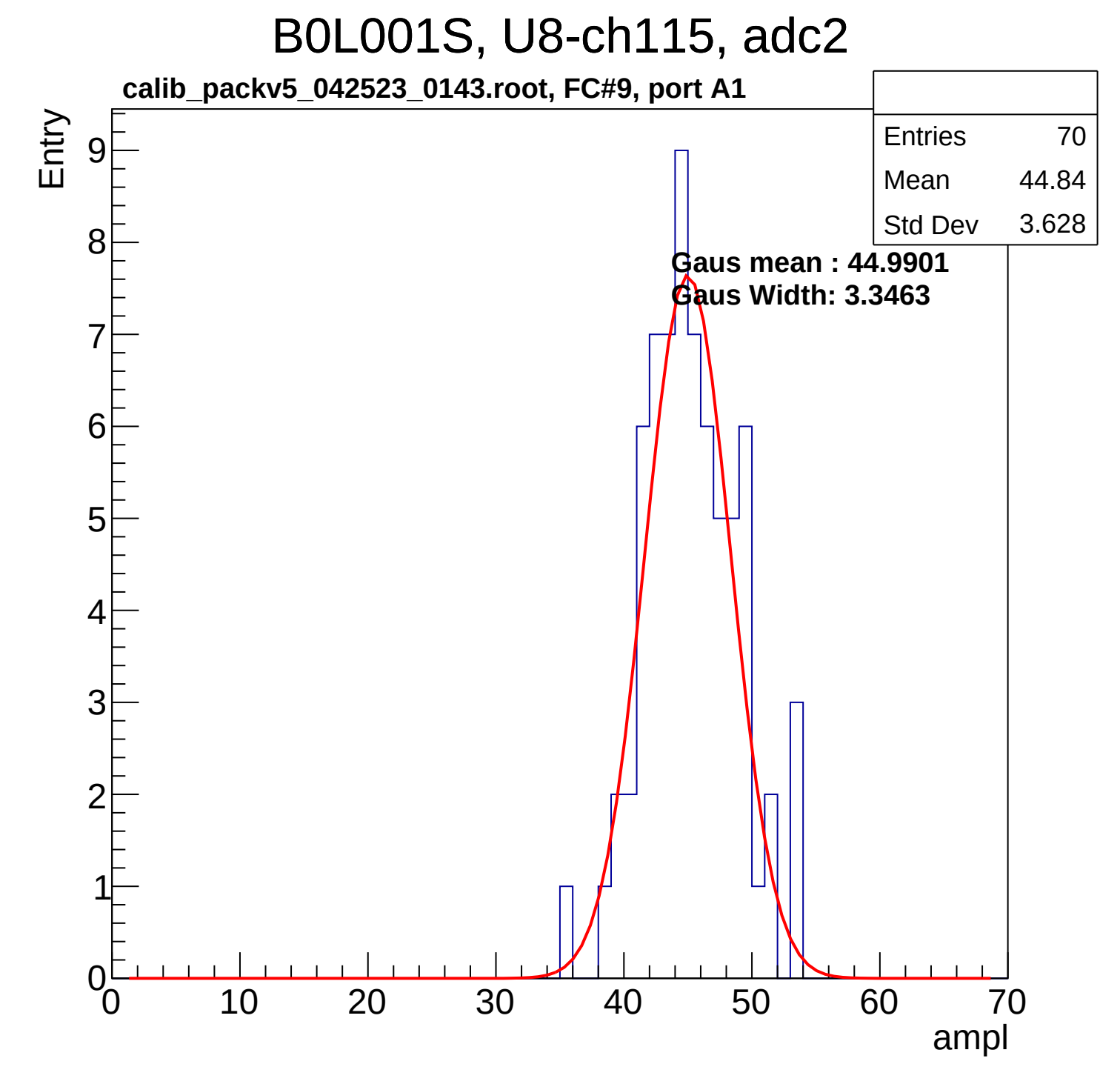
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 37.87
Std Dev 3.139
Gaus mean : 38.3742
Gaus Width: 3.5116
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.84
Std Dev 3.628
Gaus mean : 44.9901
Gaus Width: 3.3463
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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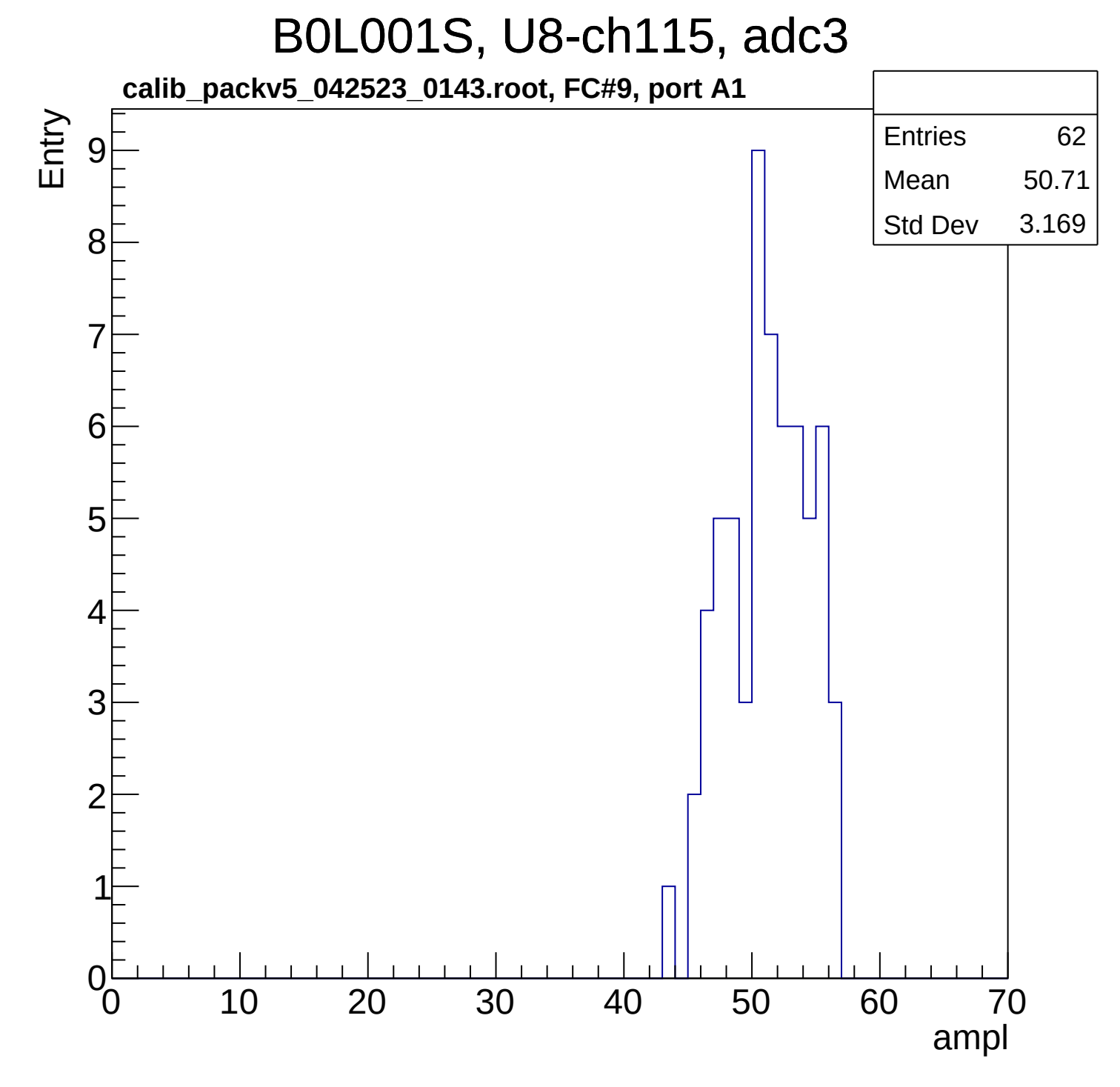
B0L001S, U8-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.71
Std Dev 3.169
Entry
ampl
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3
4
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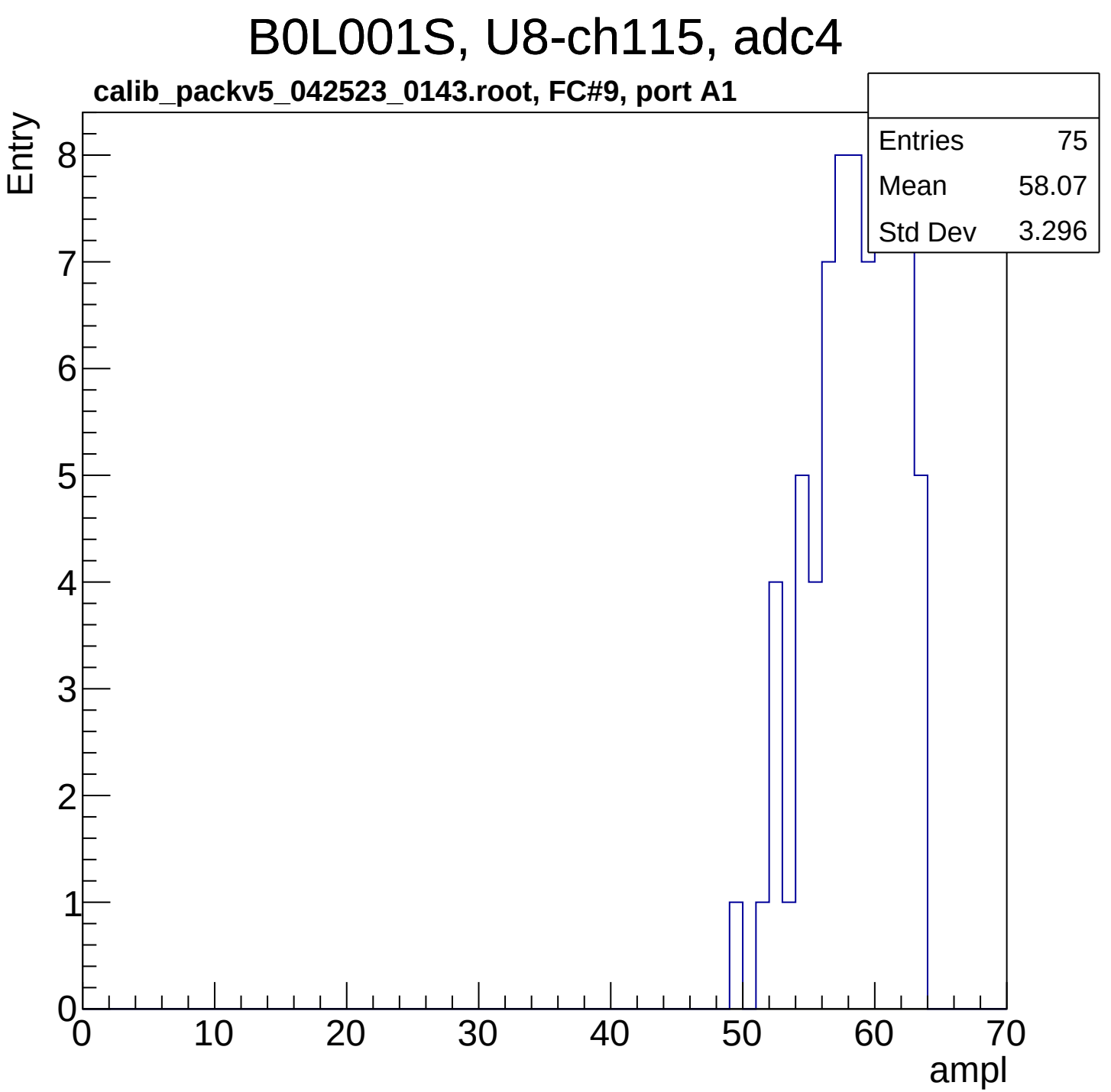
B0L001S, U8-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 58.07
Std Dev 3.296
Entry
ampl
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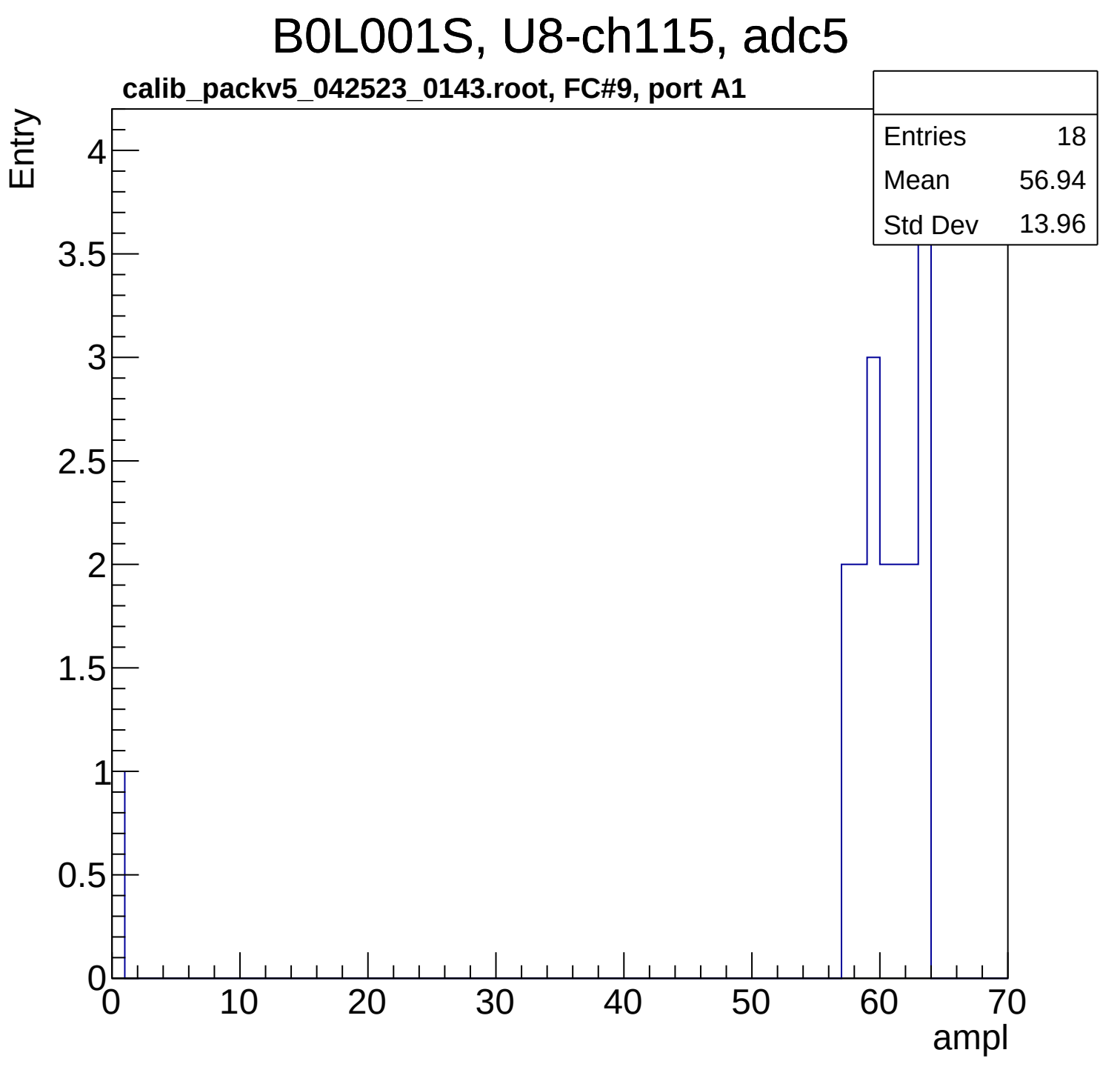
B0L001S, U8-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 56.94
Std Dev 13.96
Entry
ampl
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1.5
2
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3
3.5
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0
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60
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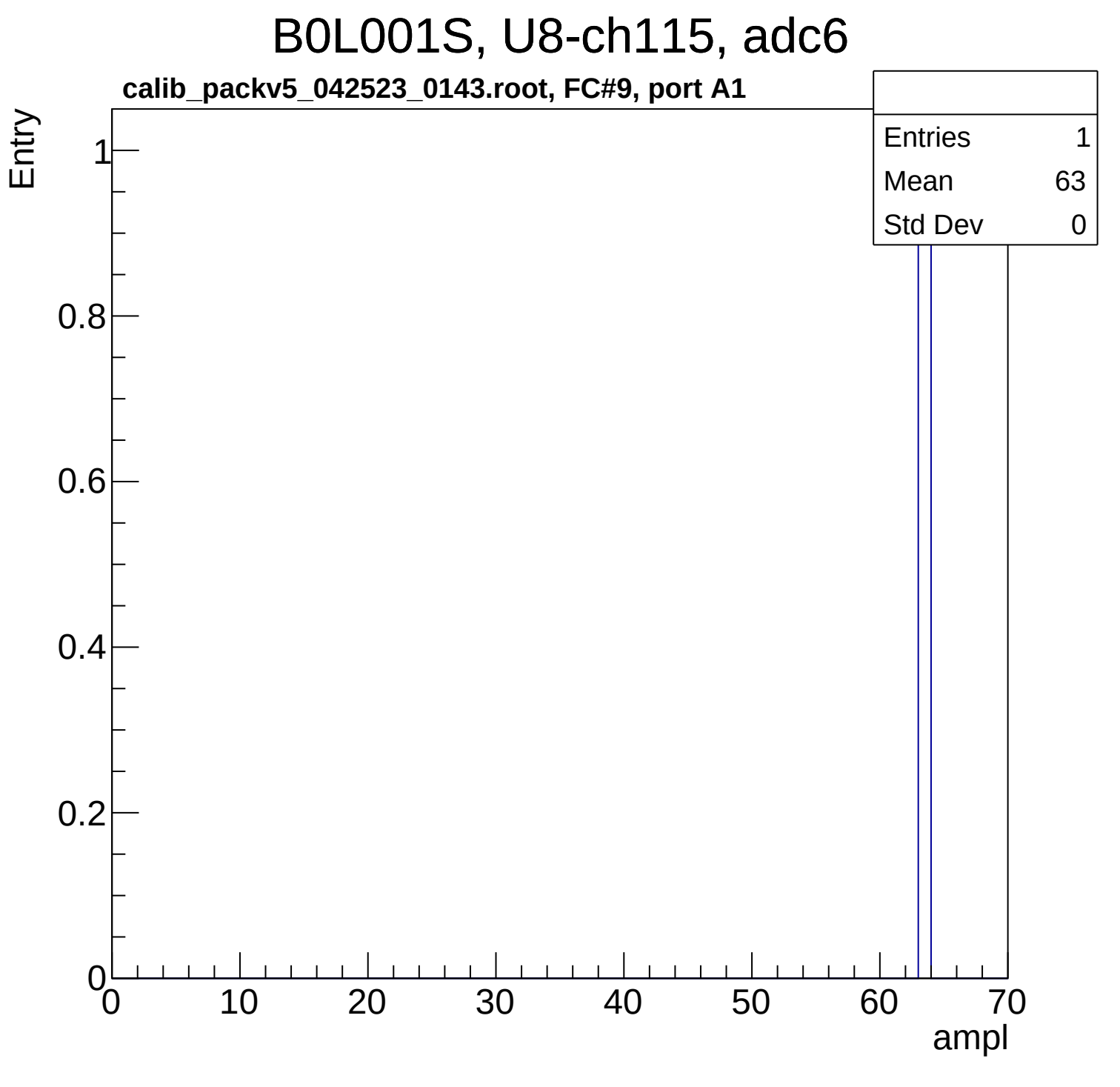
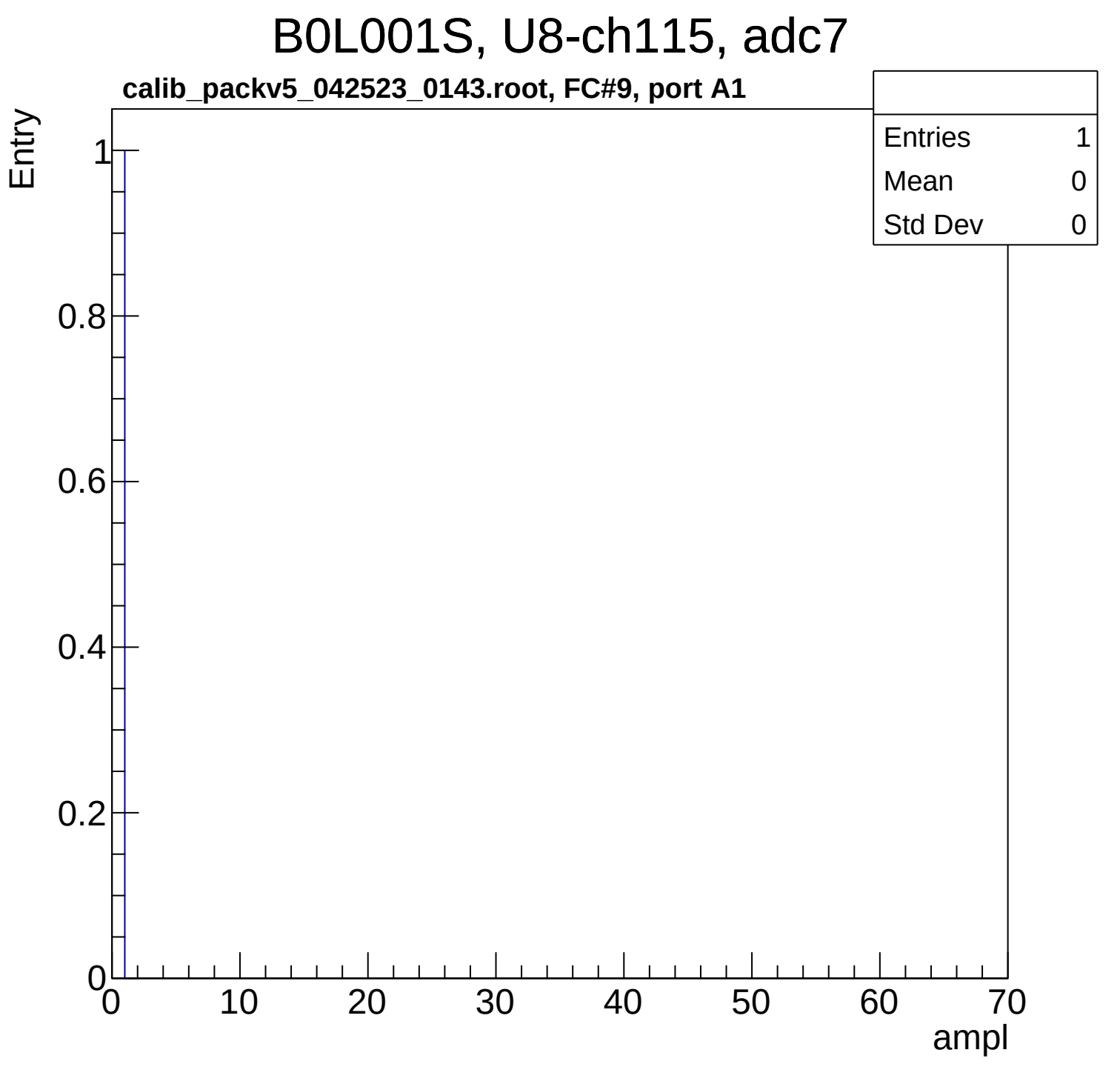

B0L001S, U8-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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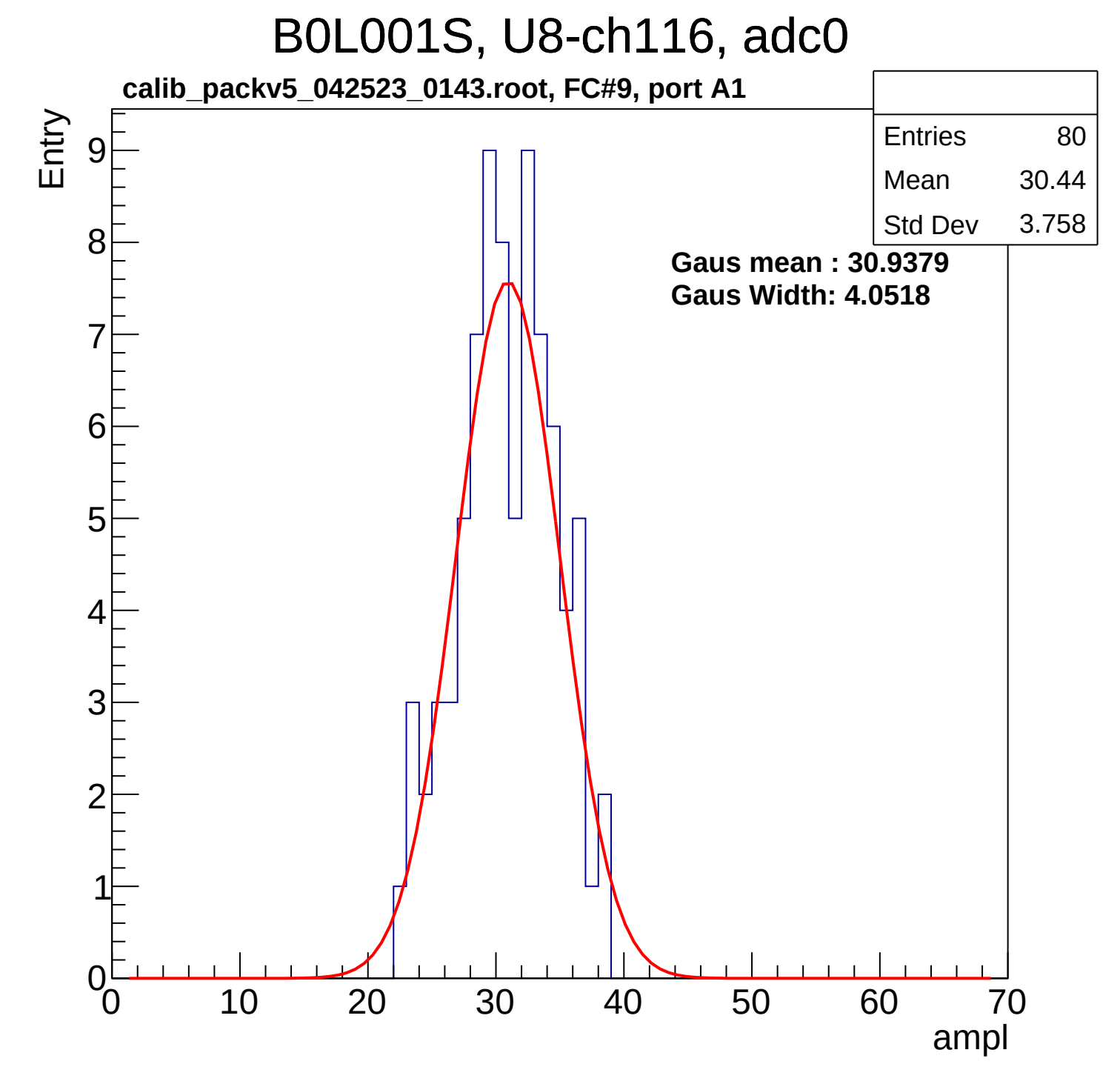
ampl


B0L001S, U8-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.44
Std Dev 3.758
Gaus mean : 30.9379
Gaus Width: 4.0518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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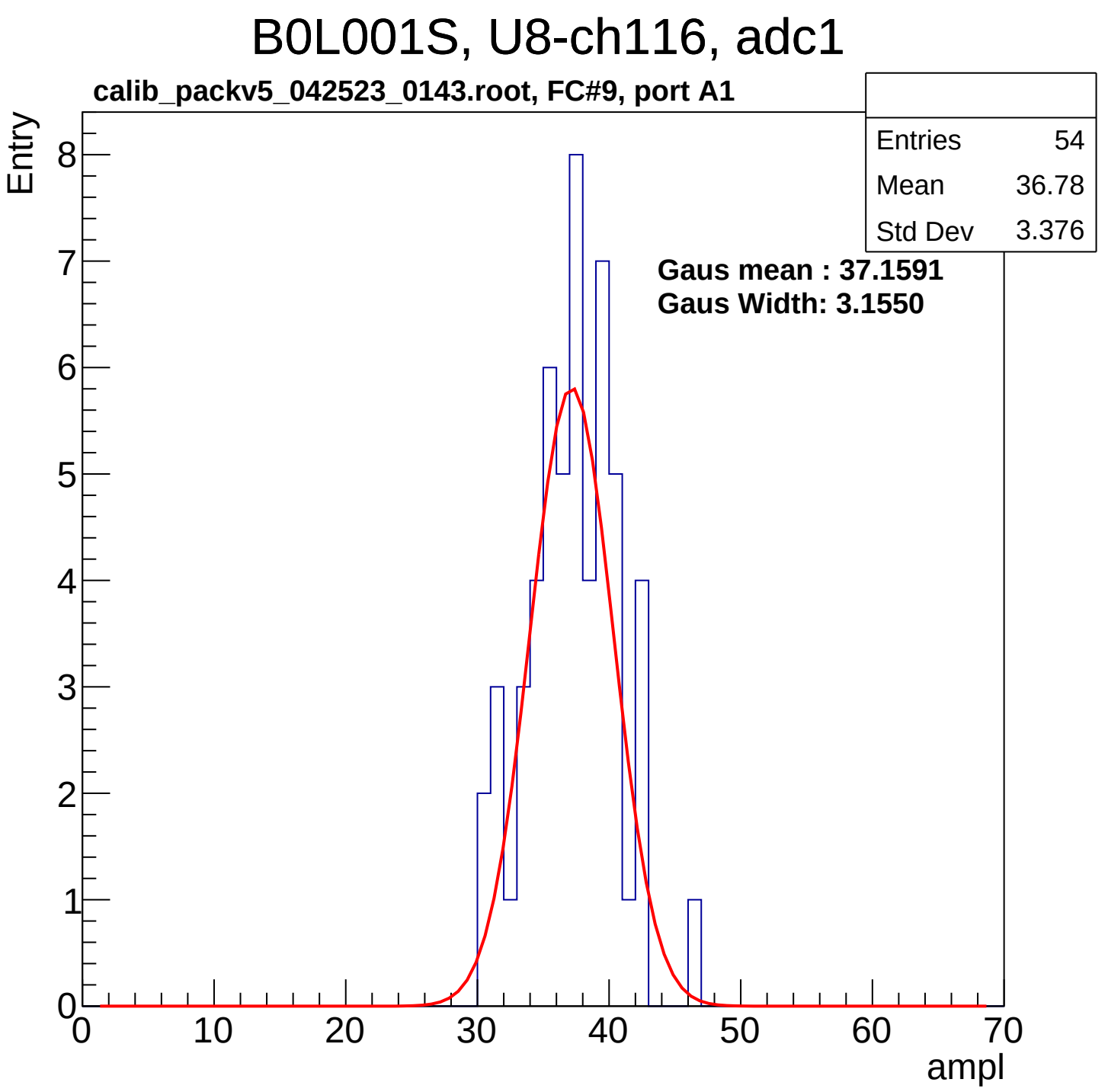
B0L001S, U8-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 36.78
Std Dev 3.376
Gaus mean : 37.1591
Gaus Width: 3.1550
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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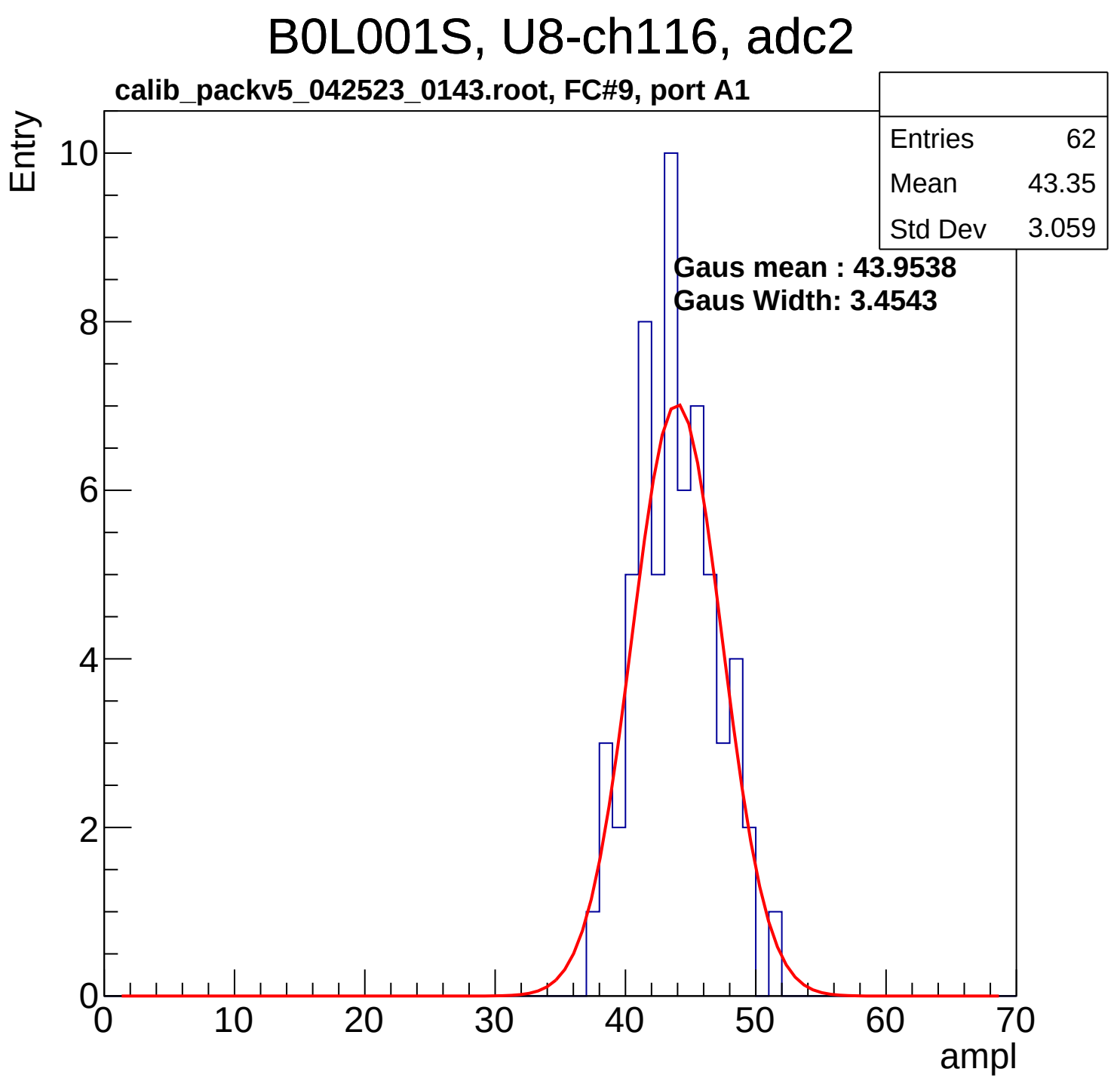
B0L001S, U8-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.35
Std Dev 3.059
Gaus mean : 43.9538
Gaus Width: 3.4543
Entry
ampl
0
2
4
6
8
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40
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60
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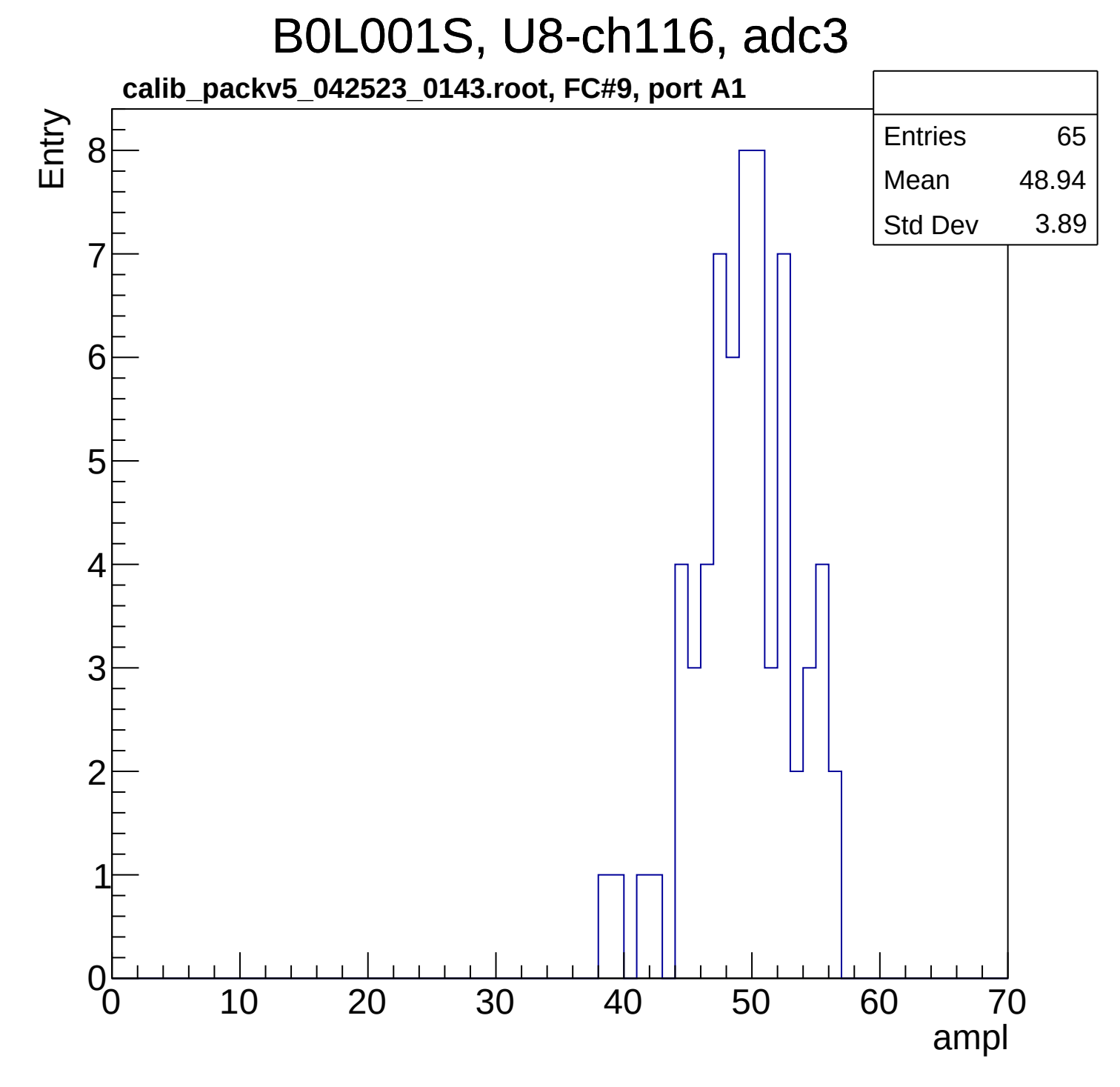
B0L001S, U8-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 48.94
Std Dev 3.89
Entry
ampl
0
1
2
3
4
5
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7
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0
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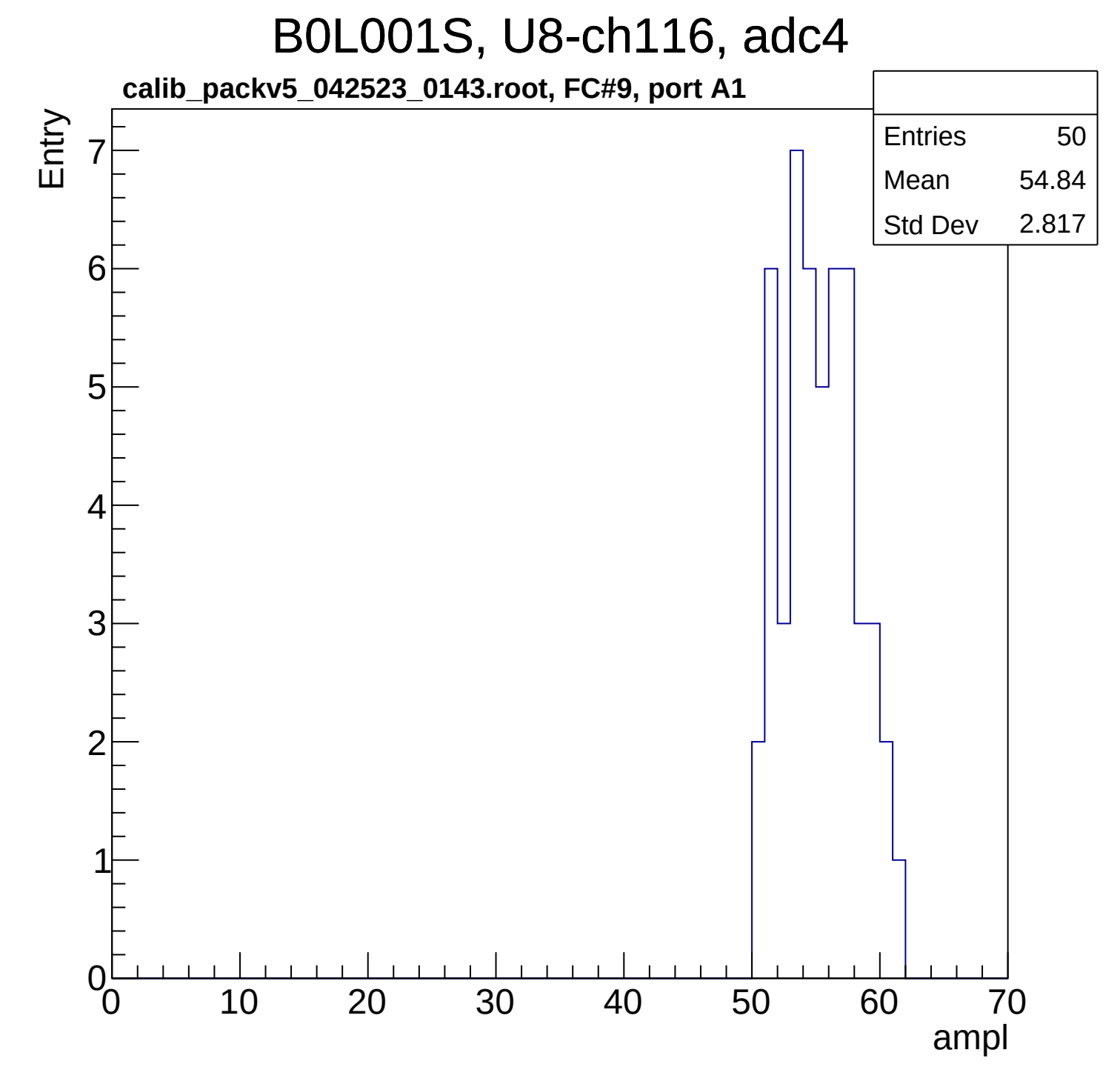
B0L001S, U8-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 54.84
Std Dev 2.817
Entry
ampl
0
1
2
3
4
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0
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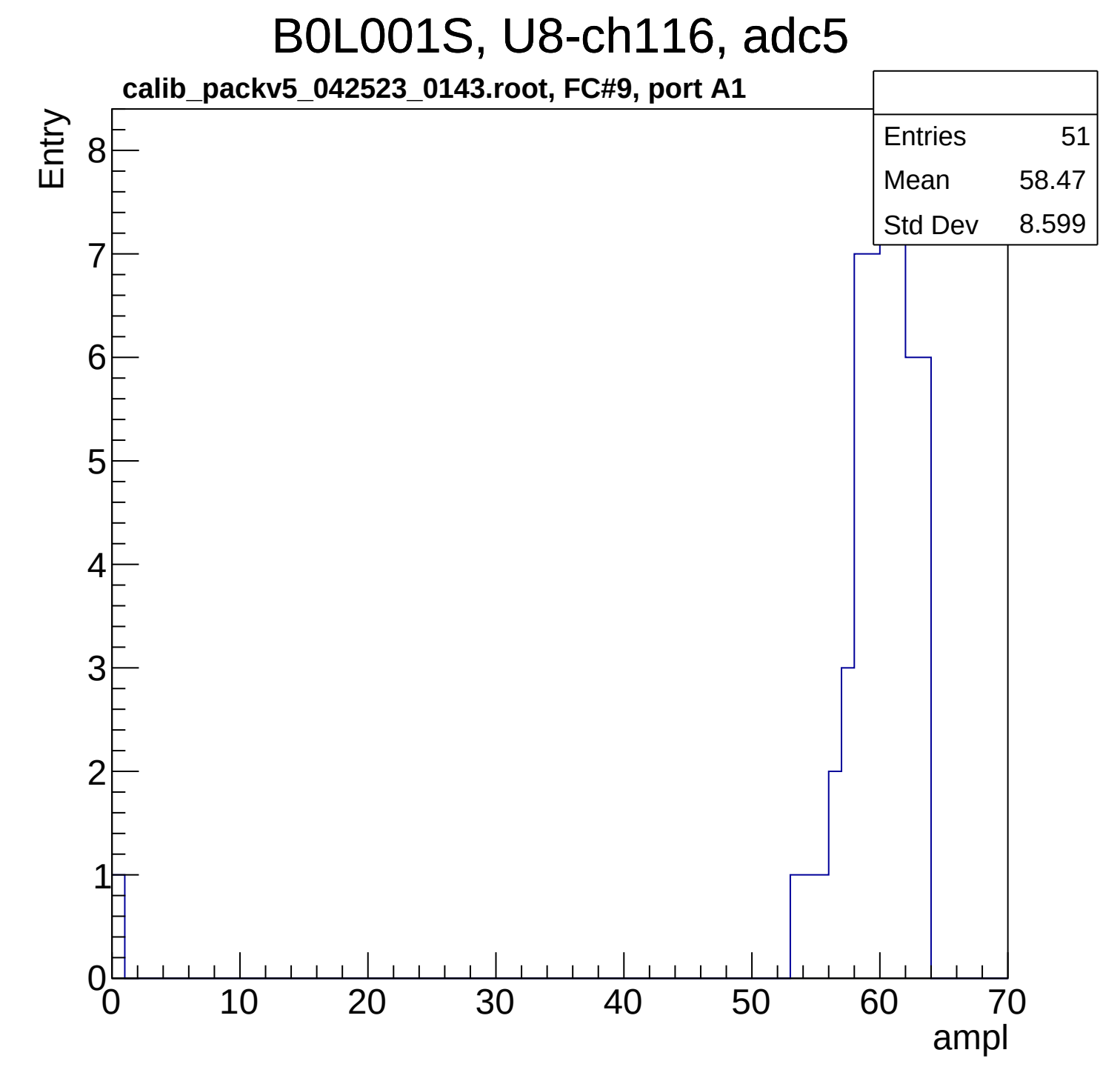
B0L001S, U8-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.47
Std Dev 8.599
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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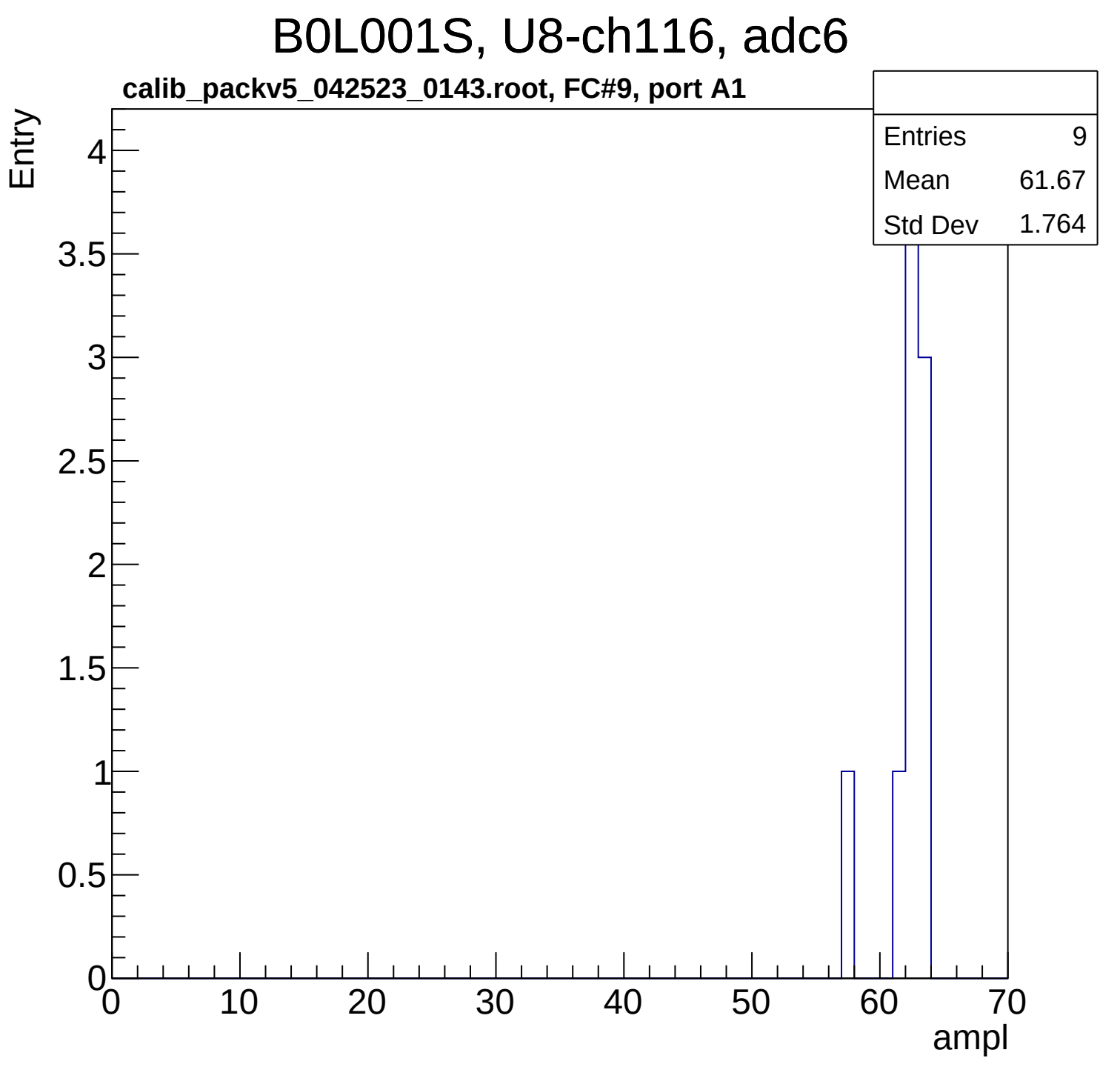
B0L001S, U8-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.67
Std Dev 1.764
Entry
ampl
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0.5
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1.5
2
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3.5
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0
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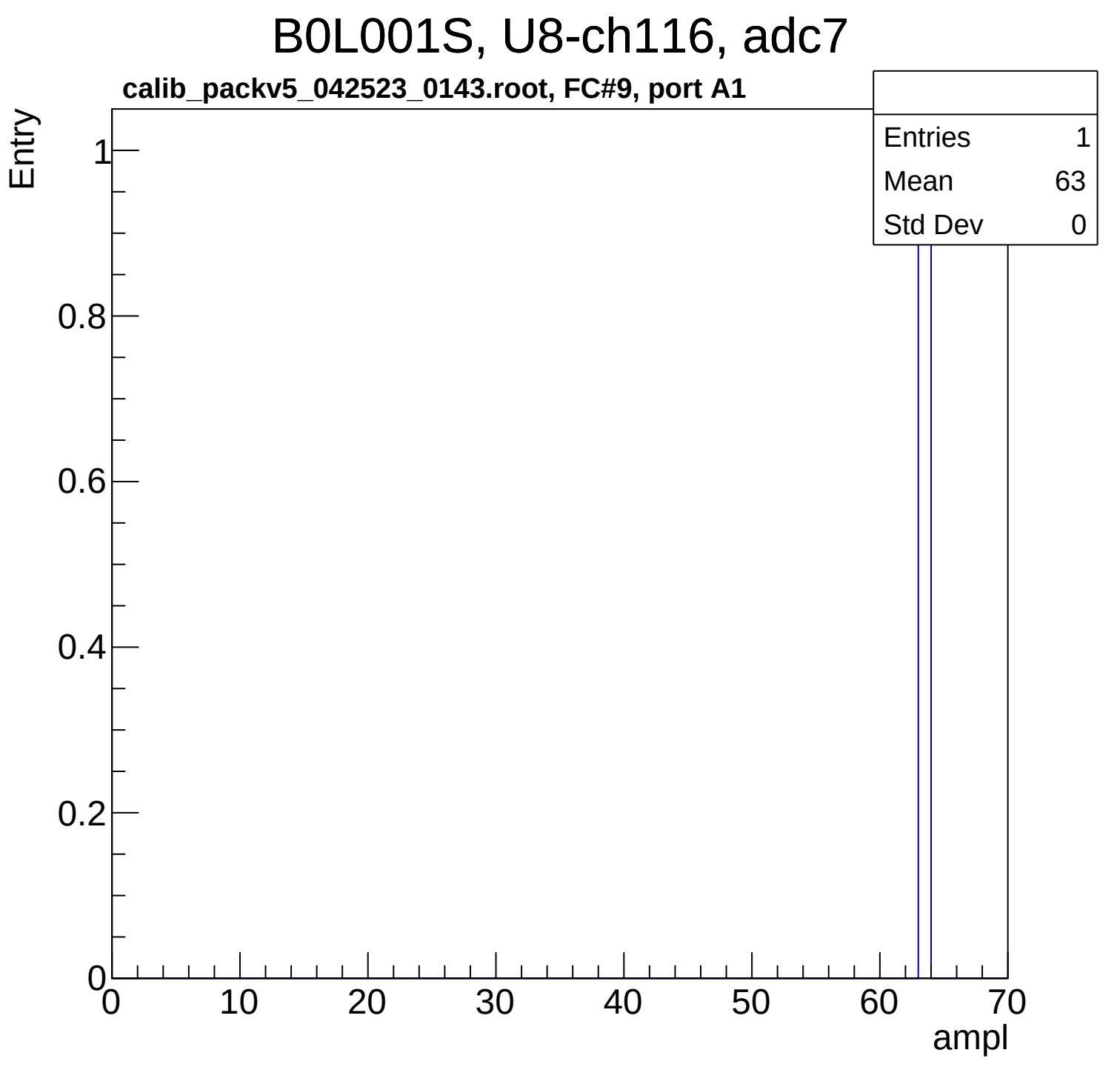
B0L001S, U8-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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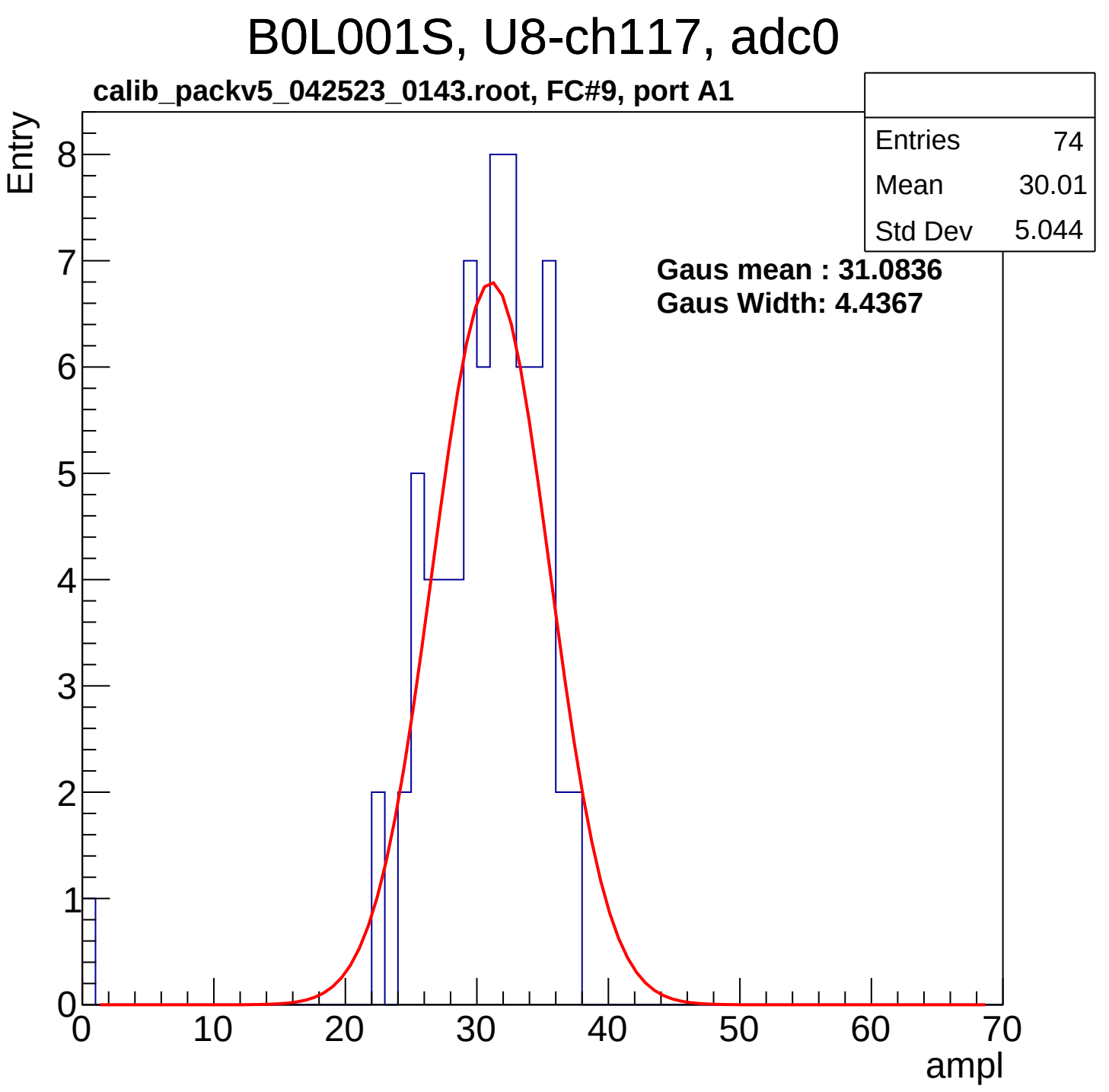
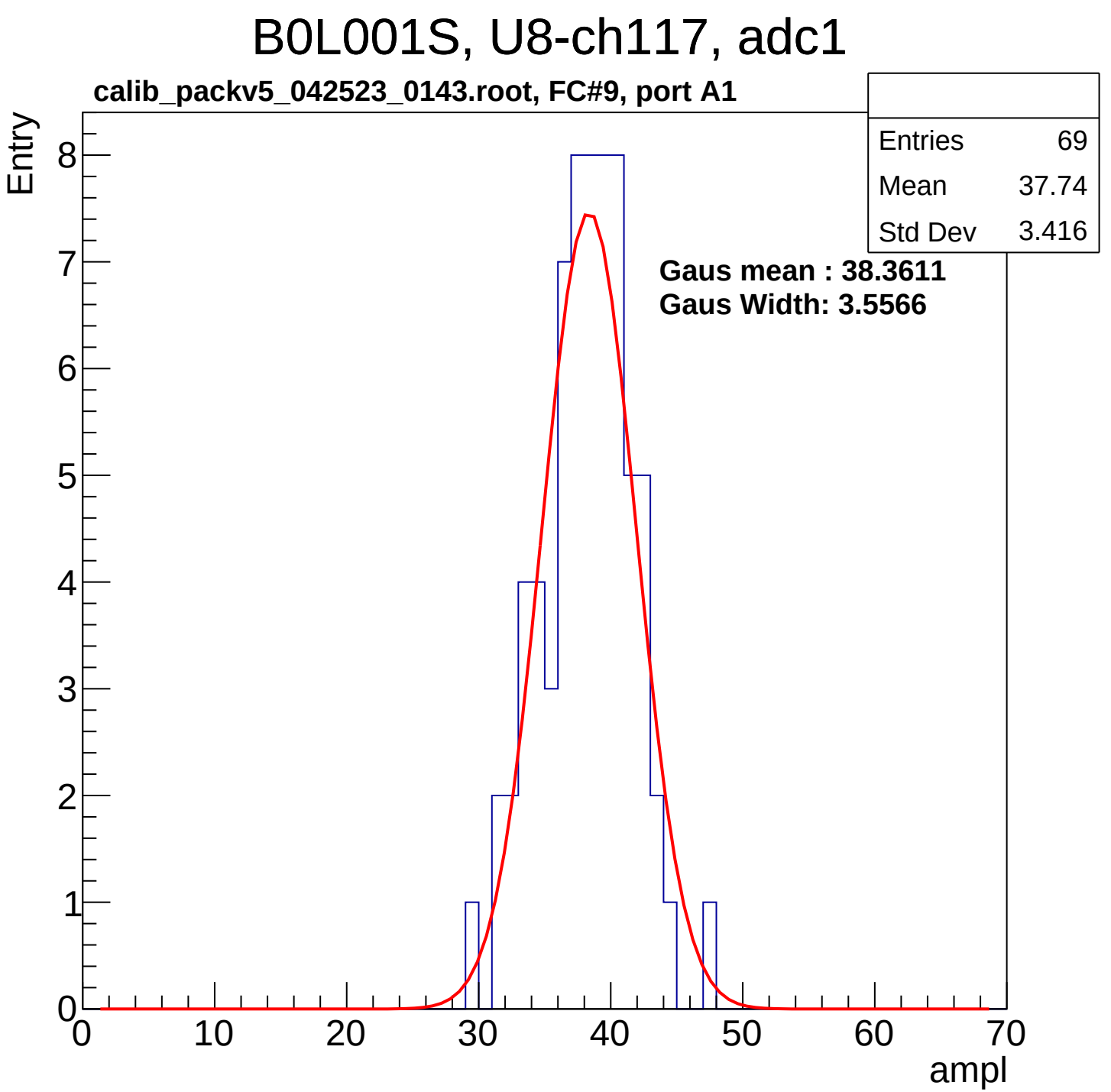
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.01
Std Dev 5.044
Gaus mean : 31.0836
Gaus Width: 4.4367
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.74
Std Dev 3.416
Gaus mean : 38.3611
Gaus Width: 3.5566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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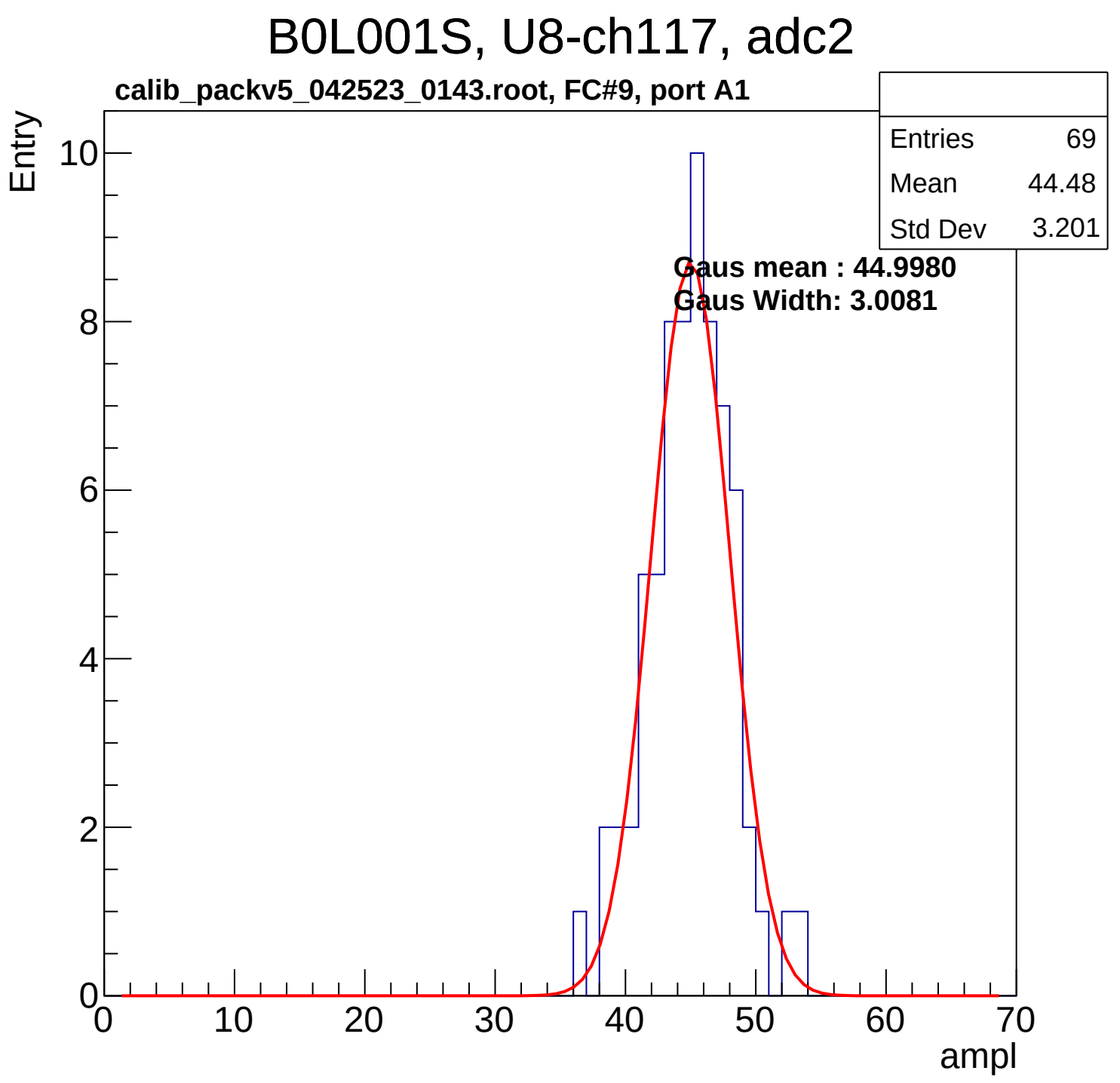
B0L001S, U8-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.48
Std Dev 3.201
Gaus mean : 44.9980
Gaus Width: 3.0081
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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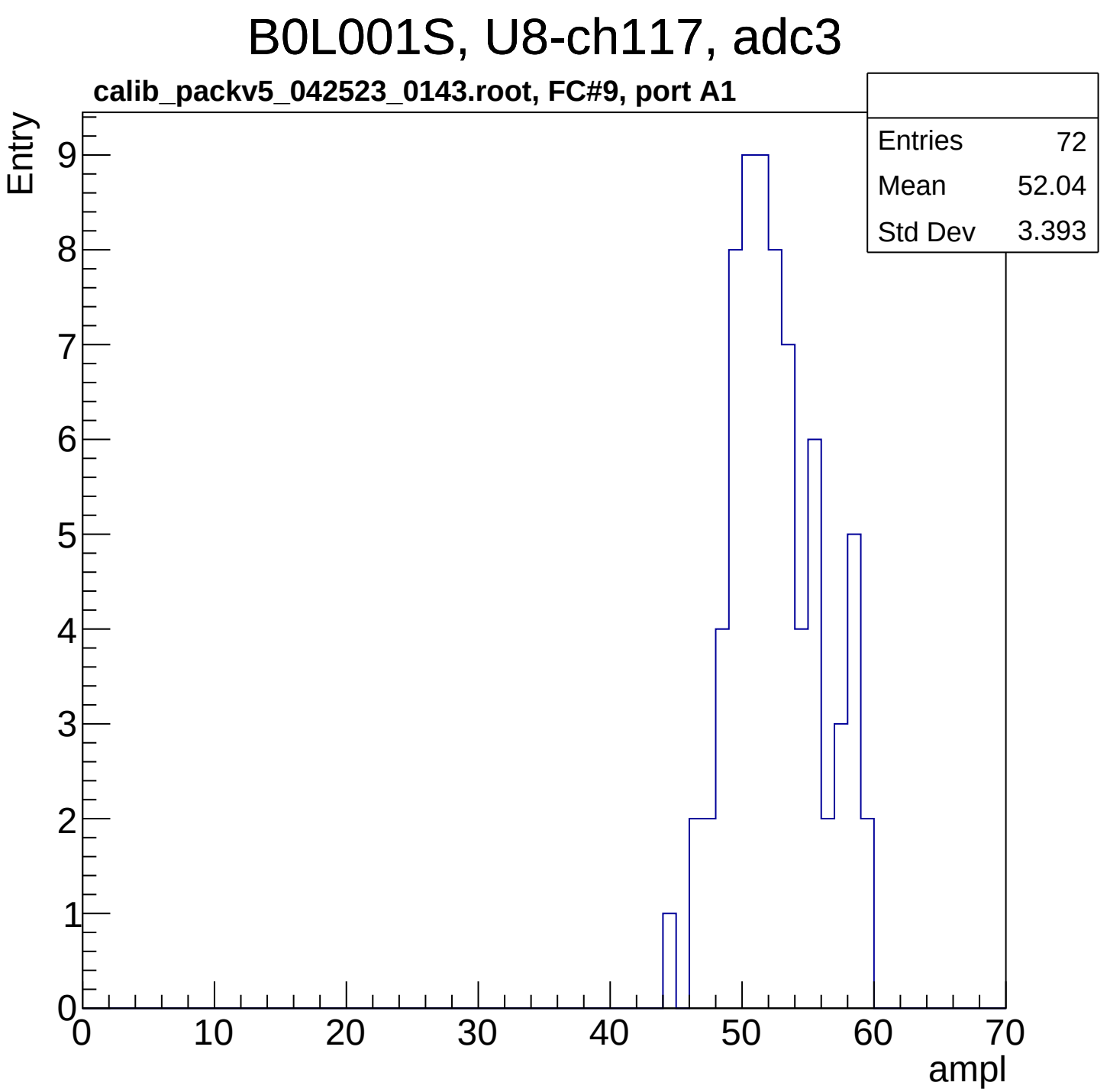
B0L001S, U8-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.04
Std Dev 3.393
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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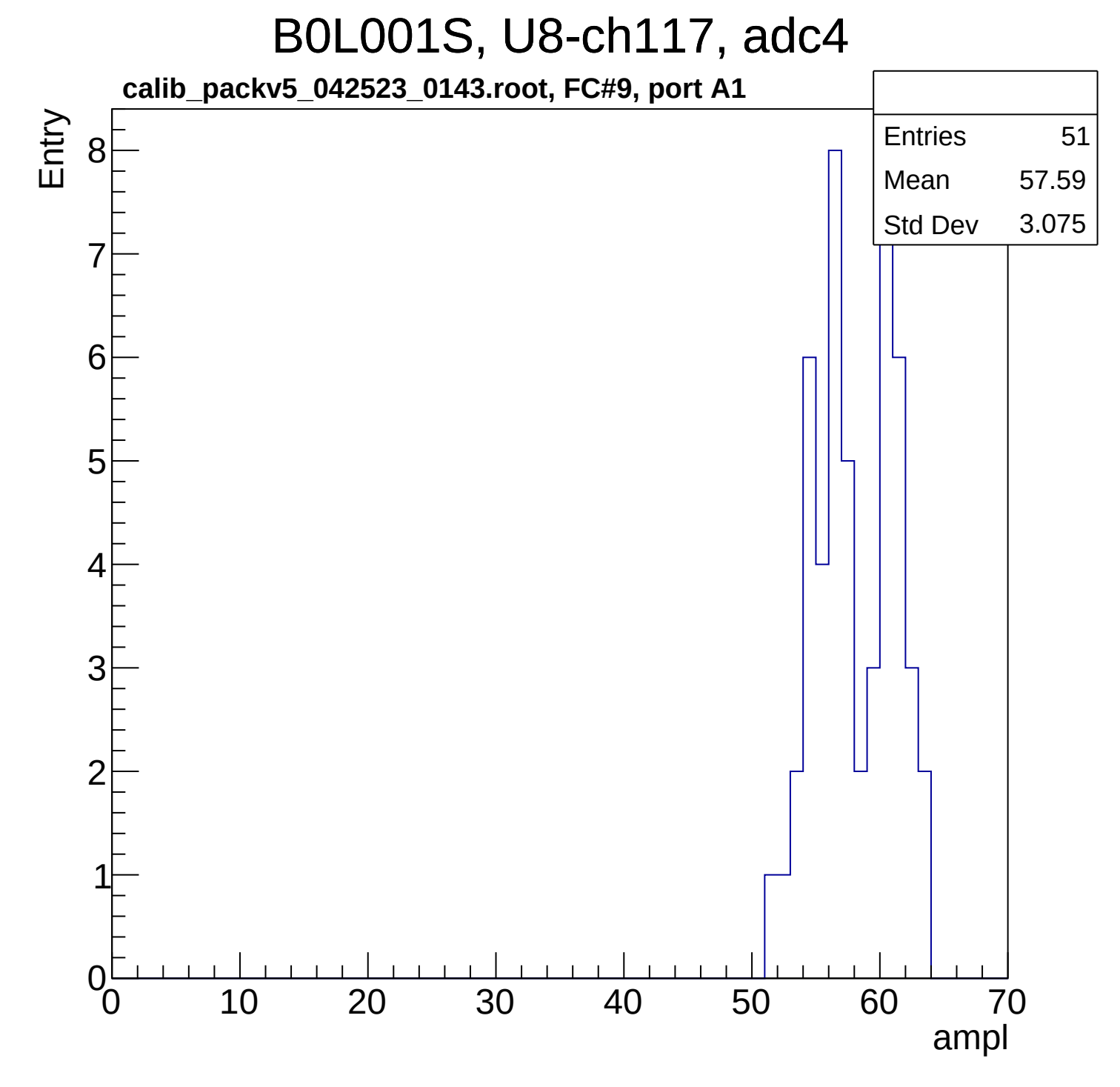
B0L001S, U8-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.59
Std Dev 3.075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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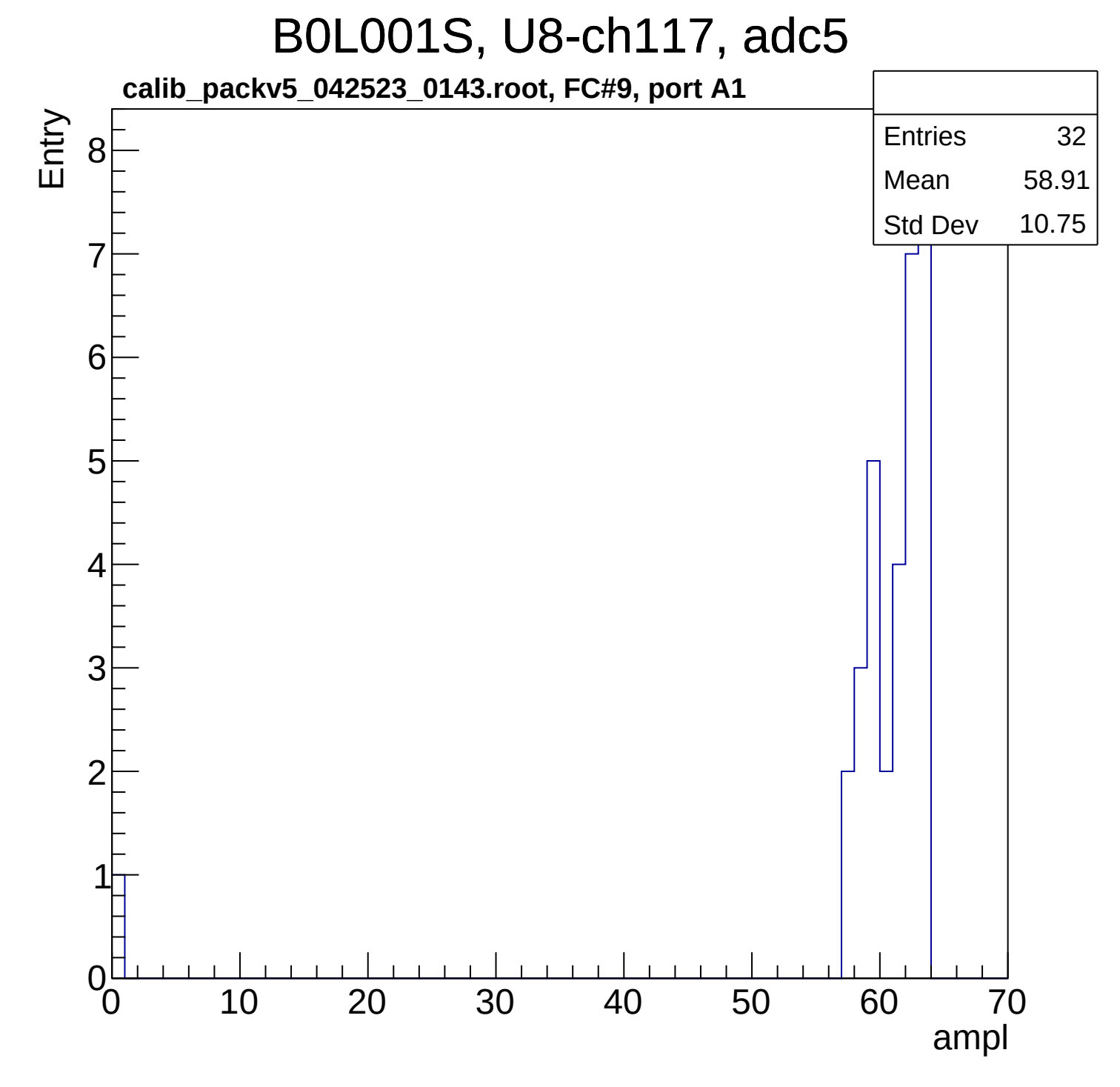
B0L001S, U8-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.91
Std Dev 10.75
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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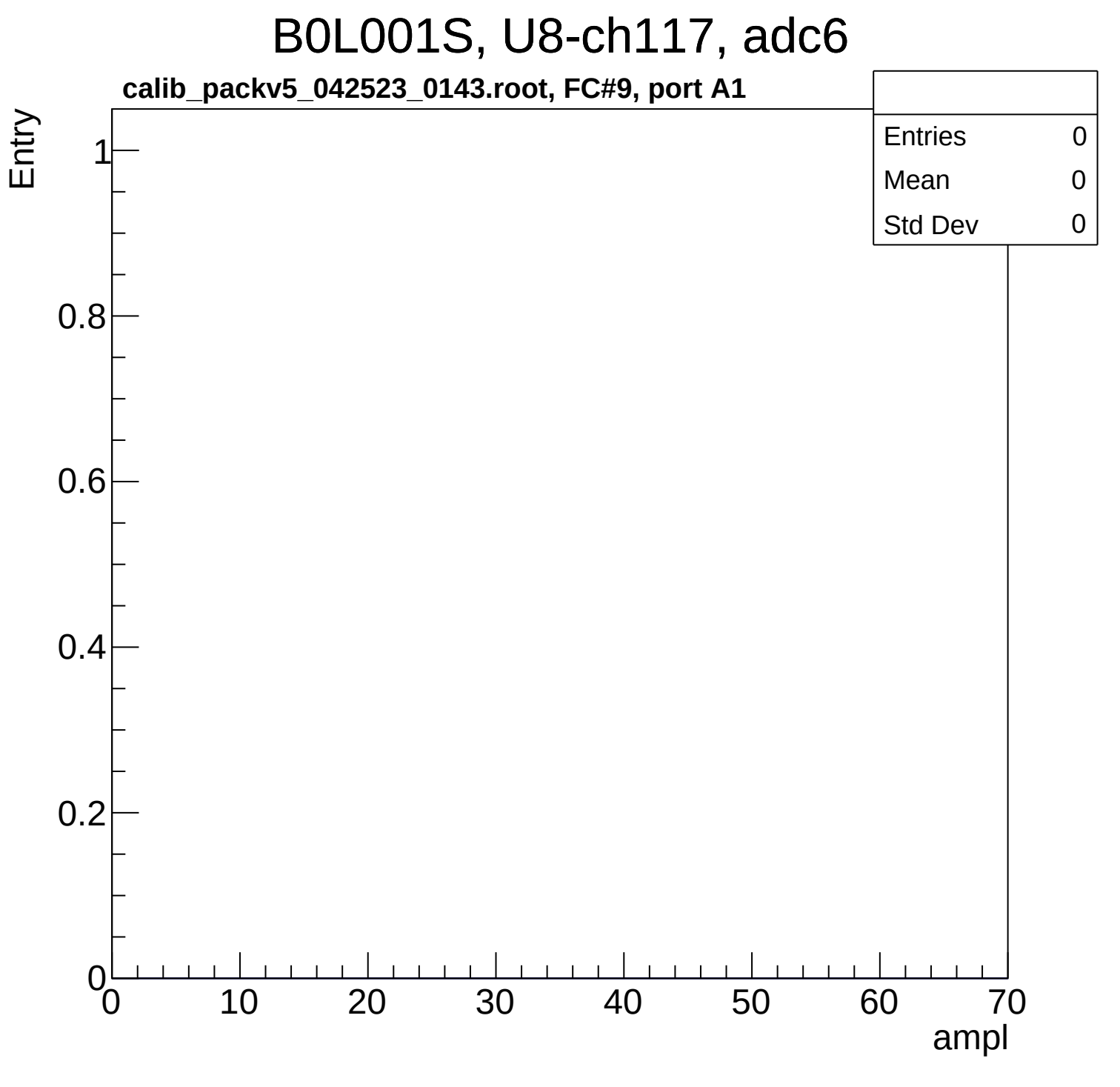
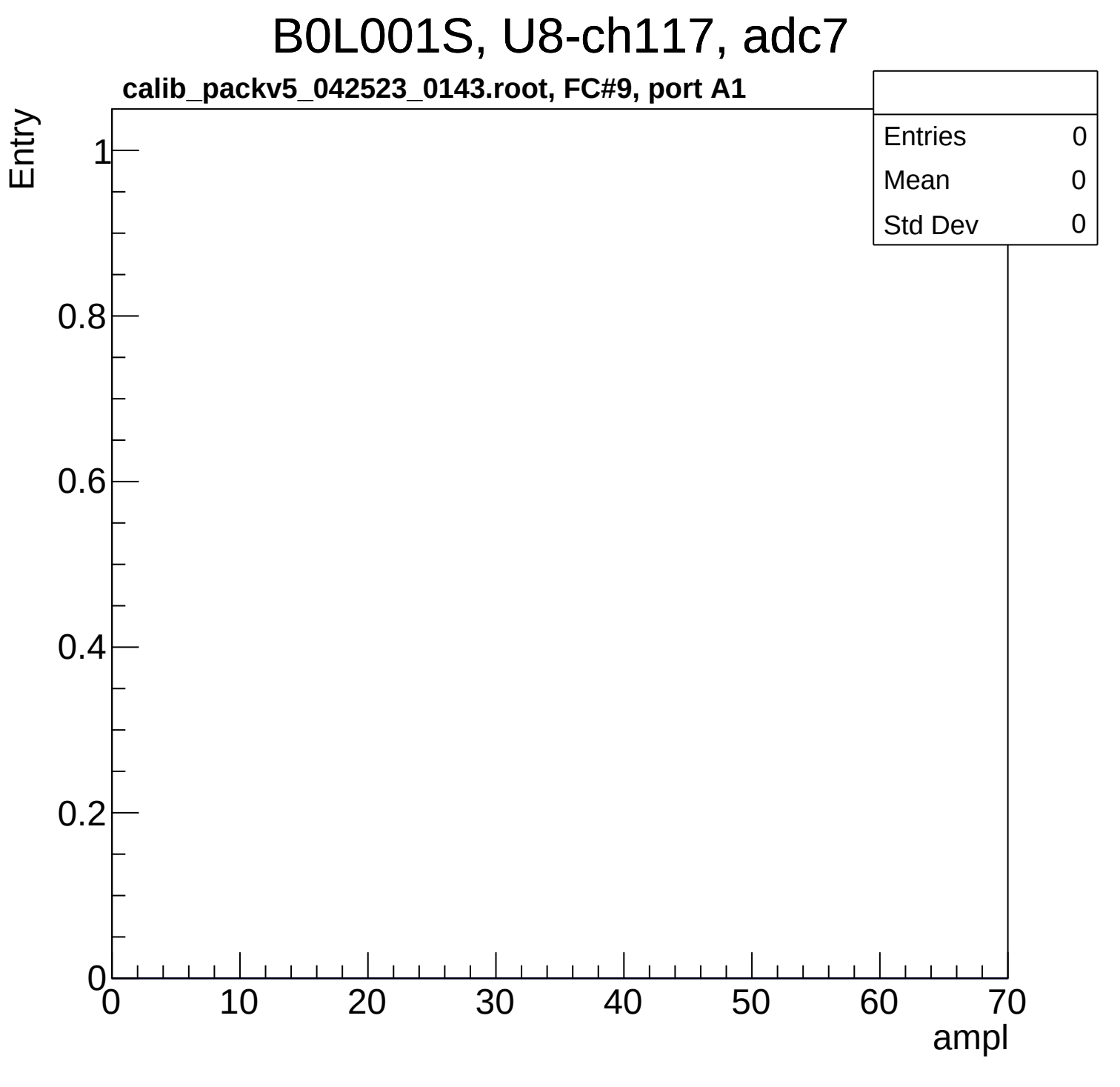

B0L001S, U8-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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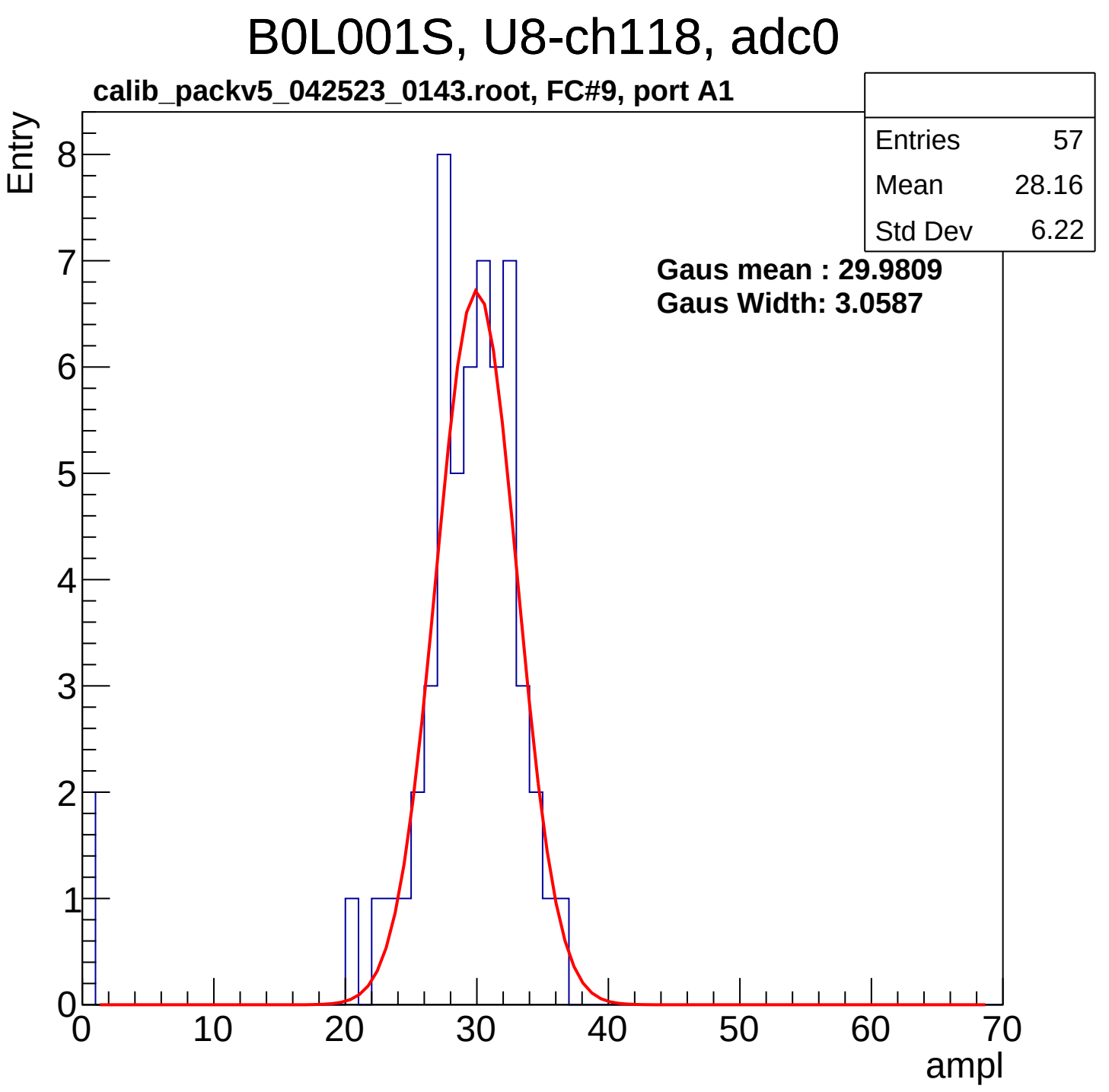
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 28.16
Std Dev 6.22
Gaus mean : 29.9809
Gaus Width: 3.0587
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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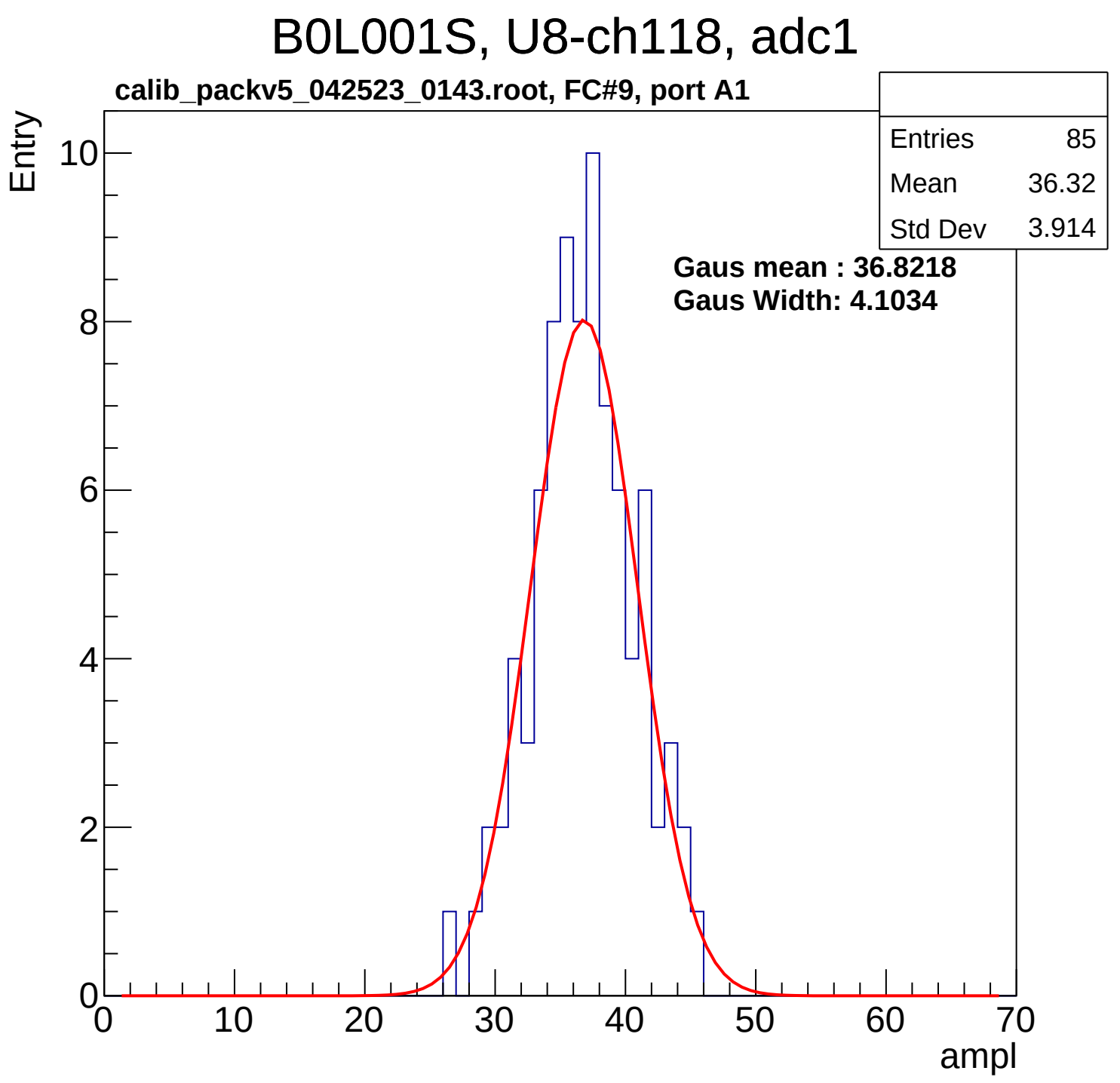
40
50
60
70


B0L001S, U8-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 36.32
Std Dev 3.914
Gaus mean : 36.8218
Gaus Width: 4.1034
Entry
ampl
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40
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60
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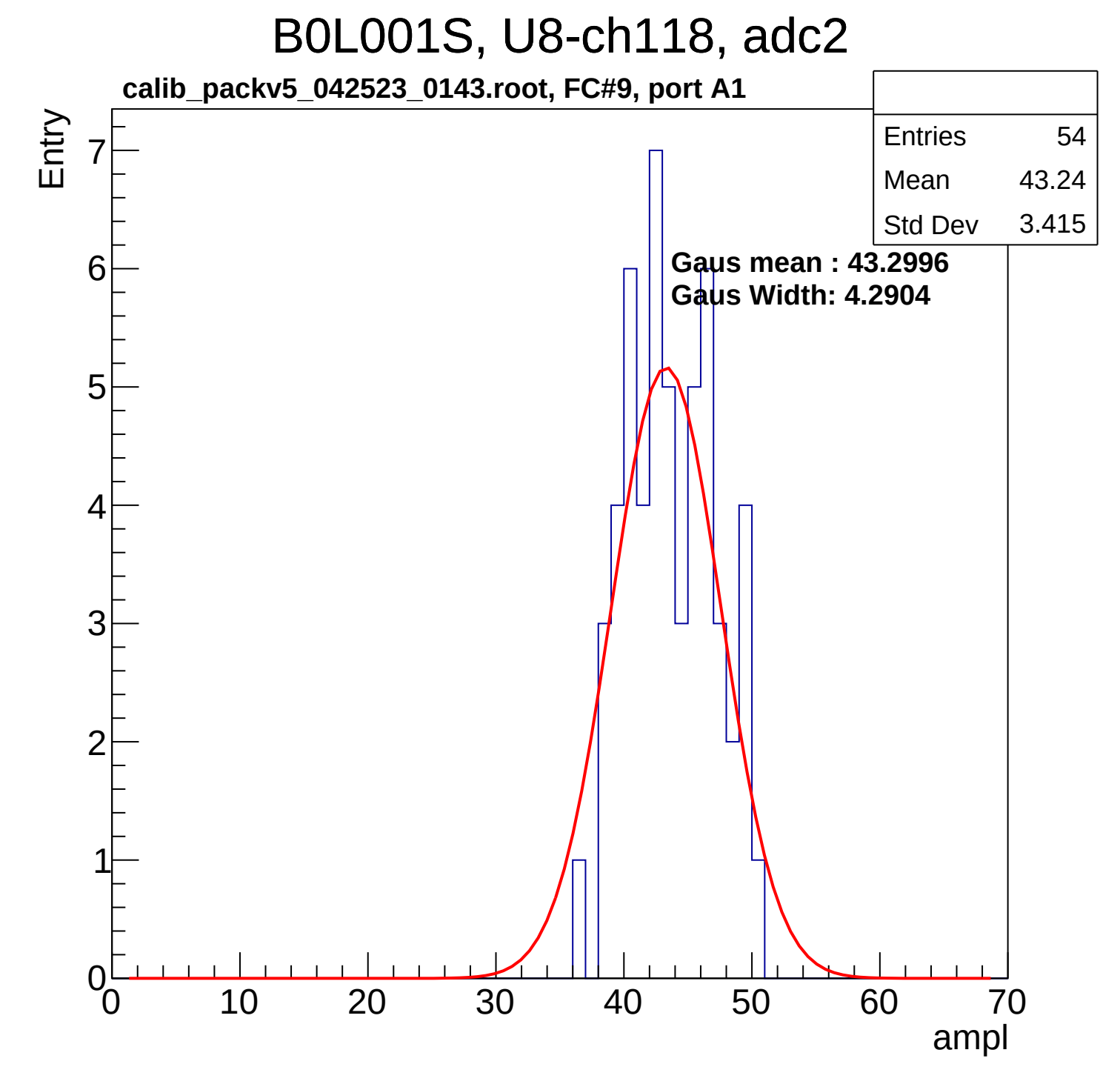
B0L001S, U8-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.24
Std Dev 3.415
Gaus mean : 43.2996
Gaus Width: 4.2904
Entry
ampl
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4
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0
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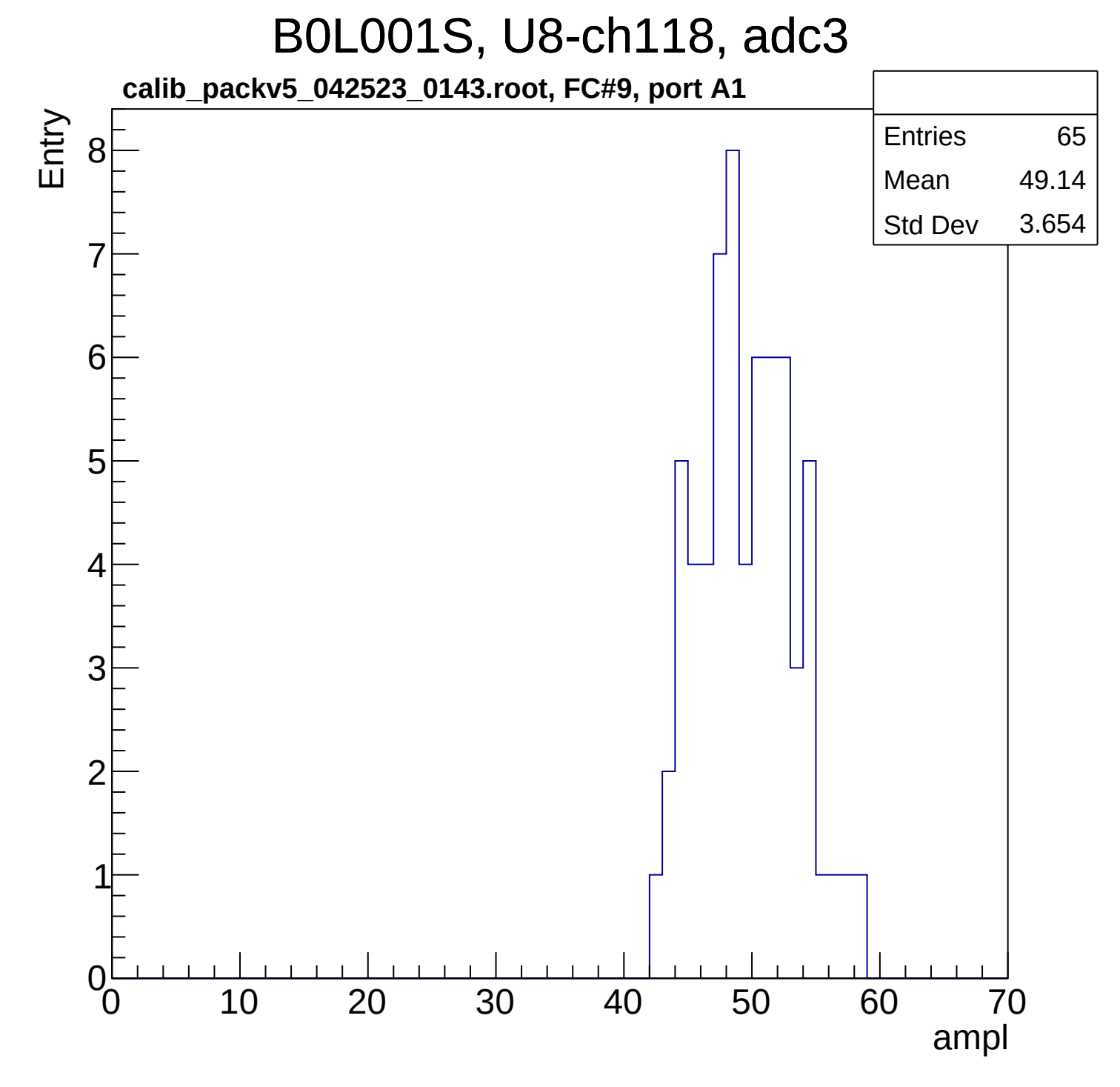
B0L001S, U8-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.14
Std Dev 3.654
Entry
ampl
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60
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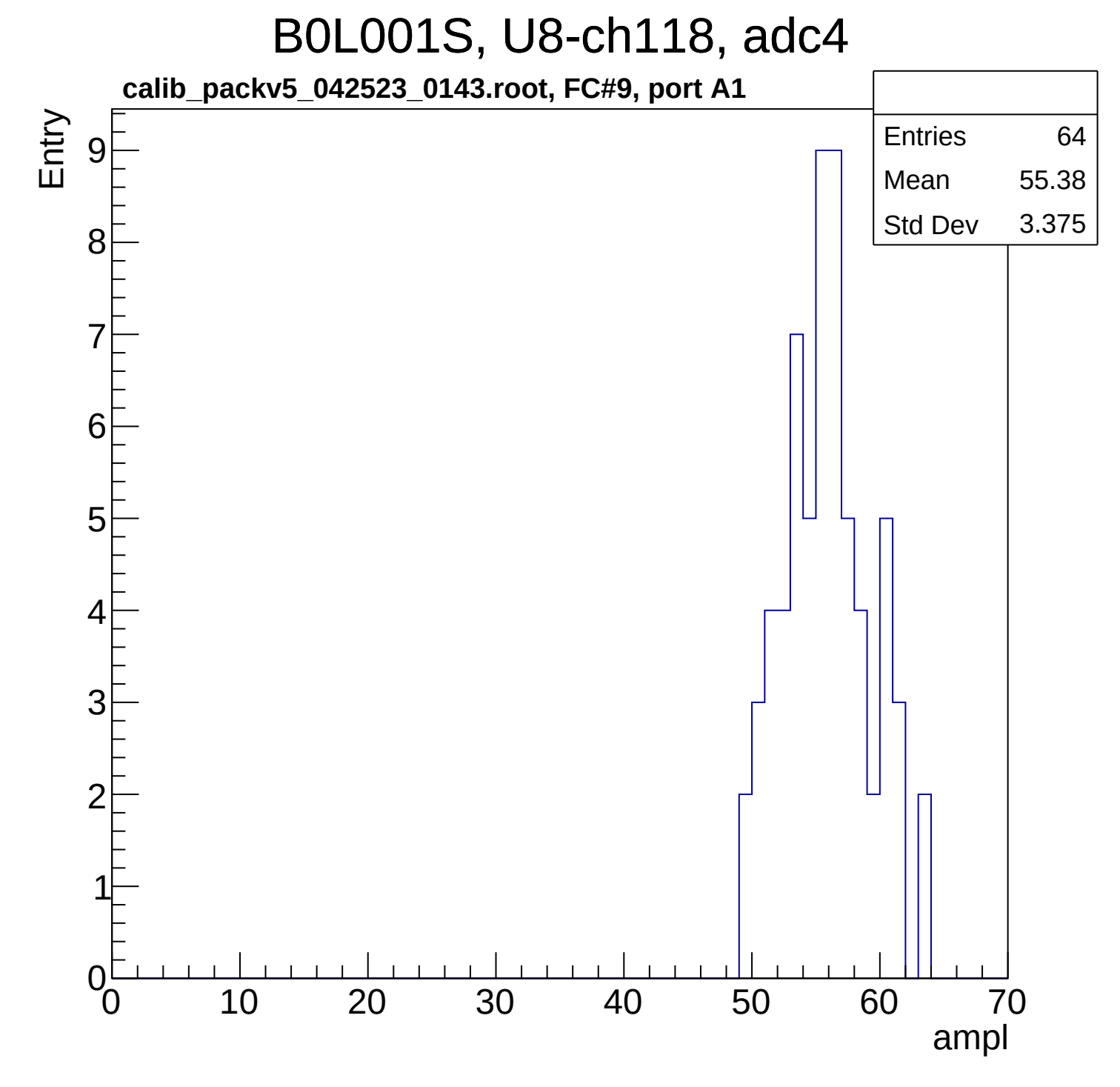
B0L001S, U8-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.38
Std Dev 3.375
Entry
ampl
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0
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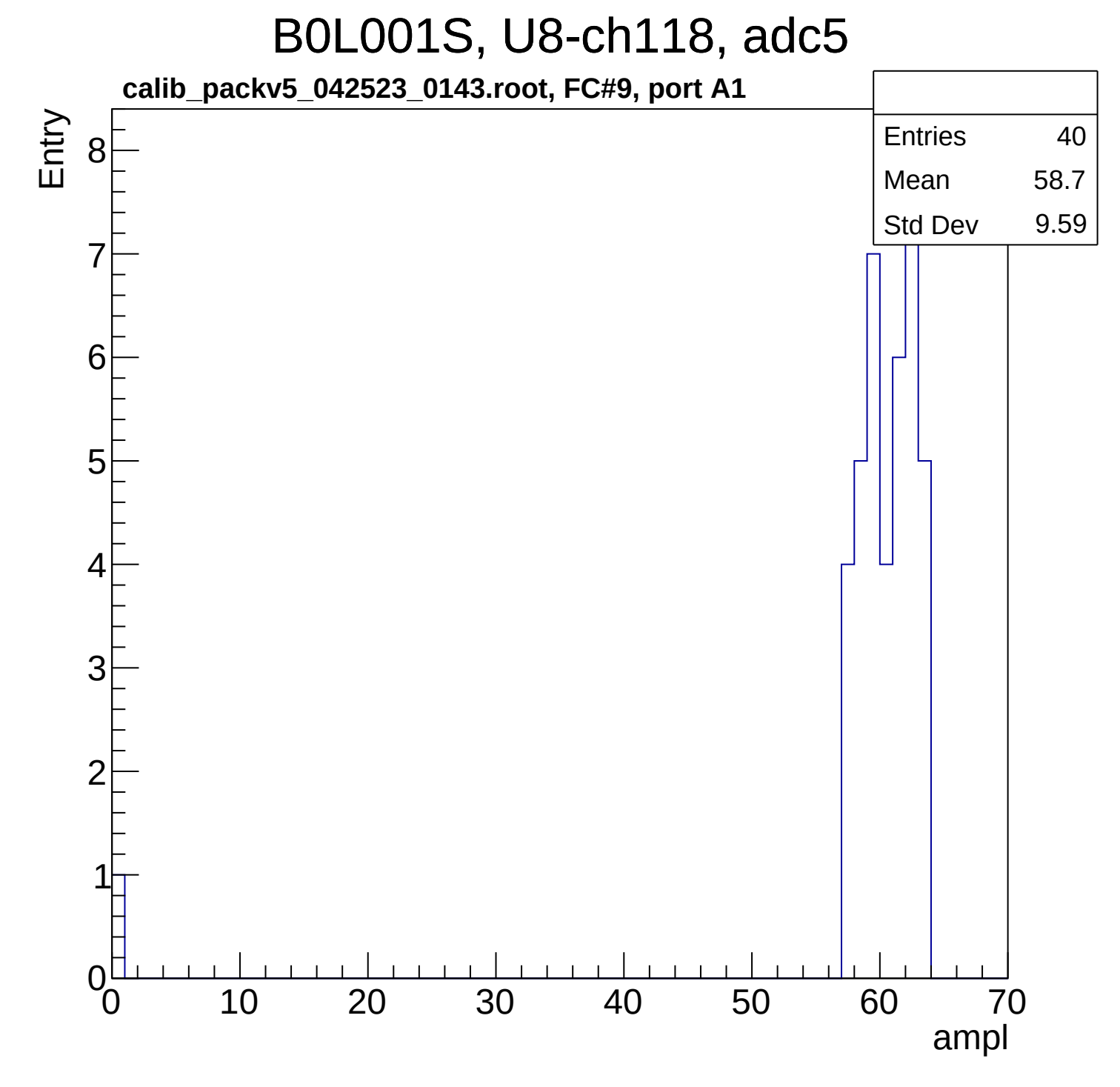
B0L001S, U8-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.7
Std Dev 9.59
Entry
ampl
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2
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5
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0
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60
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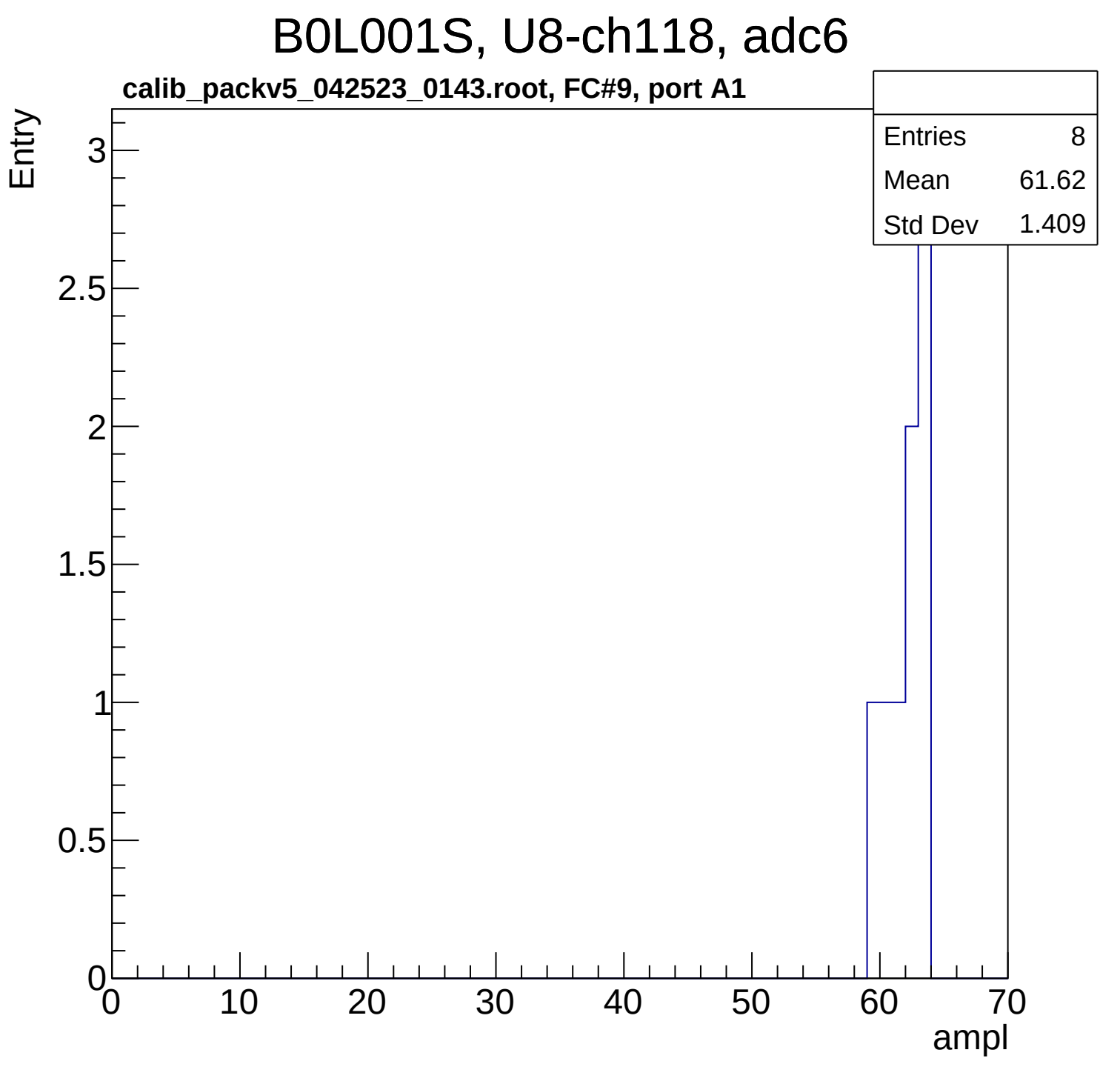
B0L001S, U8-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.62
Std Dev 1.409
Entry
ampl
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0
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60
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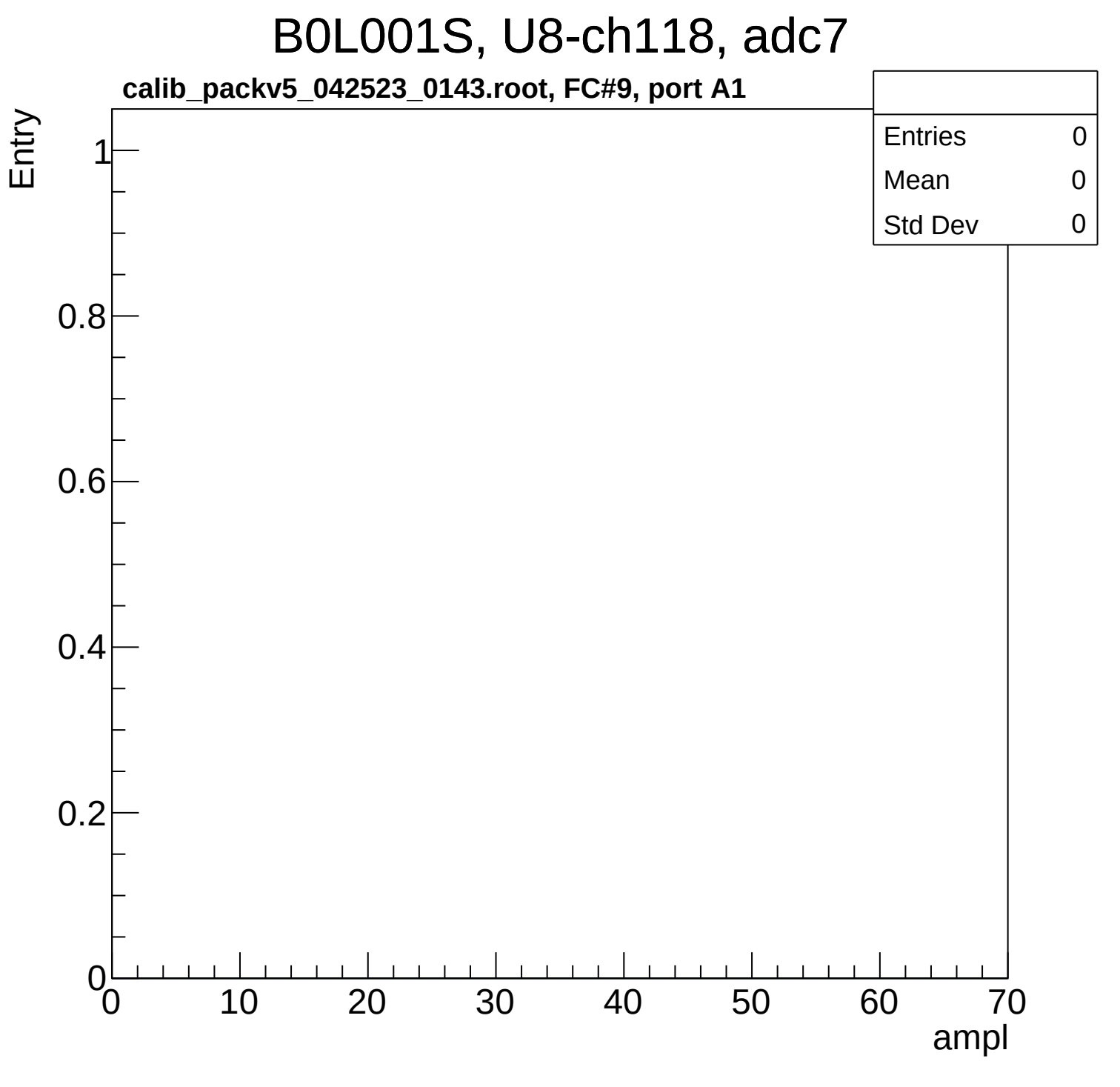
B0L001S, U8-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
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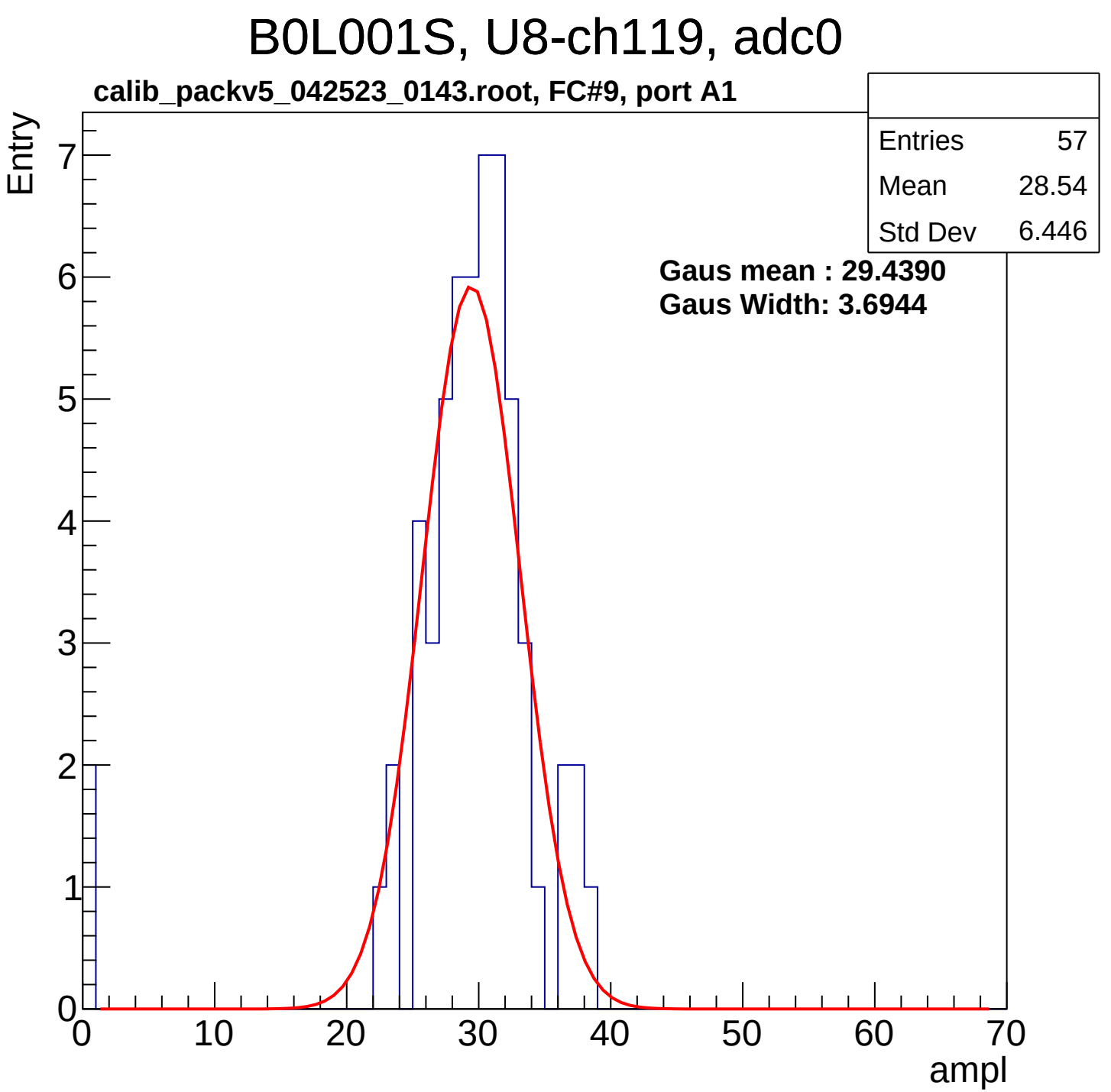
50
60
70
ampl


B0L001S, U8-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 28.54
Std Dev 6.446
Gaus mean : 29.4390
Gaus Width: 3.6944
Entry
ampl
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3
4
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0
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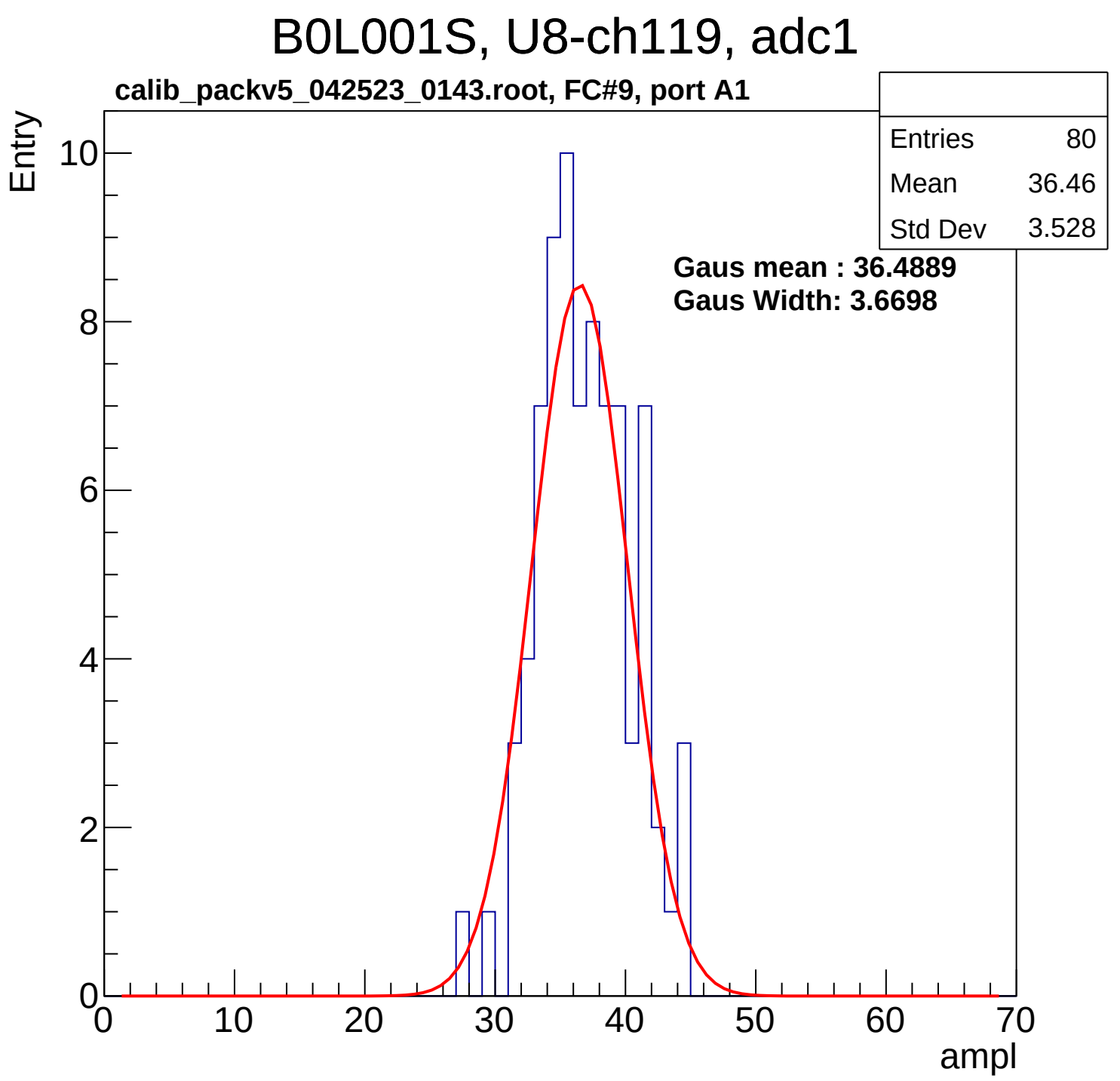
B0L001S, U8-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 36.46
Std Dev 3.528
Gaus mean : 36.4889
Gaus Width: 3.6698
Entry
ampl
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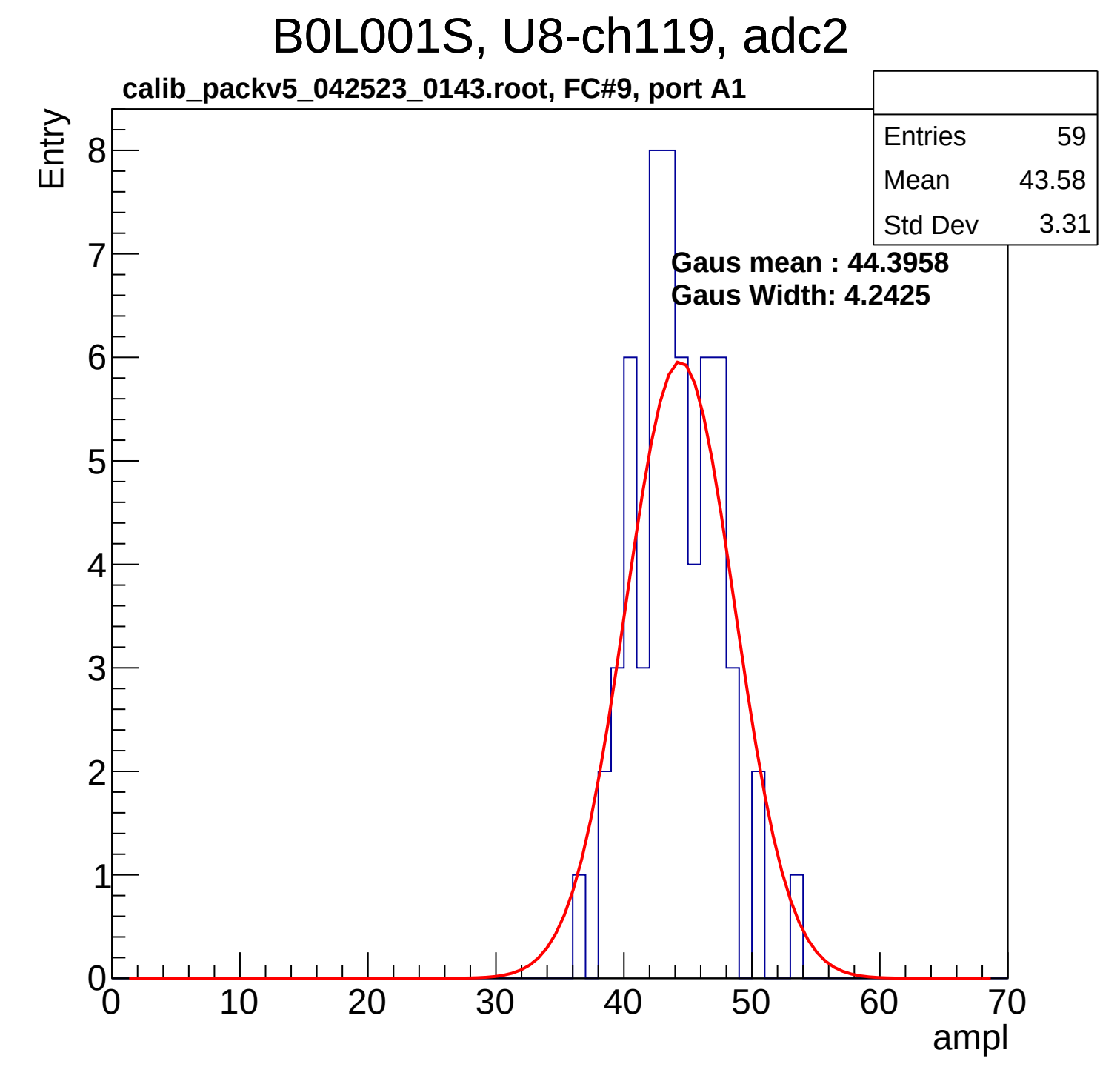
B0L001S, U8-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.58
Std Dev 3.31
Gaus mean : 44.3958
Gaus Width: 4.2425
Entry
ampl
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2
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5
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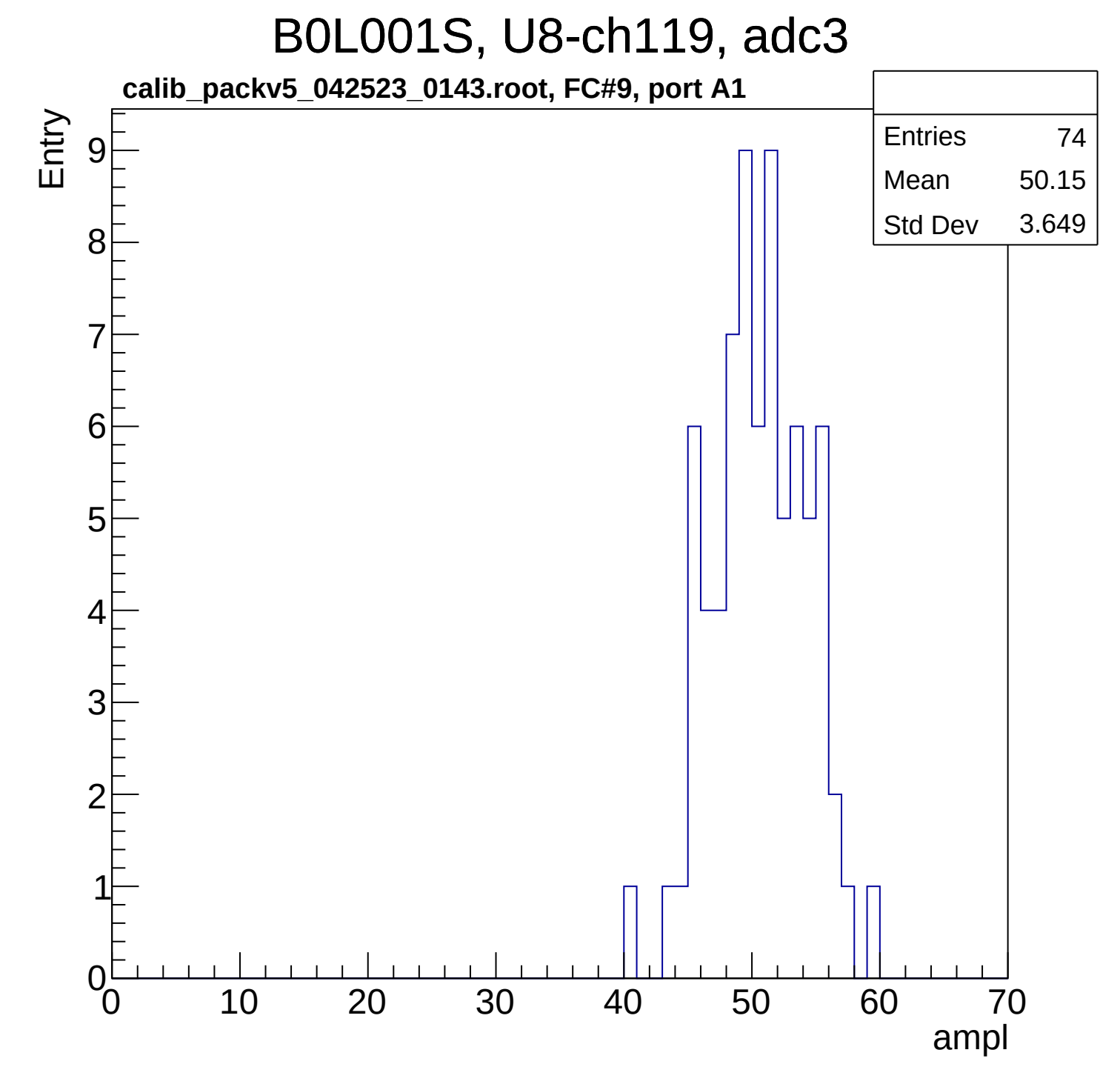
B0L001S, U8-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.15
Std Dev 3.649
Entry
ampl
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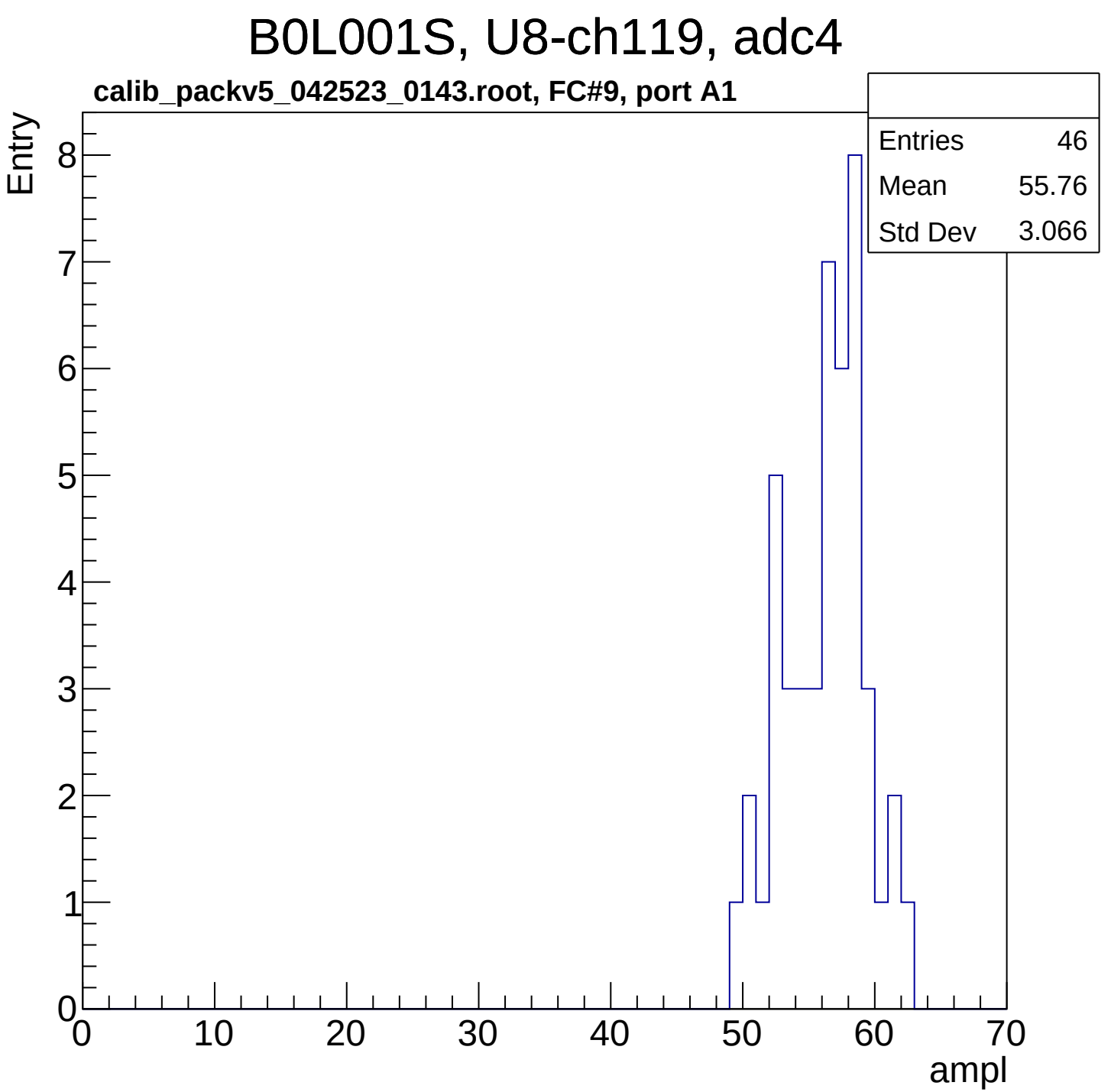
B0L001S, U8-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.76
Std Dev 3.066
Entry
ampl
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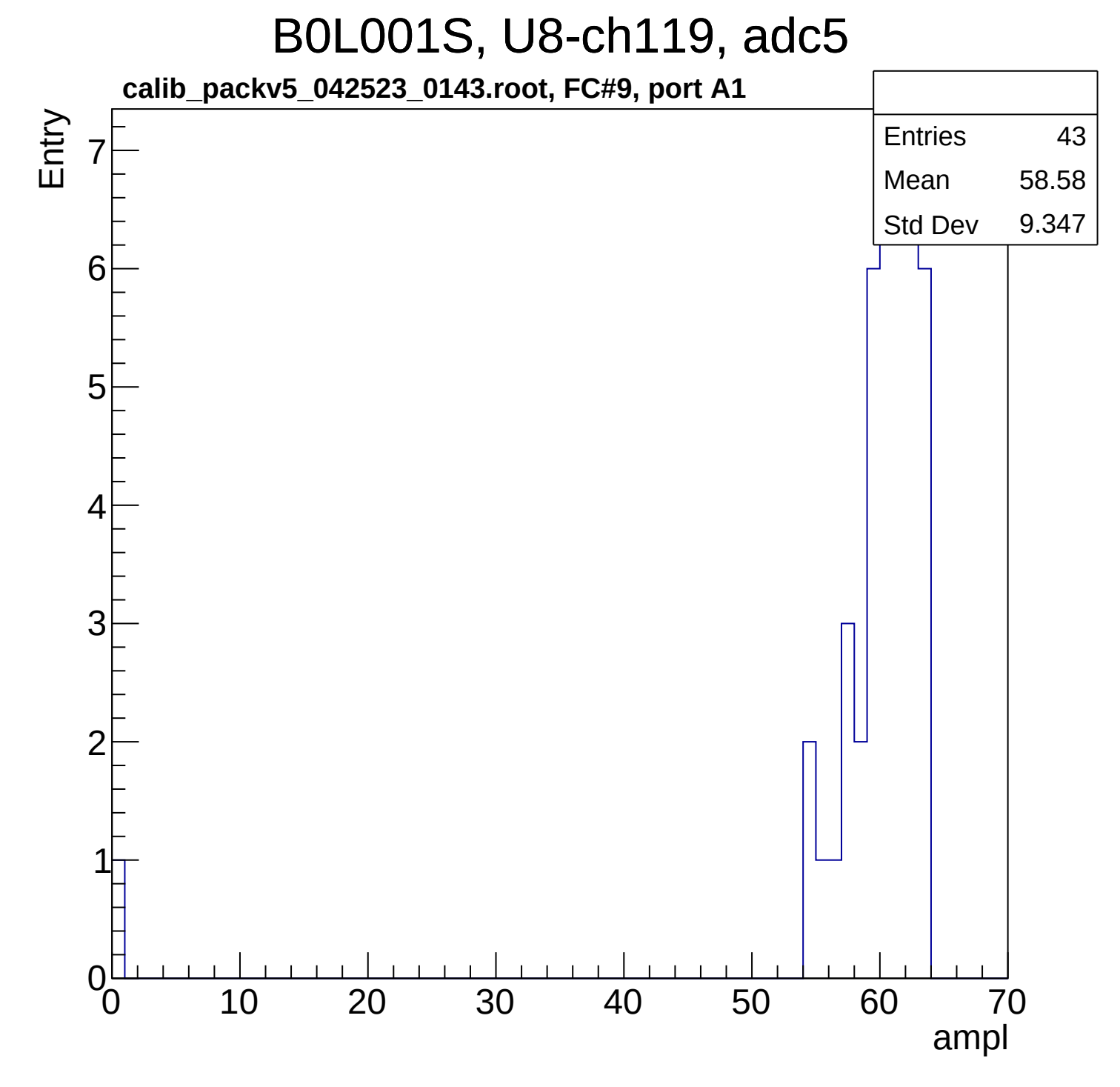
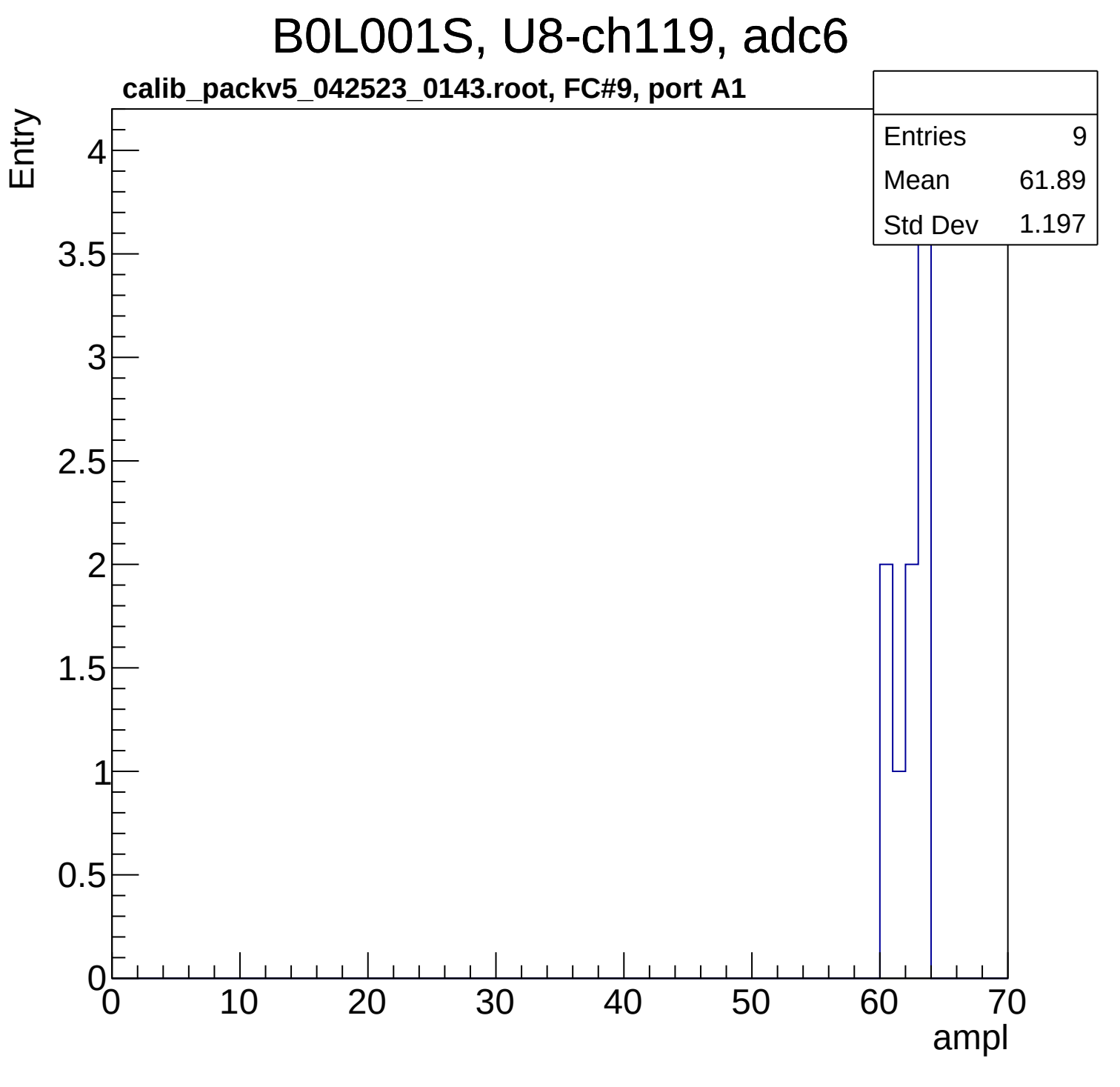
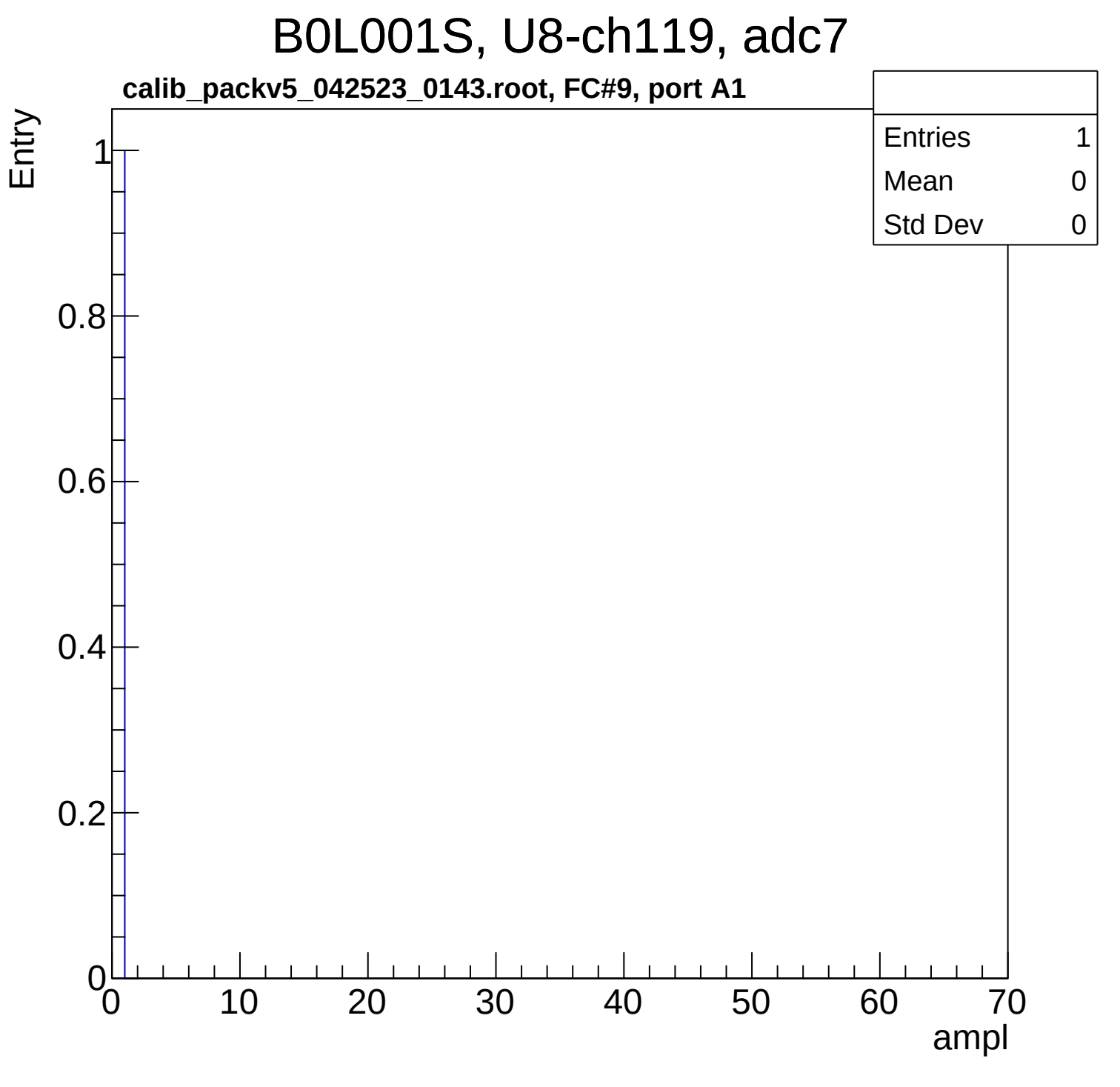
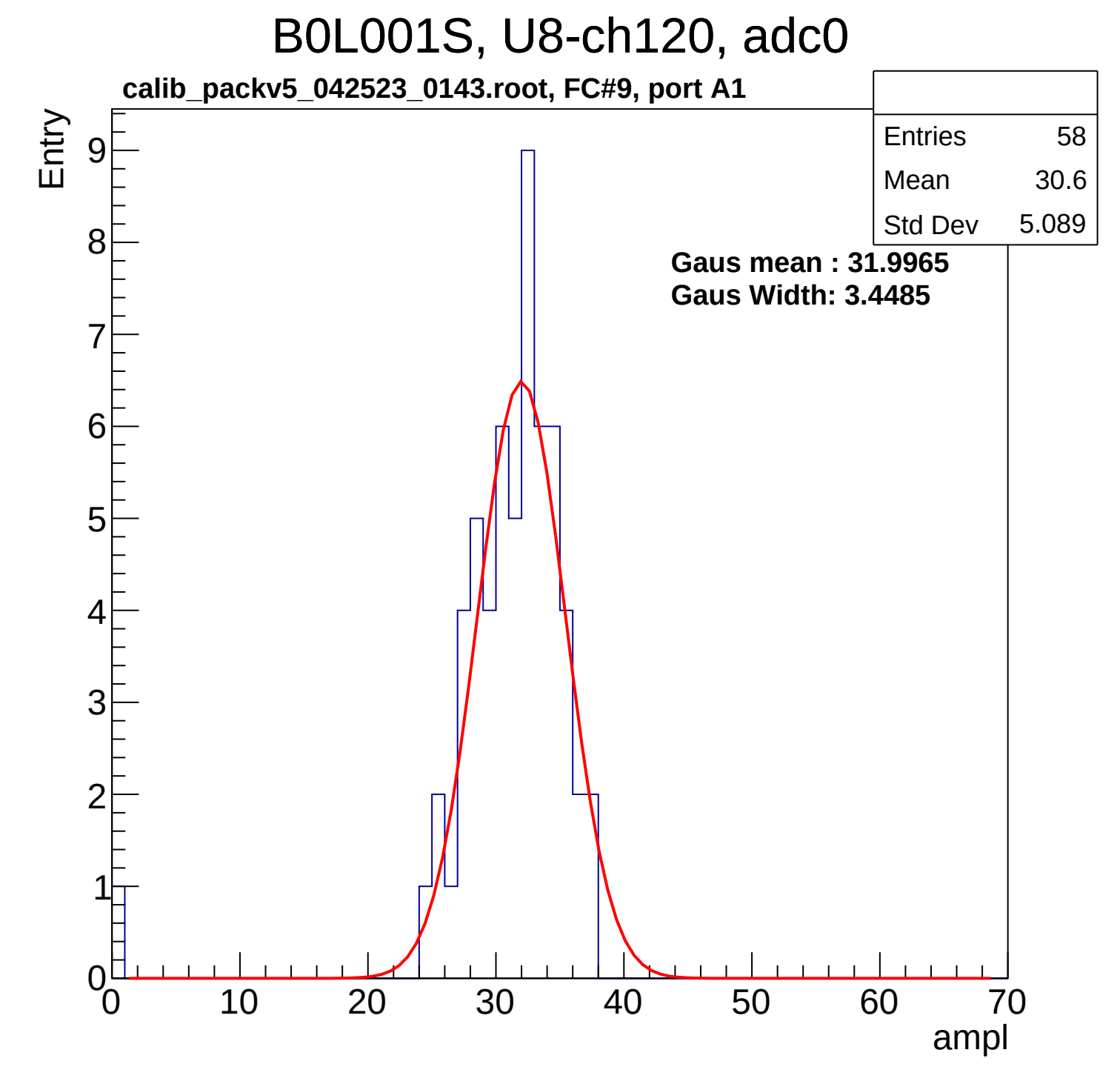
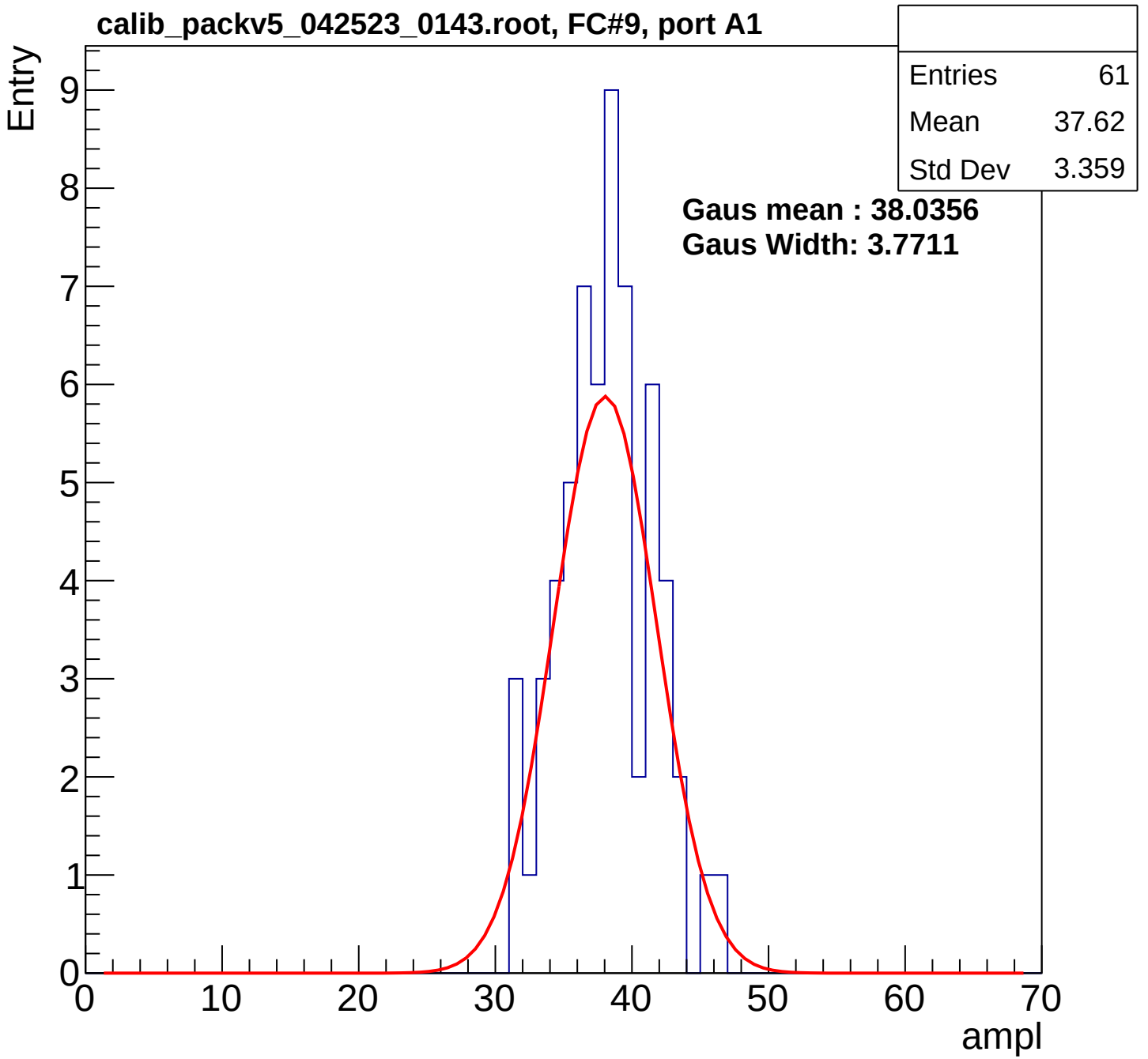
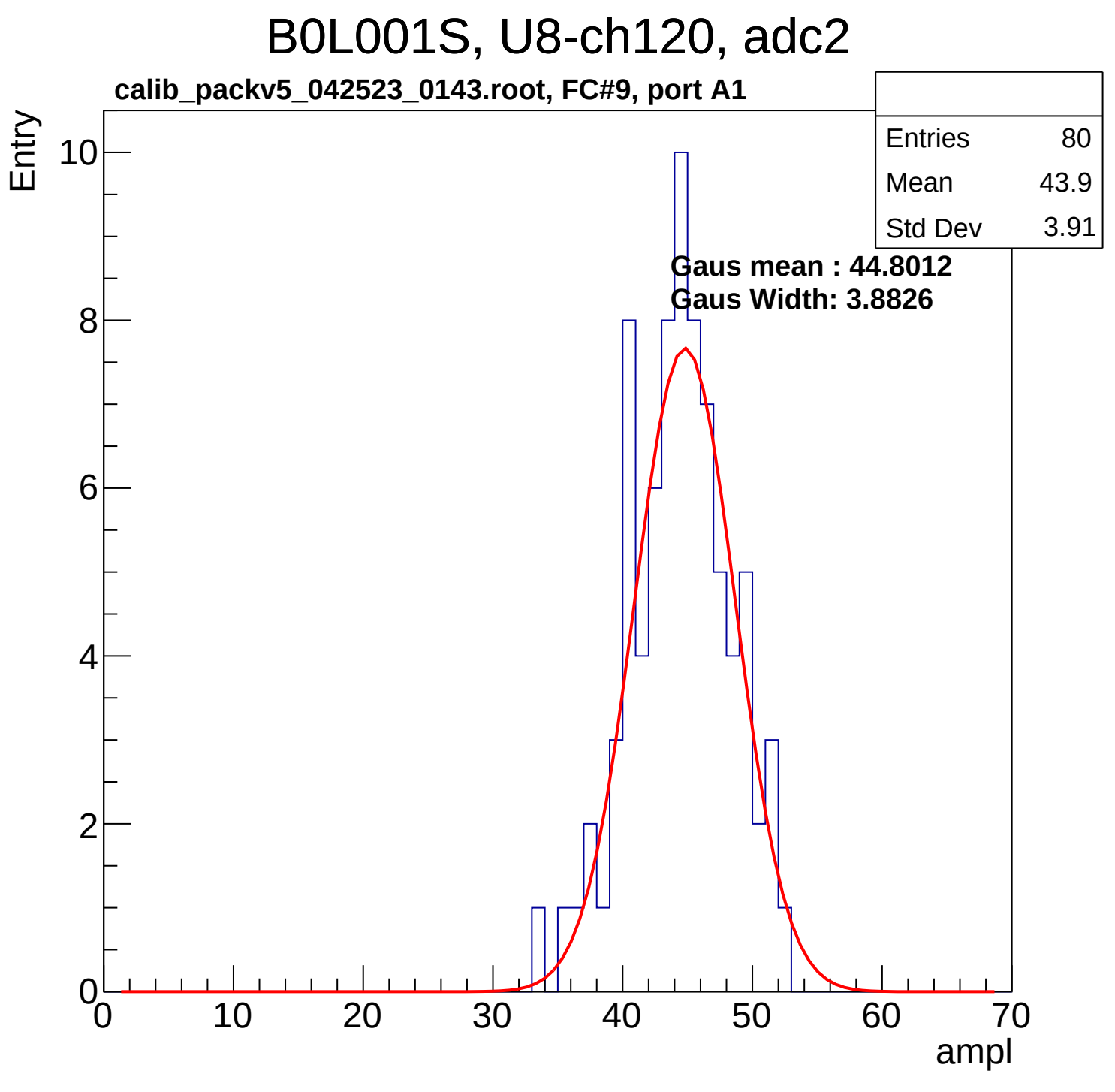

B0L001S, U8-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.58
Std Dev 9.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

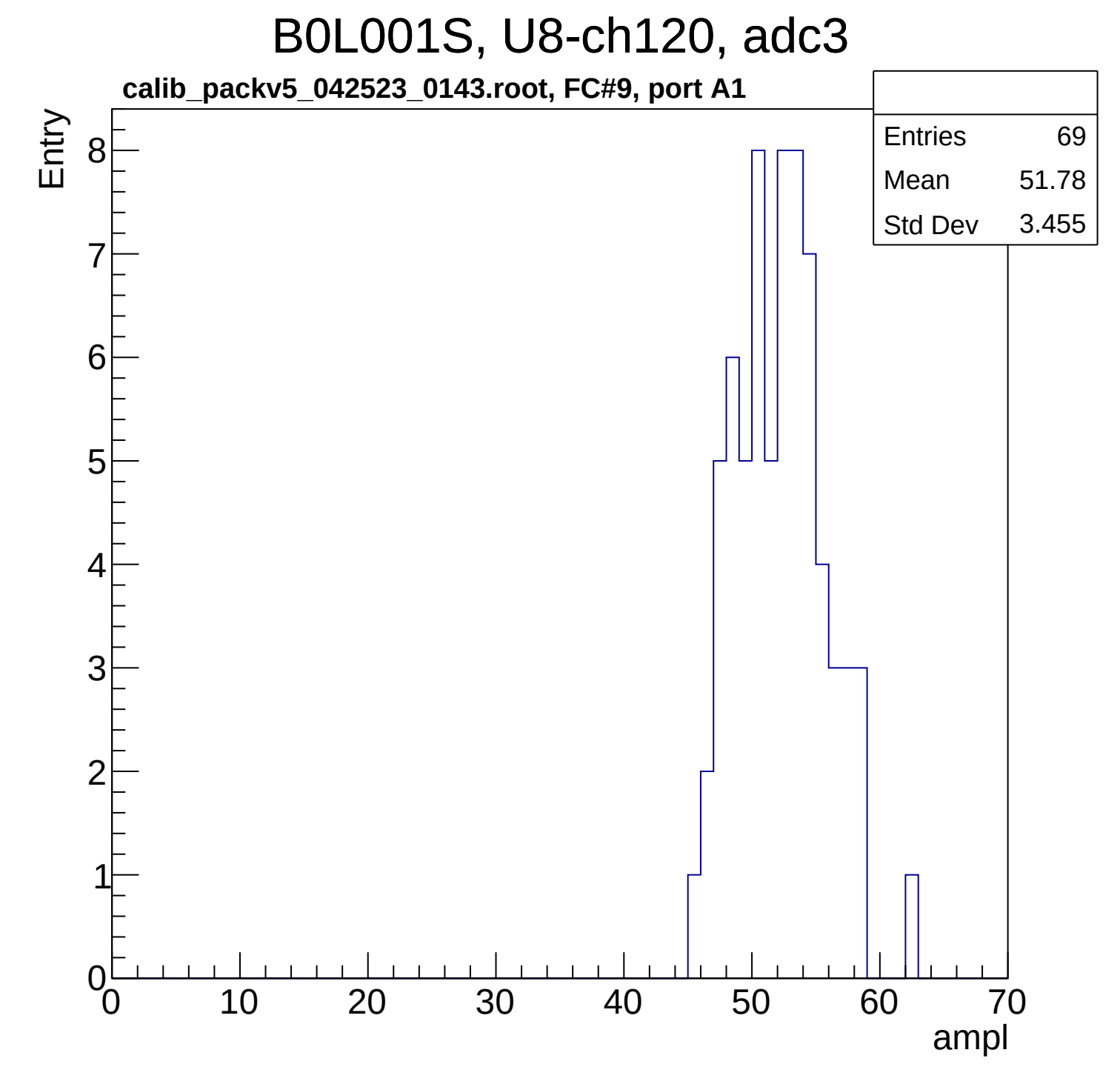

B0L001S, U8-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.89
Std Dev 1.197
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

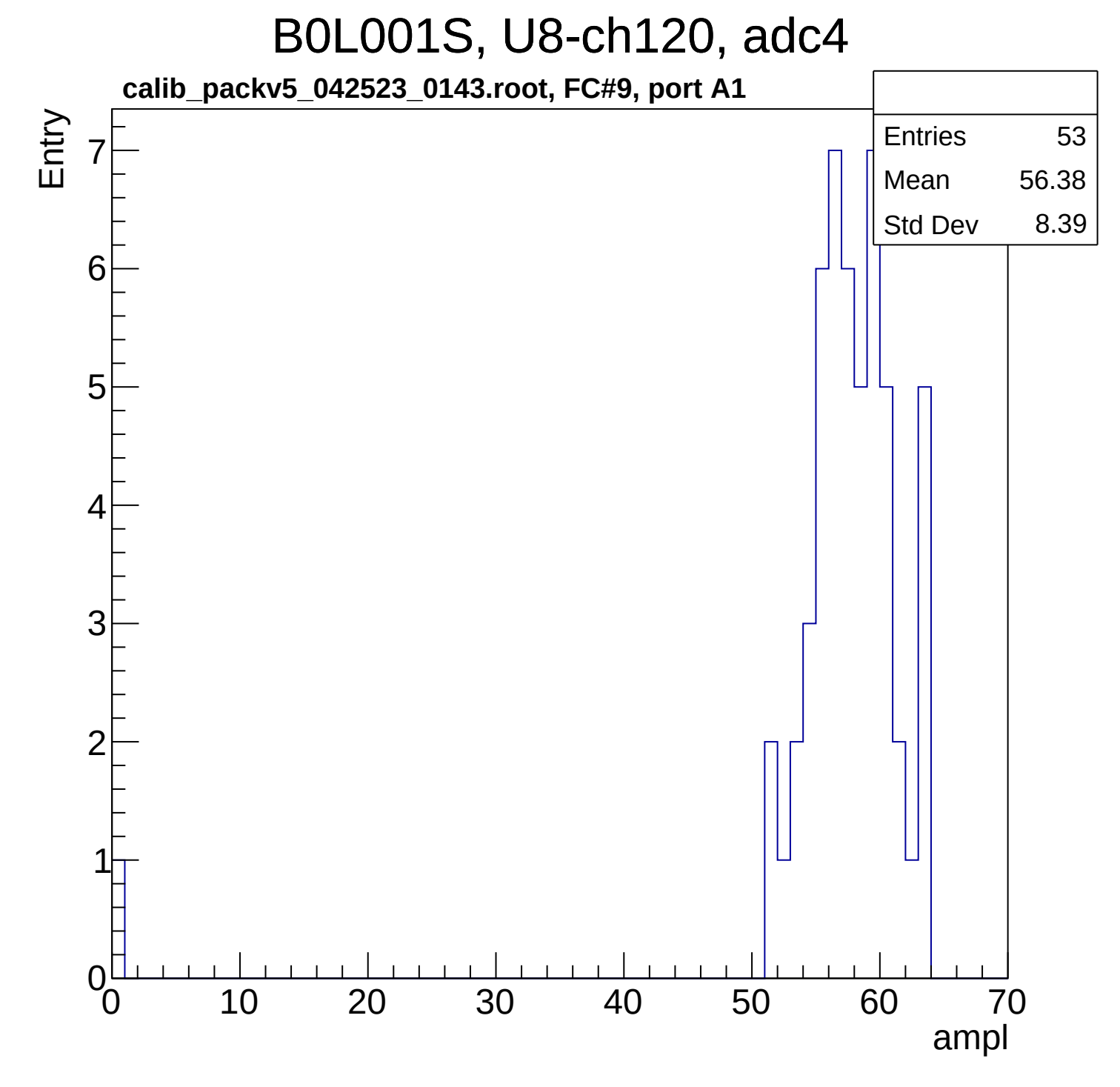

B0L001S, U8-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

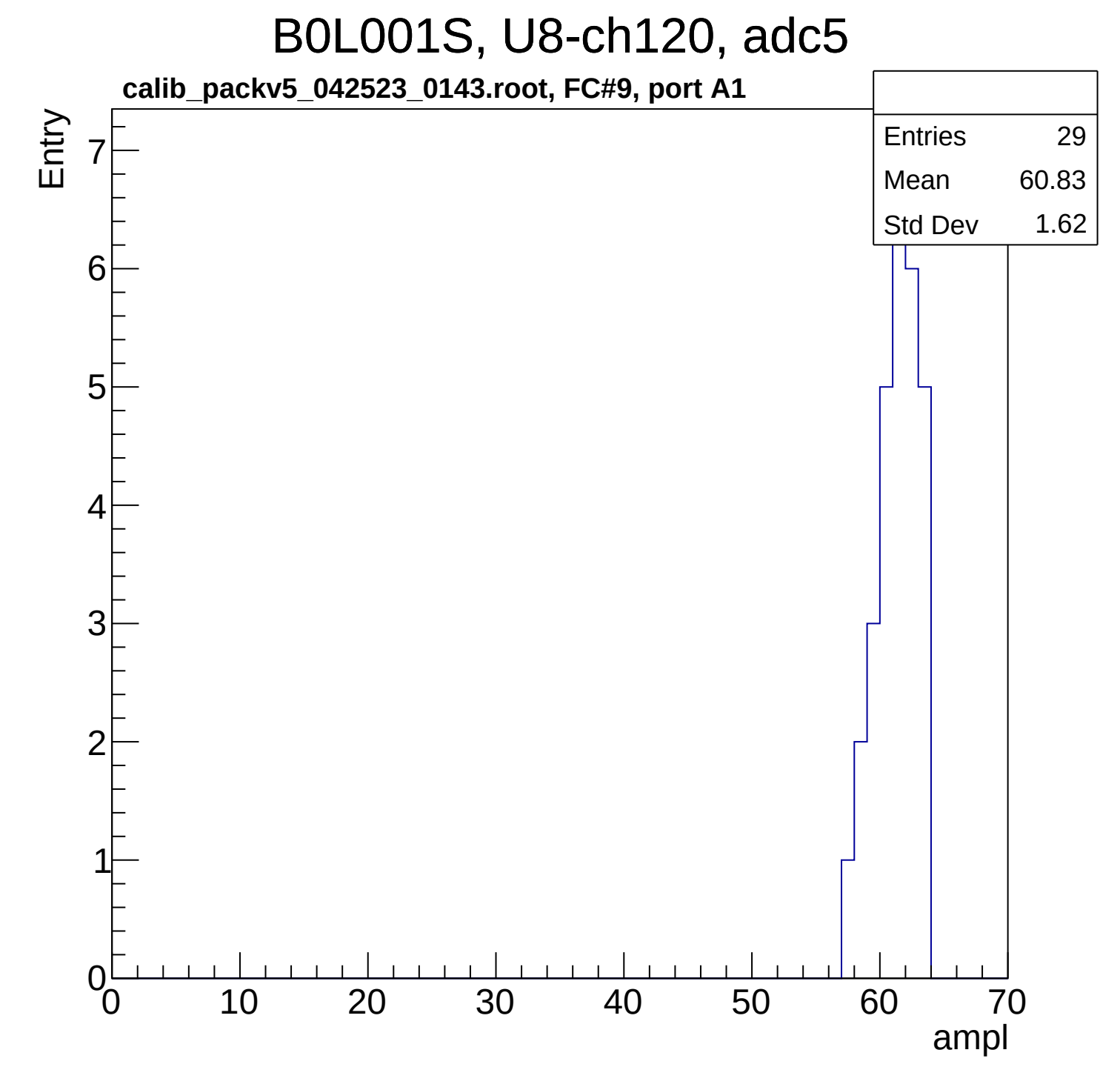

B0L001S, U8-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.6
Std Dev 5.089
Gaus mean : 31.9965
Gaus Width: 3.4485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

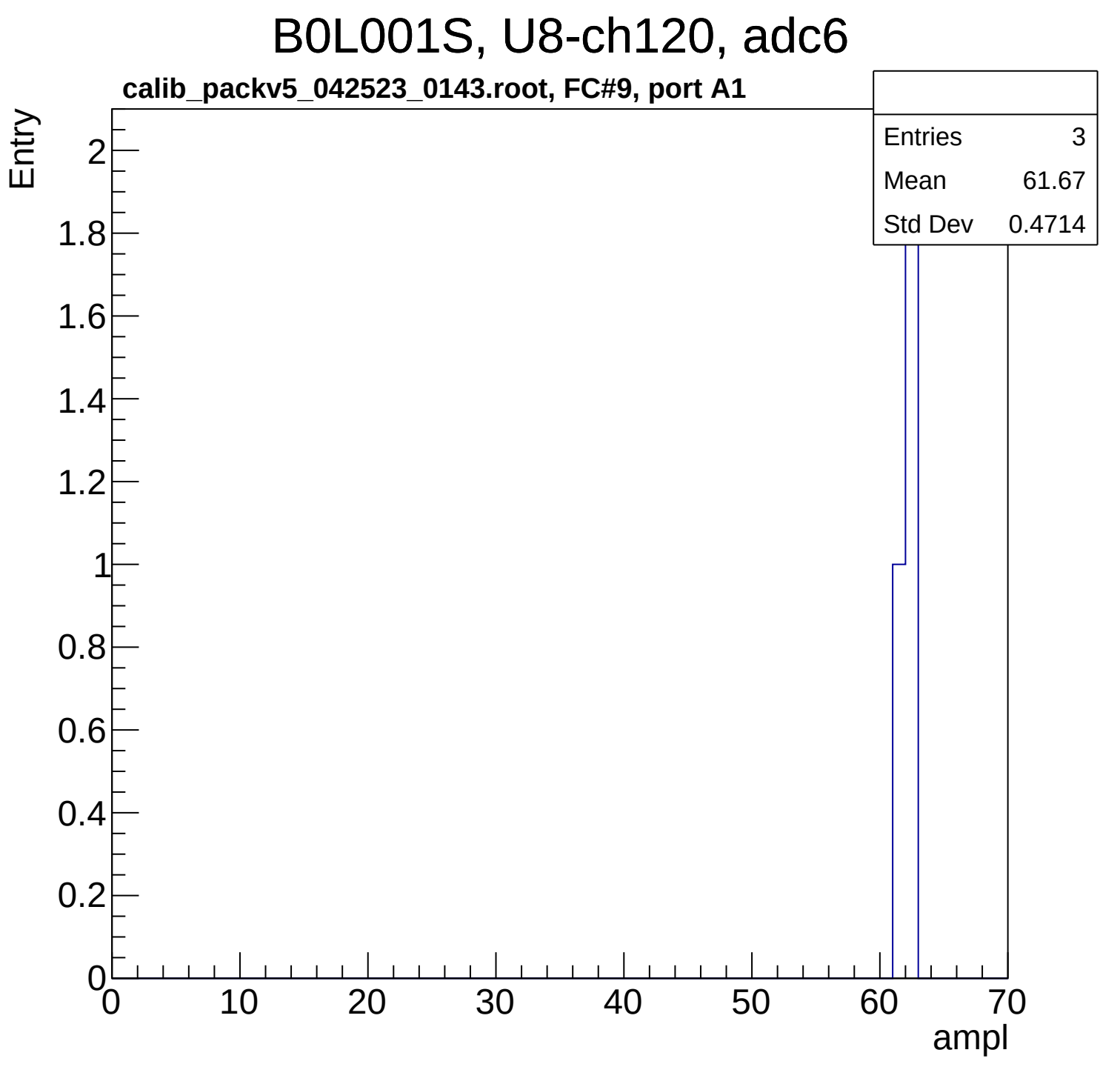

B0L001S, U8-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.62
Std Dev 3.359
Gaus mean : 38.0356
Gaus Width: 3.7711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

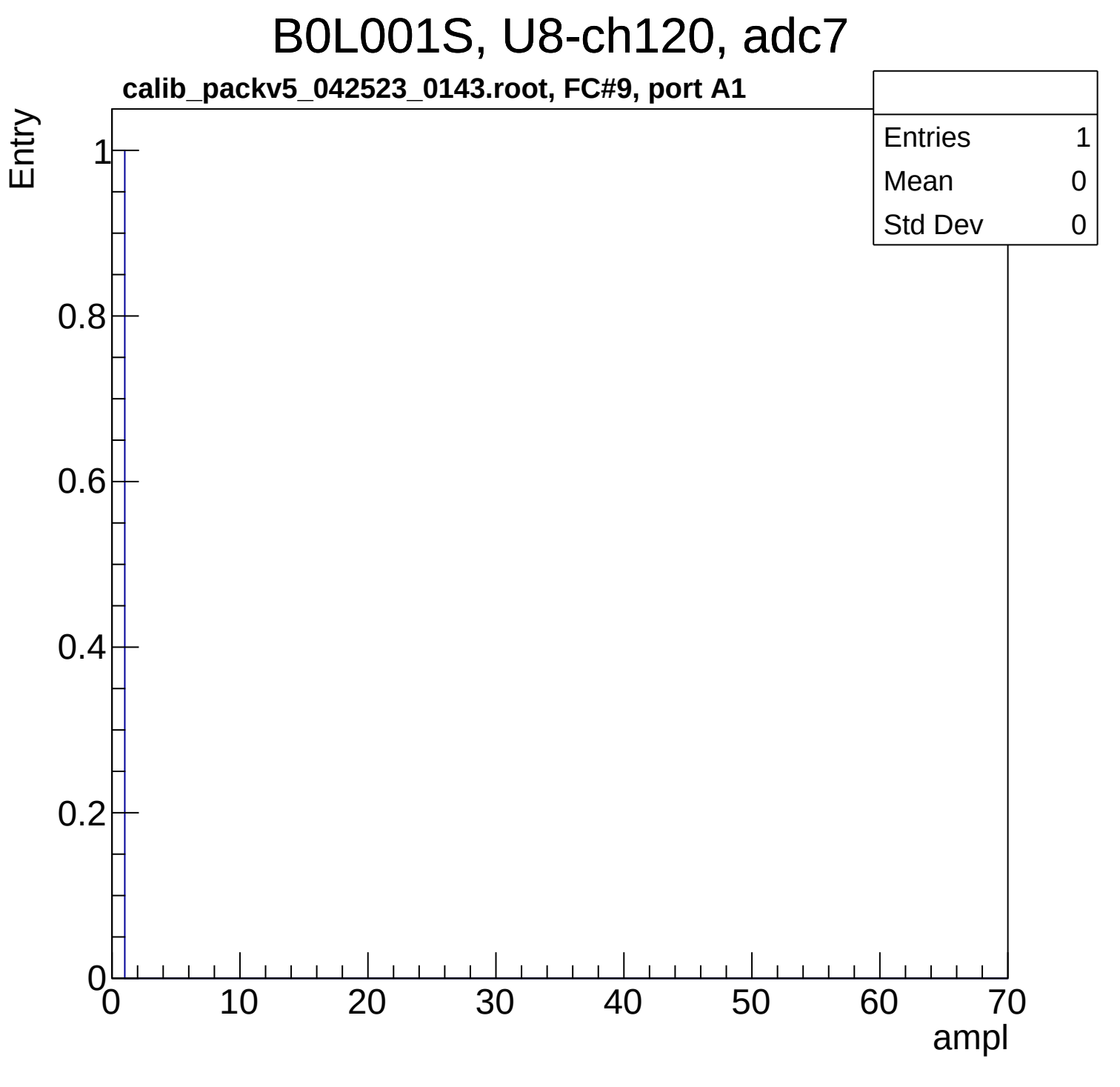

B0L001S, U8-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.9
Std Dev 3.91
Gaus mean : 44.8012
Gaus Width: 3.8826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

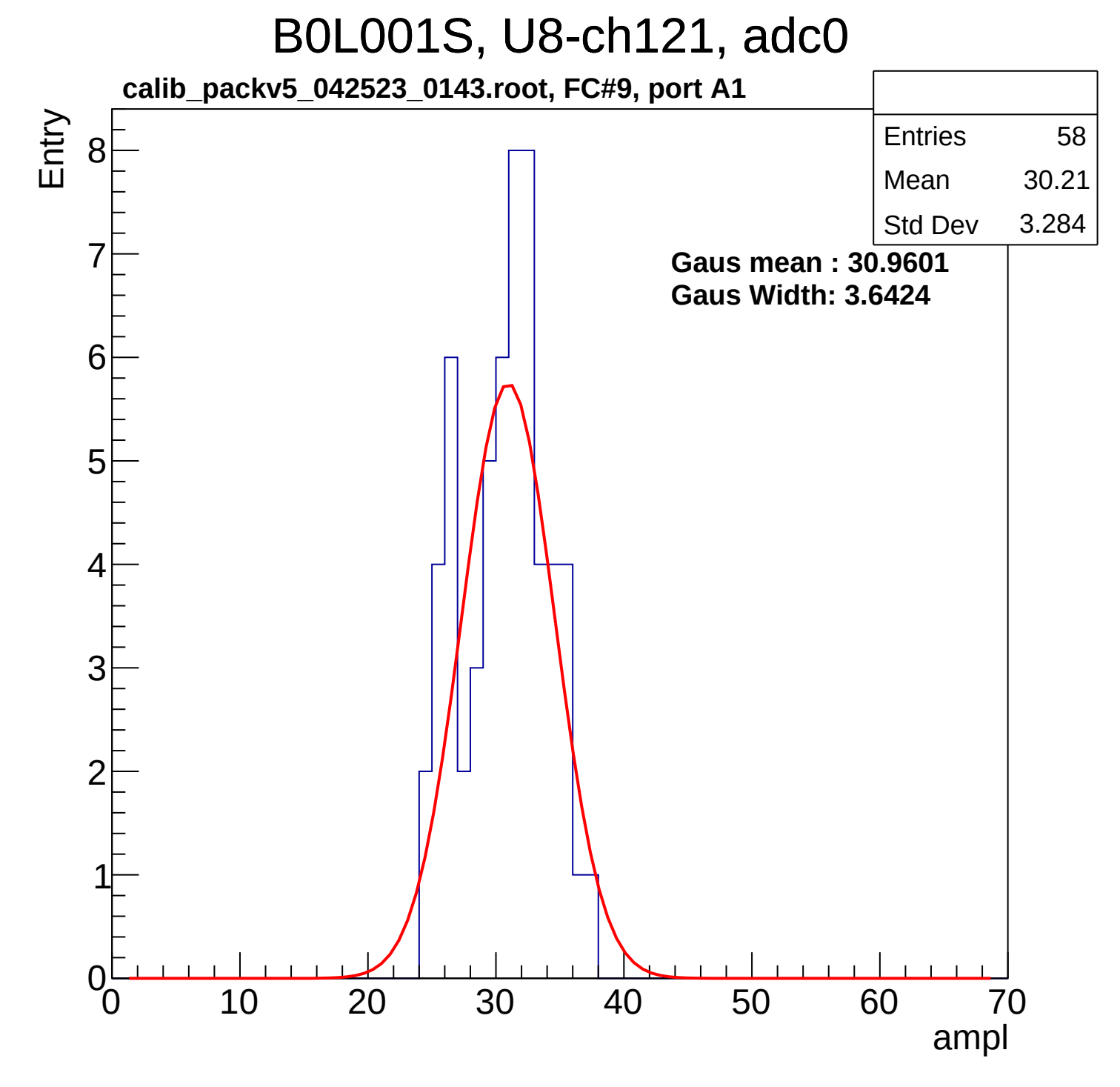

B0L001S, U8-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.78
Std Dev 3.455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

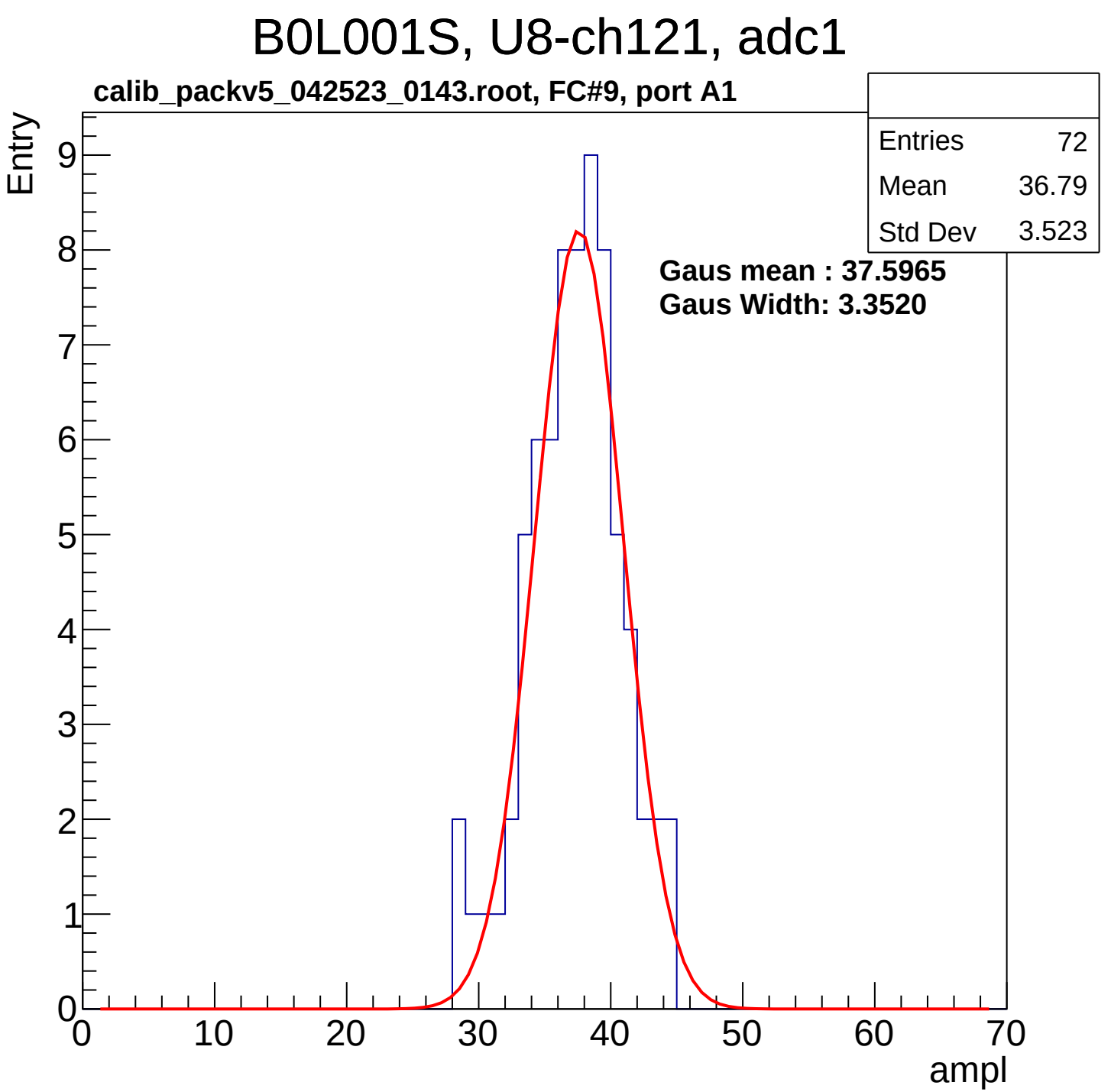

B0L001S, U8-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.38
Std Dev 8.39
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

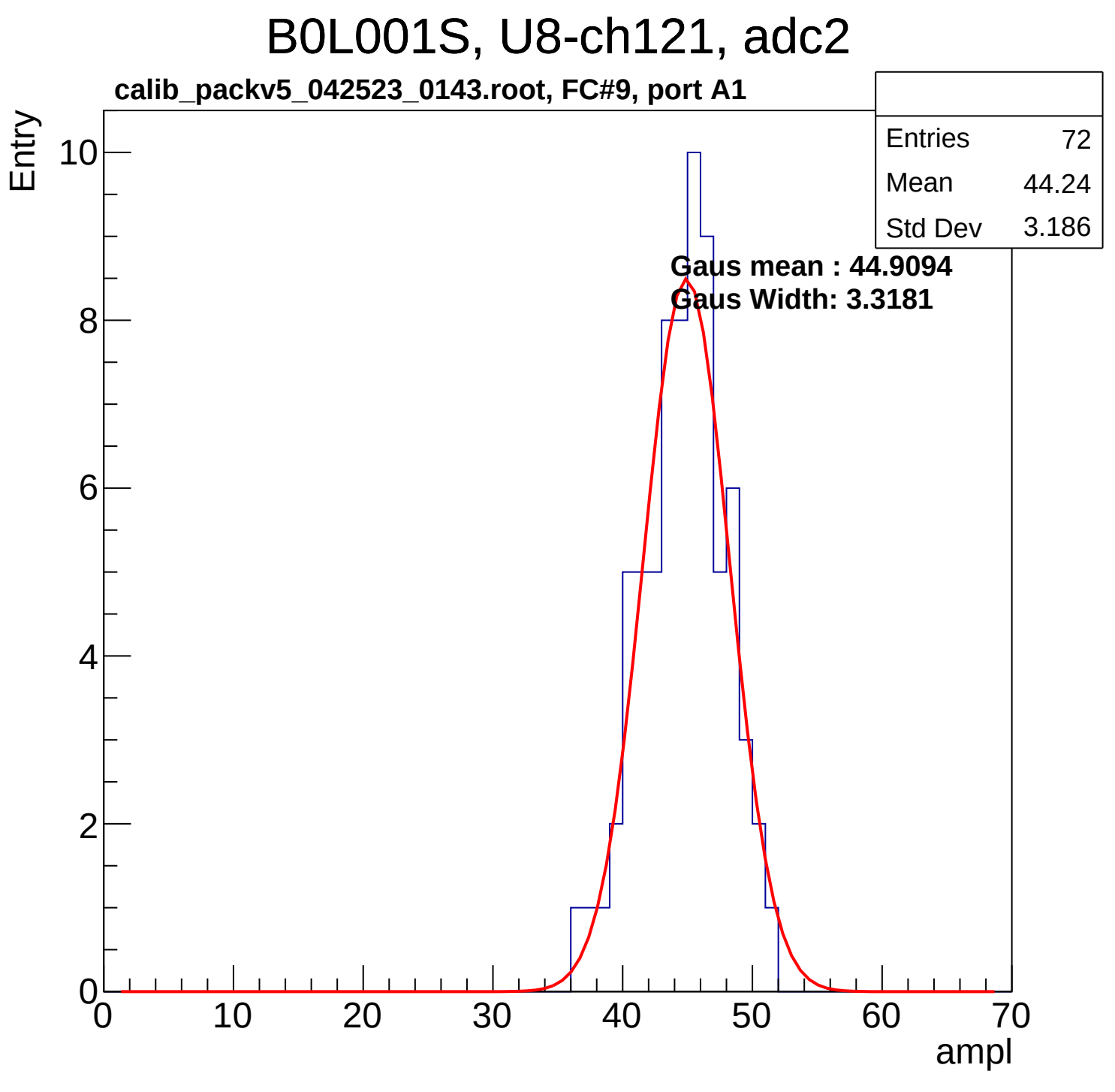

B0L001S, U8-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.83
Std Dev 1.62
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

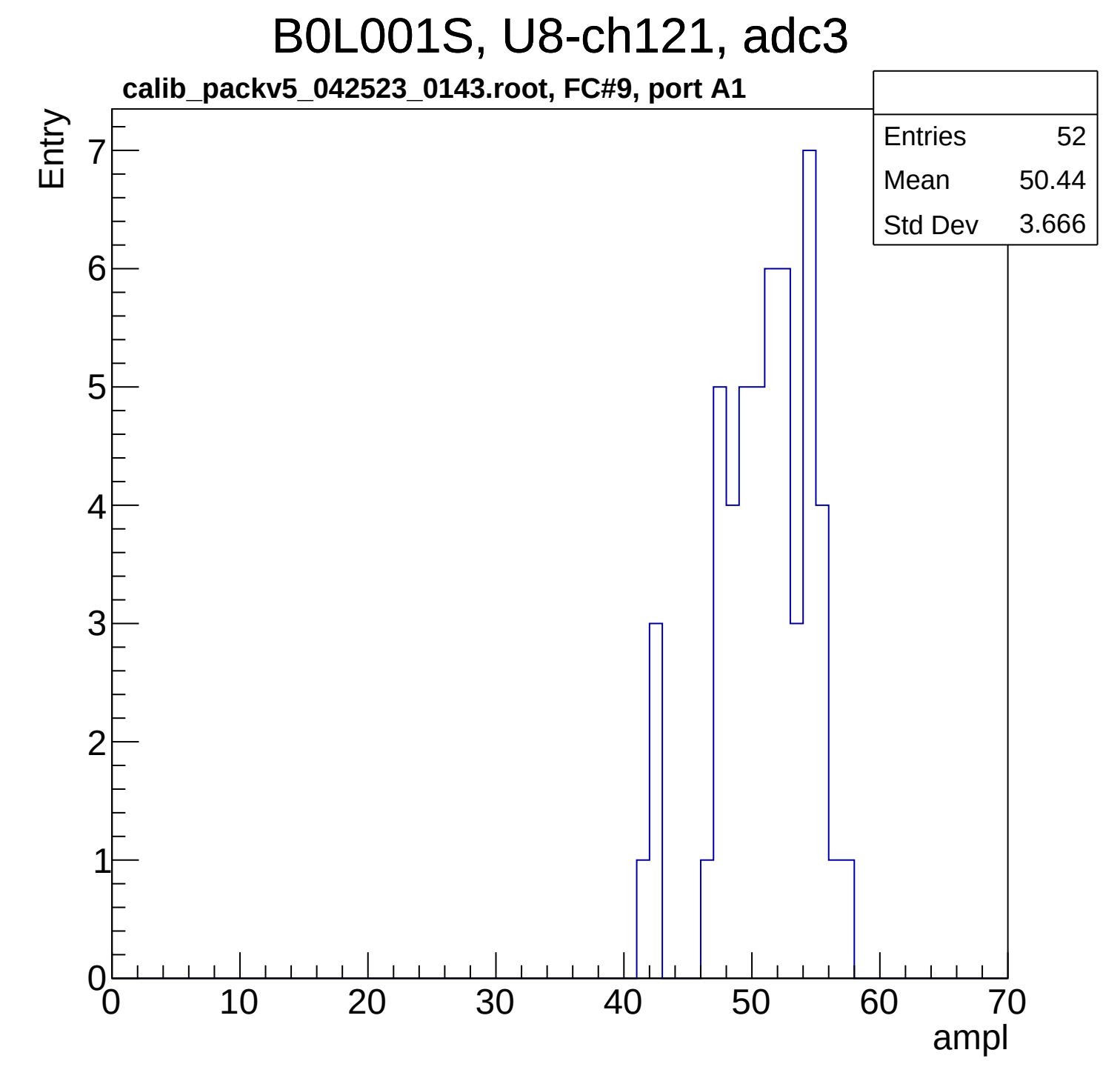

B0L001S, U8-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

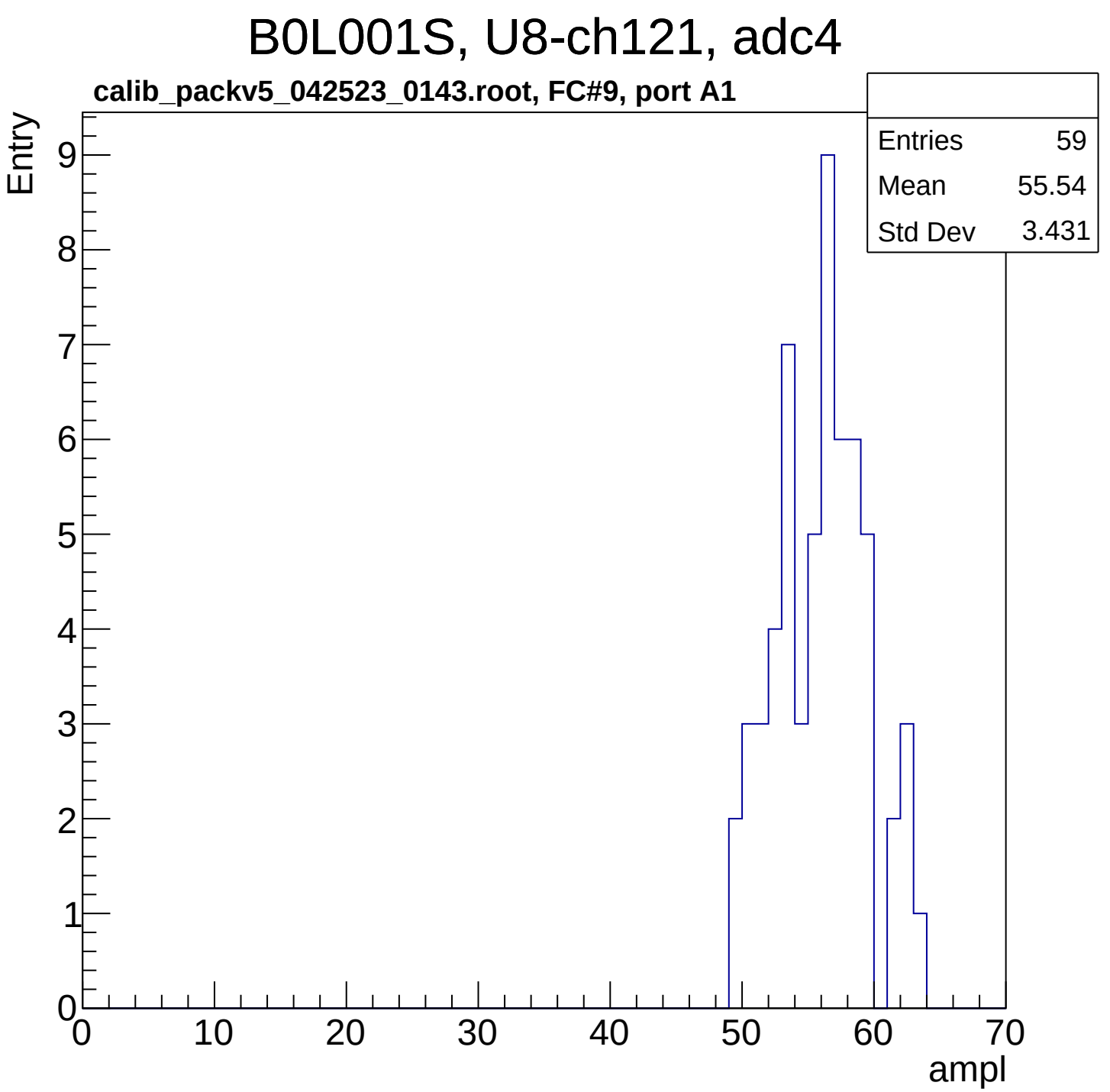

B0L001S, U8-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

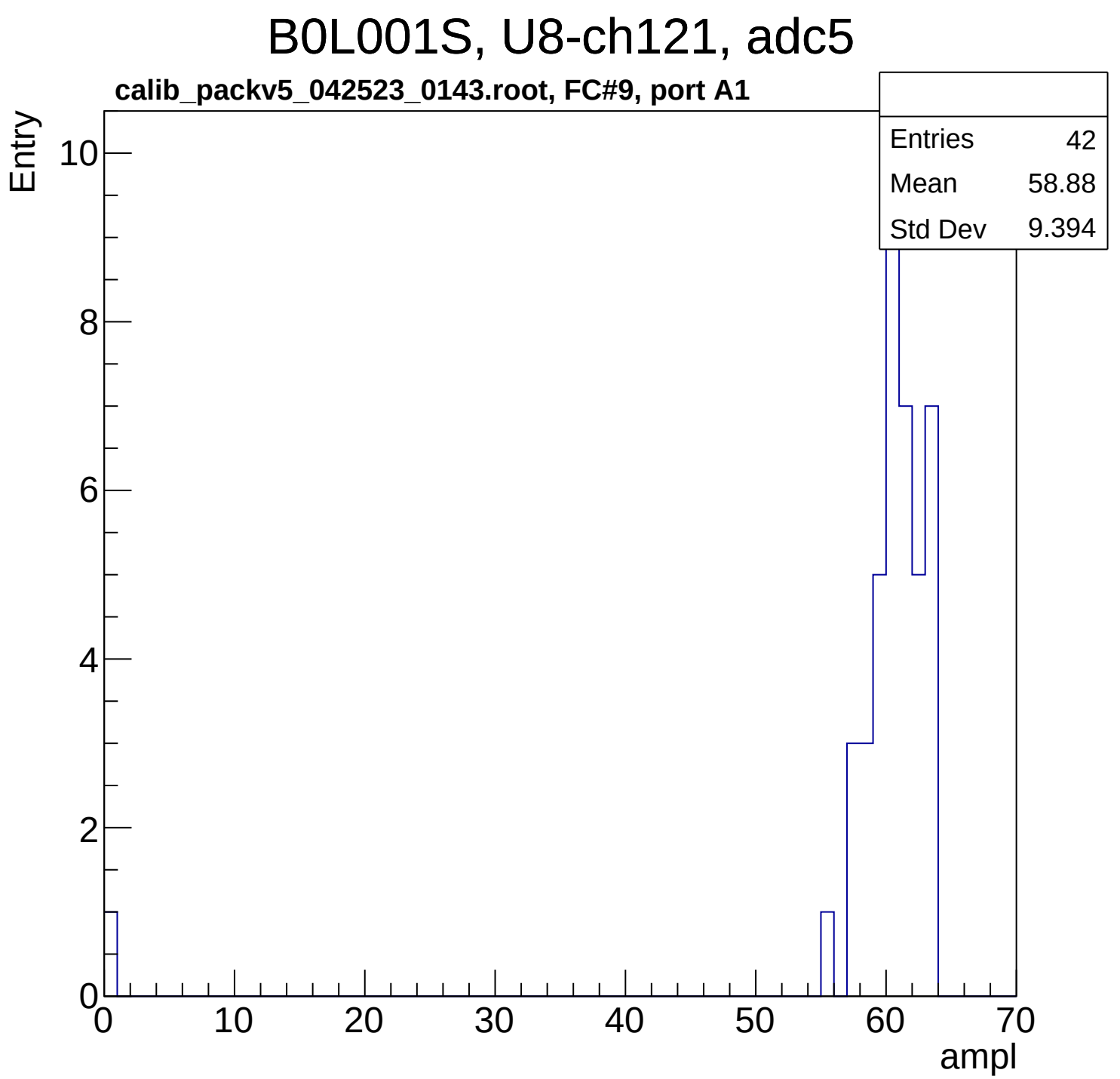

B0L001S, U8-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.21
Std Dev 3.284
Gaus mean : 30.9601
Gaus Width: 3.6424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

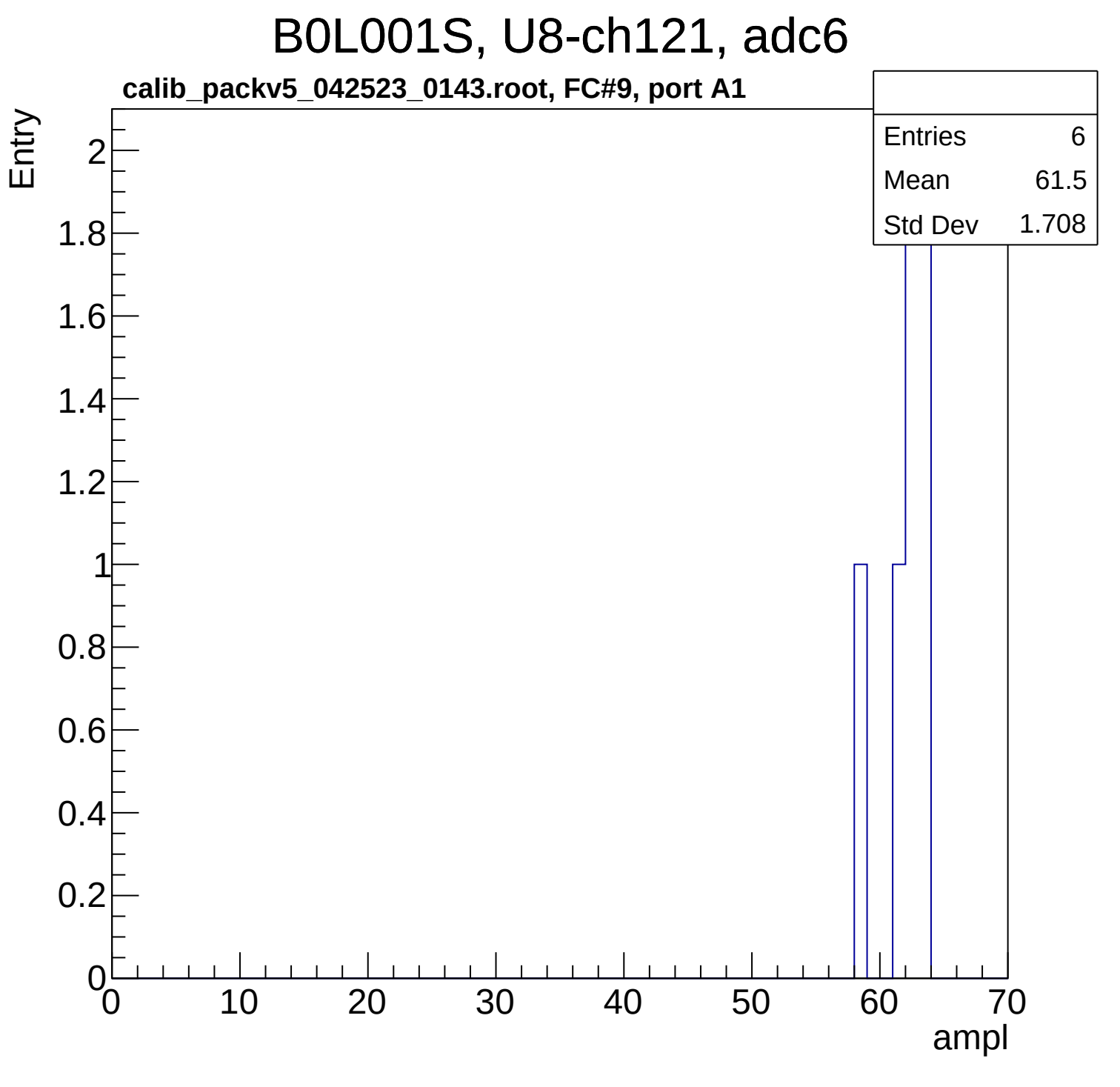

B0L001S, U8-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.79
Std Dev 3.523
Gaus mean : 37.5965
Gaus Width: 3.3520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

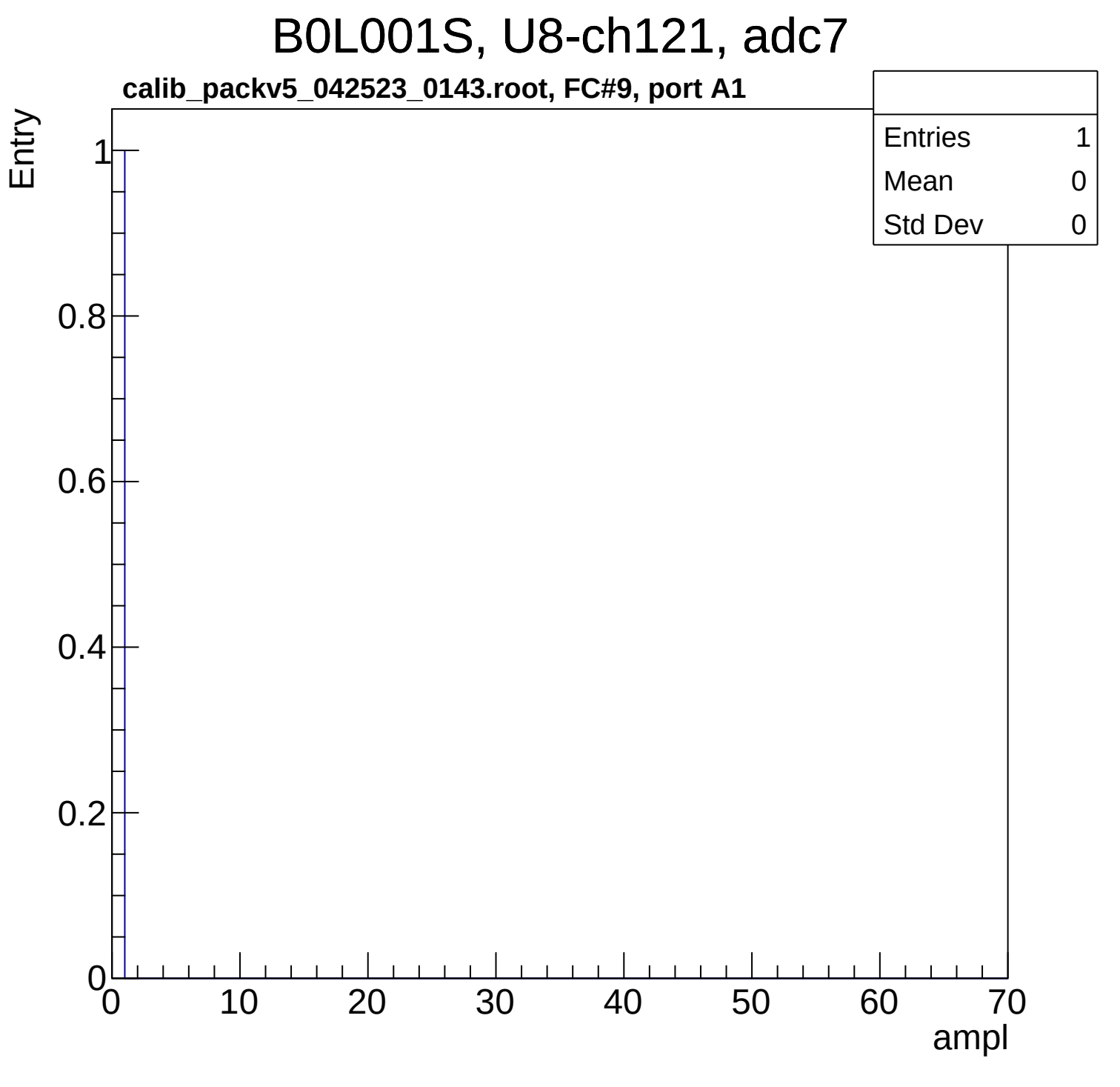

B0L001S, U8-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.24
Std Dev 3.186
Gaus mean : 44.9094
Gaus Width: 3.3181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

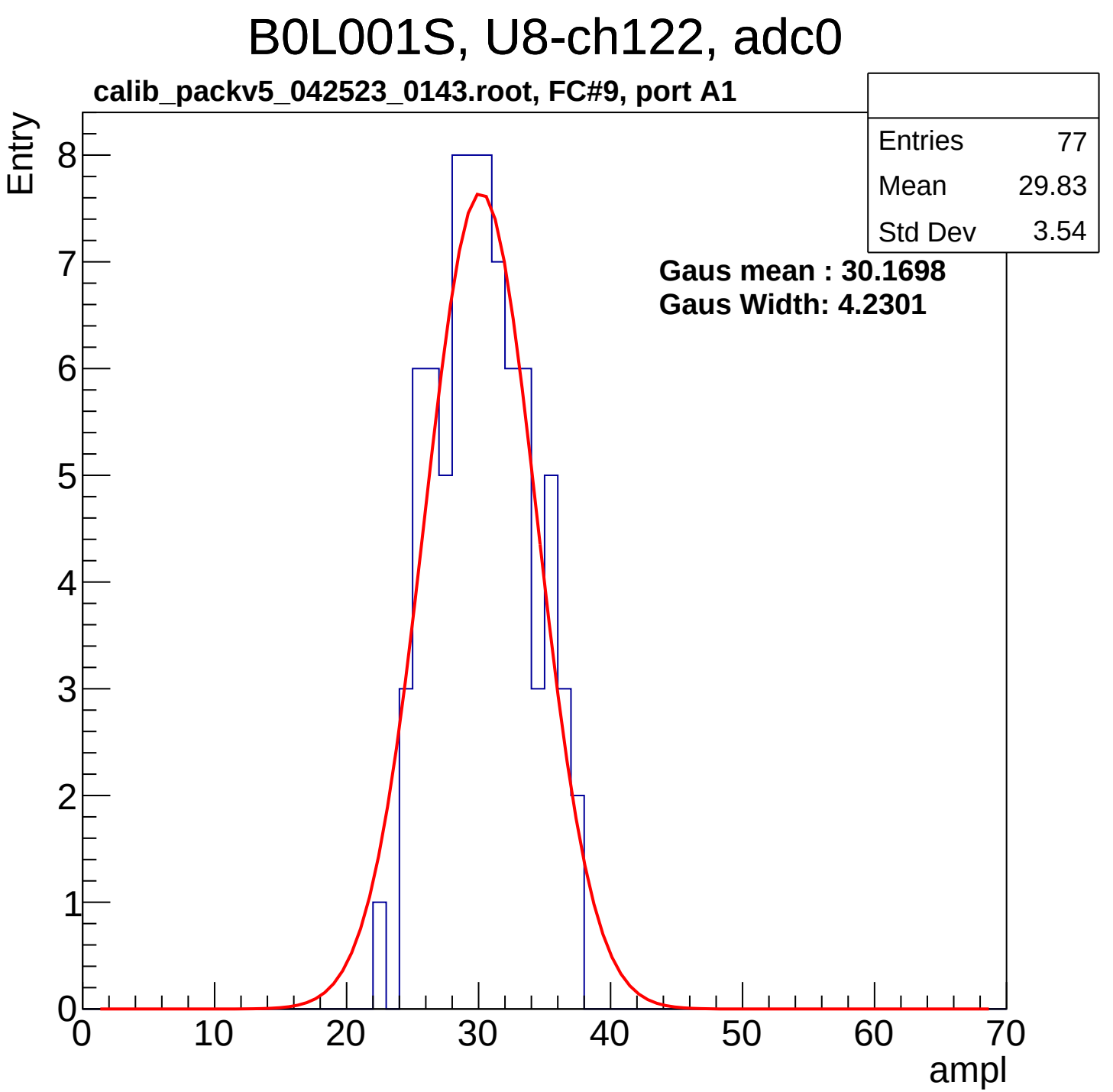

B0L001S, U8-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.44
Std Dev 3.666
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

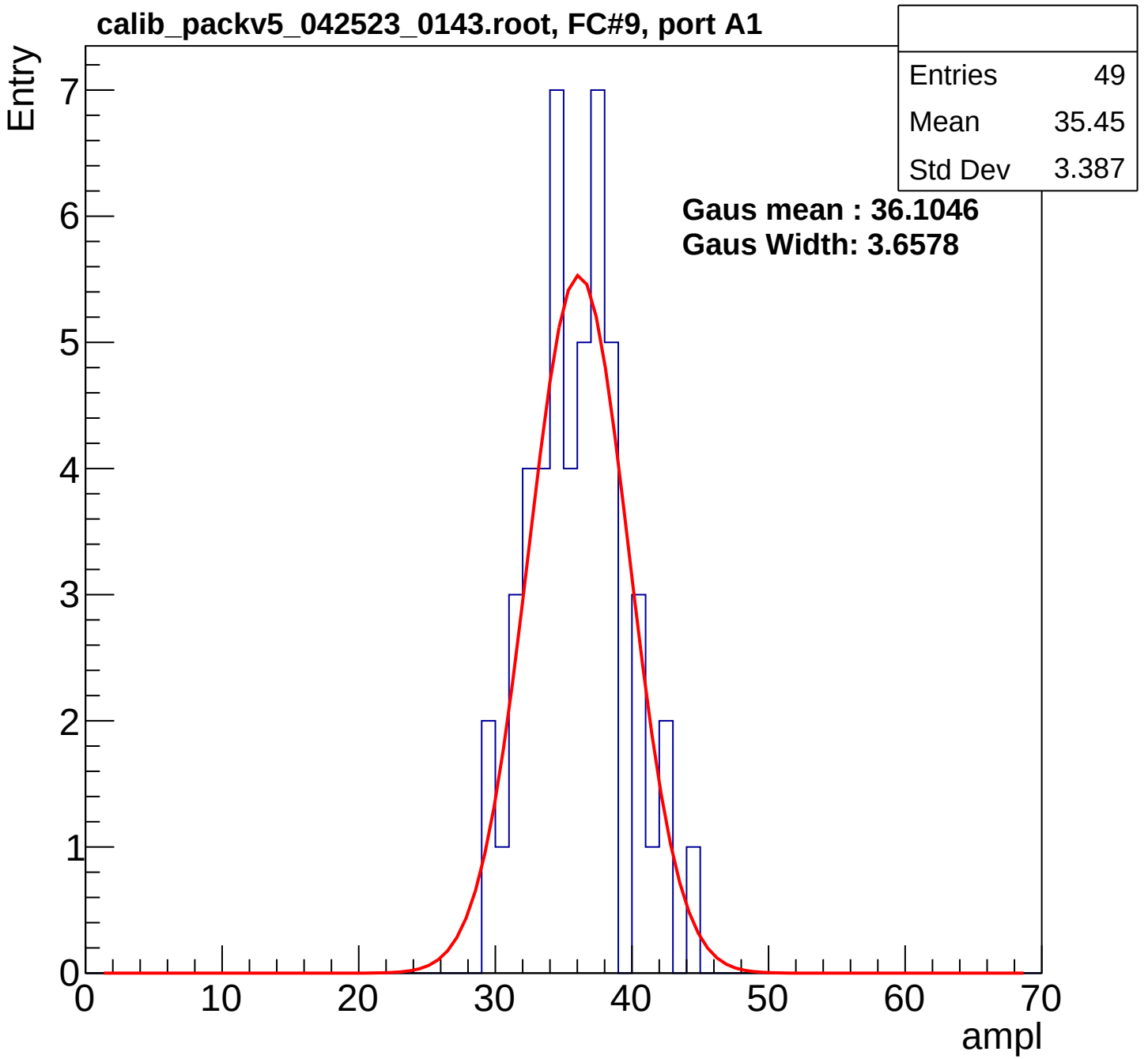

B0L001S, U8-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.54
Std Dev 3.431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

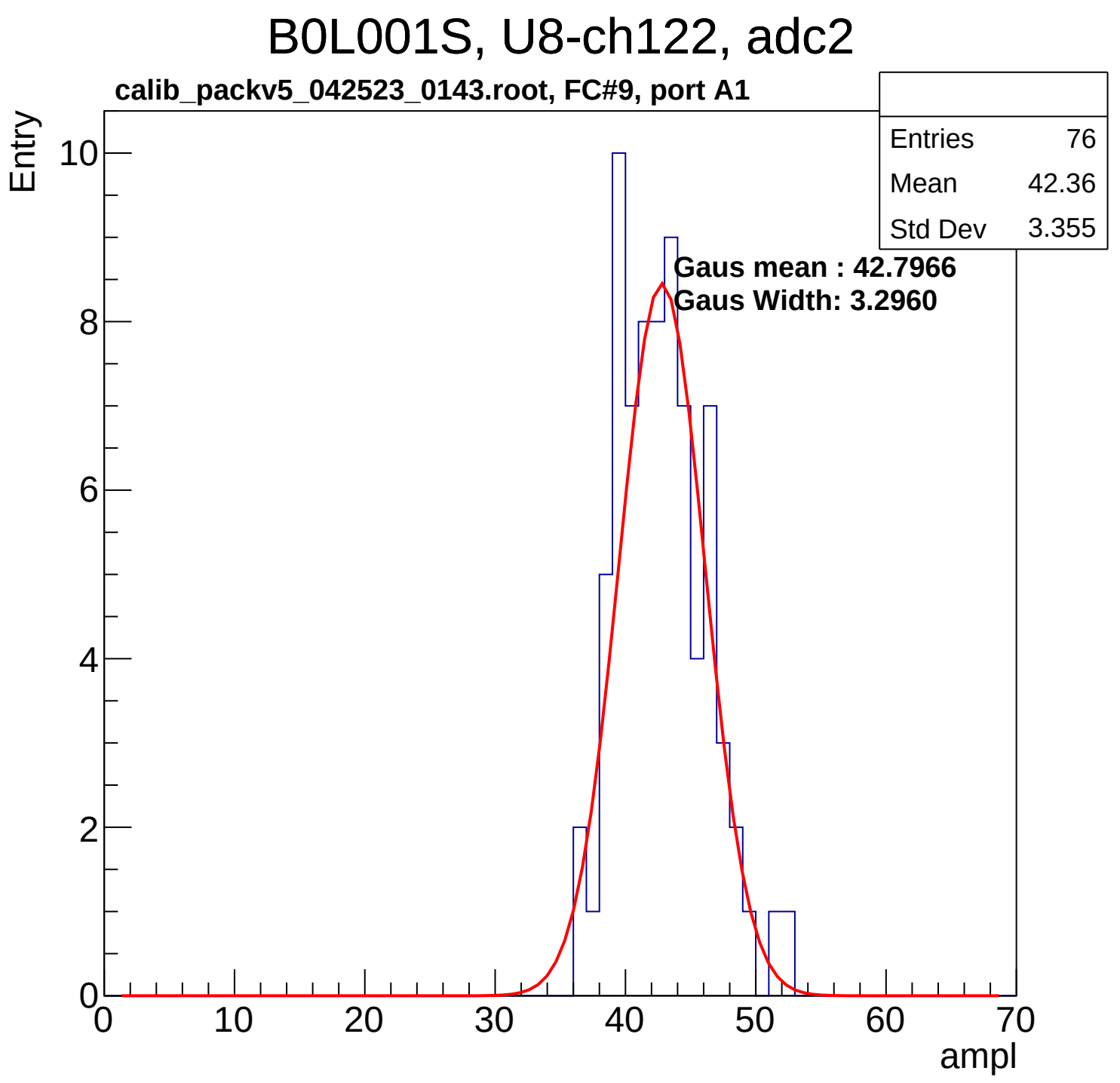

B0L001S, U8-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.88
Std Dev 9.394
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

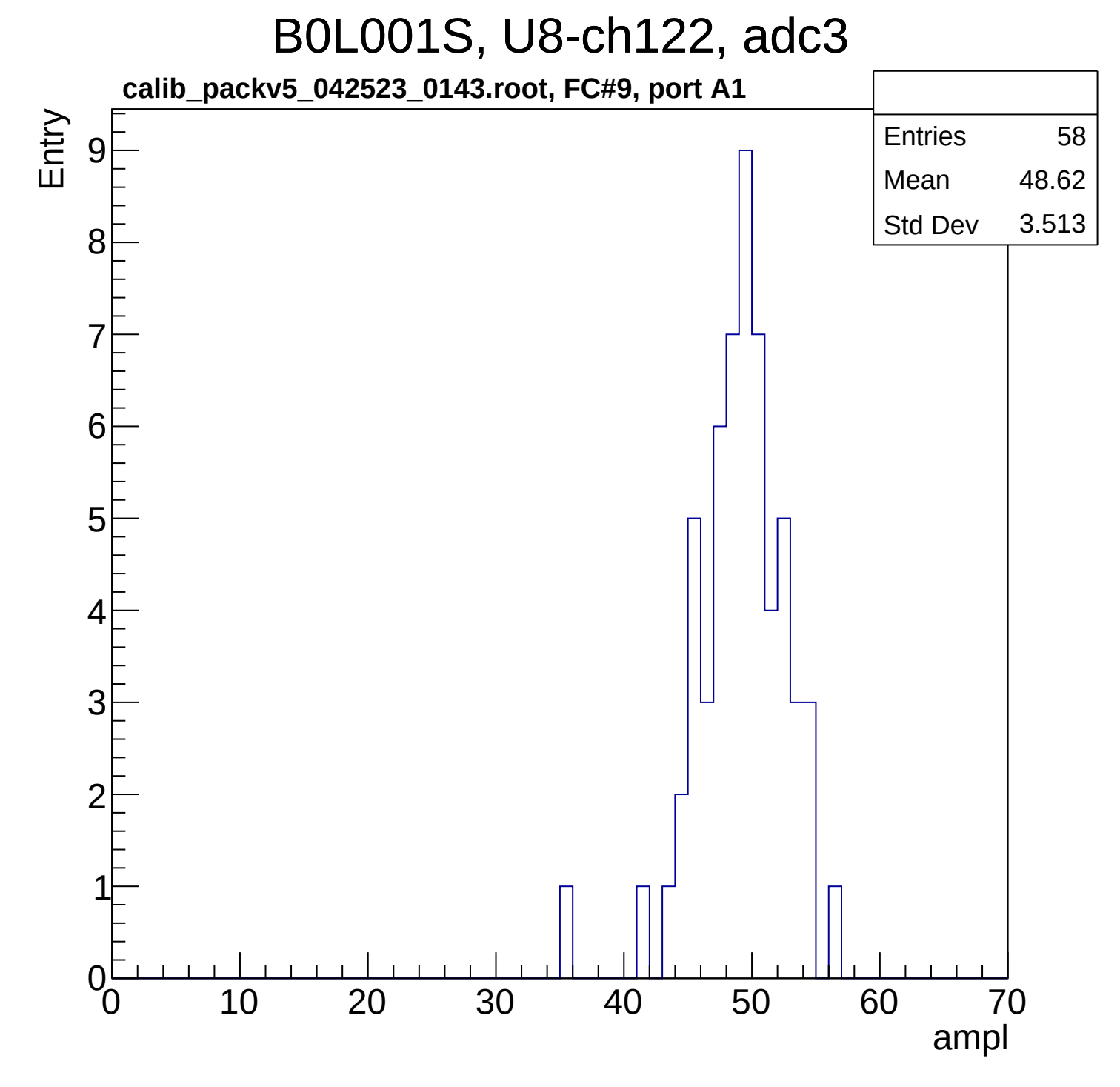

B0L001S, U8-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.5
Std Dev 1.708
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

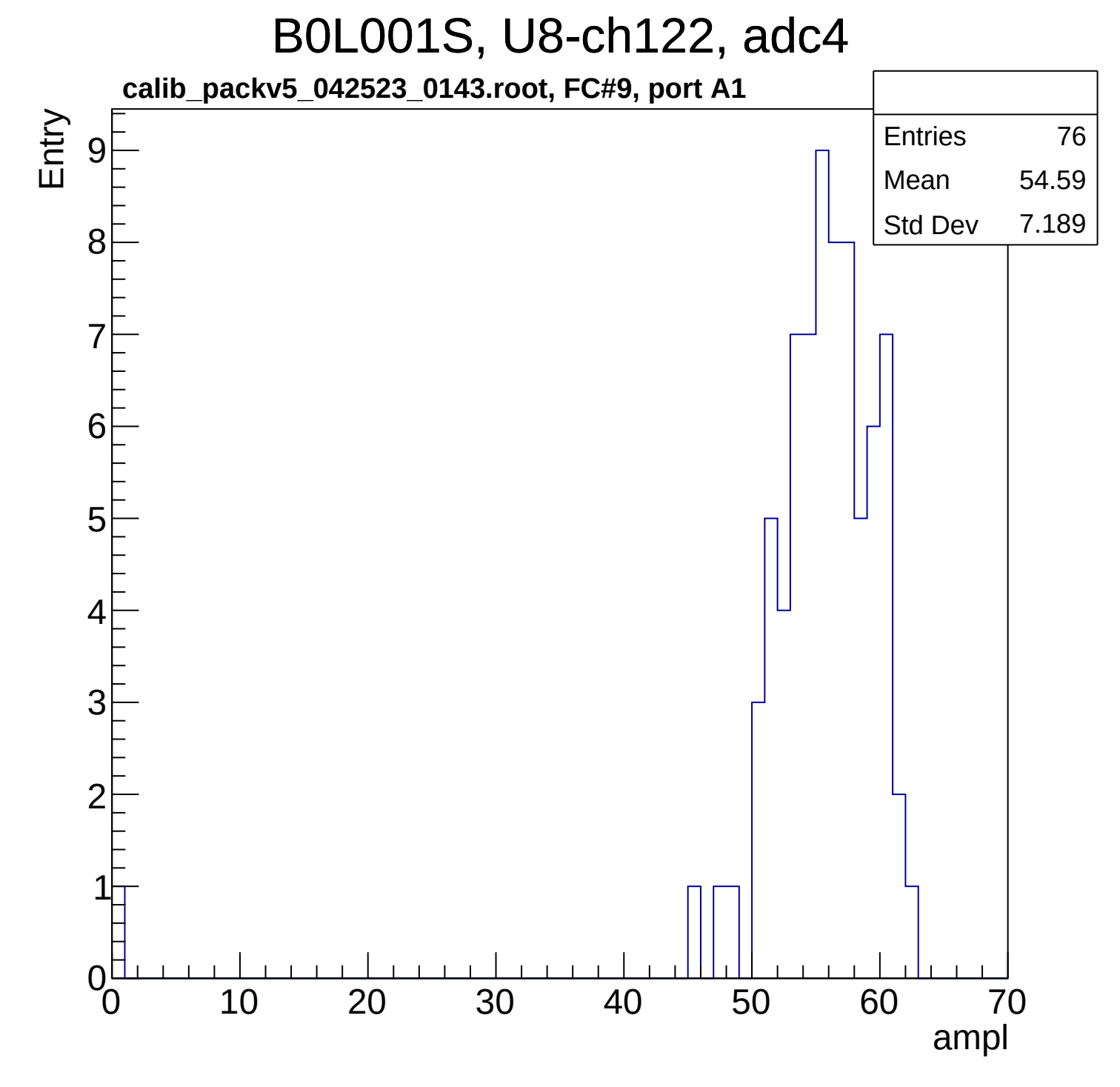

B0L001S, U8-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.83
Std Dev 3.54
Gaus mean : 30.1698
Gaus Width: 4.2301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 35.45
Std Dev 3.387
Gaus mean : 36.1046
Gaus Width: 3.6578
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 42.36
Std Dev 3.355
Gaus mean : 42.7966
Gaus Width: 3.2960
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 48.62
Std Dev 3.513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 54.59
Std Dev 7.189
Entry
ampl
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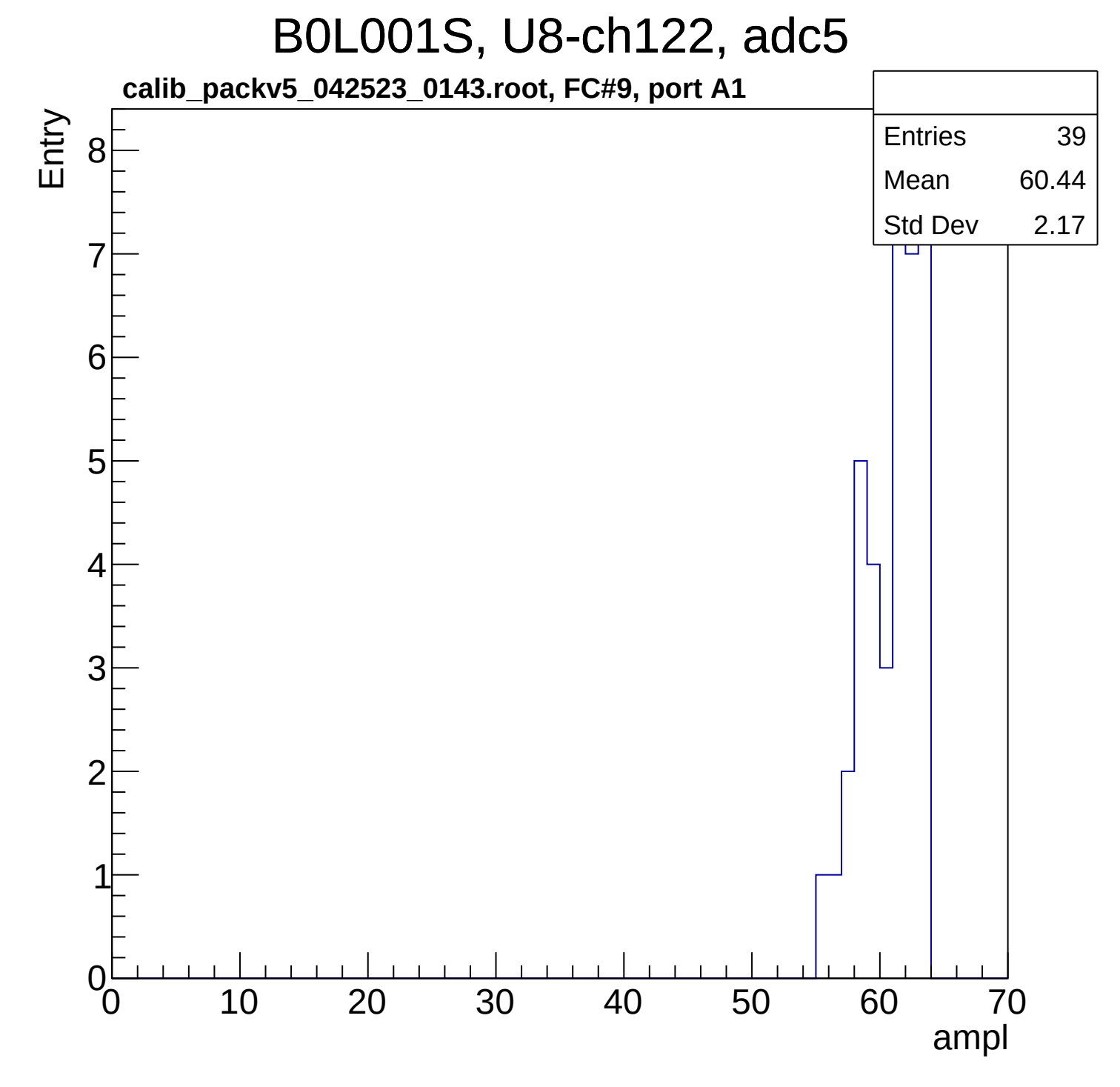
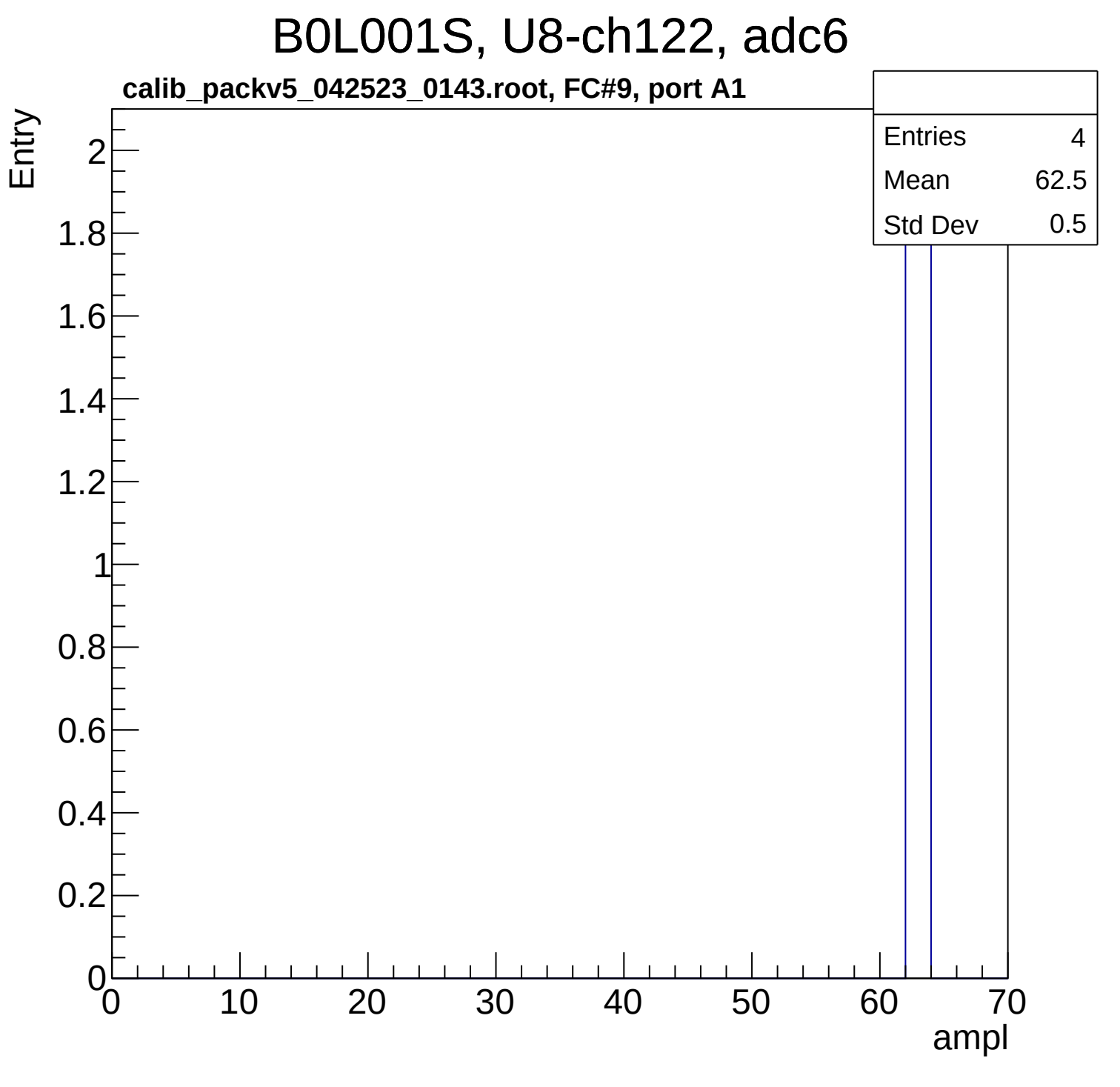
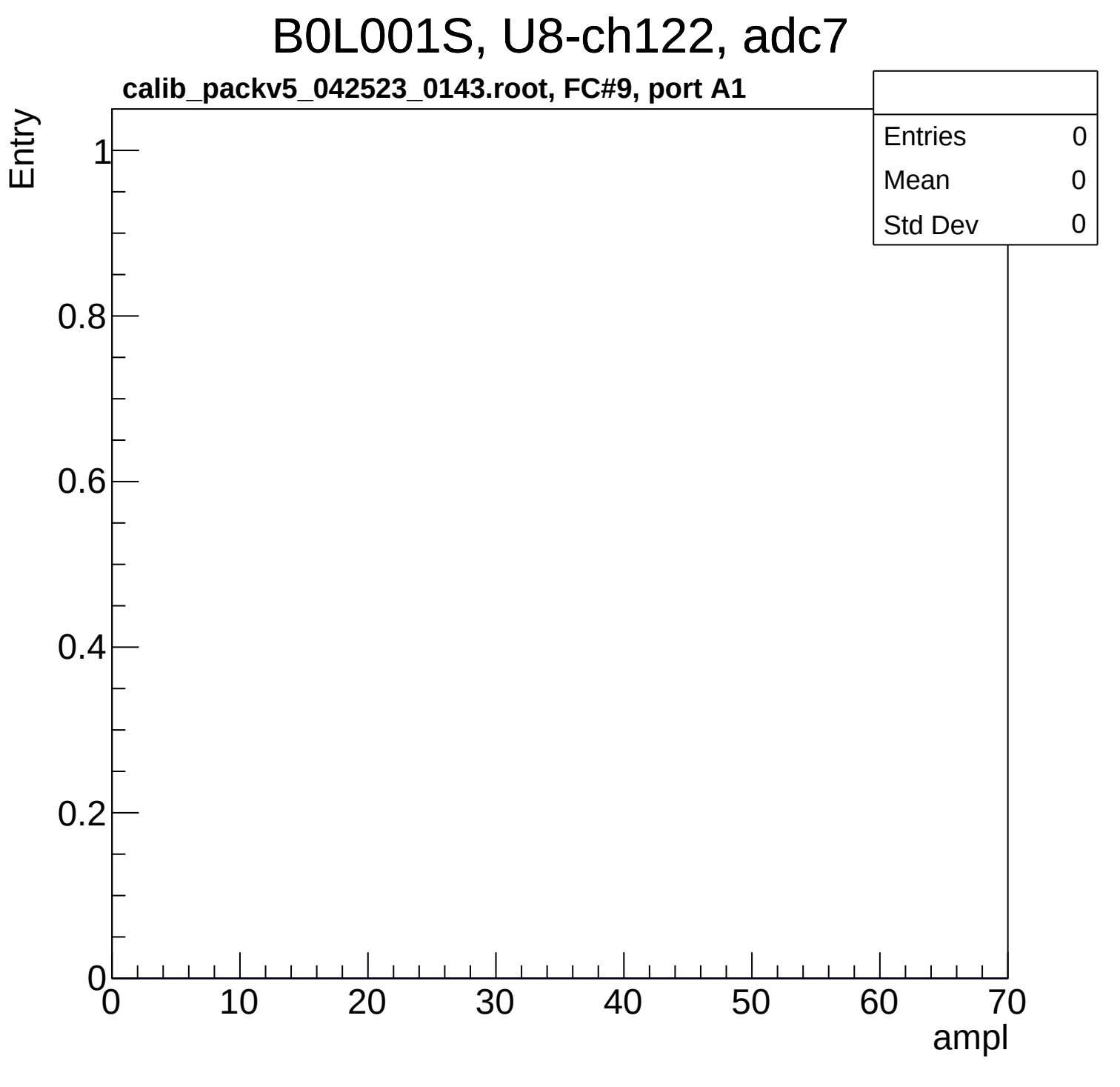
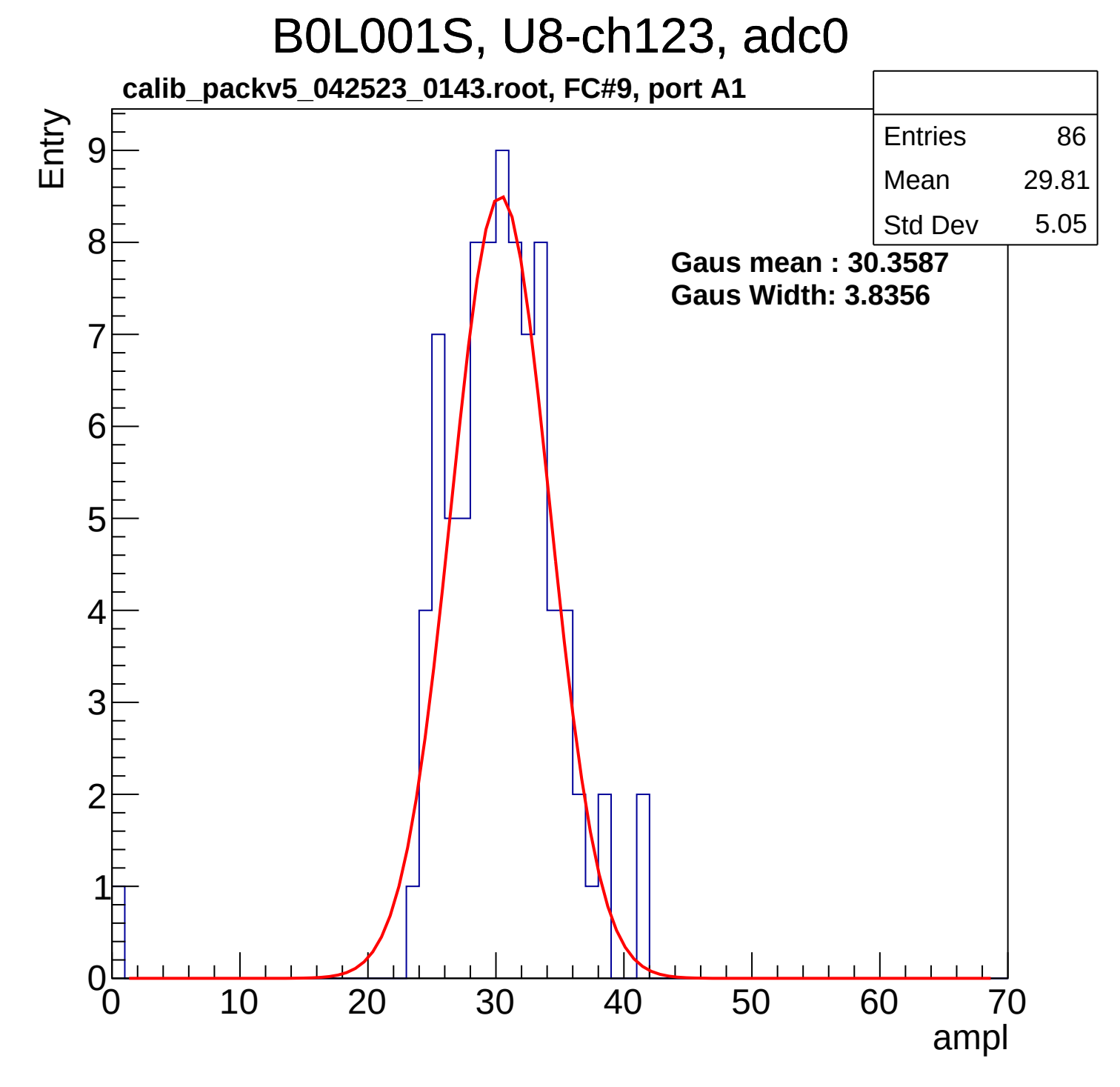
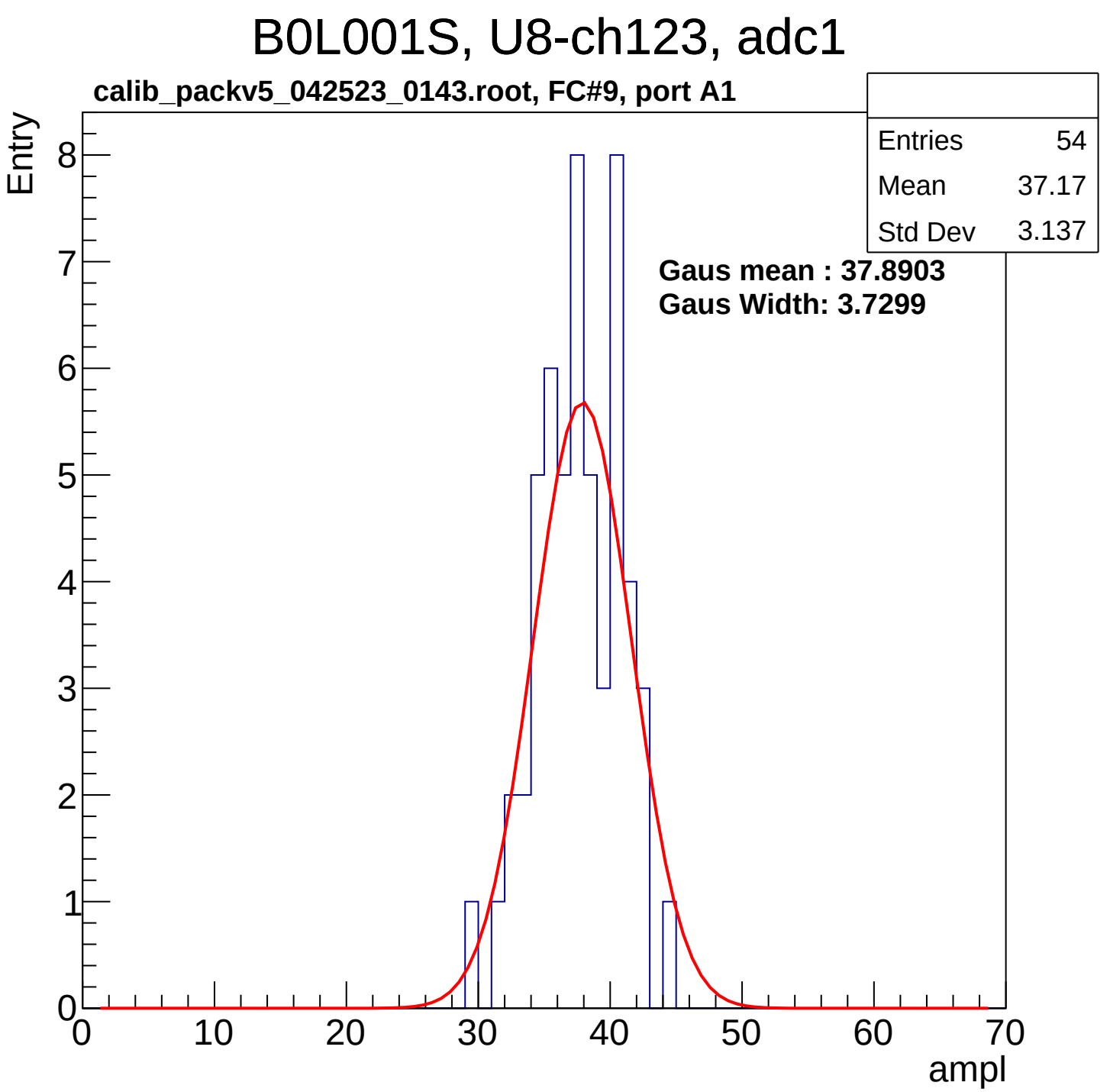
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

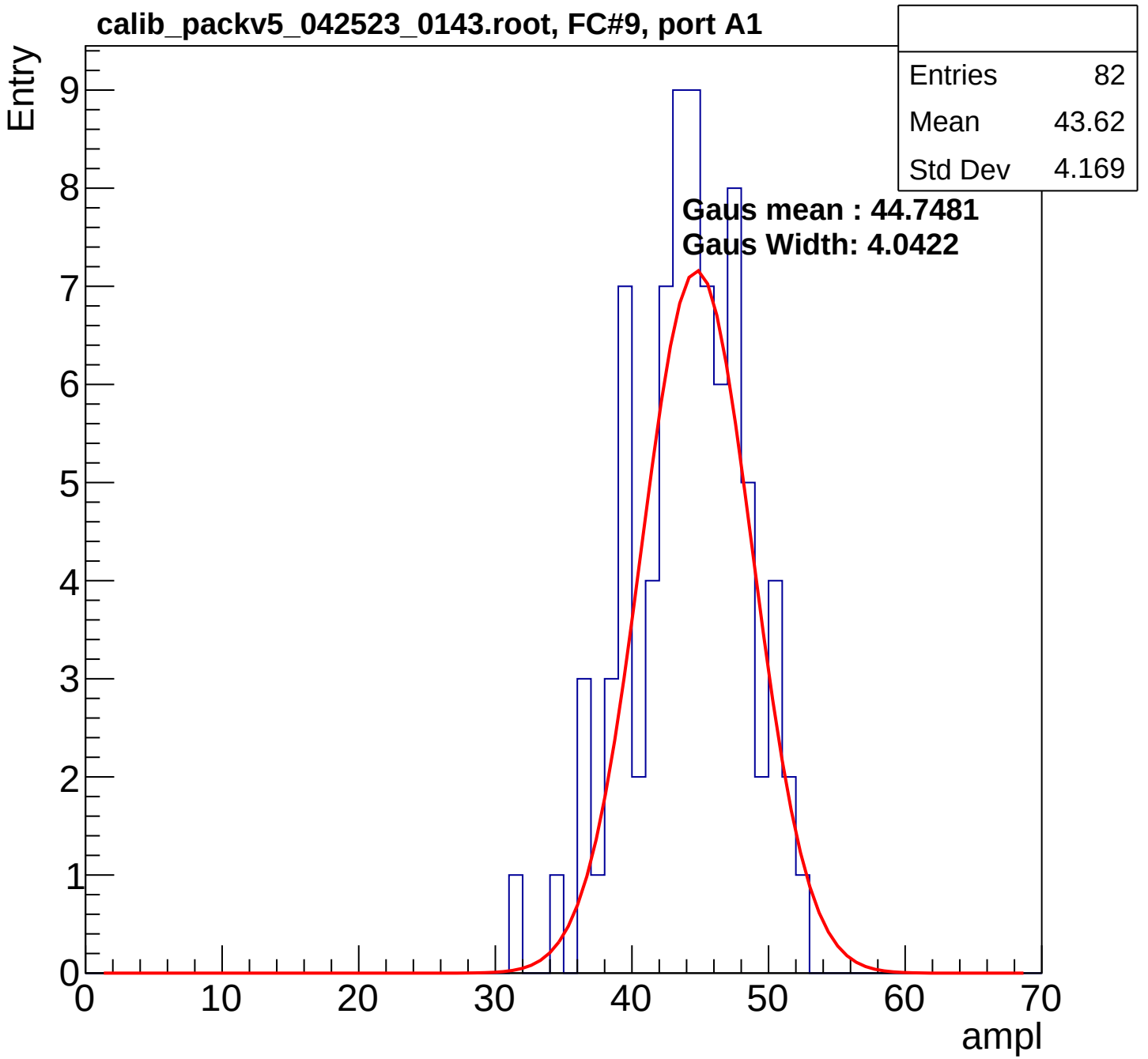

B0L001S, U8-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 60.44
Std Dev 2.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U8-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 29.81
Std Dev 5.05
Gaus mean : 30.3587
Gaus Width: 3.8356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 37.17
Std Dev 3.137
Gaus mean : 37.8903
Gaus Width: 3.7299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 43.62
Std Dev 4.169
Gaus mean : 44.7481
Gaus Width: 4.0422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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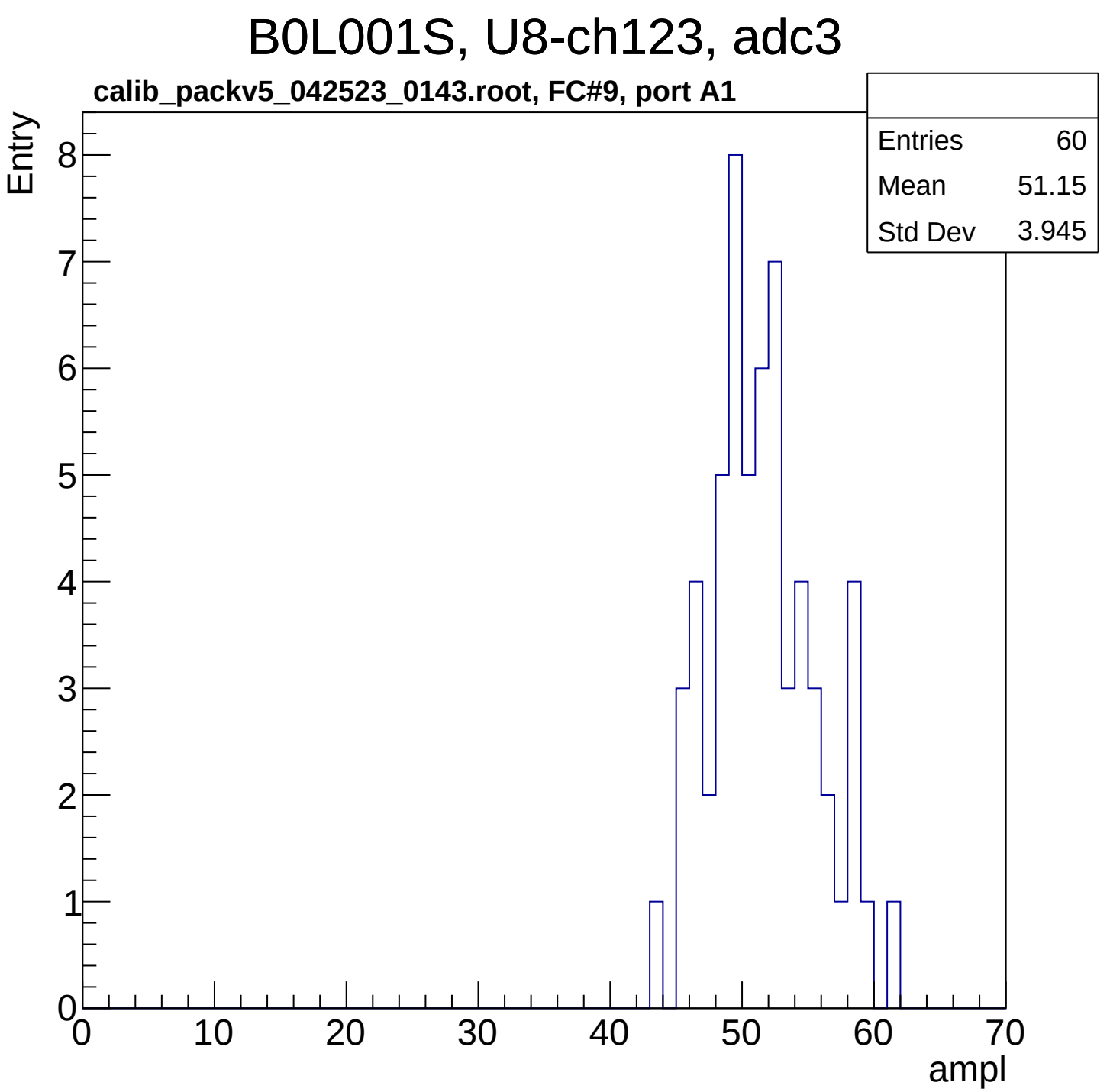
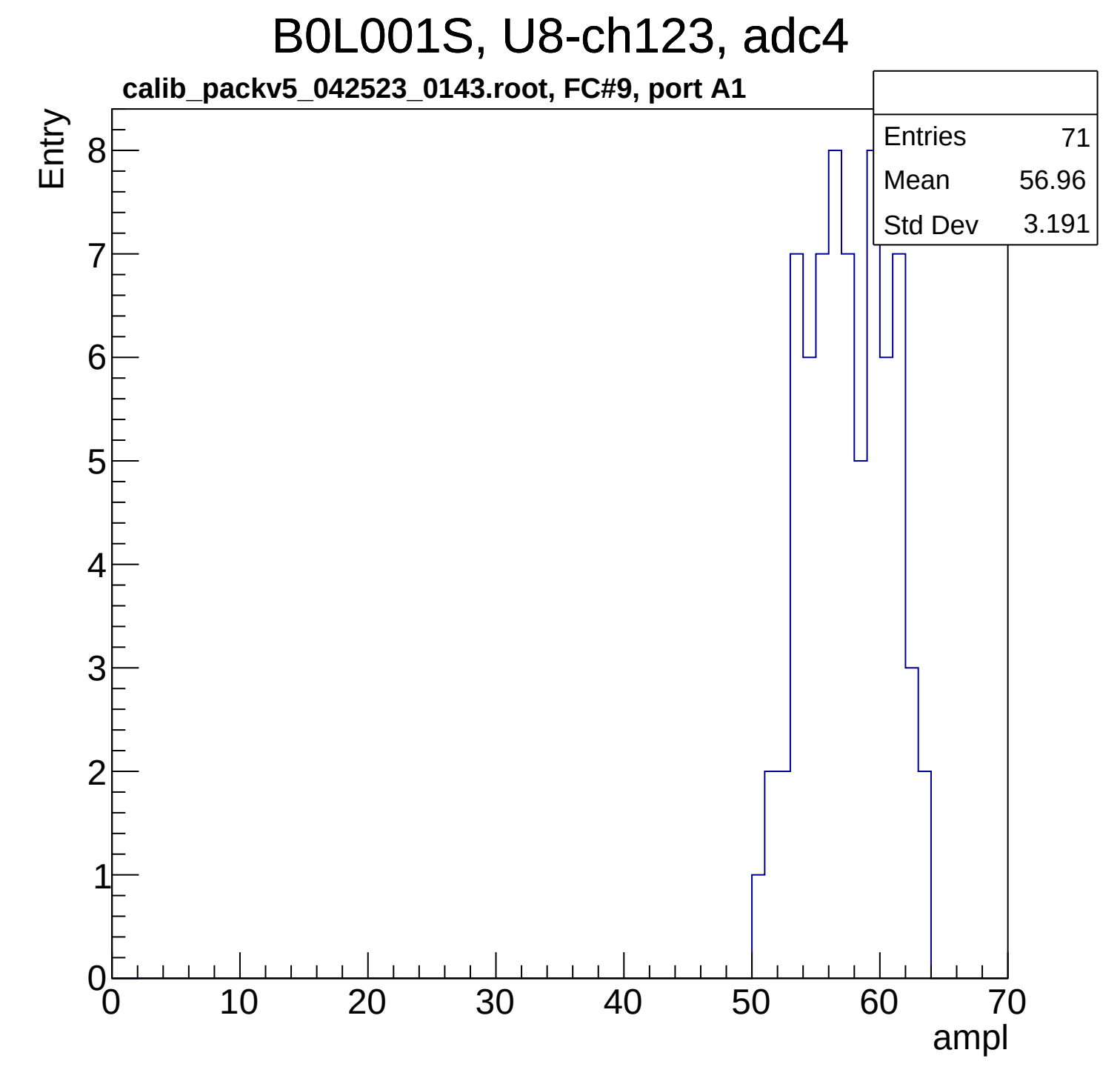

B0L001S, U8-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.15
Std Dev 3.945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 56.96
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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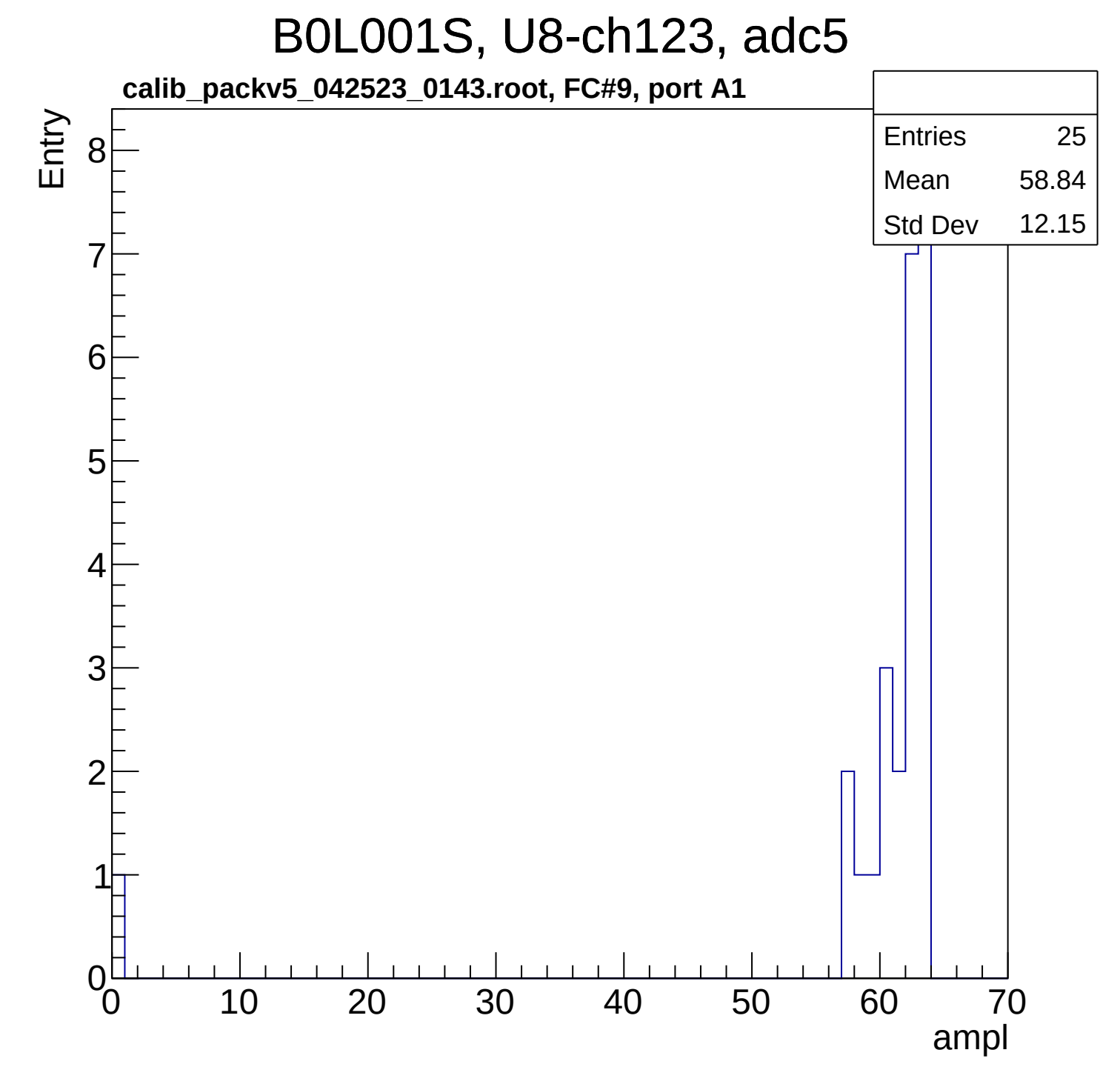
B0L001S, U8-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.84
Std Dev 12.15
Entry
ampl
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1
2
3
4
5
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0
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60
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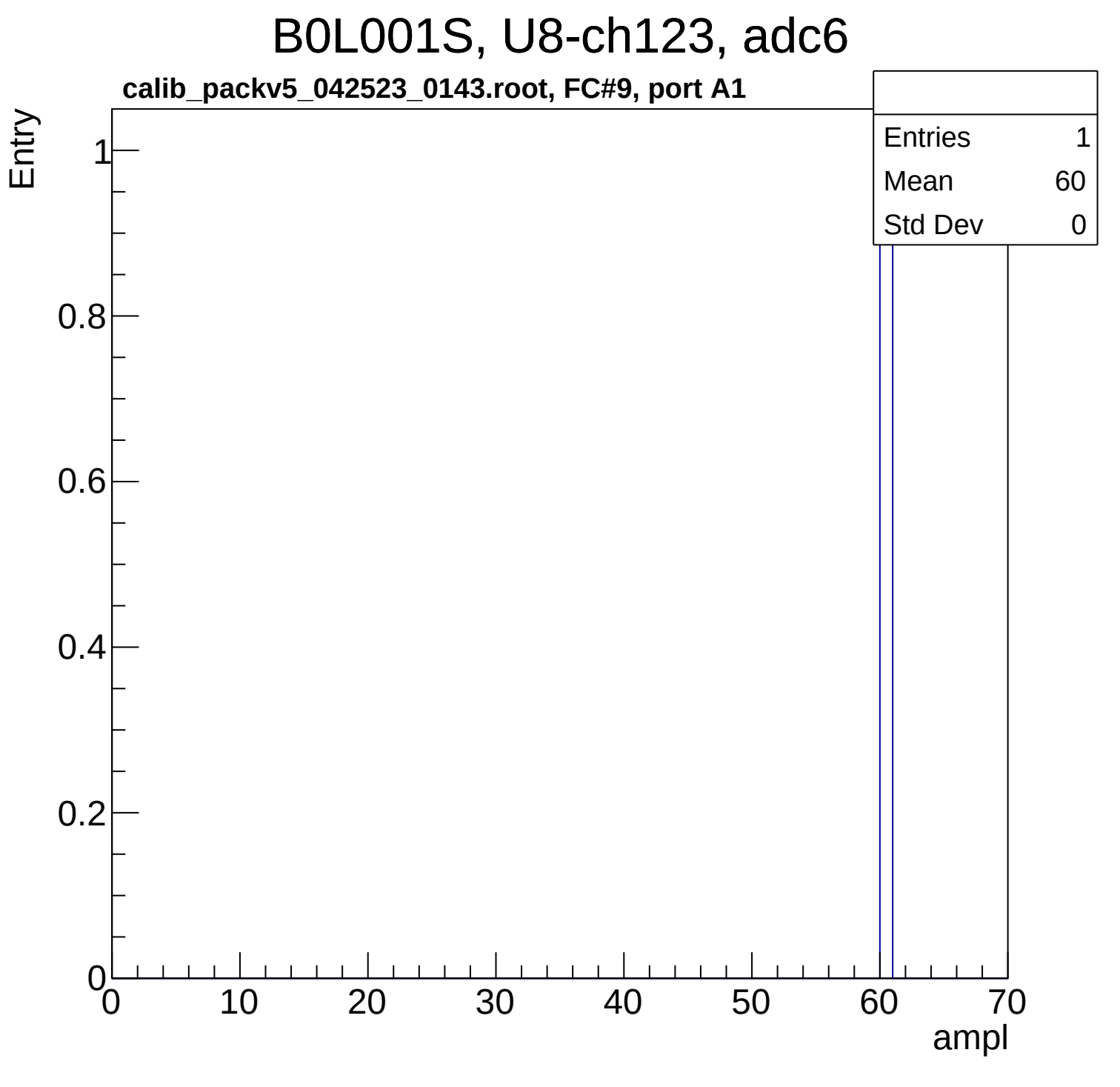
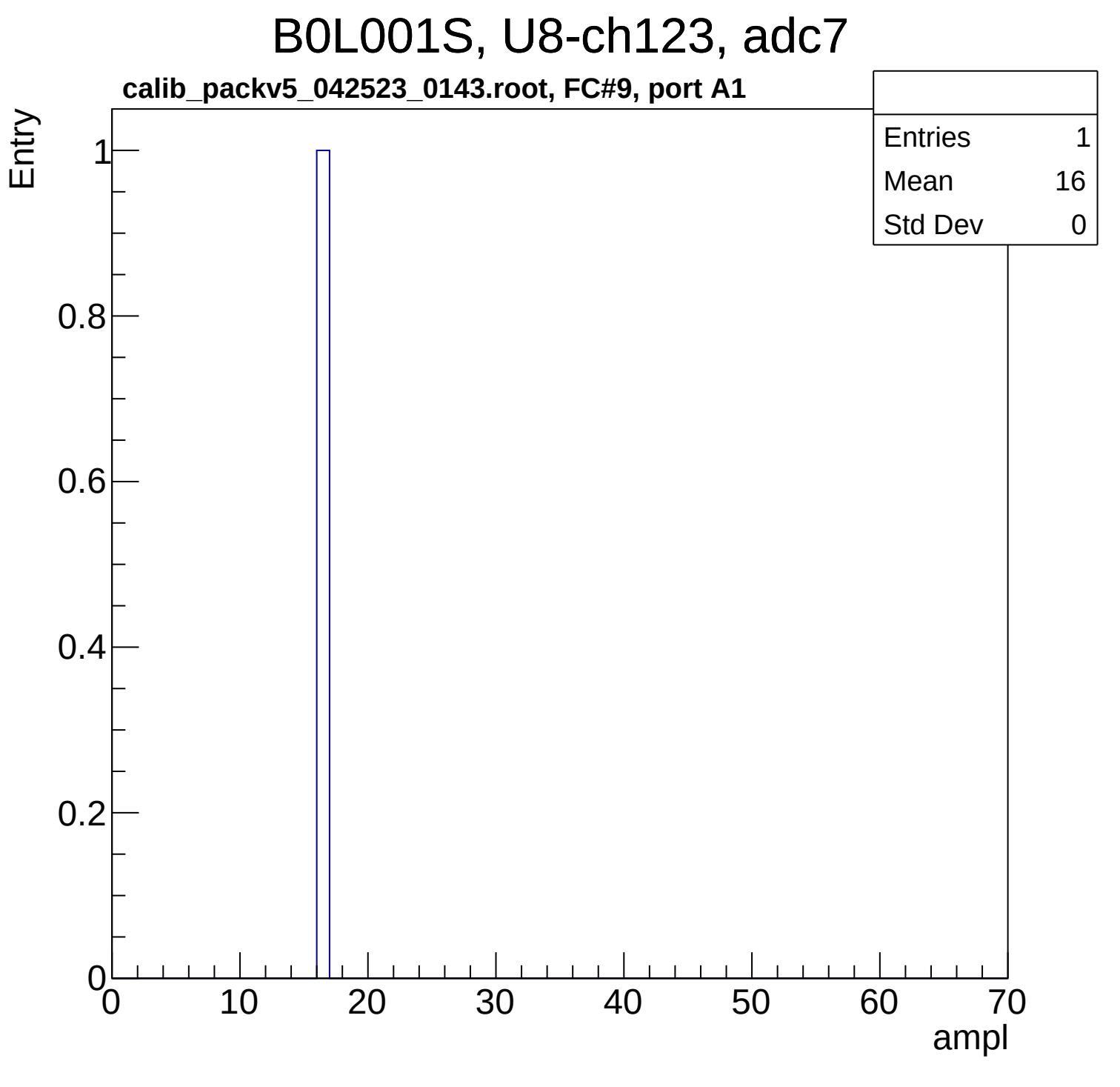
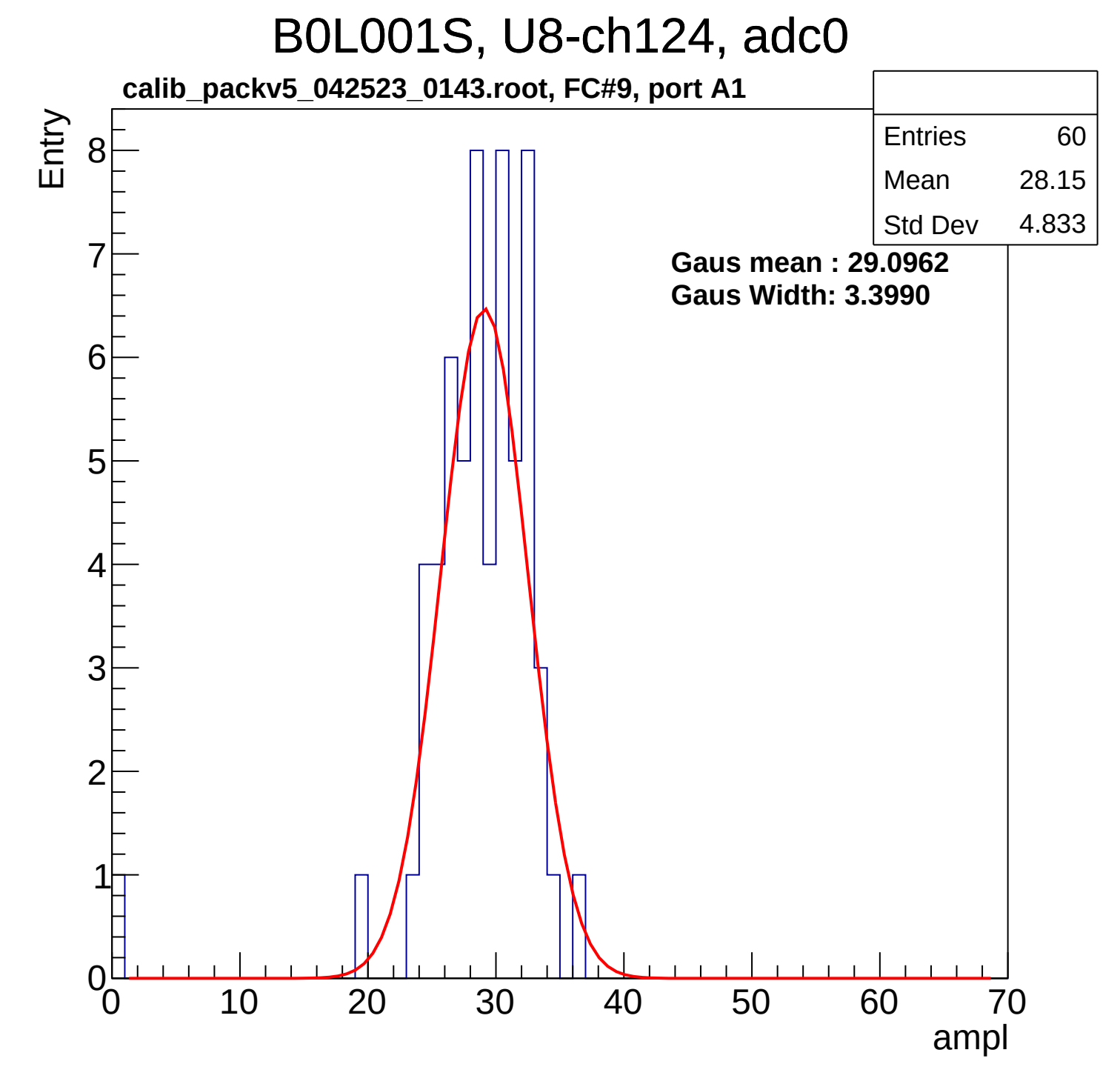

B0L001S, U8-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 16
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 28.15
Std Dev 4.833
Gaus mean : 29.0962
Gaus Width: 3.3990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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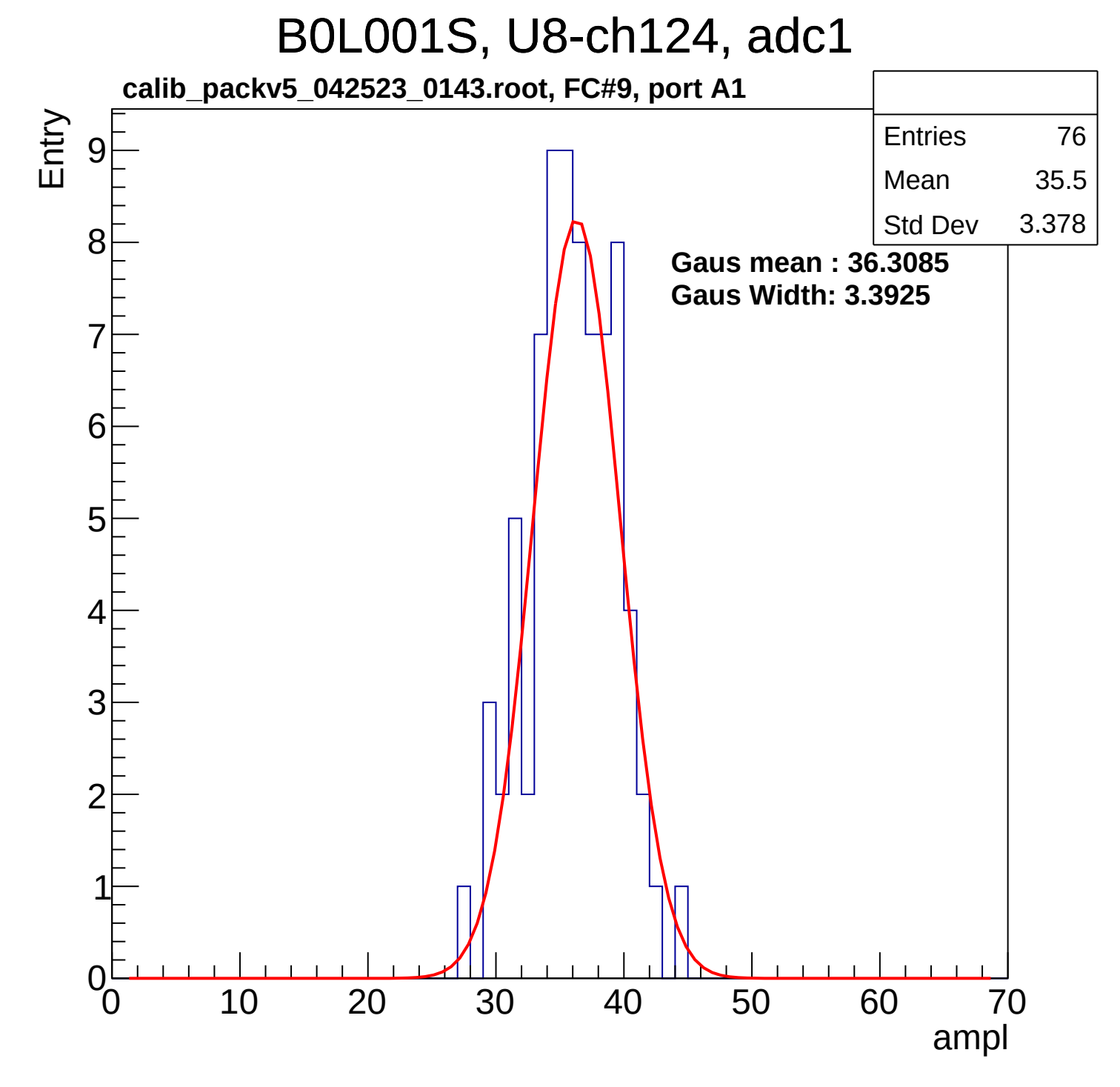
B0L001S, U8-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 35.5
Std Dev 3.378
Gaus mean : 36.3085
Gaus Width: 3.3925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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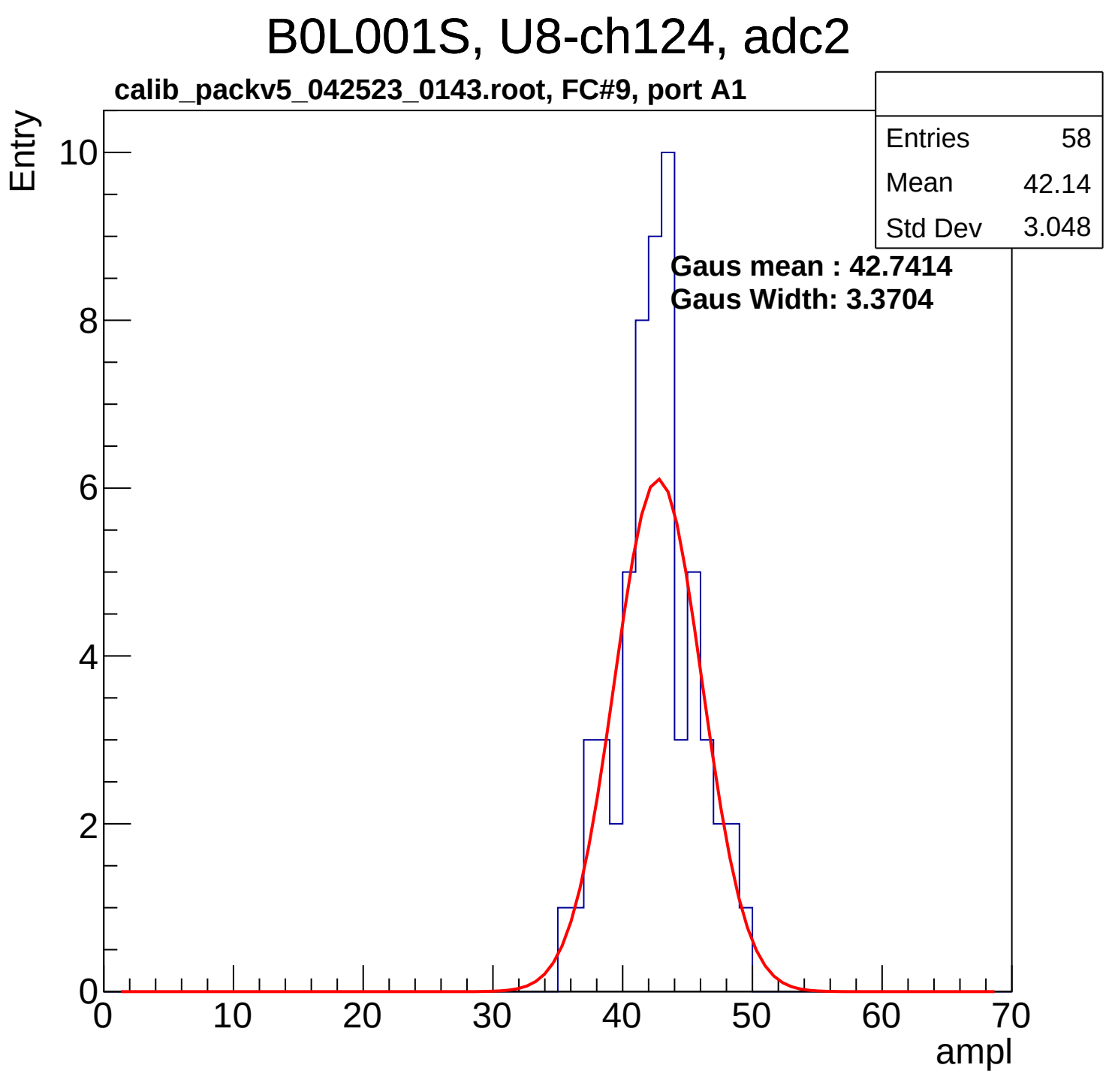
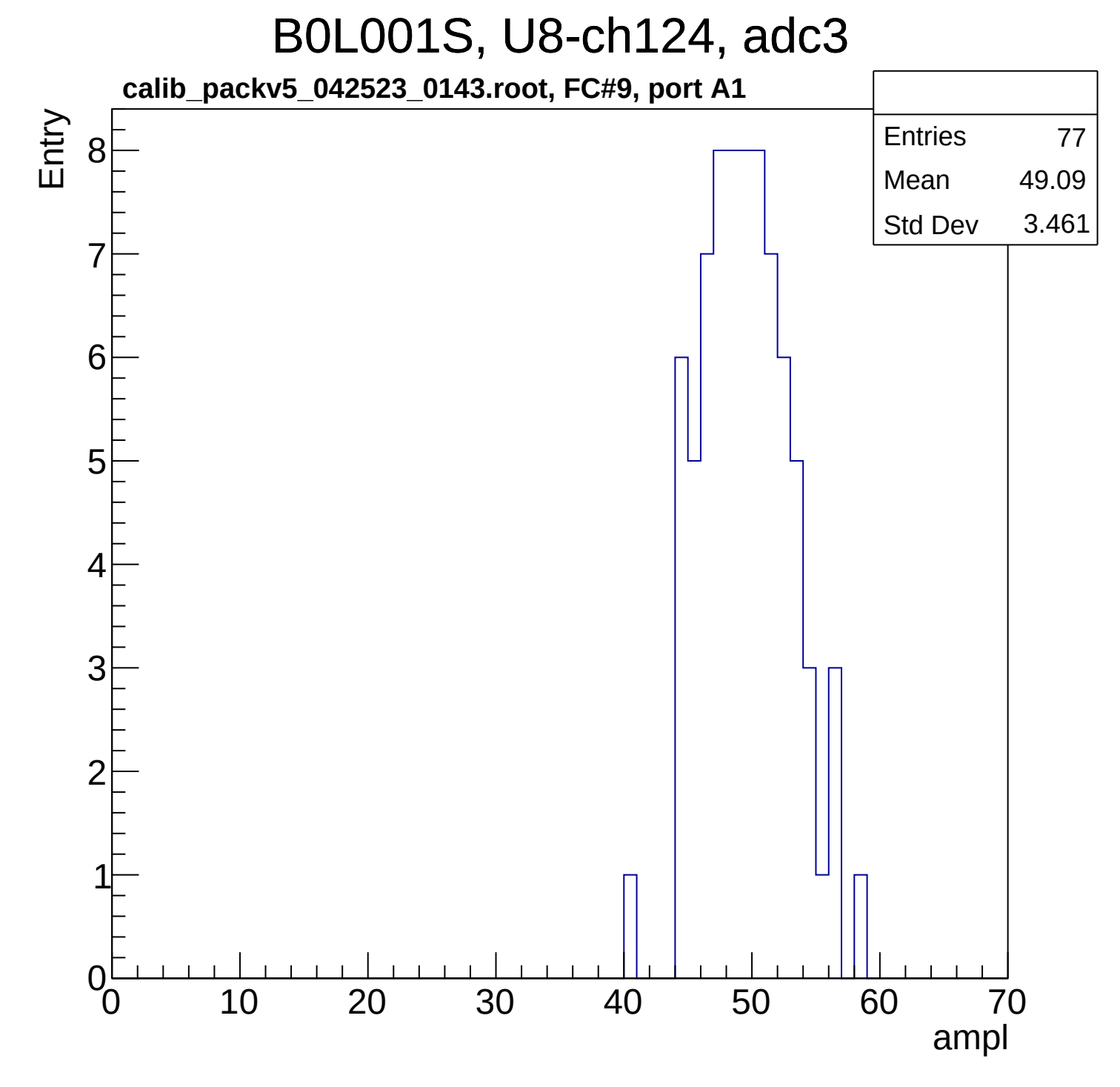

B0L001S, U8-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 42.14
Std Dev 3.048
Gaus mean : 42.7414
Gaus Width: 3.3704
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.09
Std Dev 3.461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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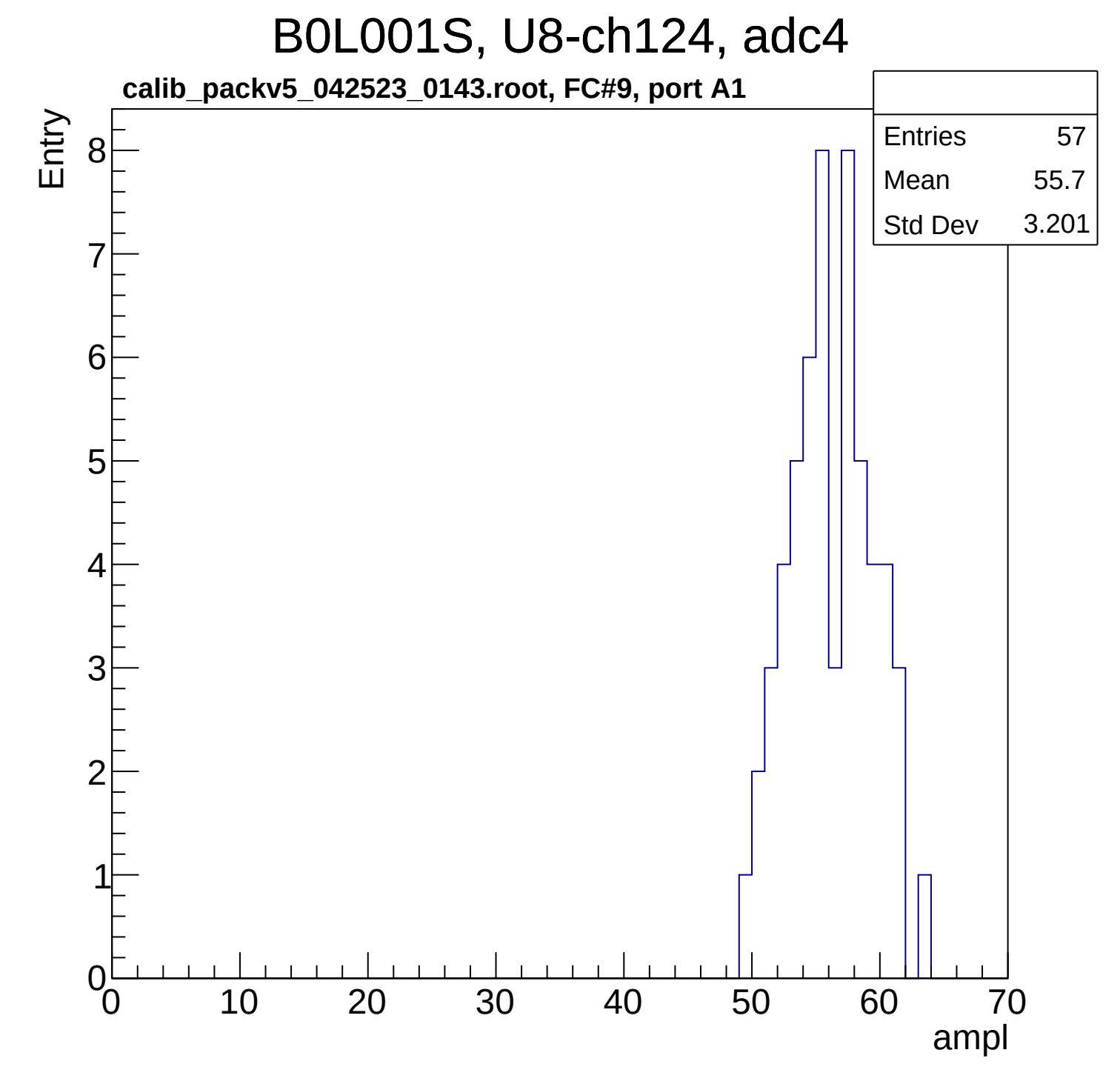
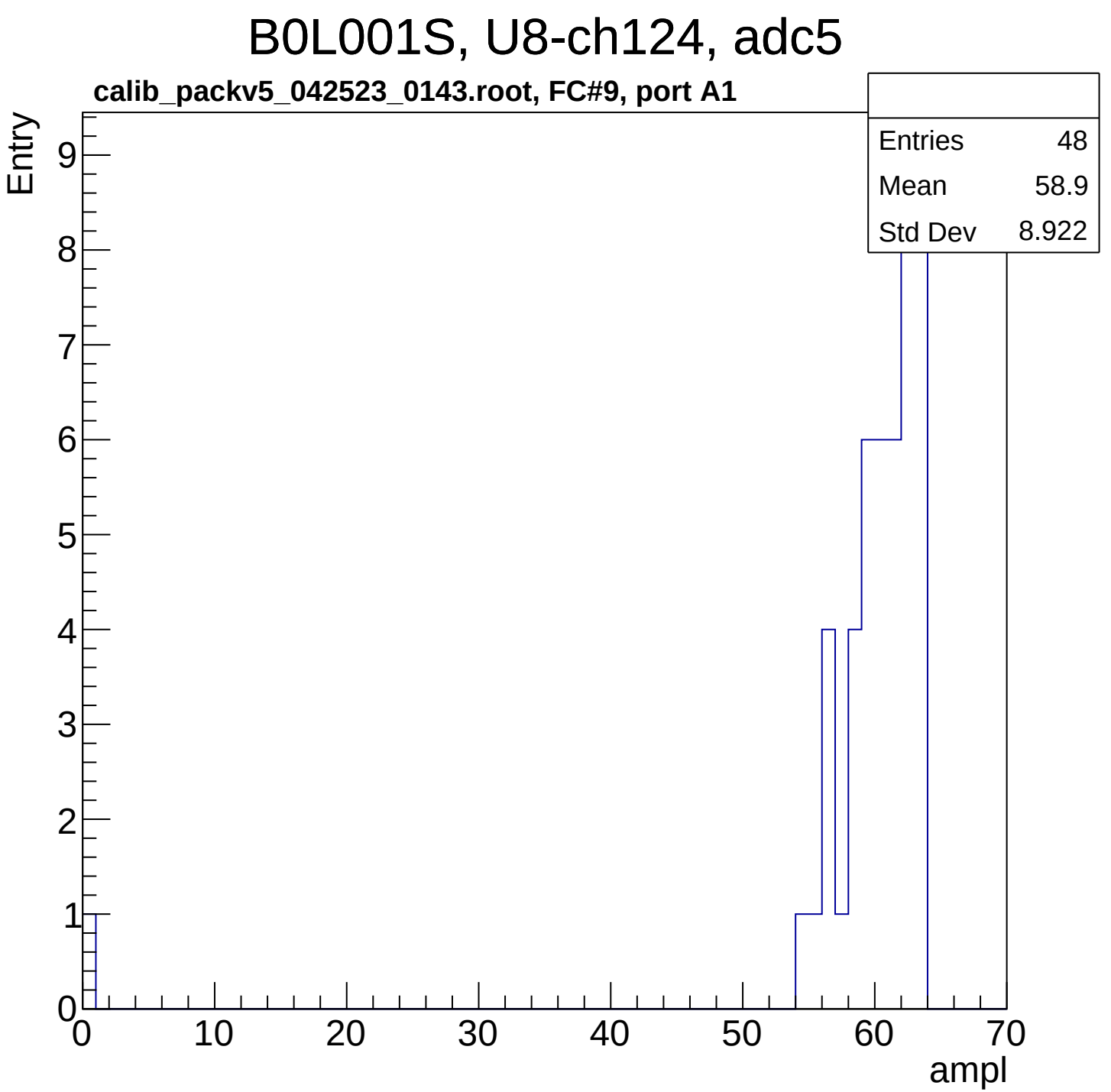
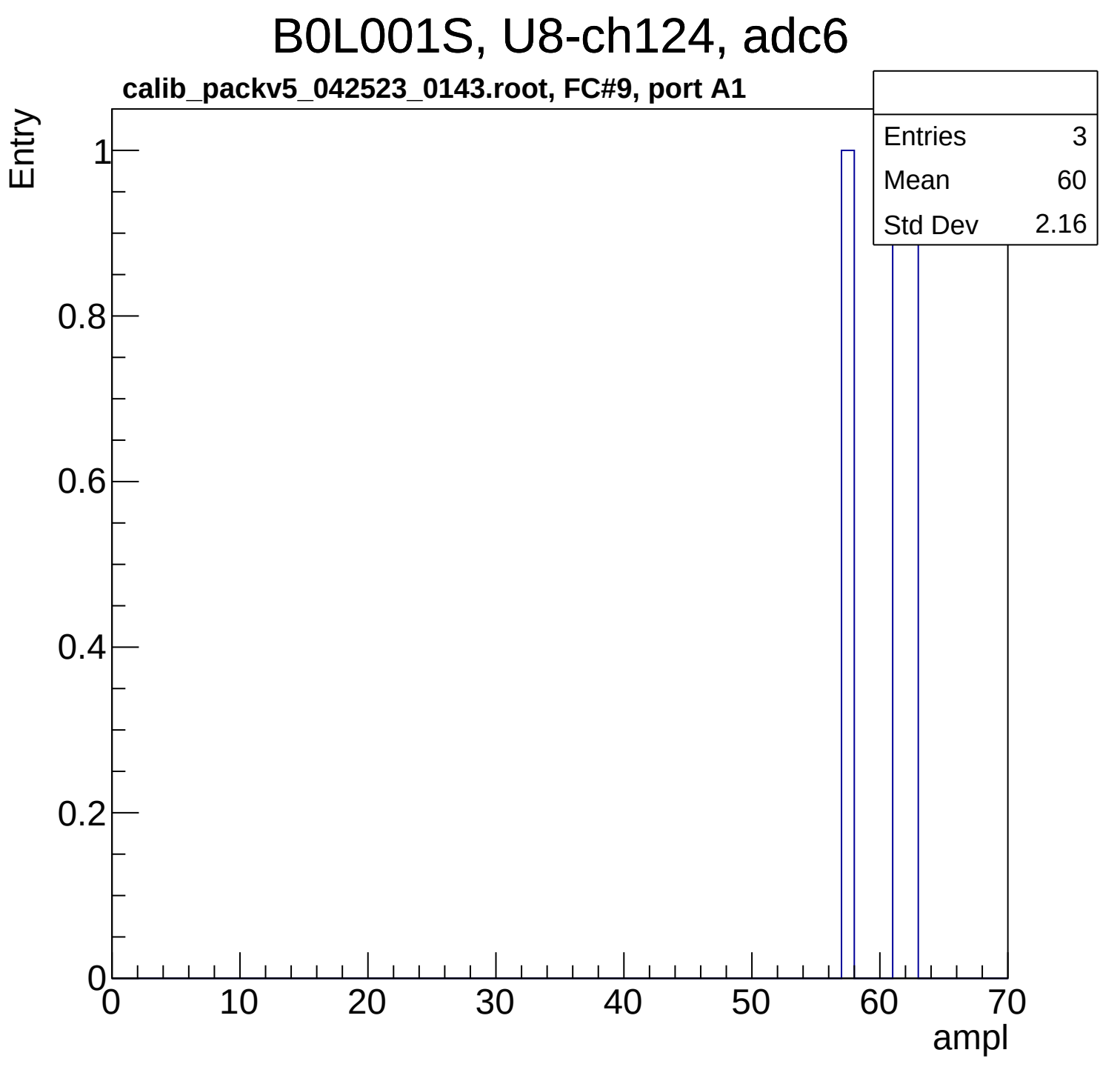
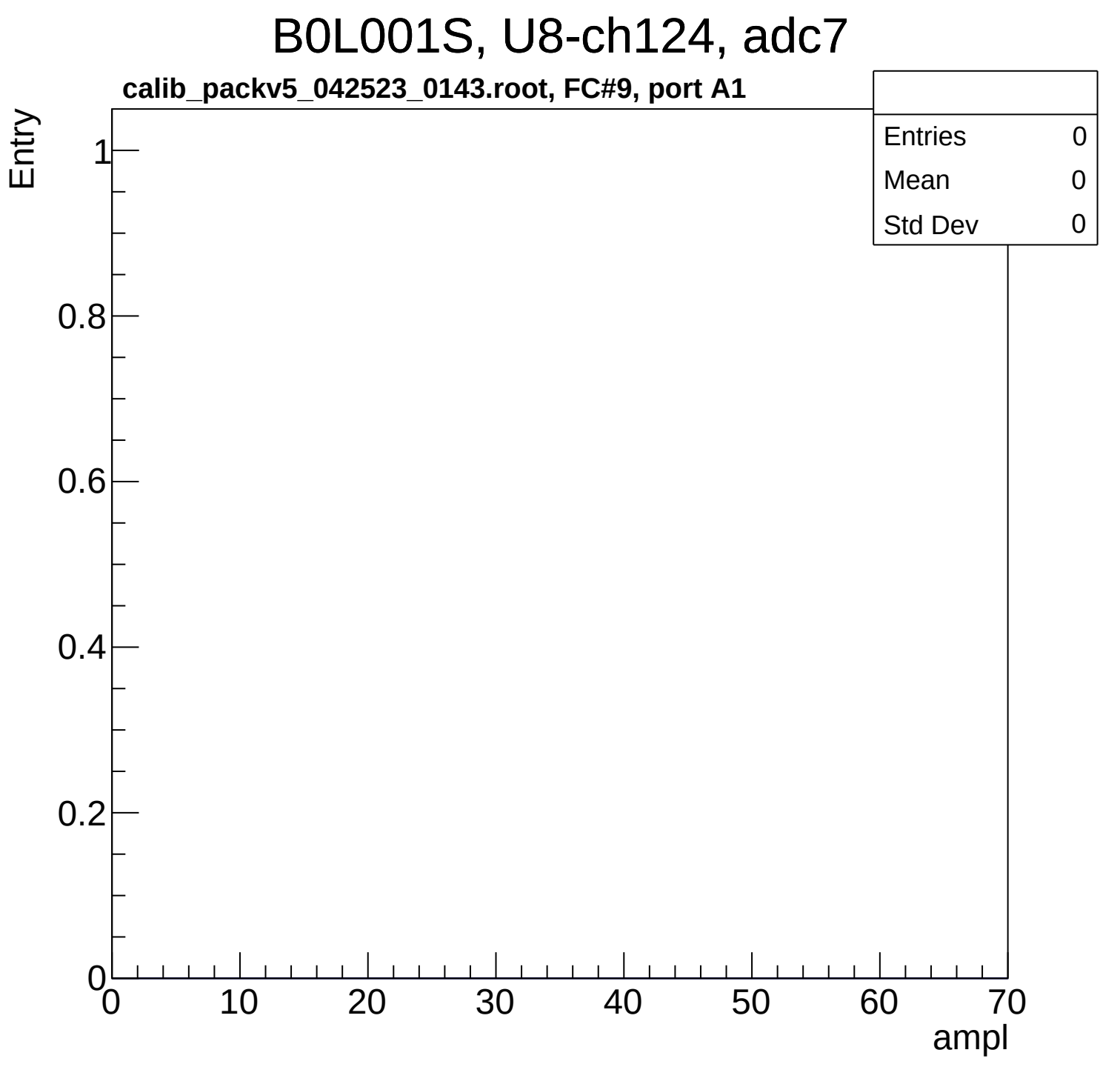
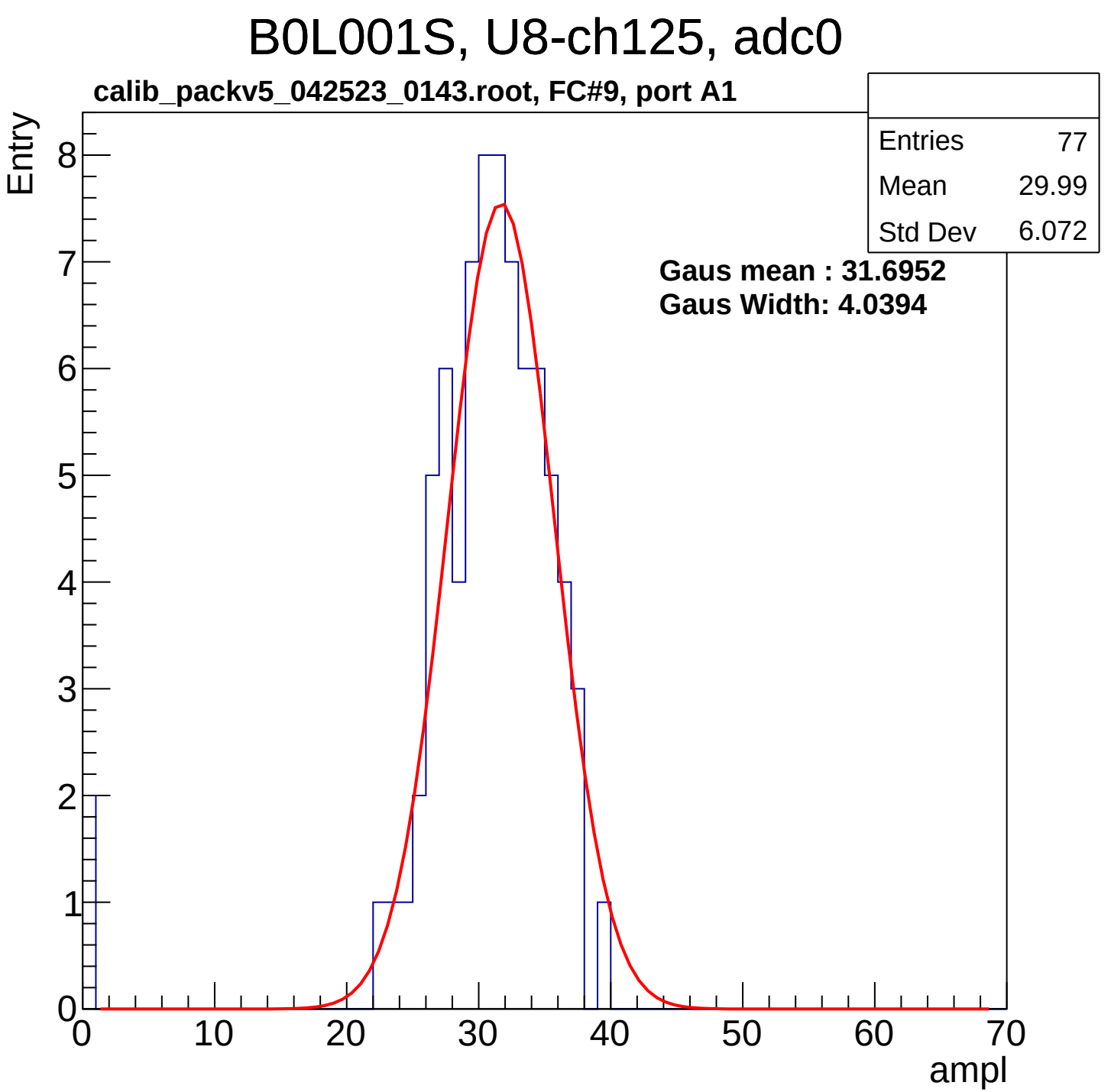
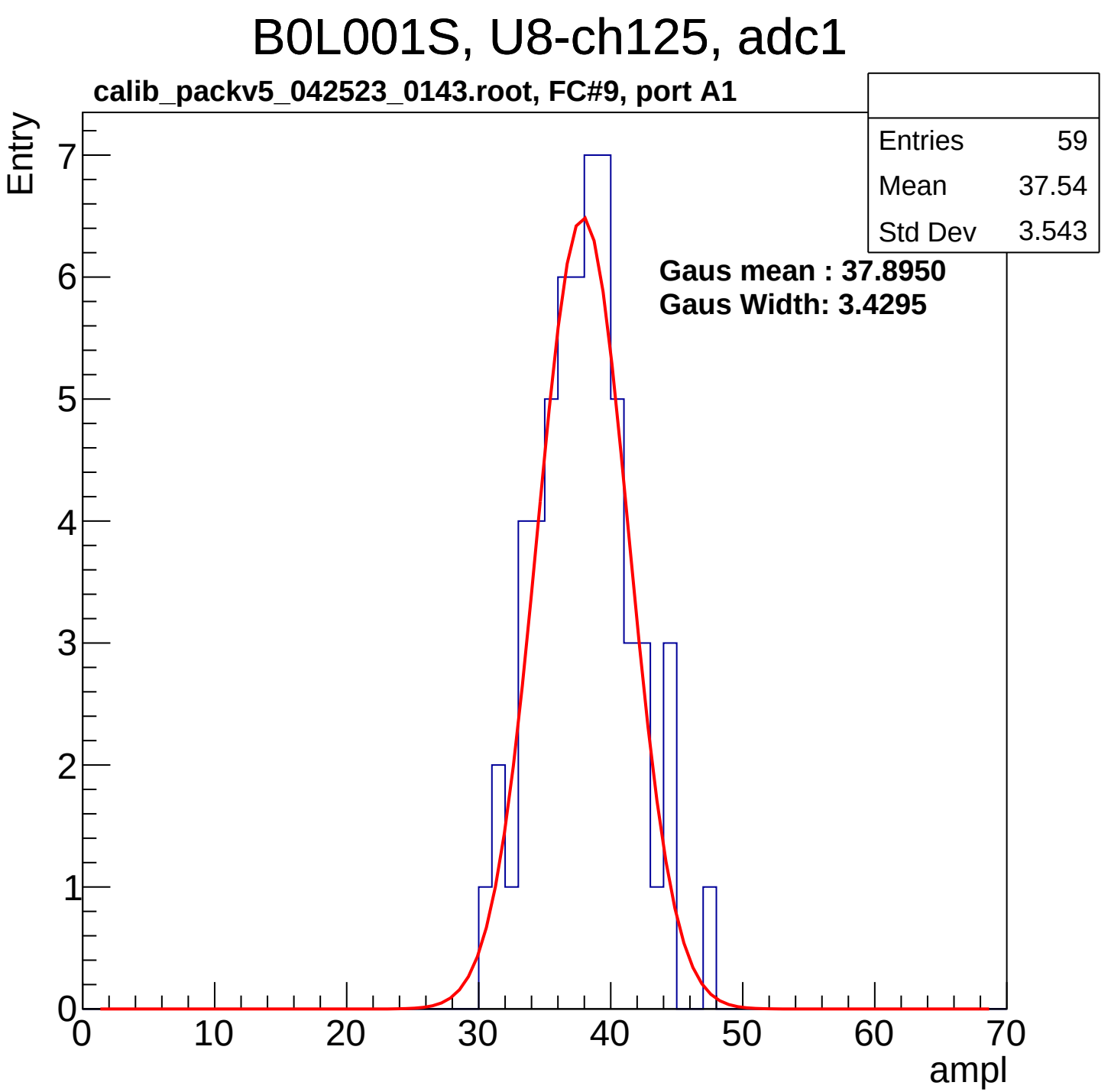

B0L001S, U8-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.7
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

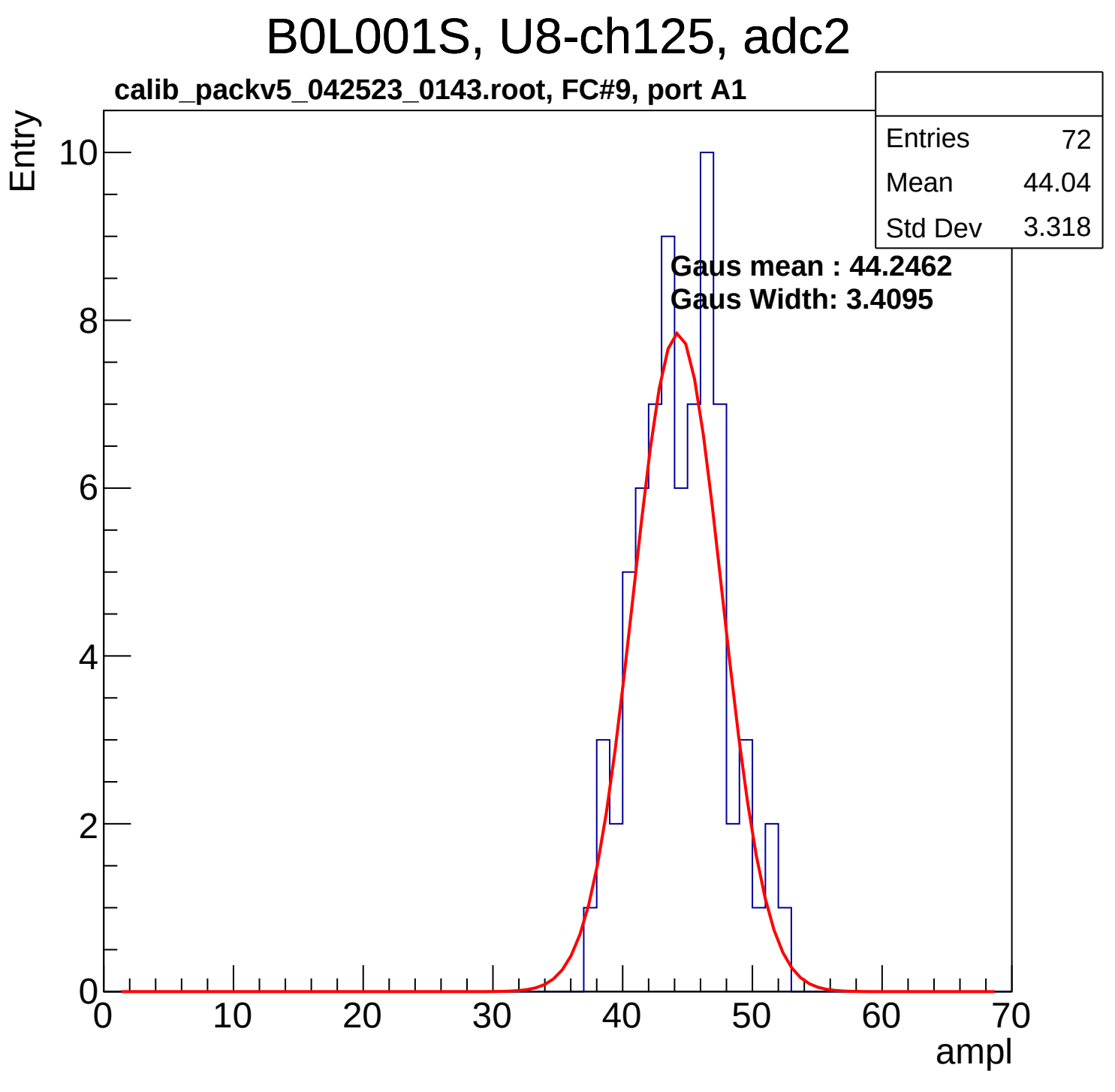

B0L001S, U8-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.9
Std Dev 8.922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

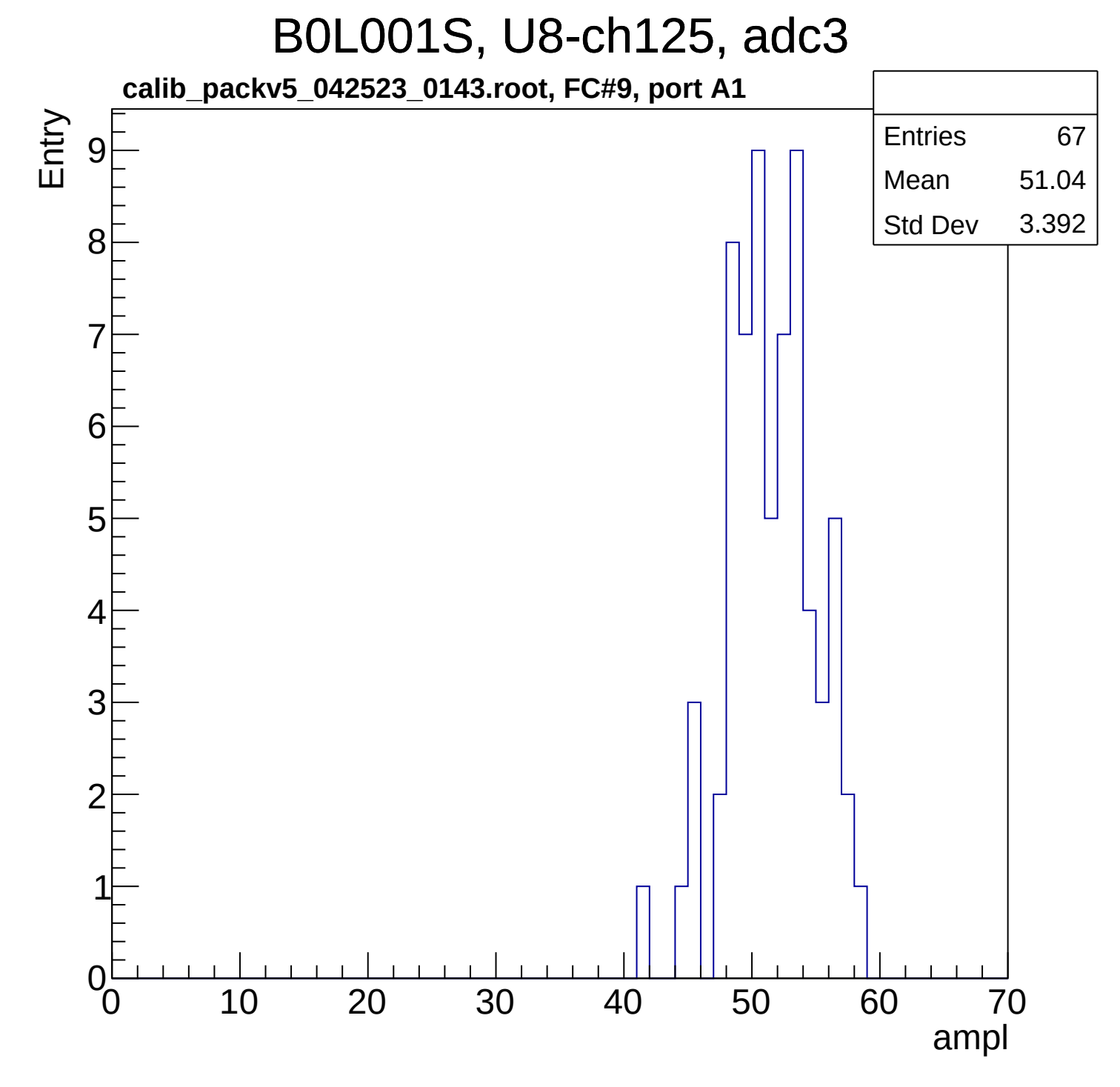

B0L001S, U8-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 60
Std Dev 2.16
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

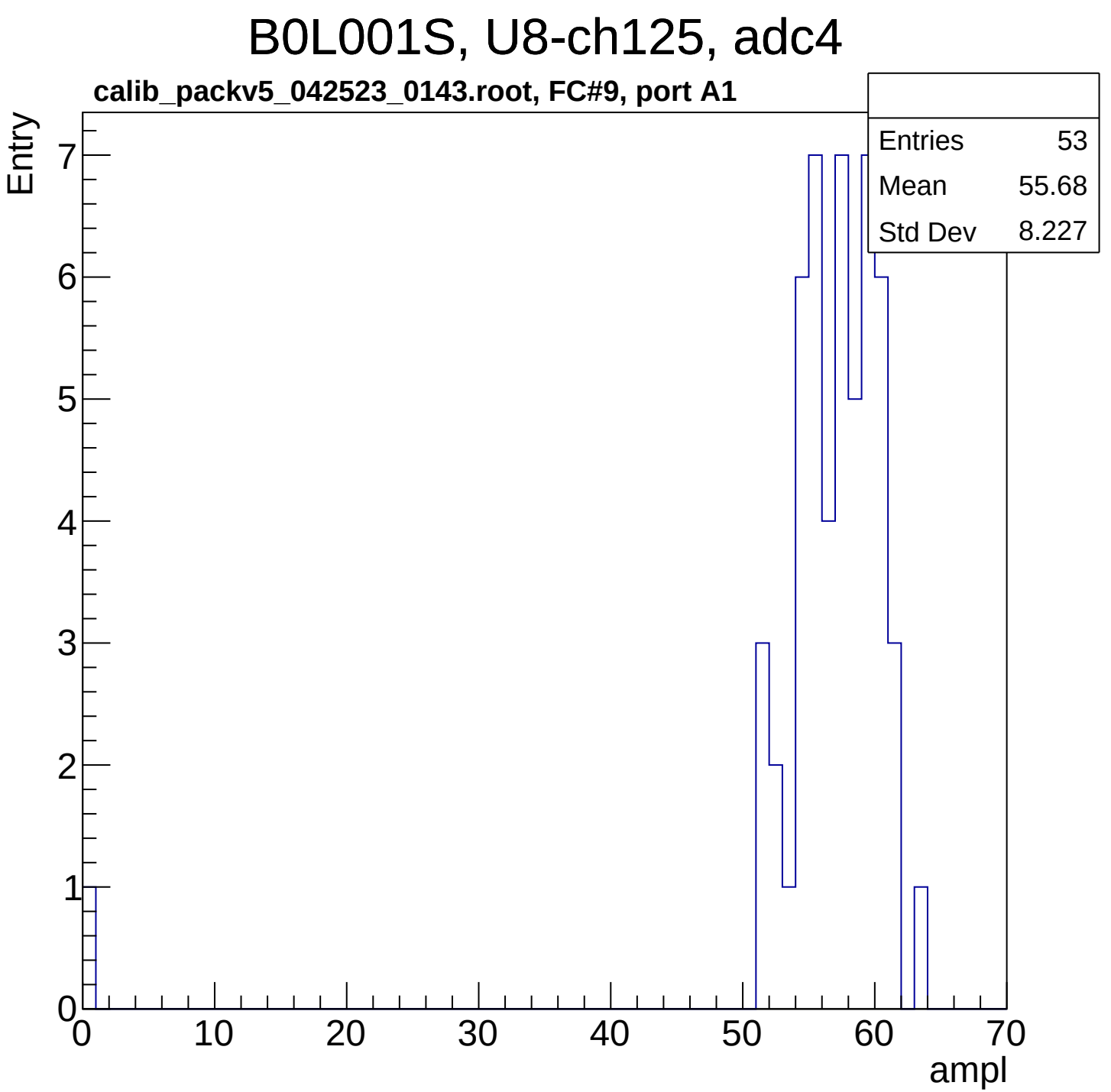

B0L001S, U8-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

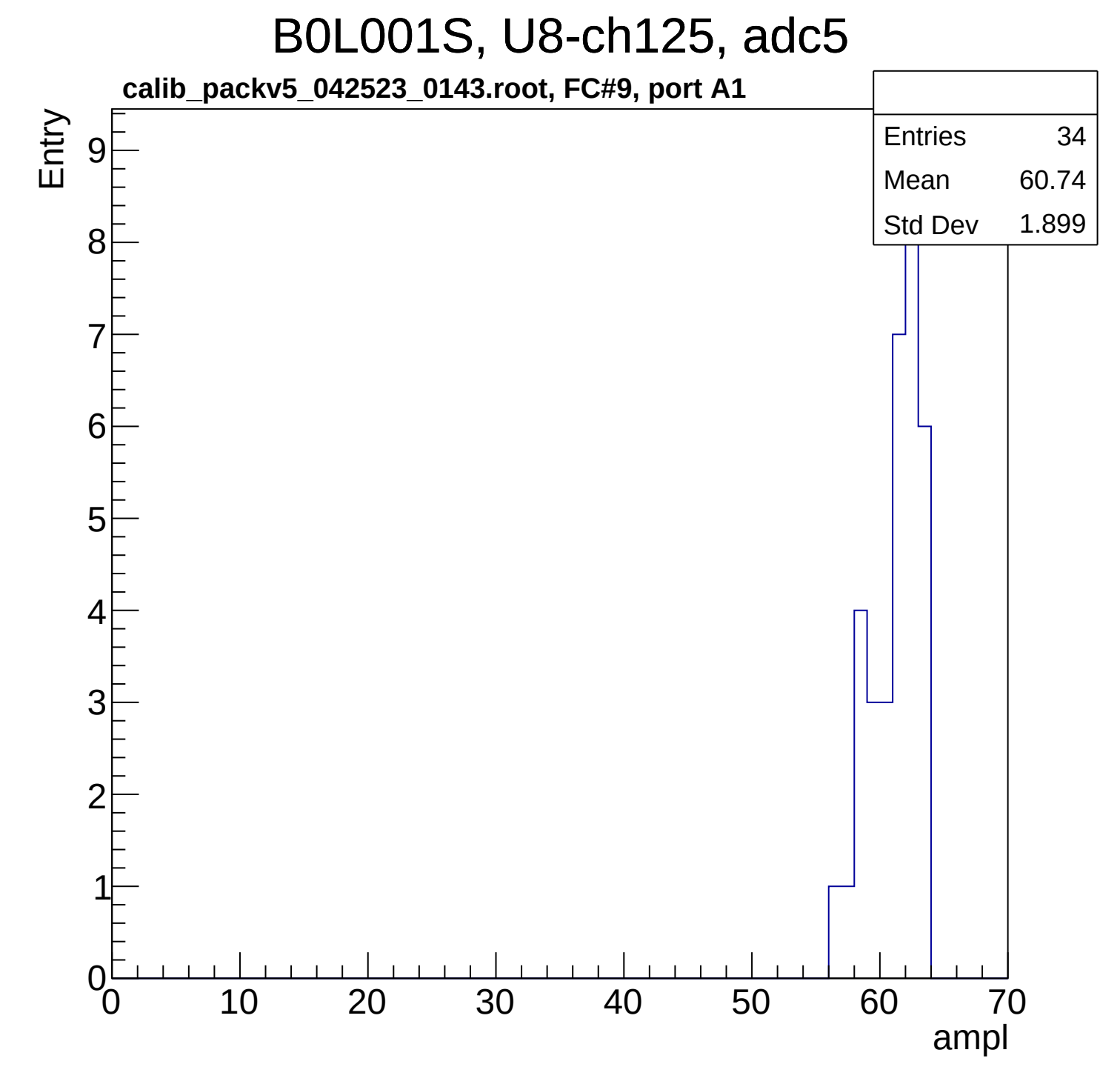

B0L001S, U8-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.99
Std Dev 6.072
Gaus mean : 31.6952
Gaus Width: 4.0394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

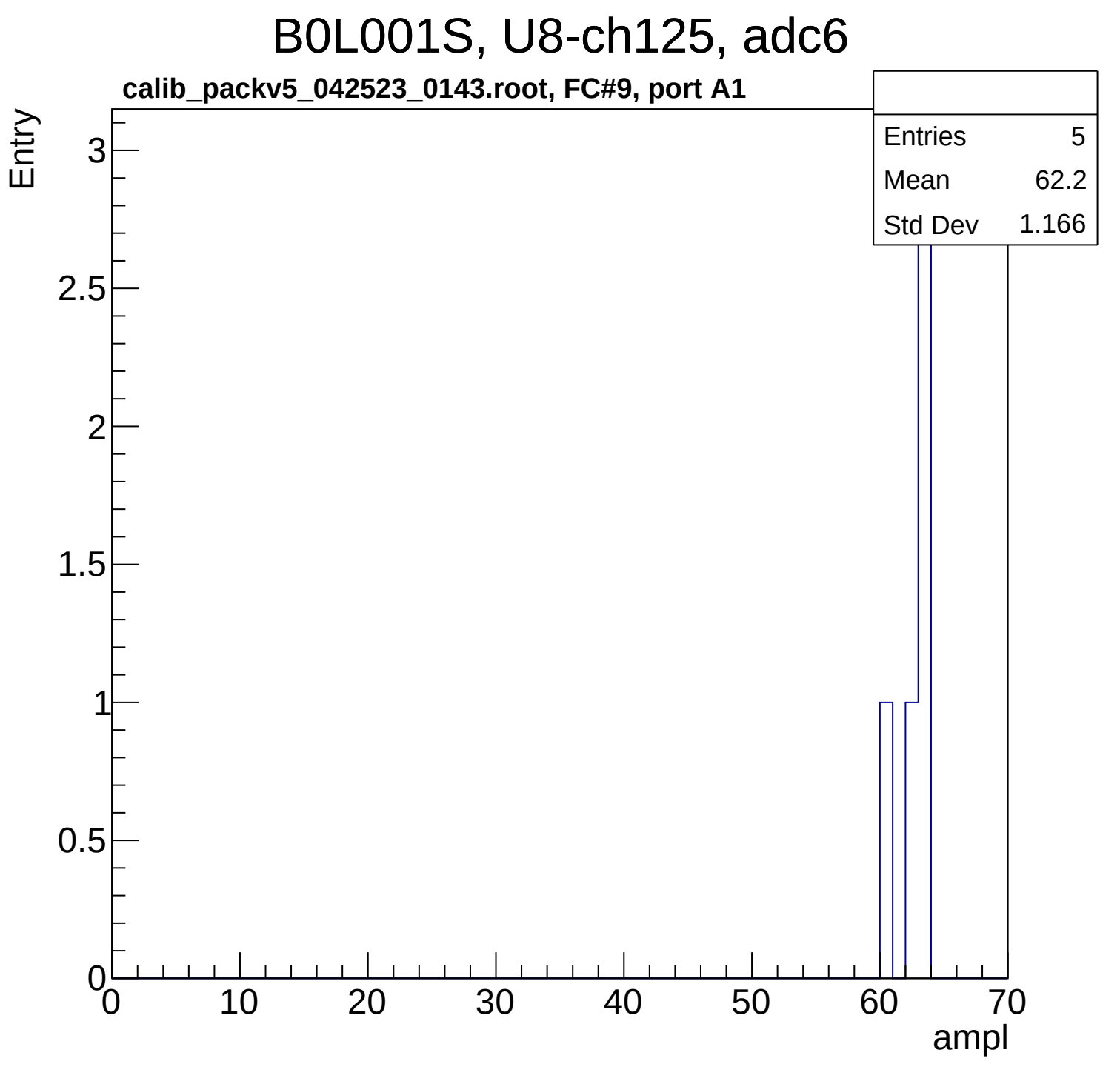

B0L001S, U8-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.54
Std Dev 3.543
Gaus mean : 37.8950
Gaus Width: 3.4295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.04
Std Dev 3.318
Gaus mean : 44.2462
Gaus Width: 3.4095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.04
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.68
Std Dev 8.227
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.74
Std Dev 1.899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
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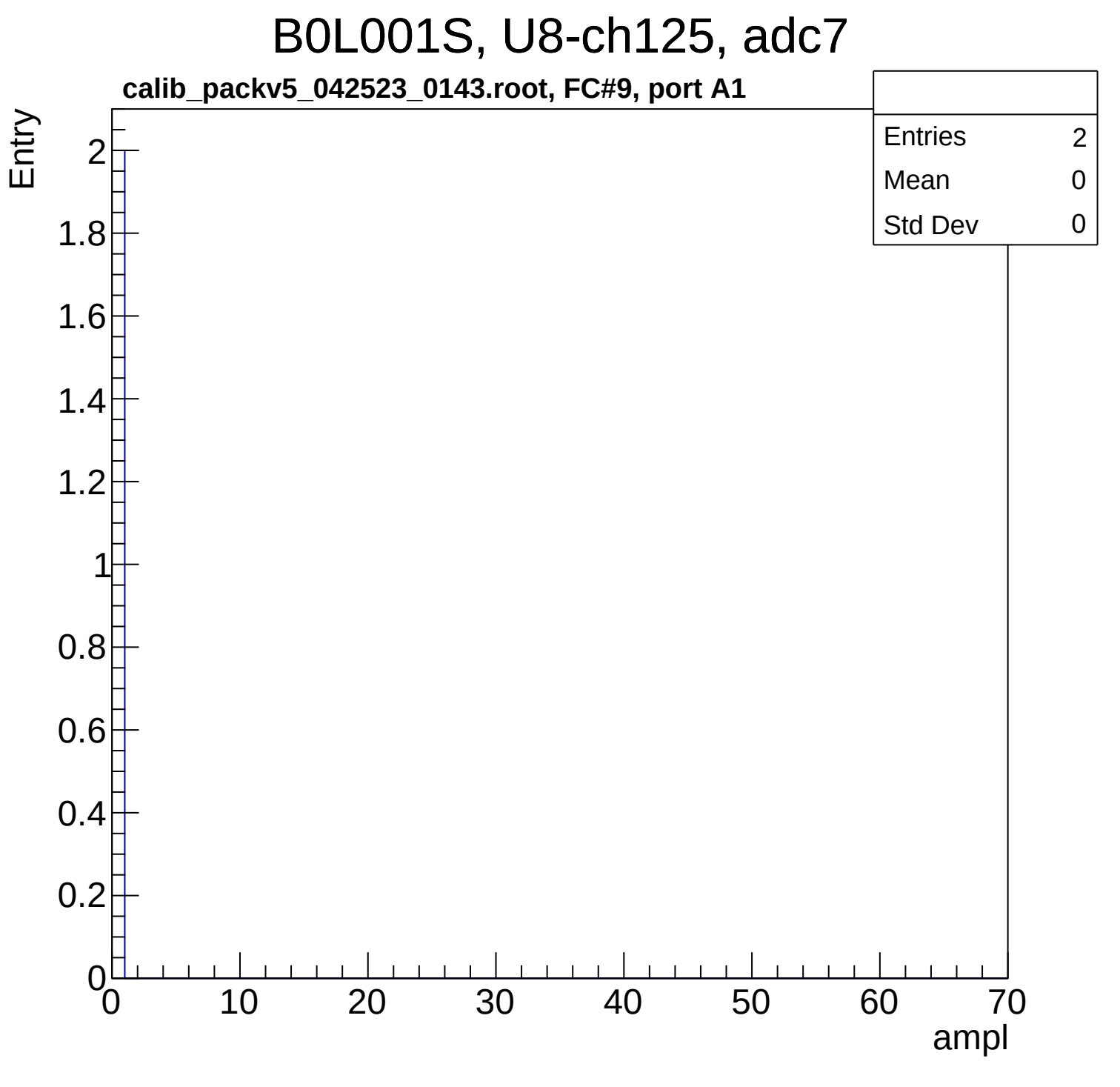
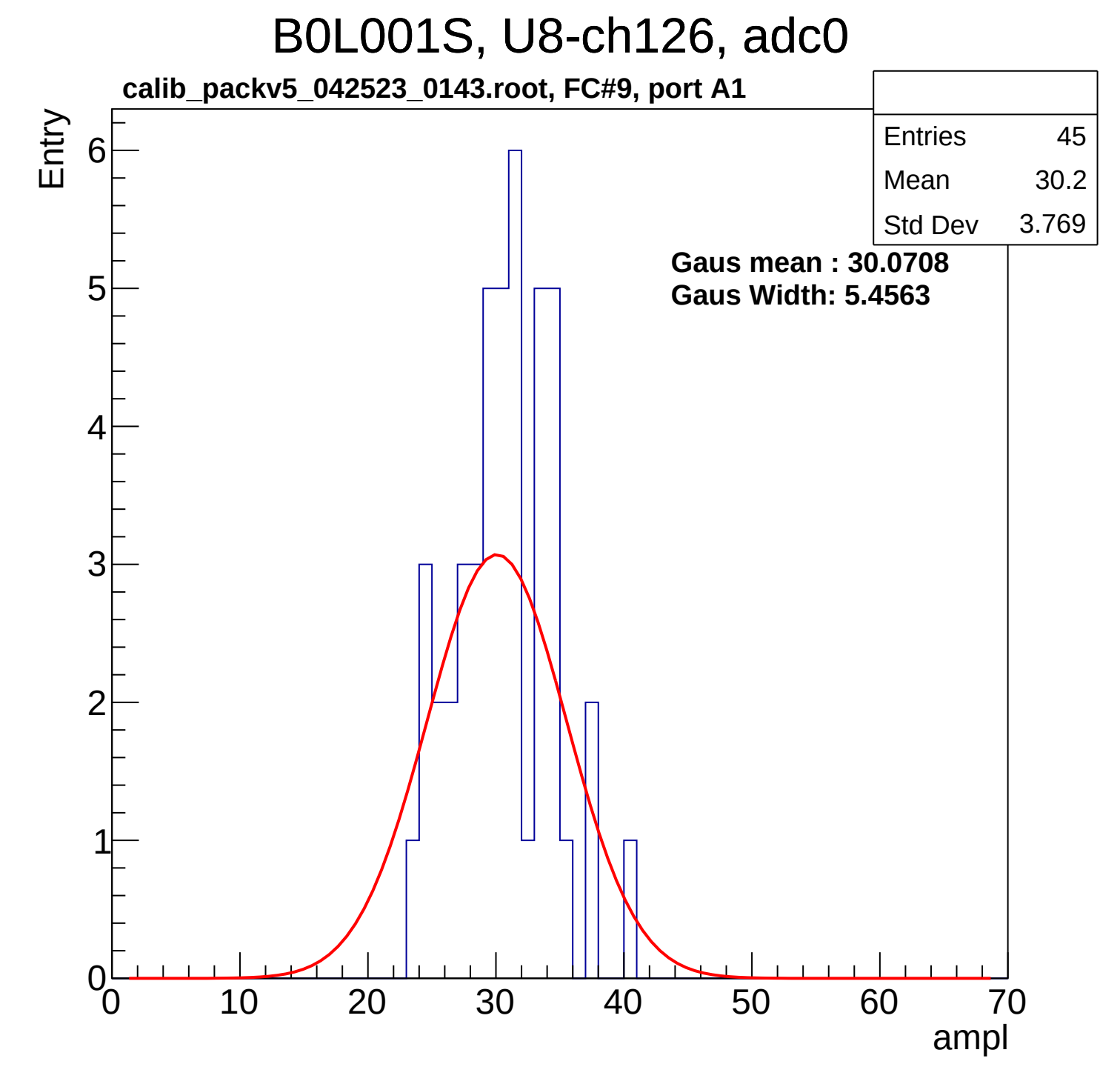
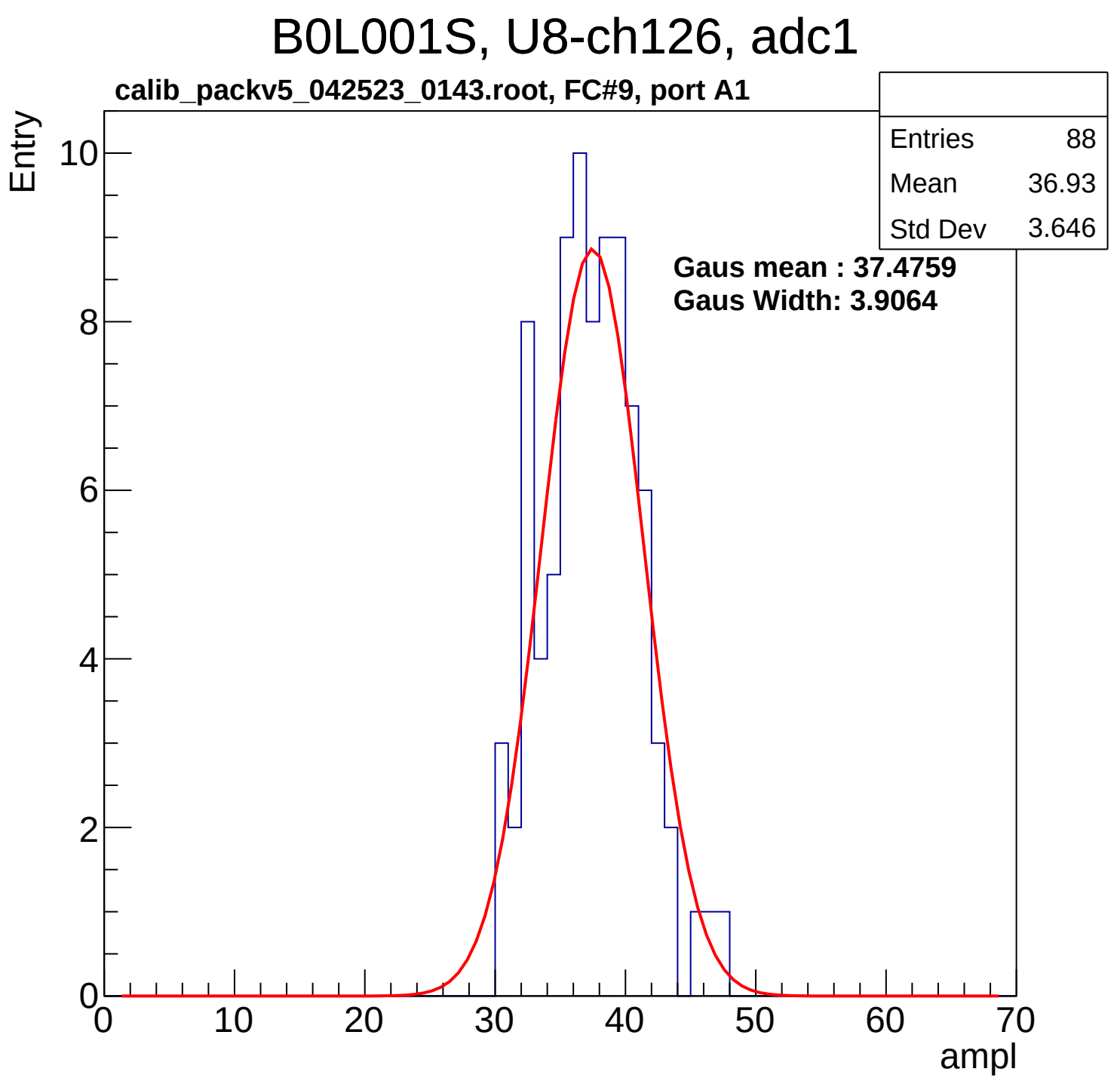
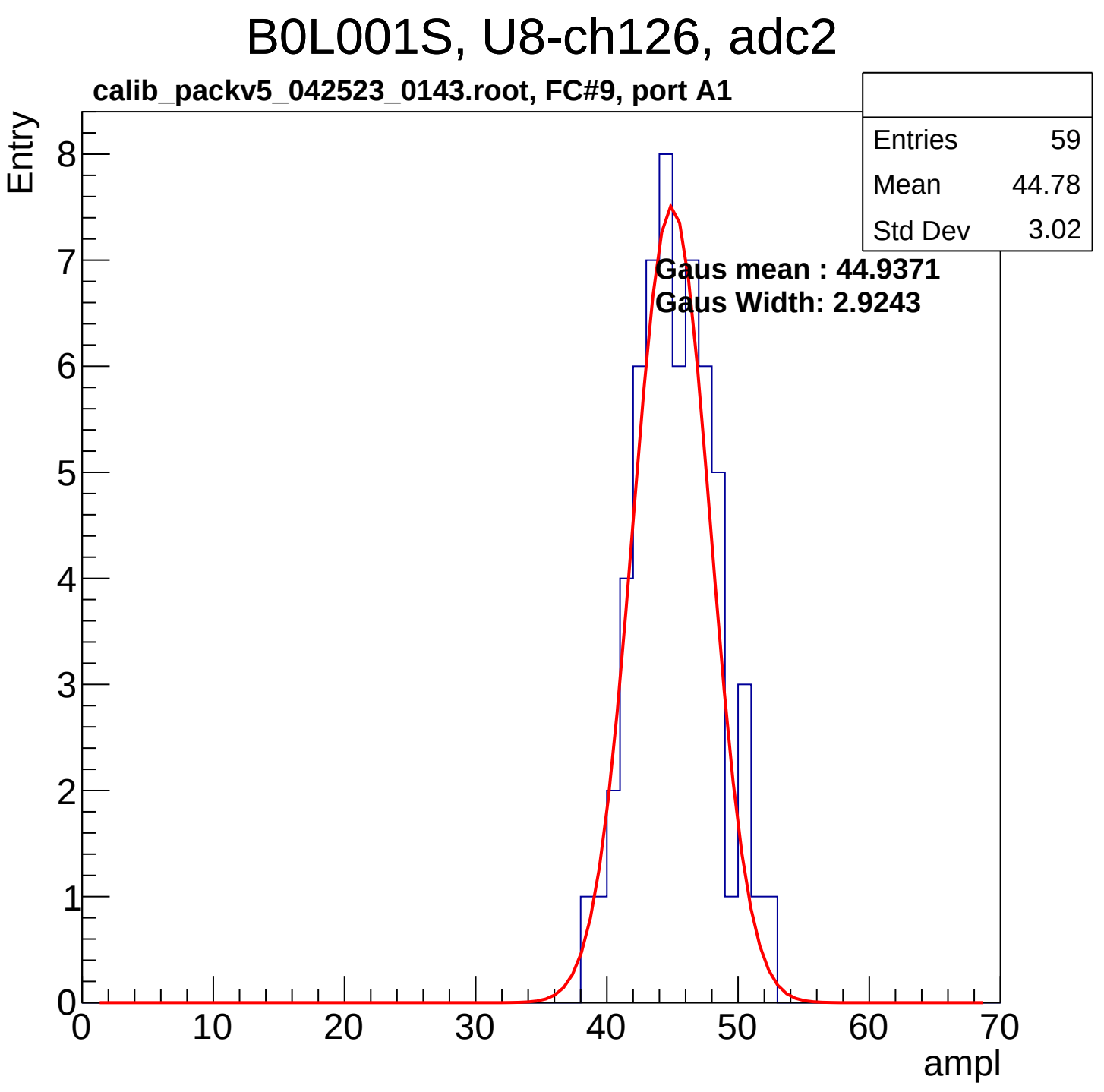
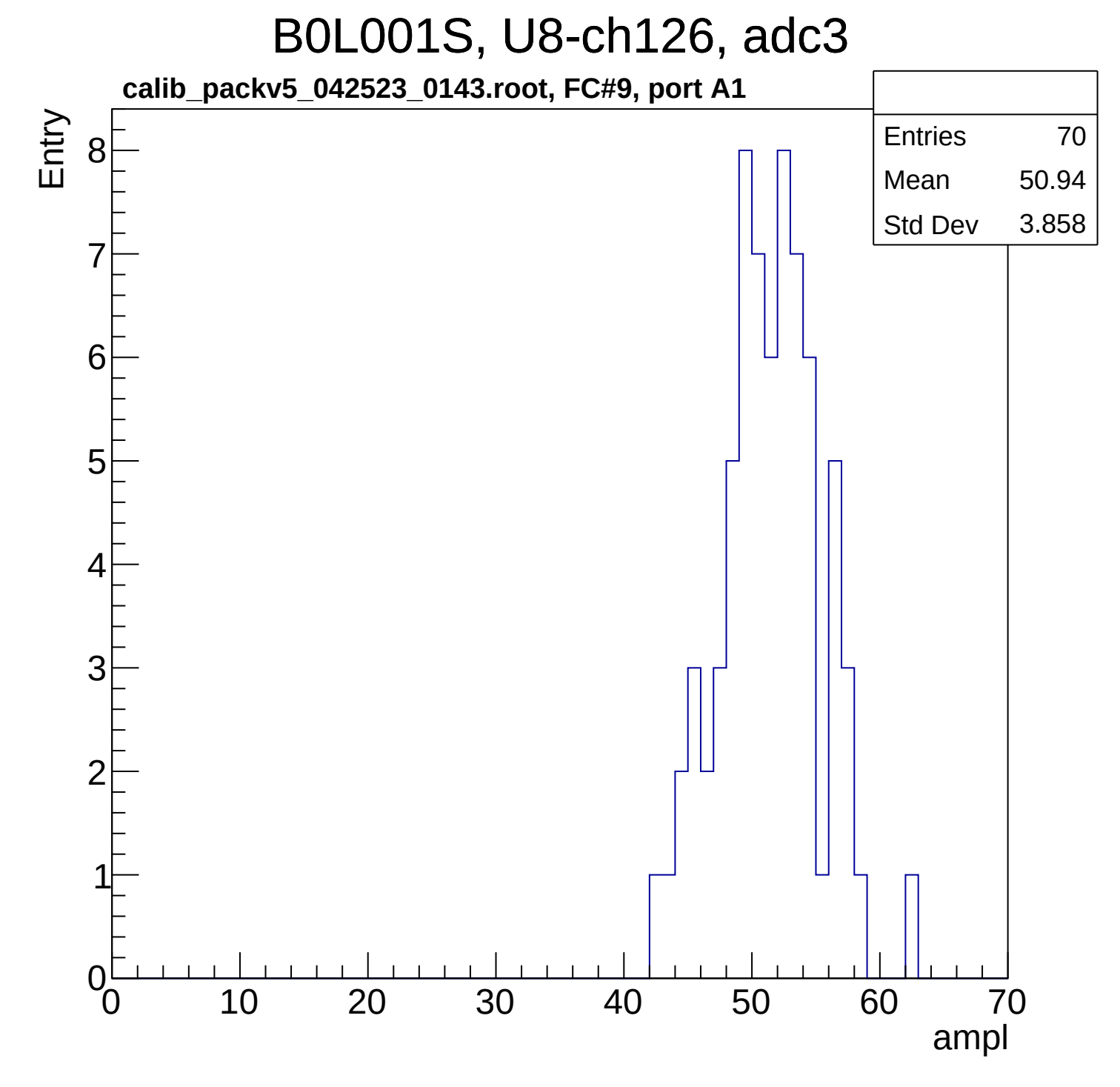
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 30.2
Std Dev 3.769
Gaus mean : 30.0708
Gaus Width: 5.4563
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 36.93
Std Dev 3.646
Gaus mean : 37.4759
Gaus Width: 3.9064
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.78
Std Dev 3.02
Gaus mean : 44.9371
Gaus Width: 2.9243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.94
Std Dev 3.858
Entry
ampl
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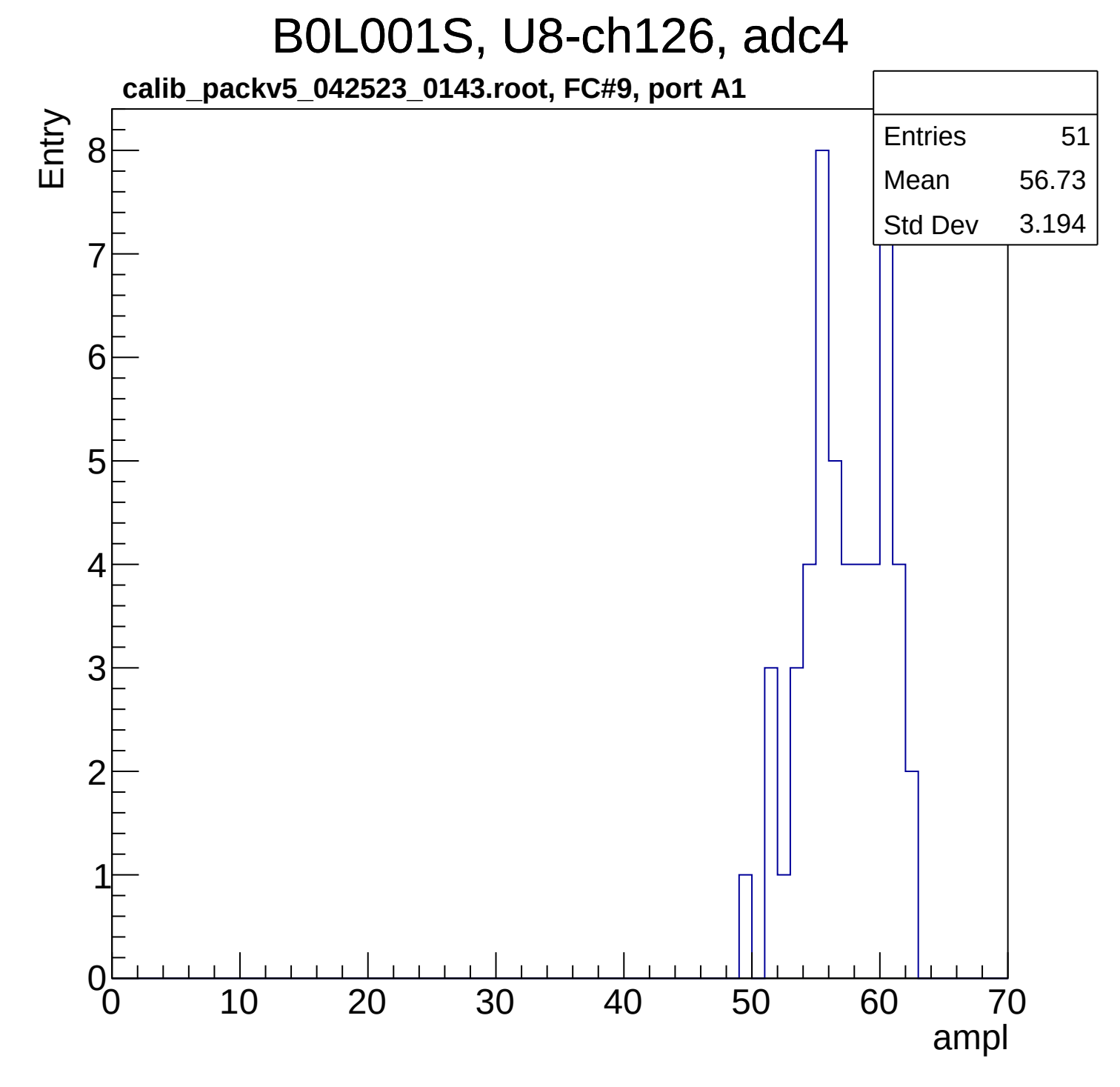
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.73
Std Dev 3.194
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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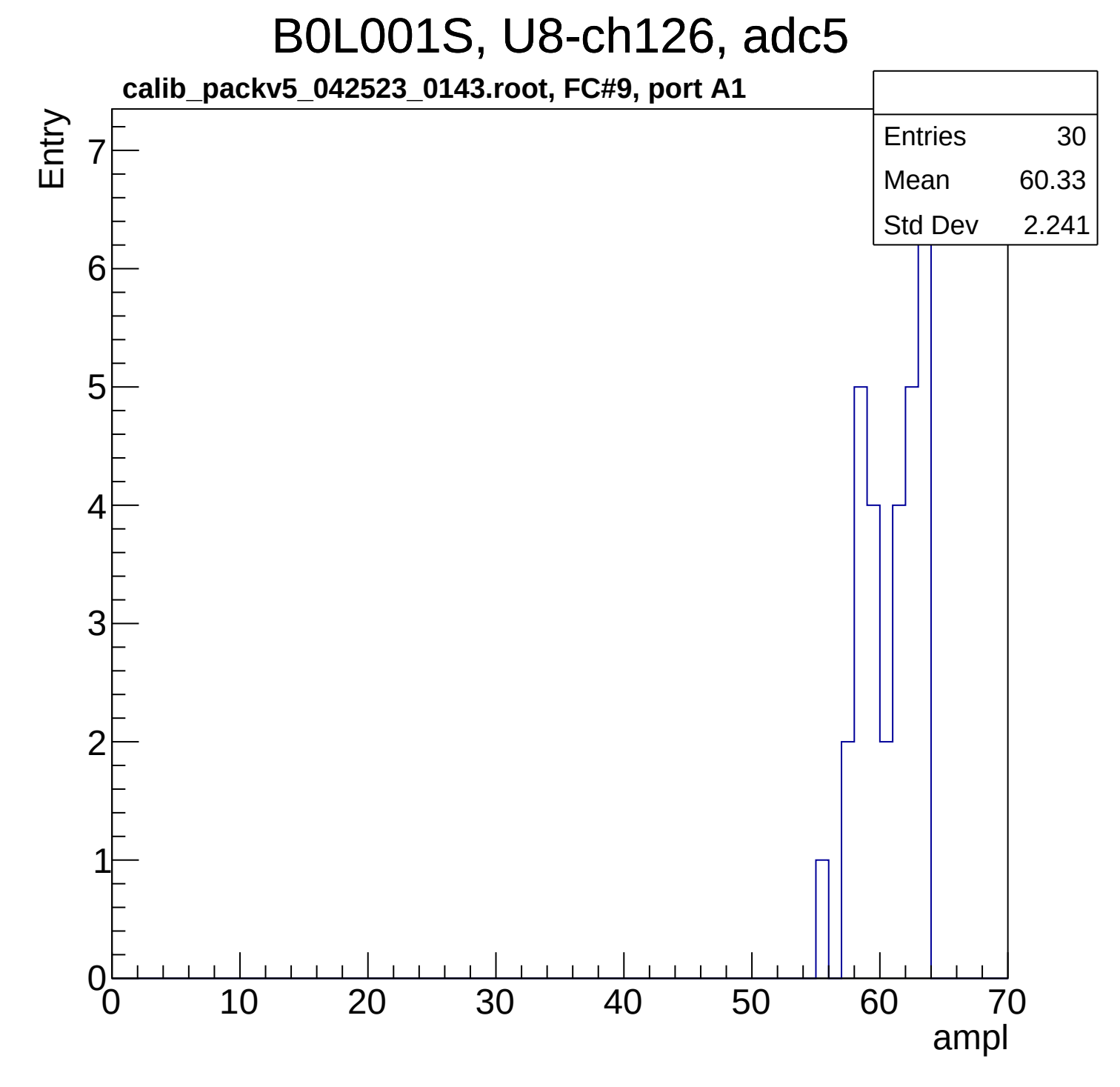
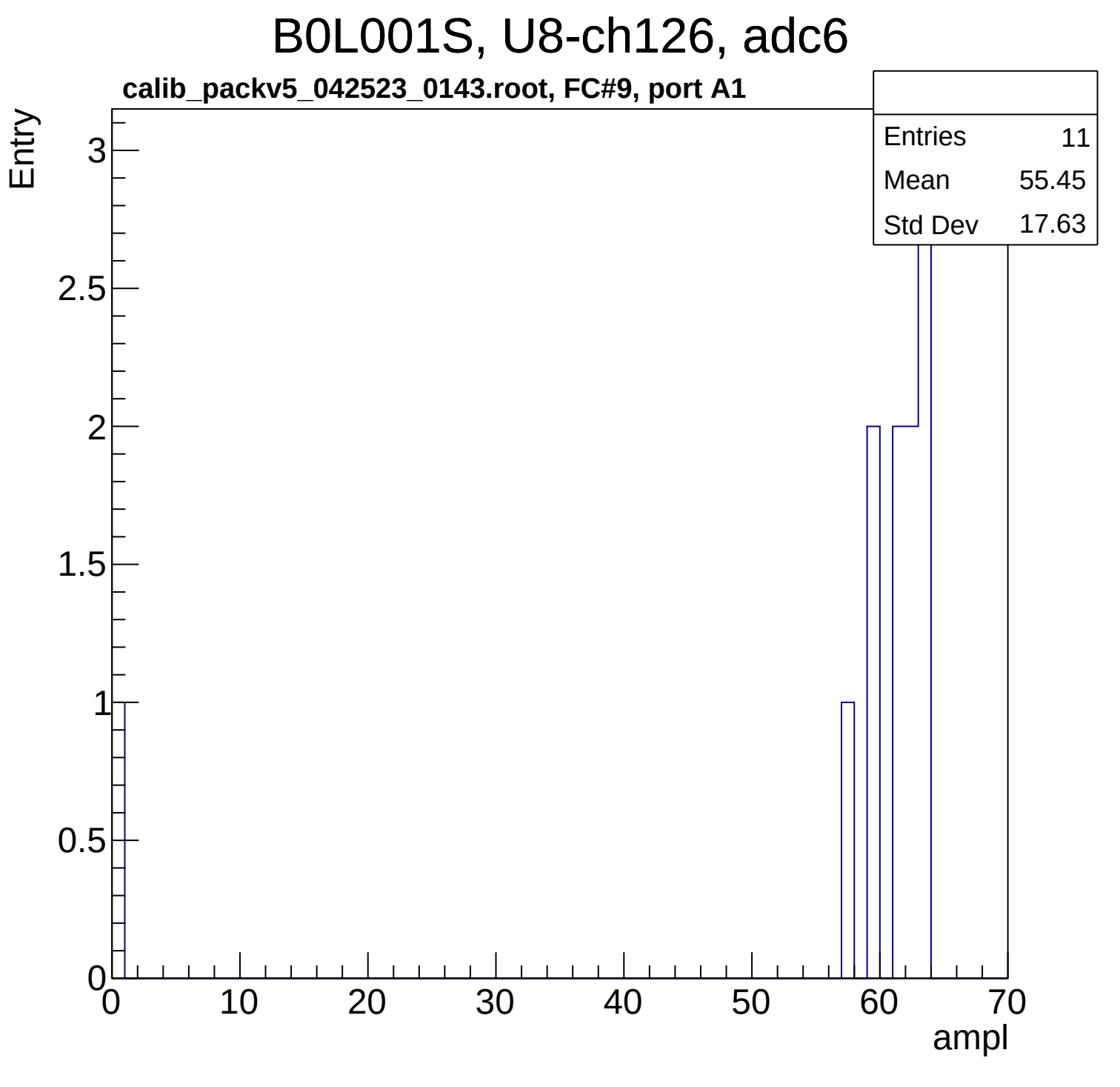

B0L001S, U8-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.33
Std Dev 2.241
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 55.45
Std Dev 17.63
Entry
ampl
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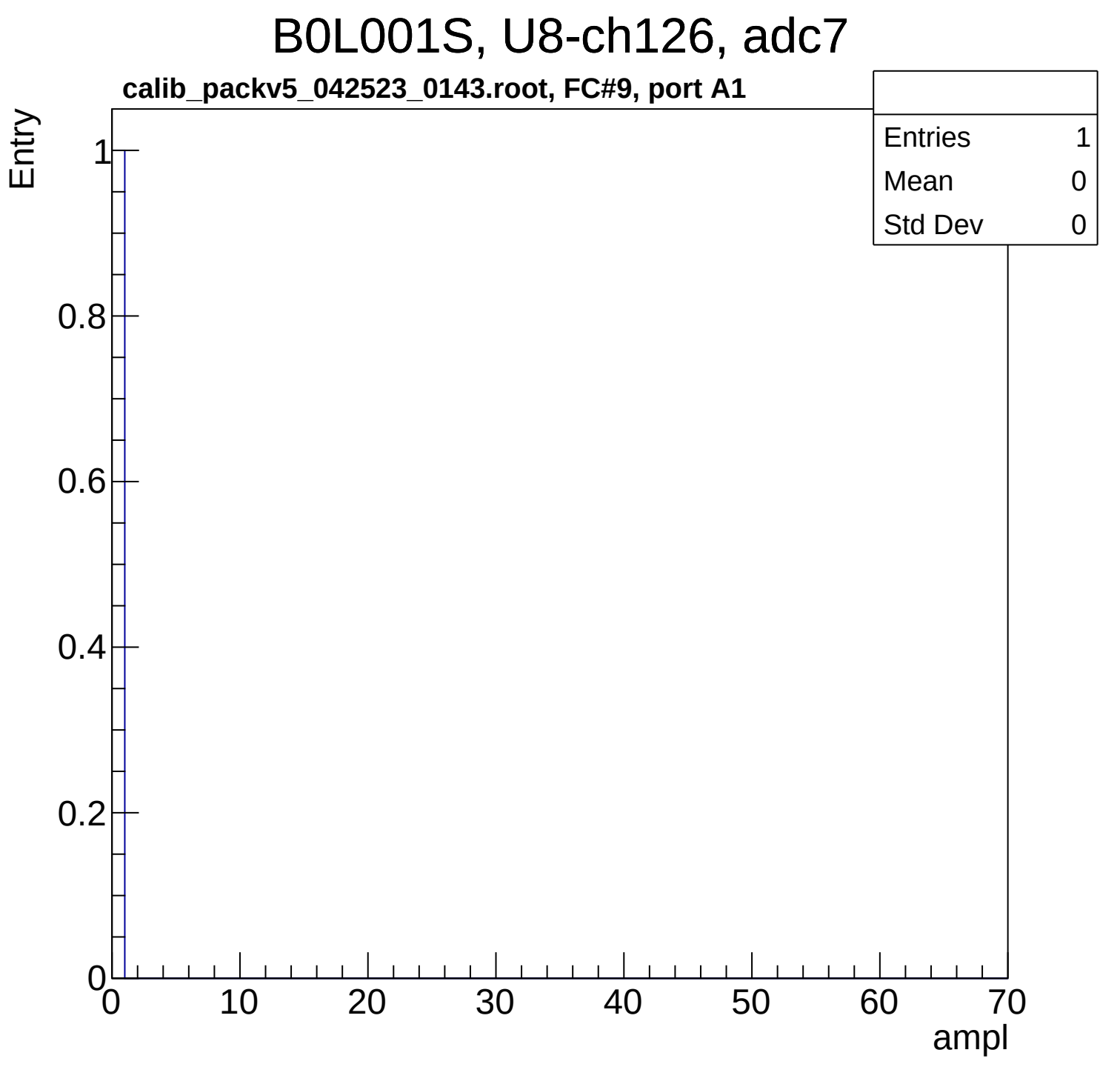
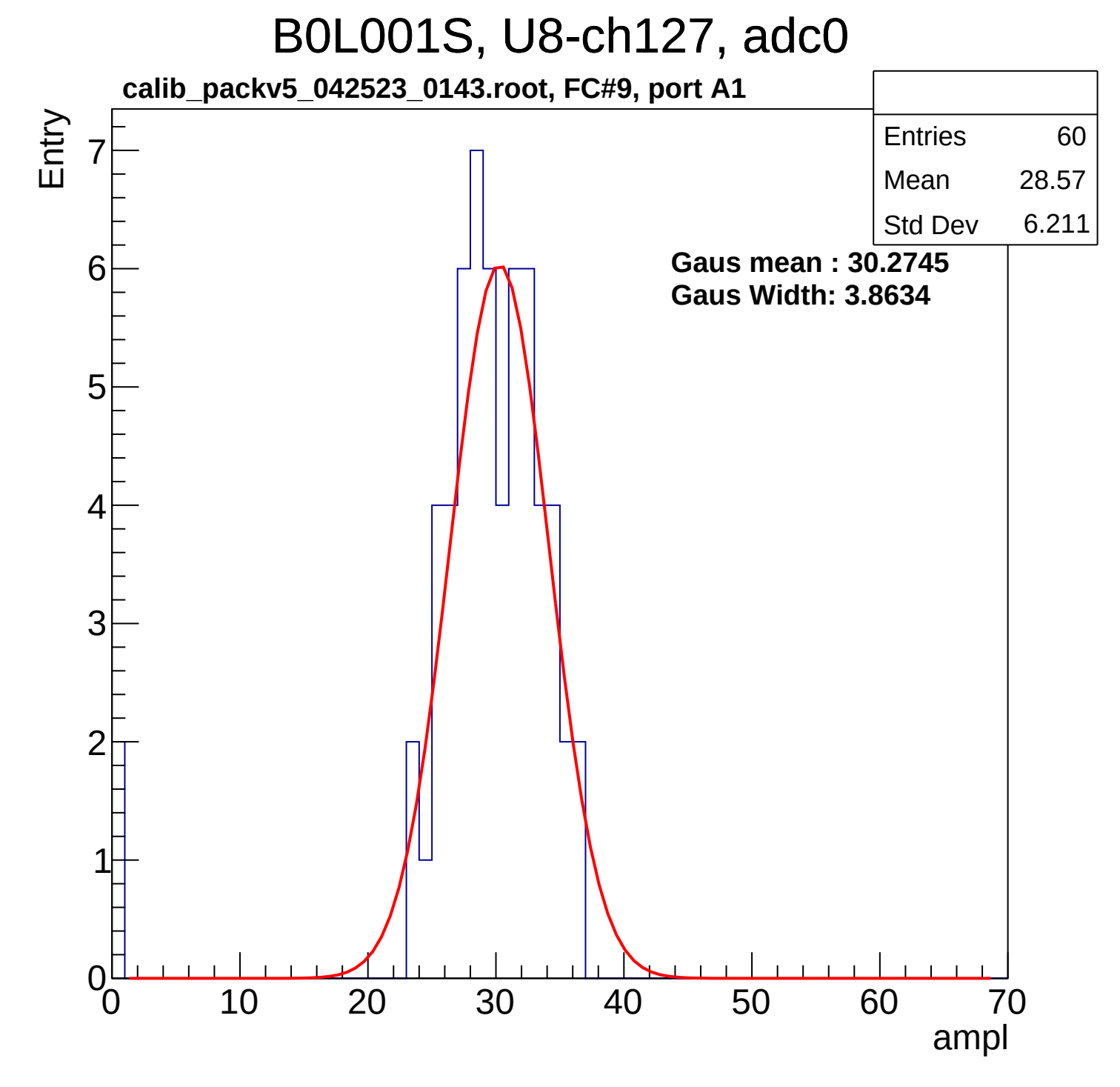
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U8-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 28.57
Std Dev 6.211
Gaus mean : 30.2745
Gaus Width: 3.8634
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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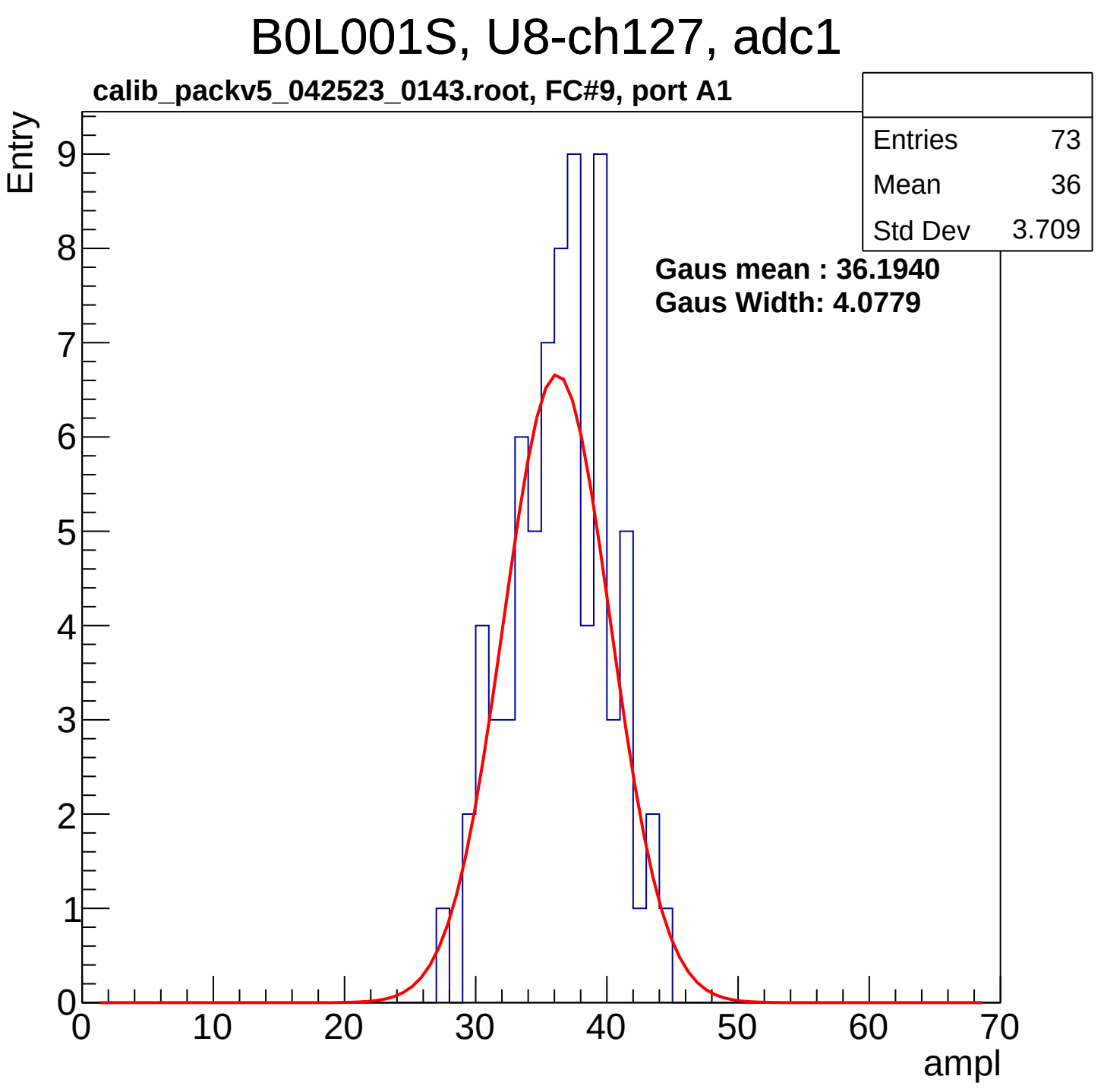
B0L001S, U8-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36
Std Dev 3.709
Gaus mean : 36.1940
Gaus Width: 4.0779
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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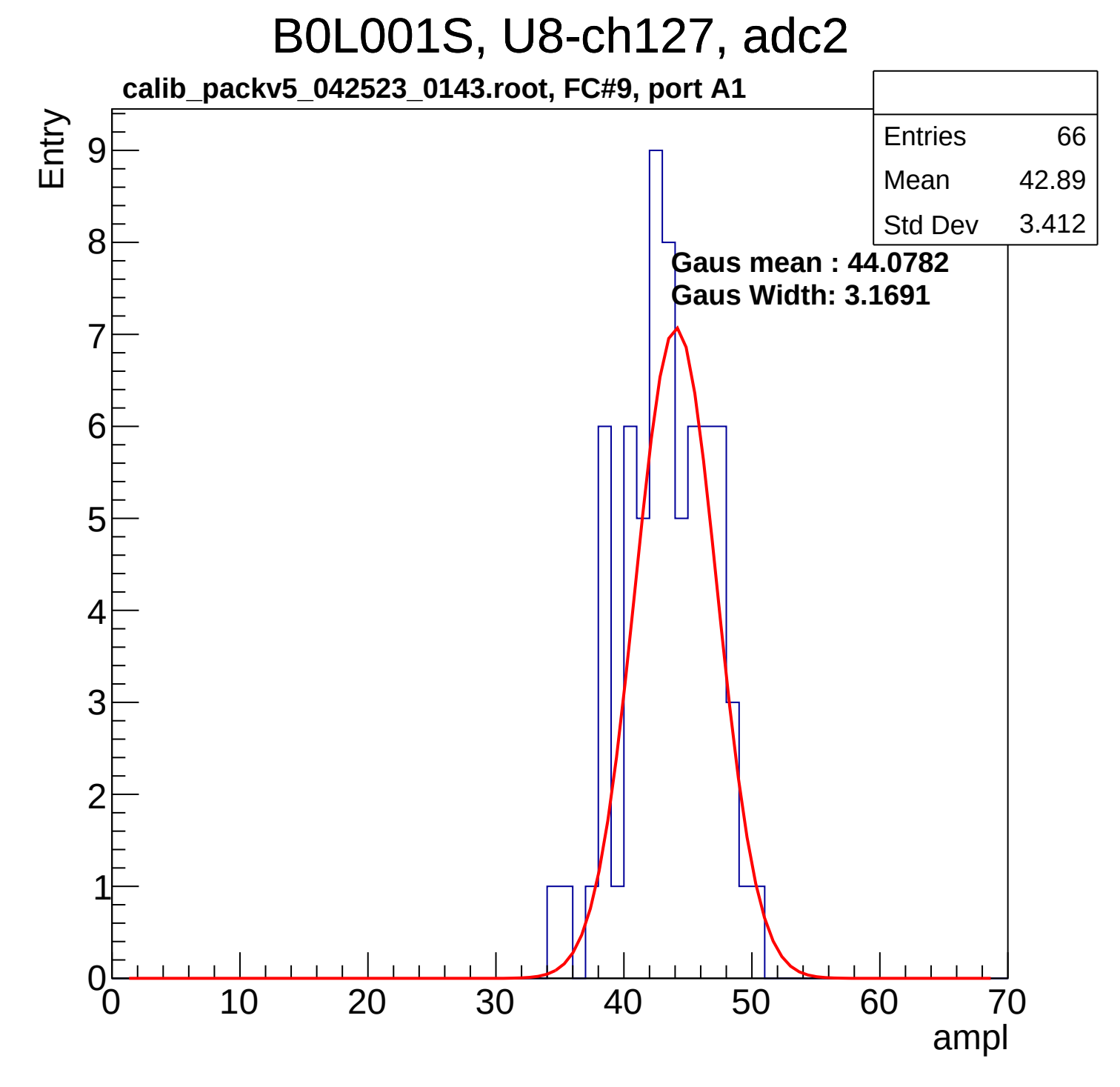
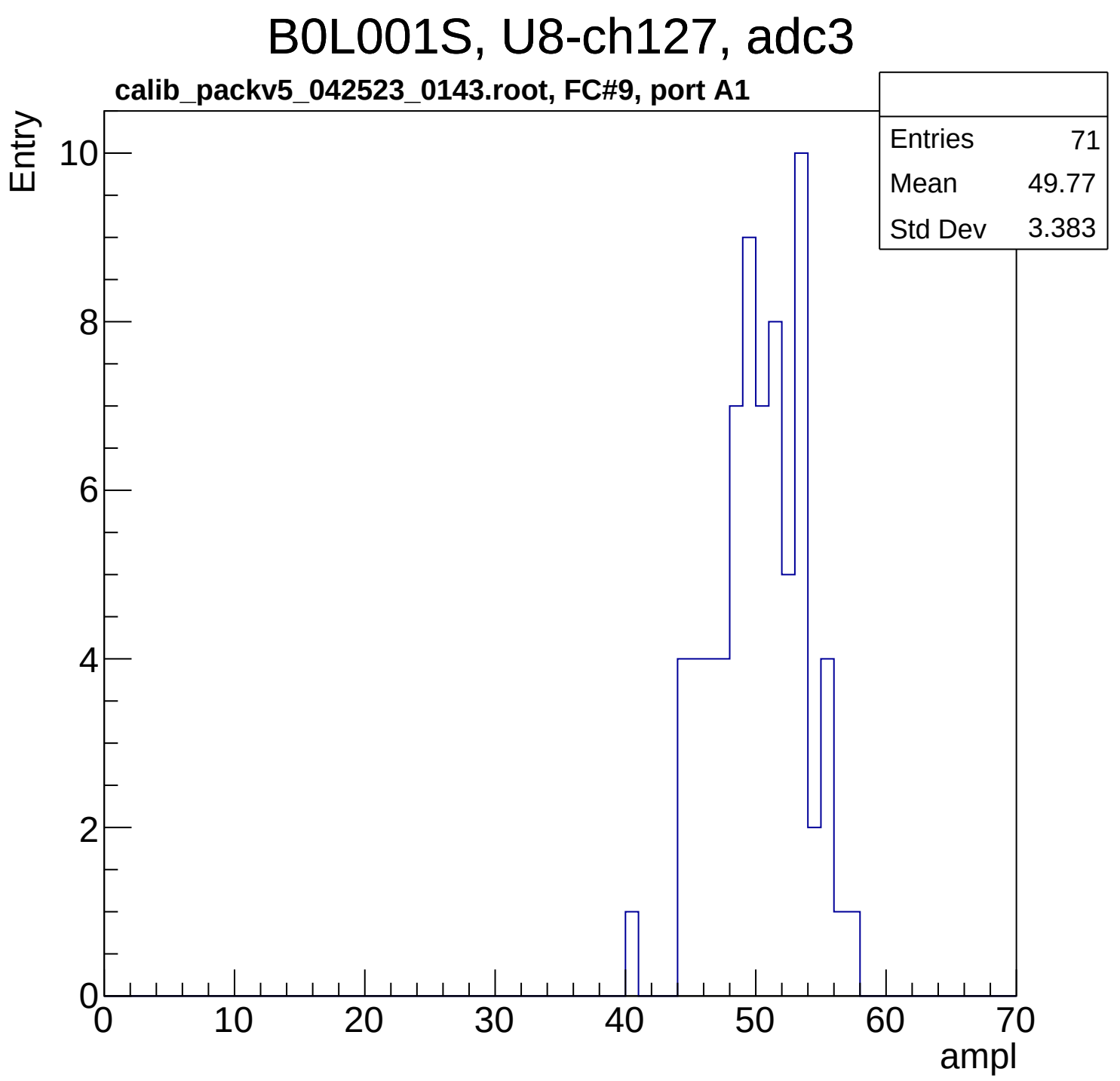
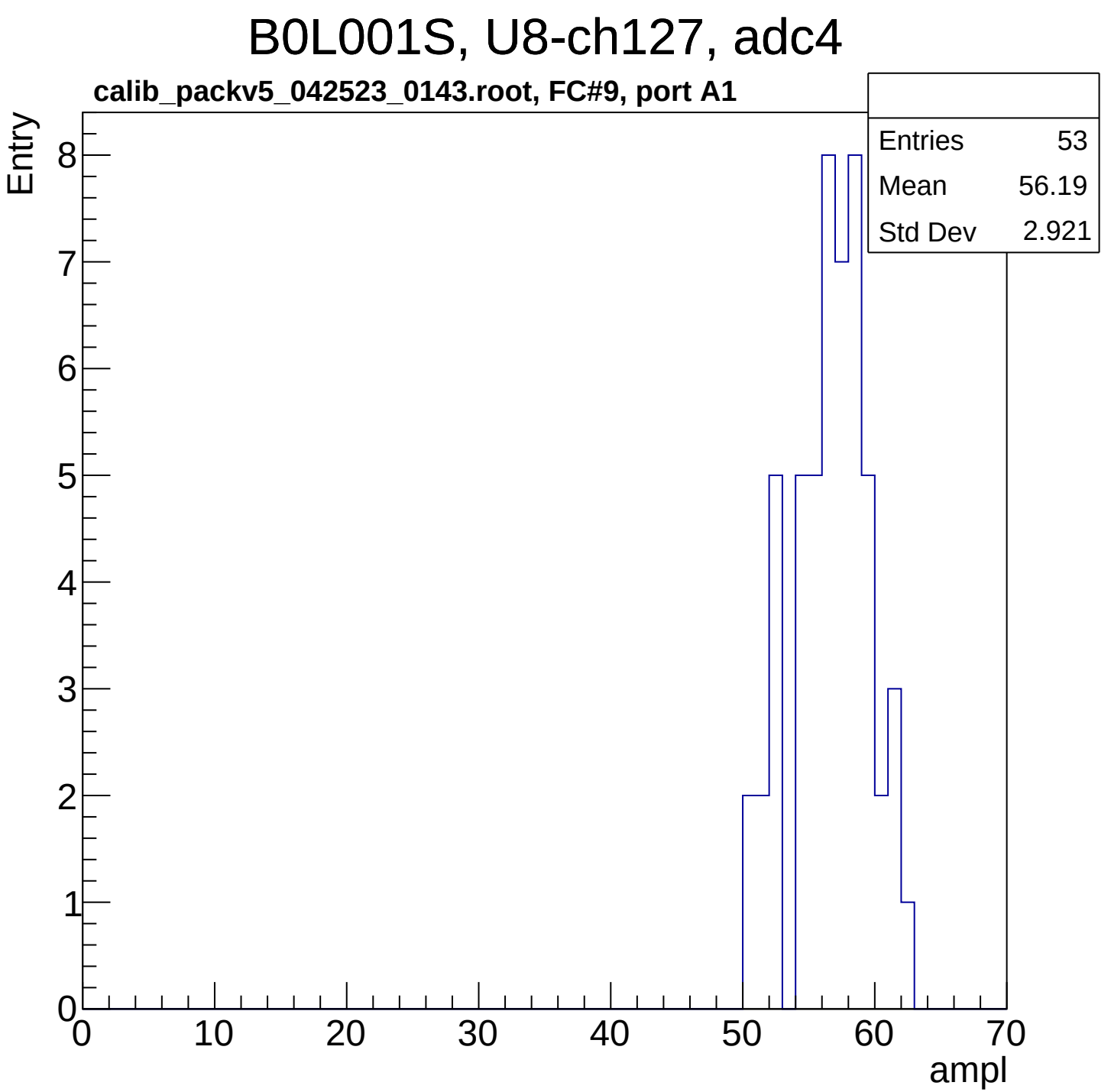
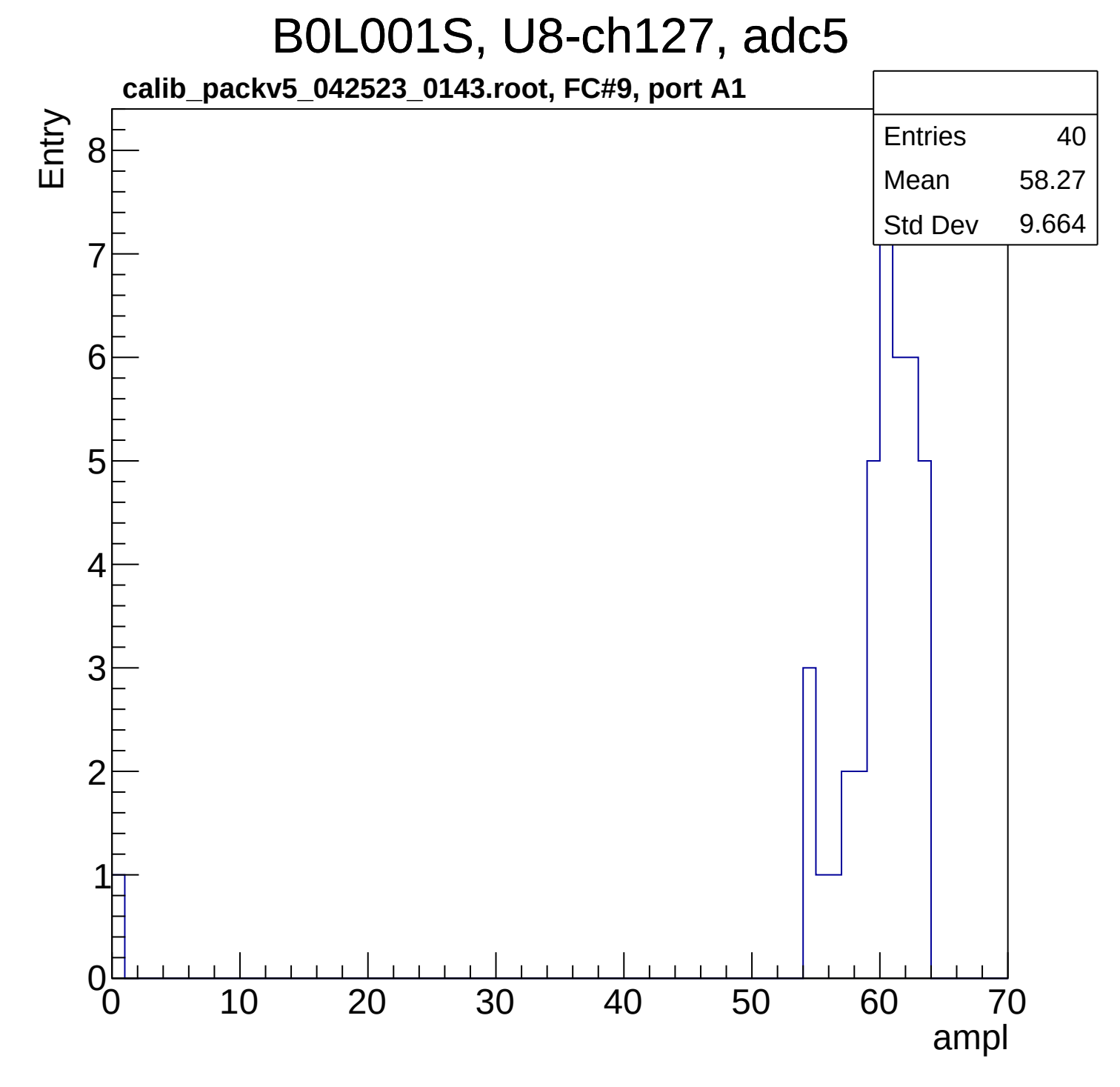
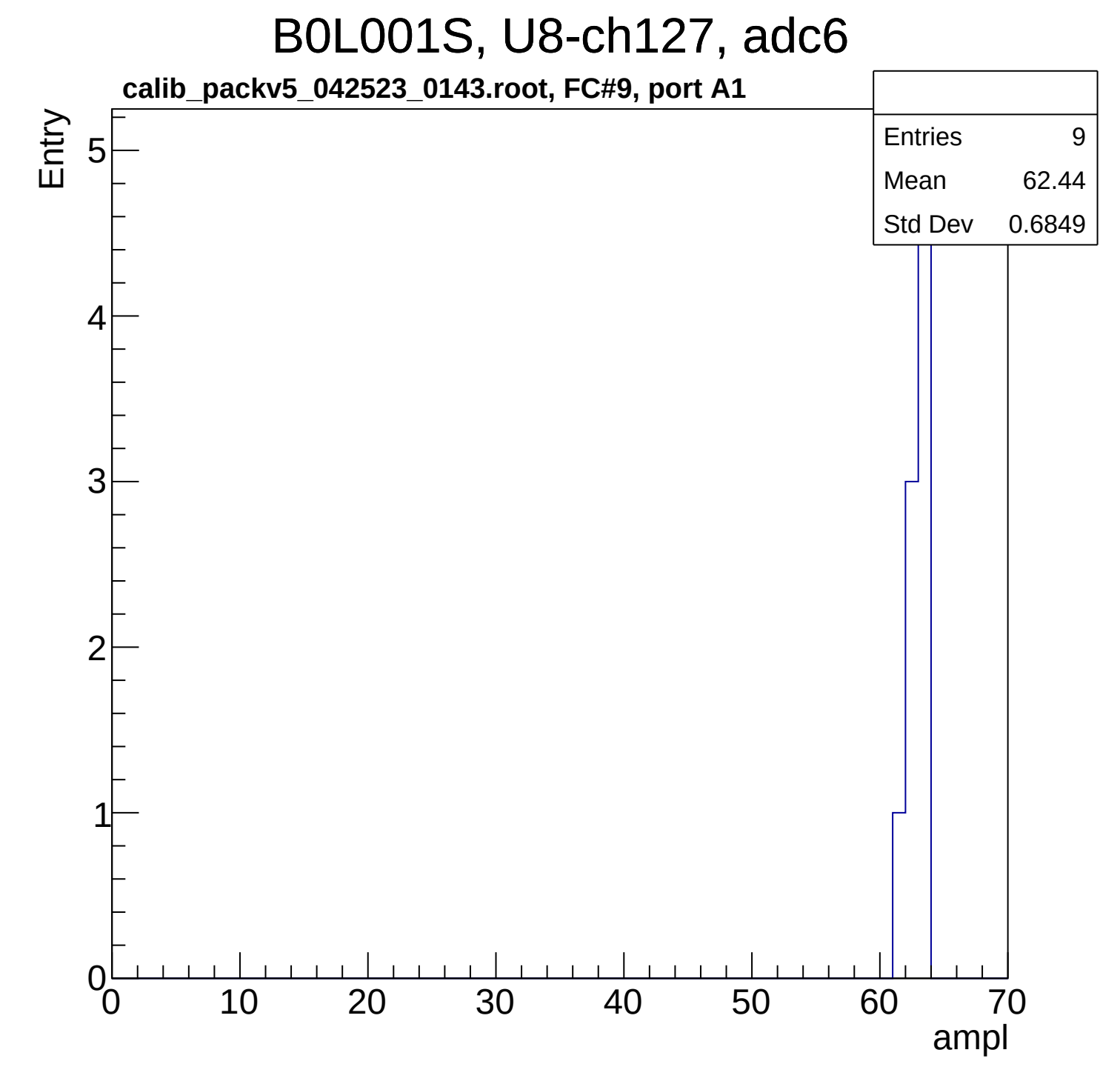
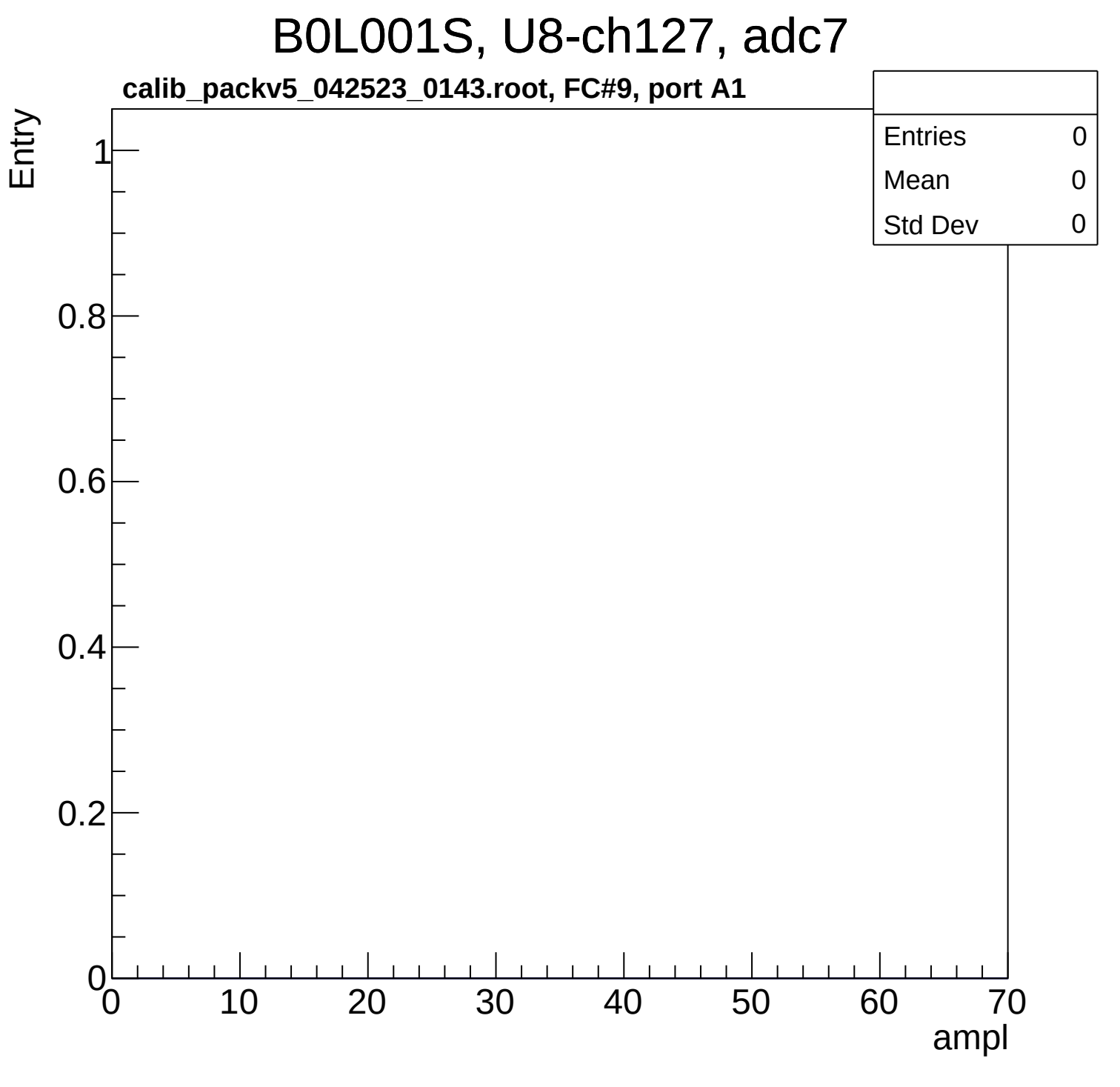

B0L001S, U8-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.89
Std Dev 3.412
Gaus mean : 44.0782
Gaus Width: 3.1691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

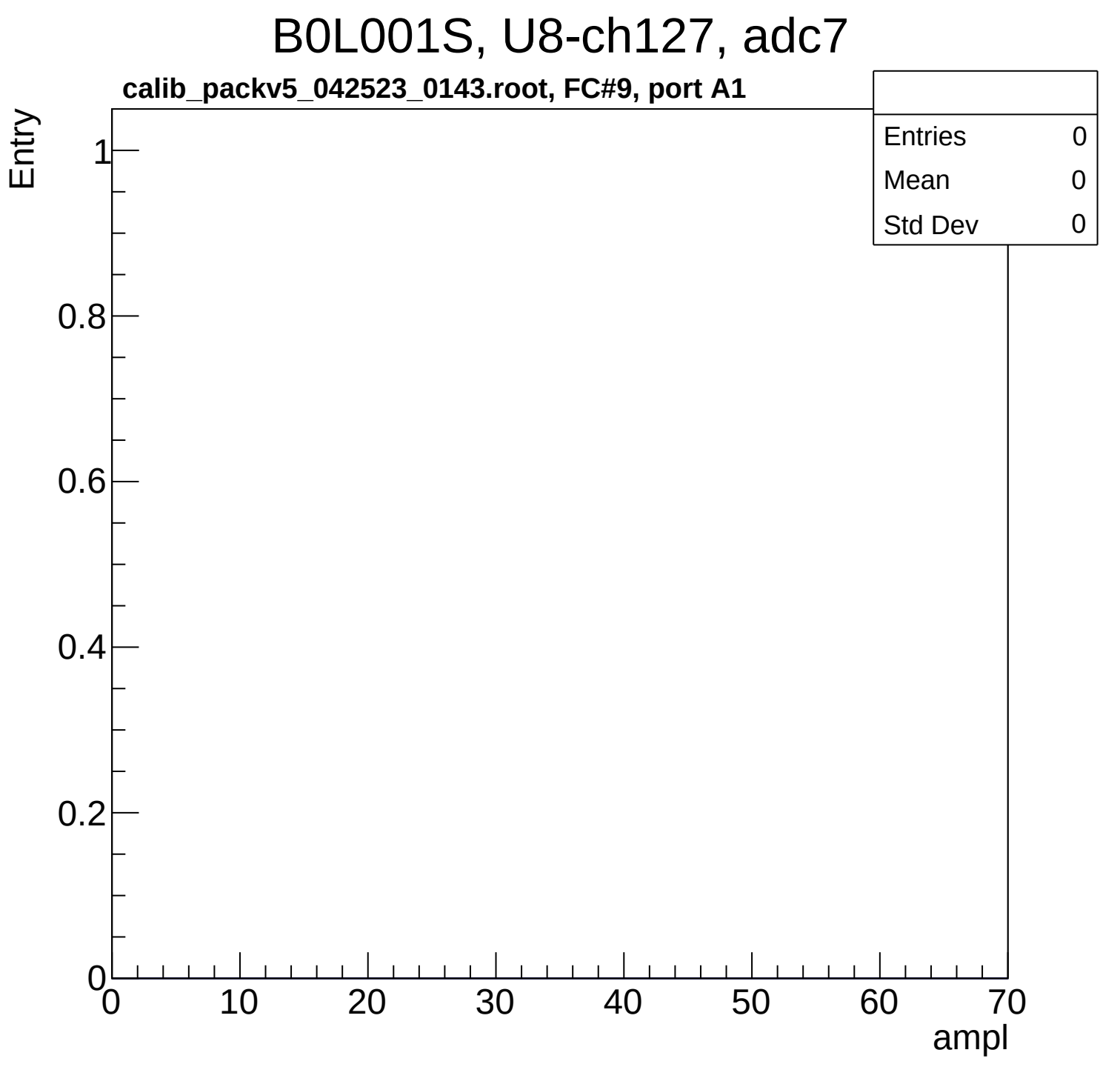

B0L001S, U8-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.77
Std Dev 3.383
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.19
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U8-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.27
Std Dev 9.664
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 62.44
Std Dev 0.6849
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U8-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl