



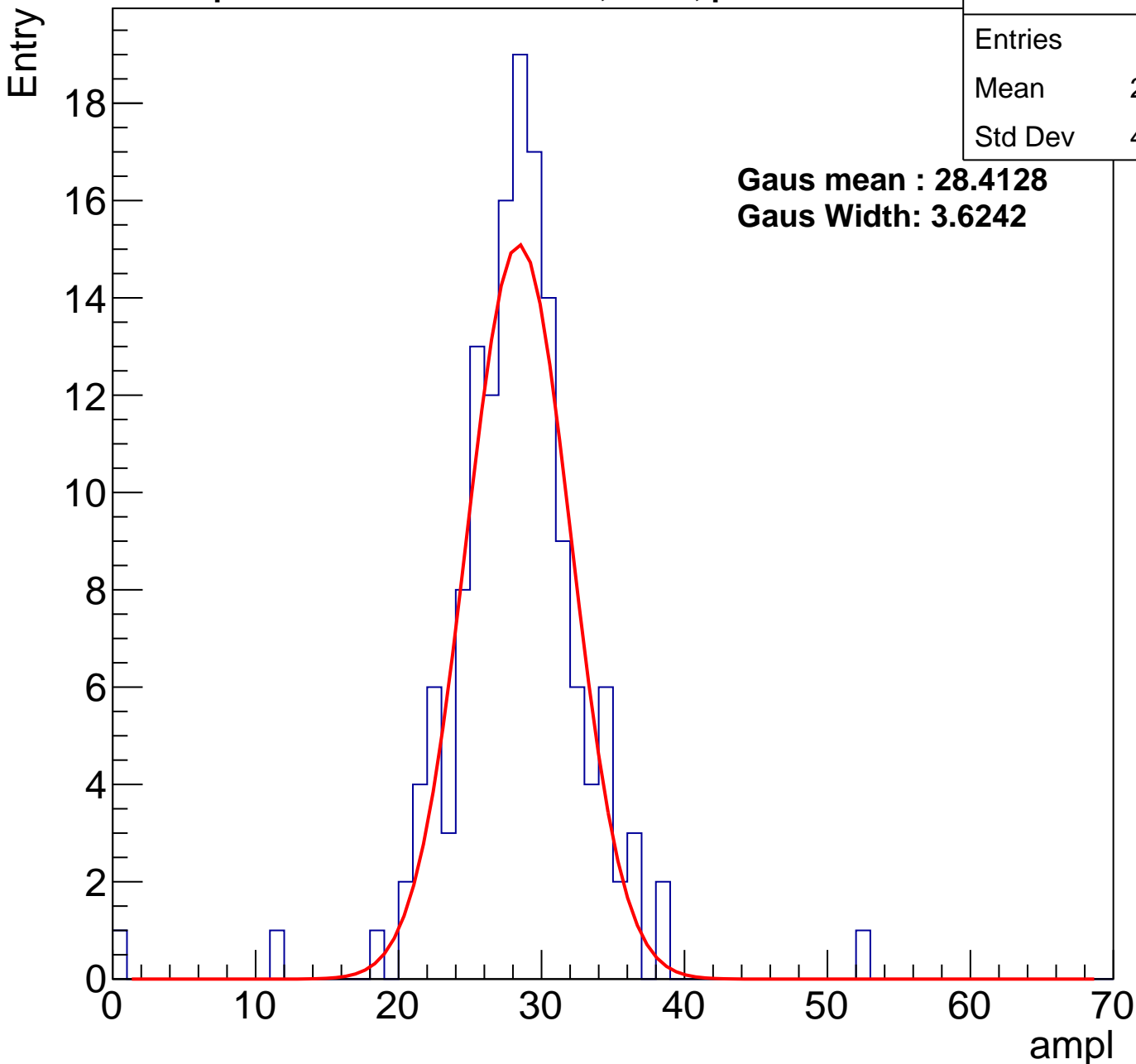
# B0L101S, U23-ch0, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	27.82
Std Dev	4.983

**Gaus mean : 28.4128**

**Gaus Width: 3.6242**



# B0L101S, U23-ch0, adc1

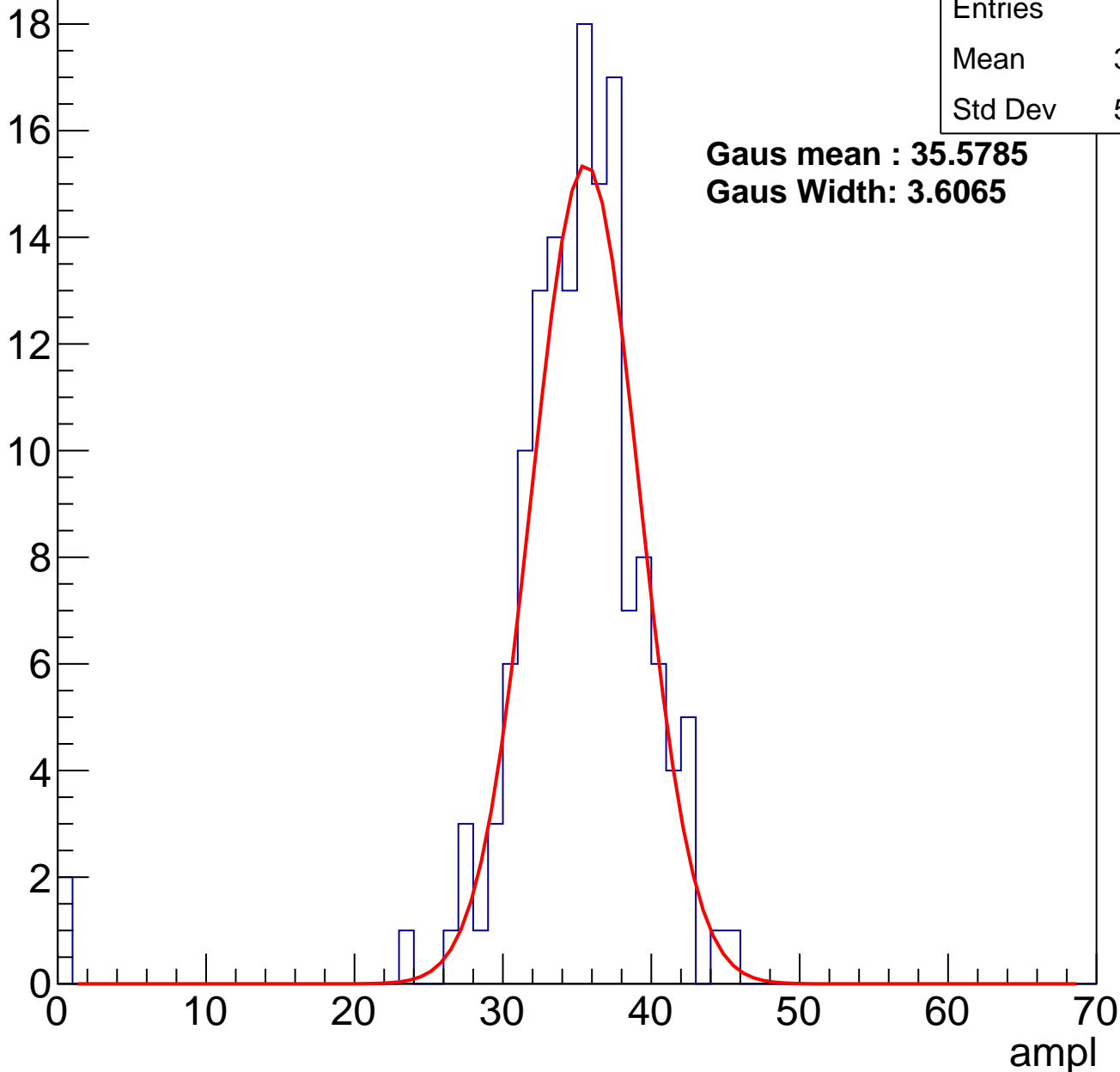
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	34.46
Std Dev	5.478

**Gaus mean : 35.5785**

**Gaus Width: 3.6065**

Entry



# B0L101S, U23-ch0, adc2

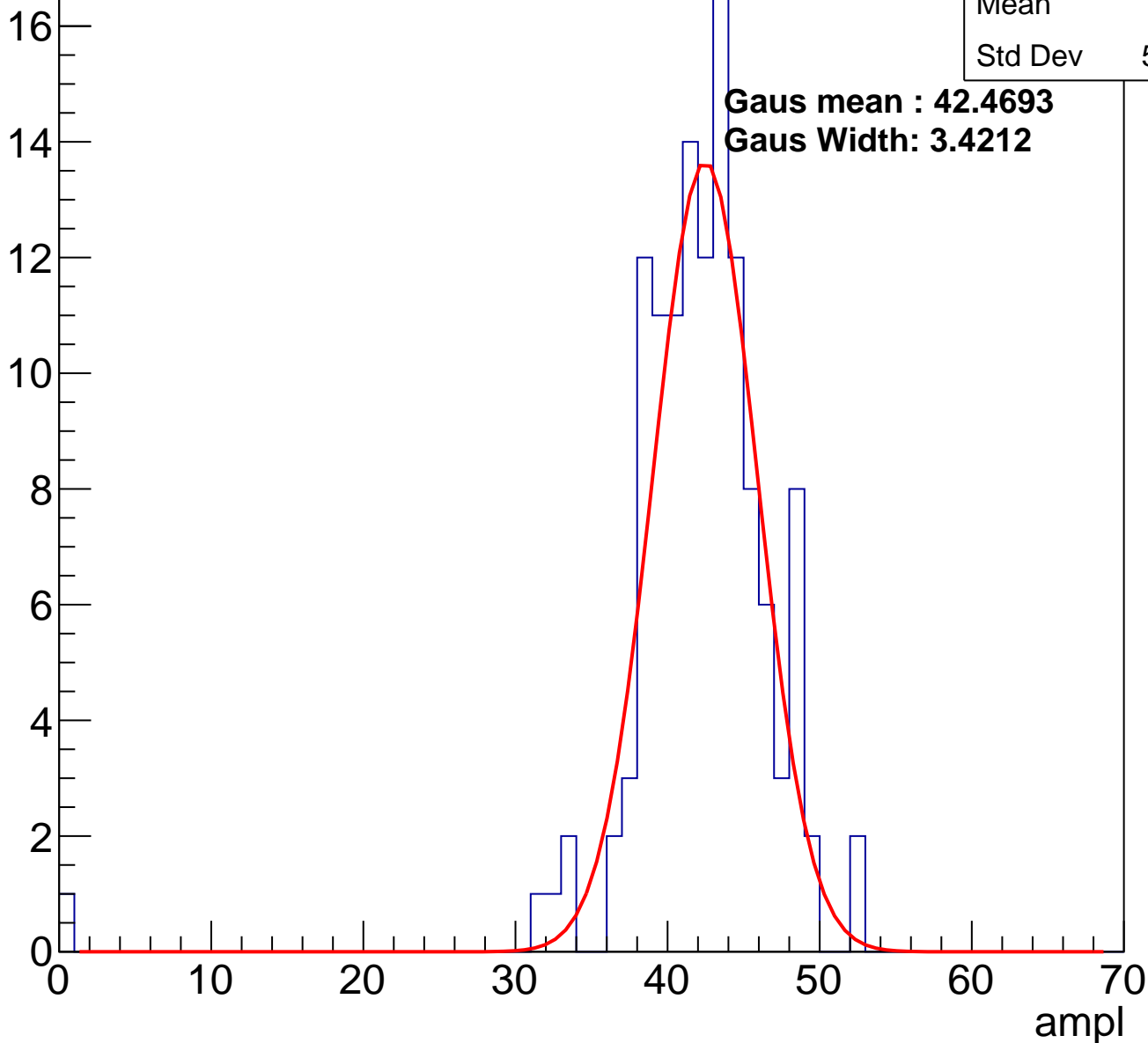
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	128
Mean	41.7
Std Dev	5.248

**Gaus mean : 42.4693**

**Gaus Width: 3.4212**



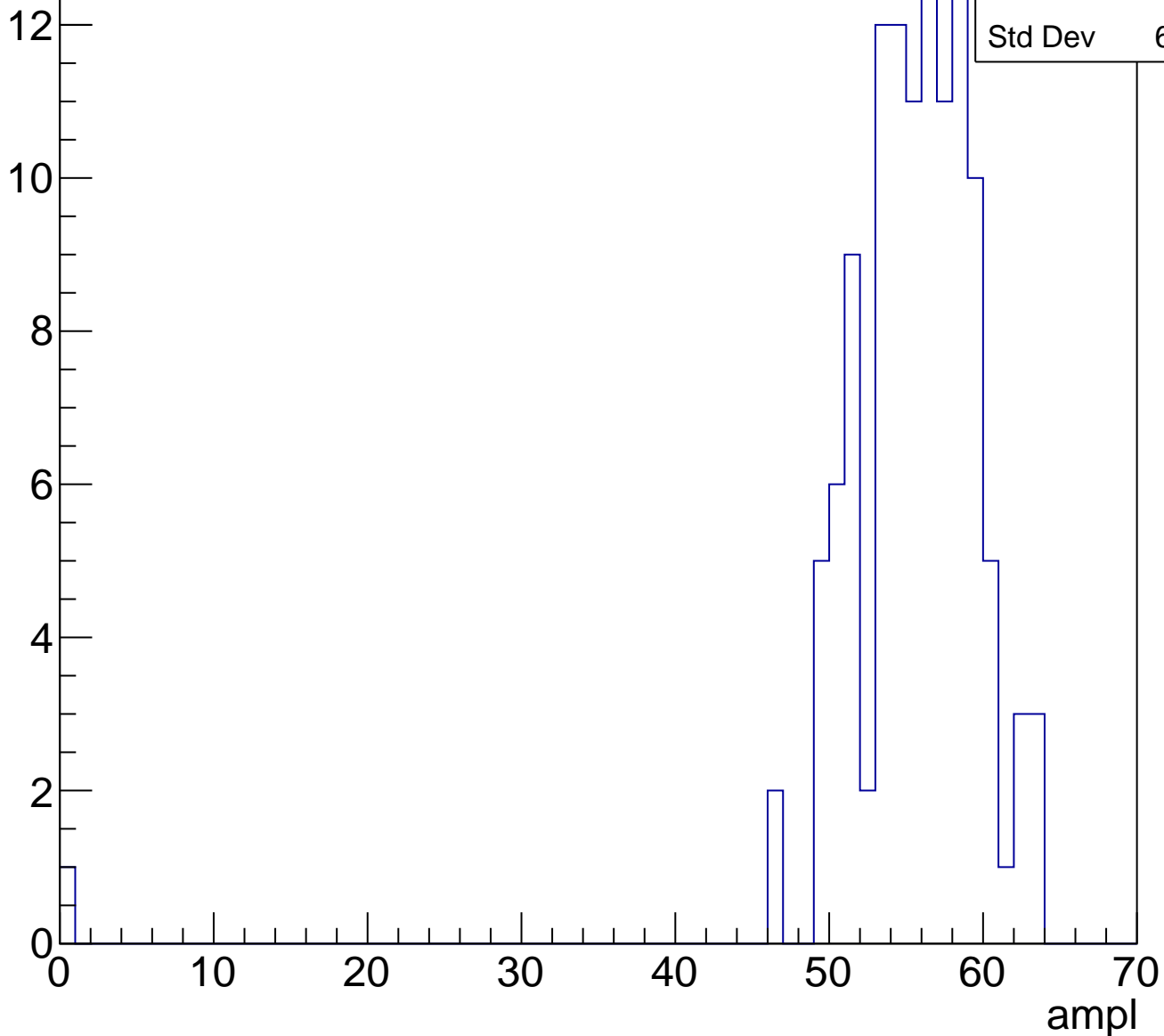


# B0L101S, U23-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	54.82
Std Dev	6.197

Entry



# B0L101S, U23-ch0, adc5

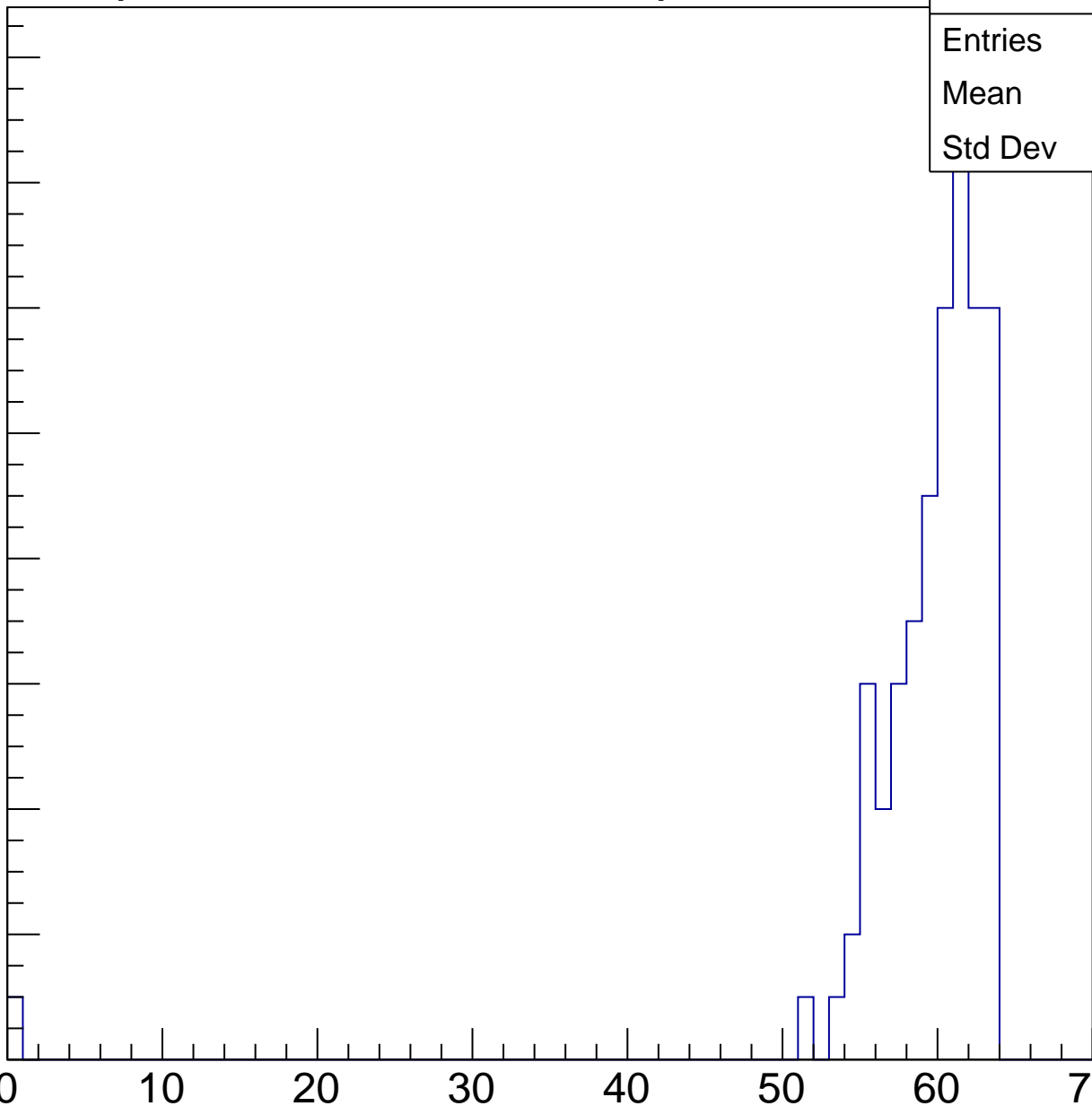
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	89
Mean	58.89
Std Dev	6.844

ampl

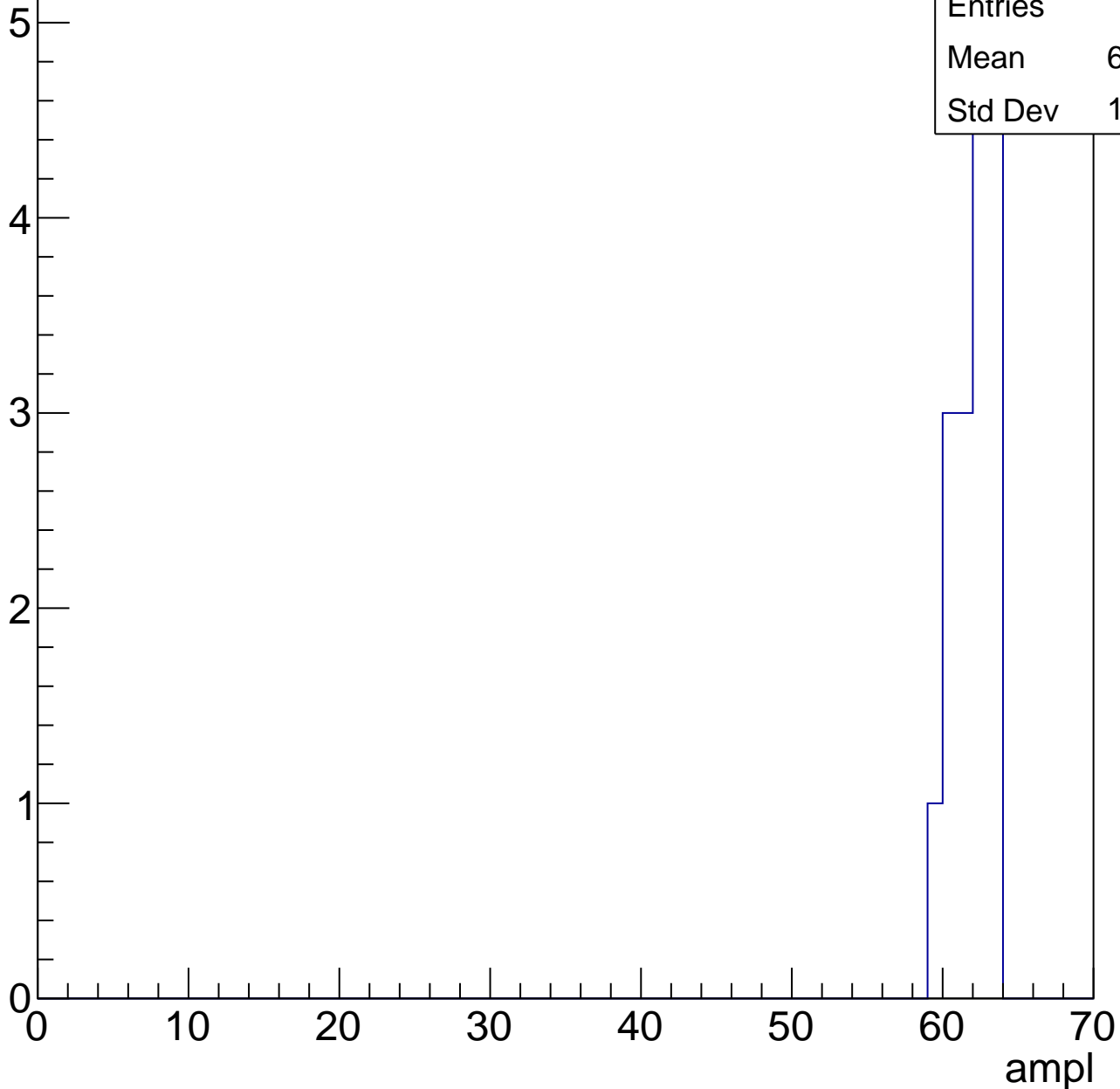


# B0L101S, U23-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	61.59
Std Dev	1.239





# B0L101S, U23-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch1, adc0

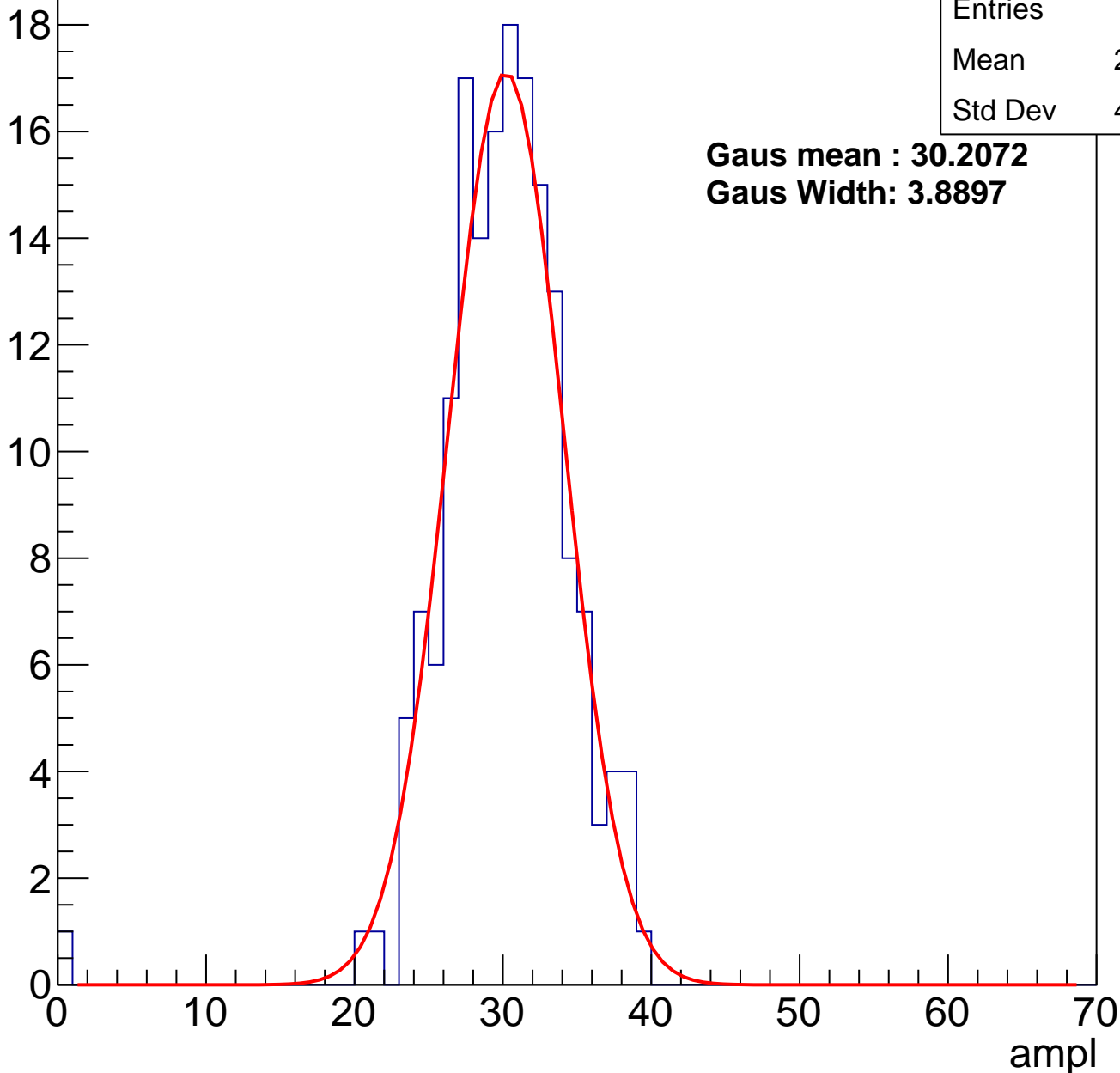
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	29.67
Std Dev	4.385

**Gaus mean : 30.2072**

**Gaus Width: 3.8897**

Entry



# B0L101S, U23-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	37.66
Std Dev	3.498

**Gaus mean : 37.9862**

**Gaus Width: 3.5866**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

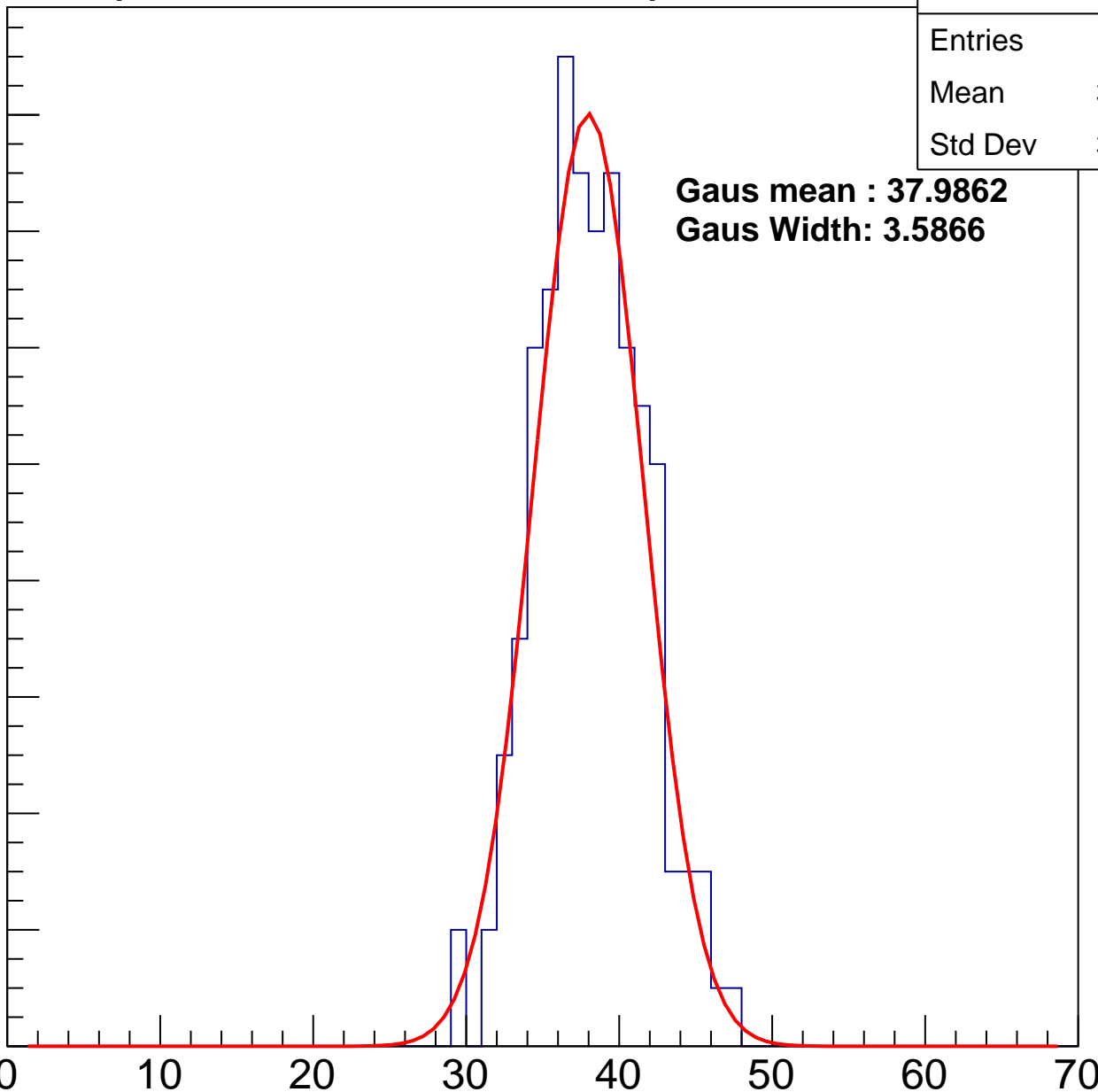
40

50

60

70

ampl



# B0L101S, U23-ch1, adc2

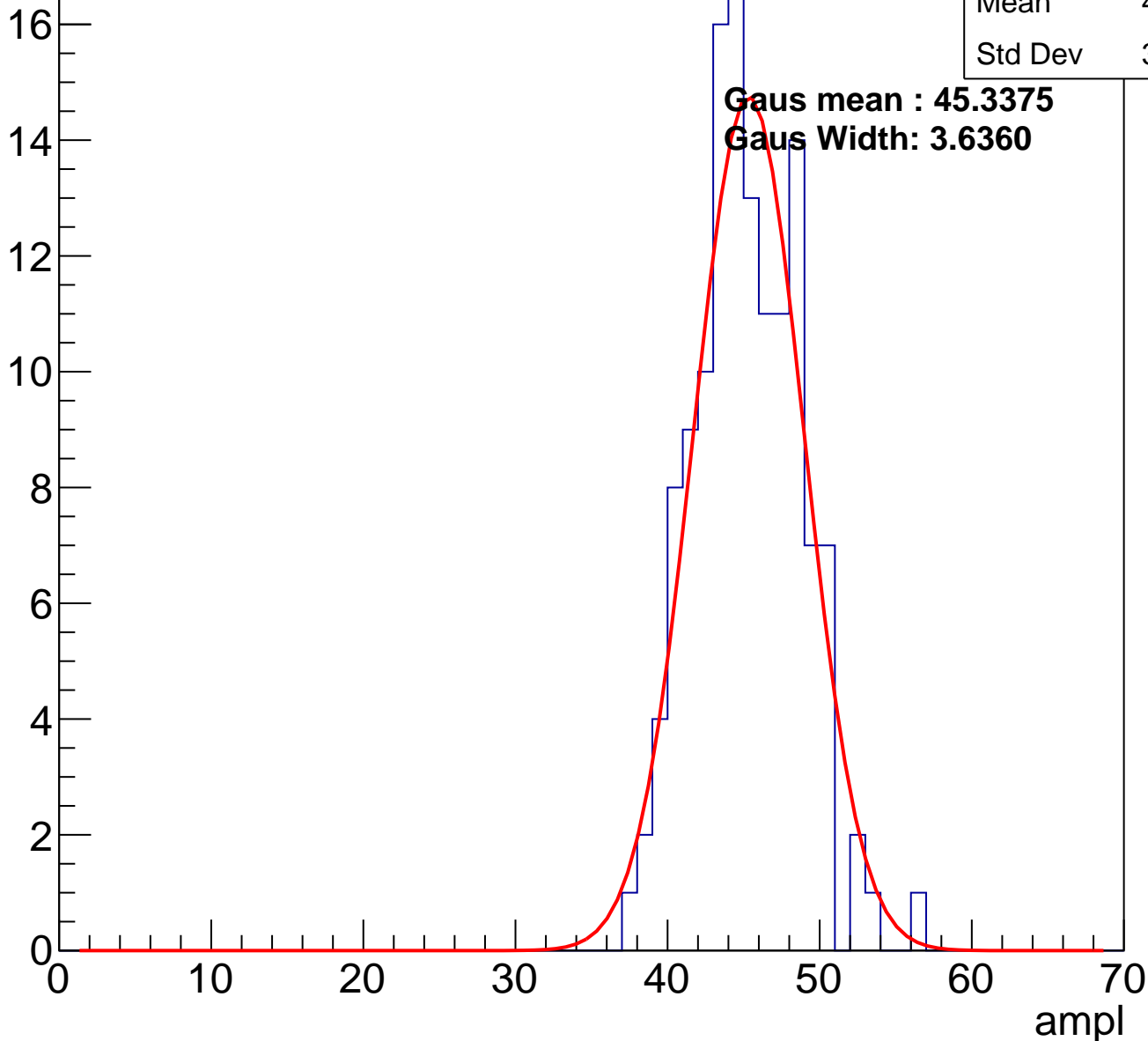
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	134
Mean	44.78
Std Dev	3.418

**Gaus mean : 45.3375**

**Gaus Width: 3.6360**



# B0L101S, U23-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

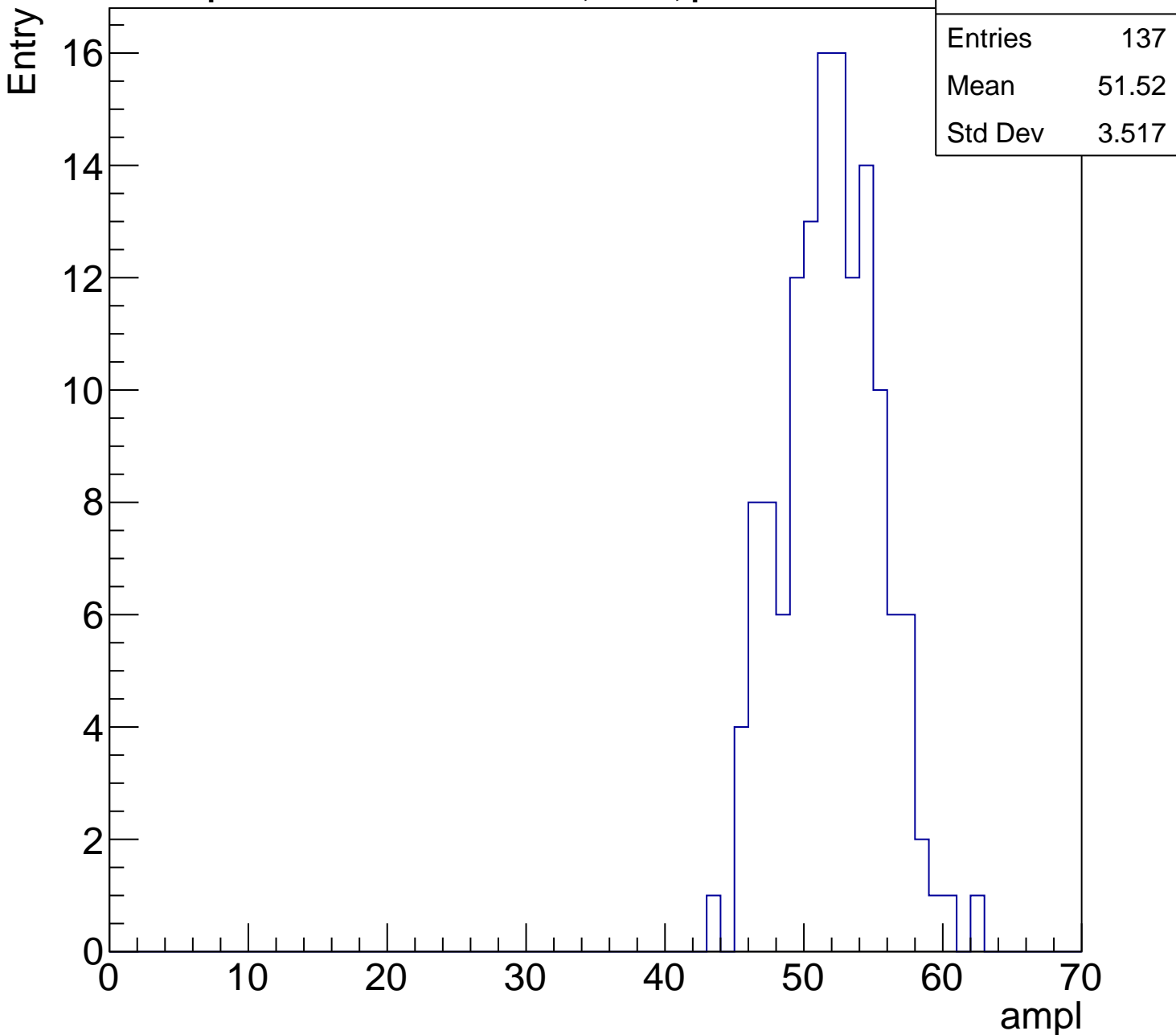
Entries	137
Mean	51.52
Std Dev	3.517

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch1, adc4

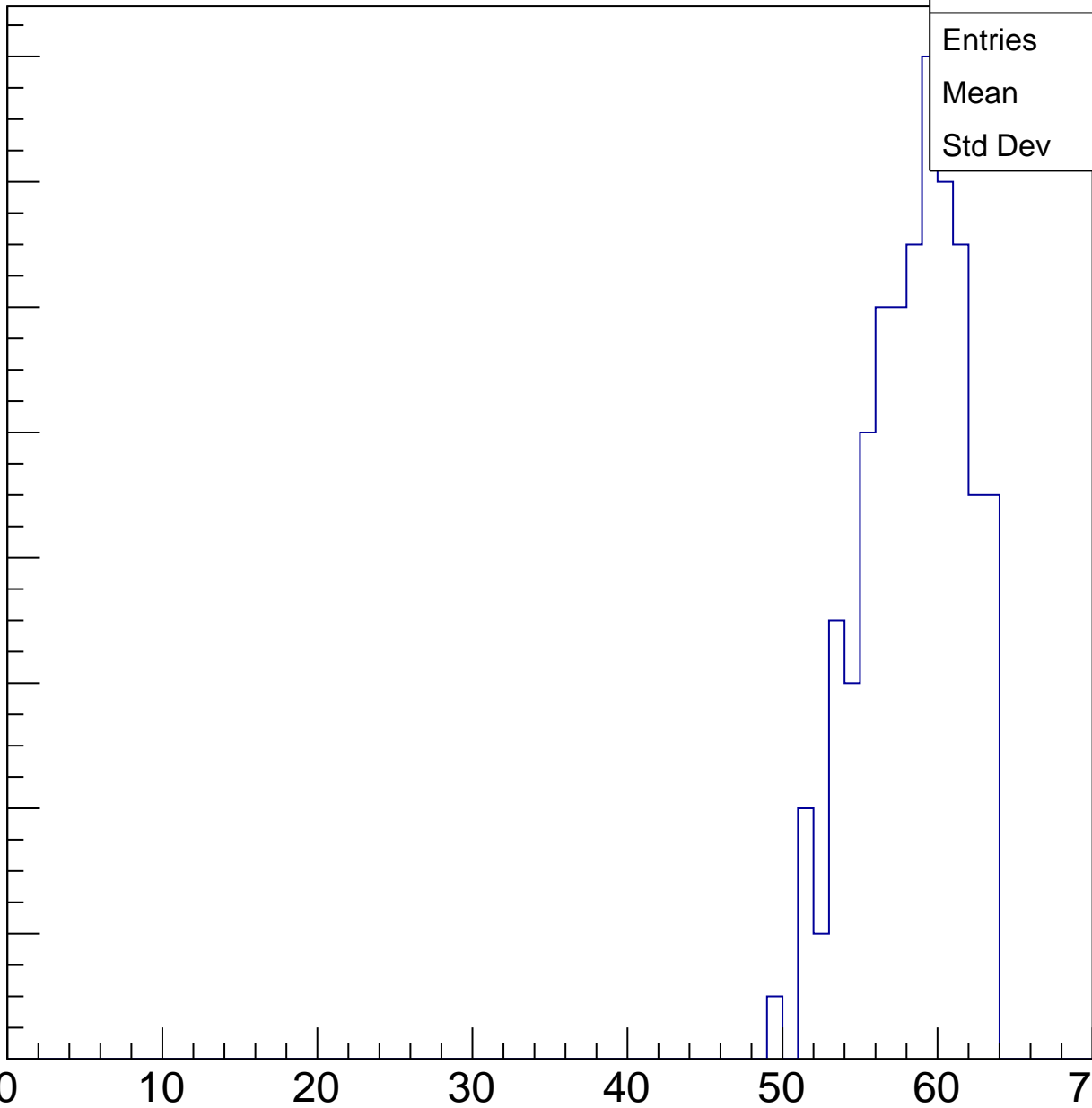
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	128
Mean	57.92
Std Dev	3.22

ampl



# B0L101S, U23-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

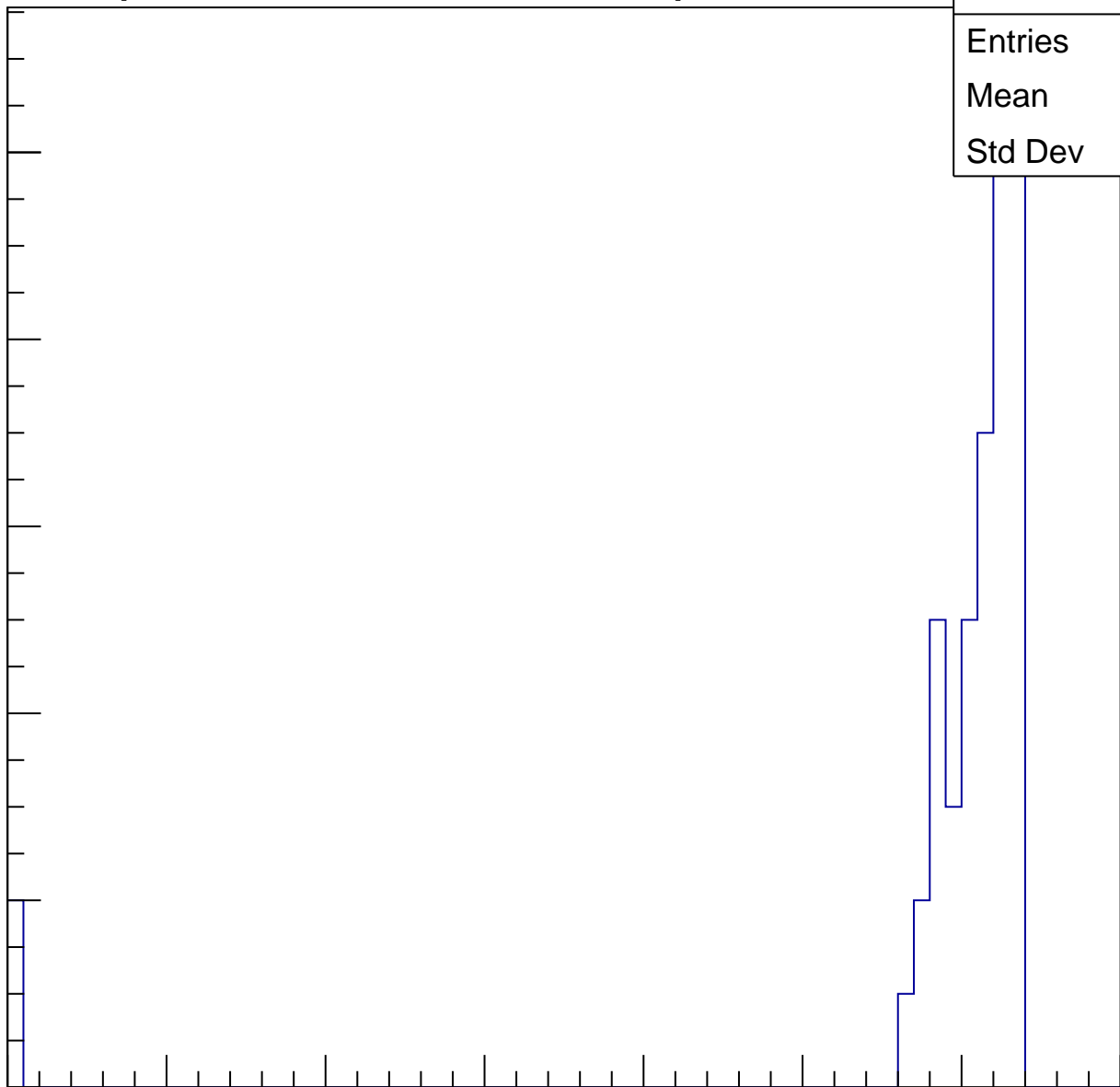
40

50

60

ampl

Entries	46
Mean	58.2
Std Dev	12.56



# B0L101S, U23-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch2, adc0

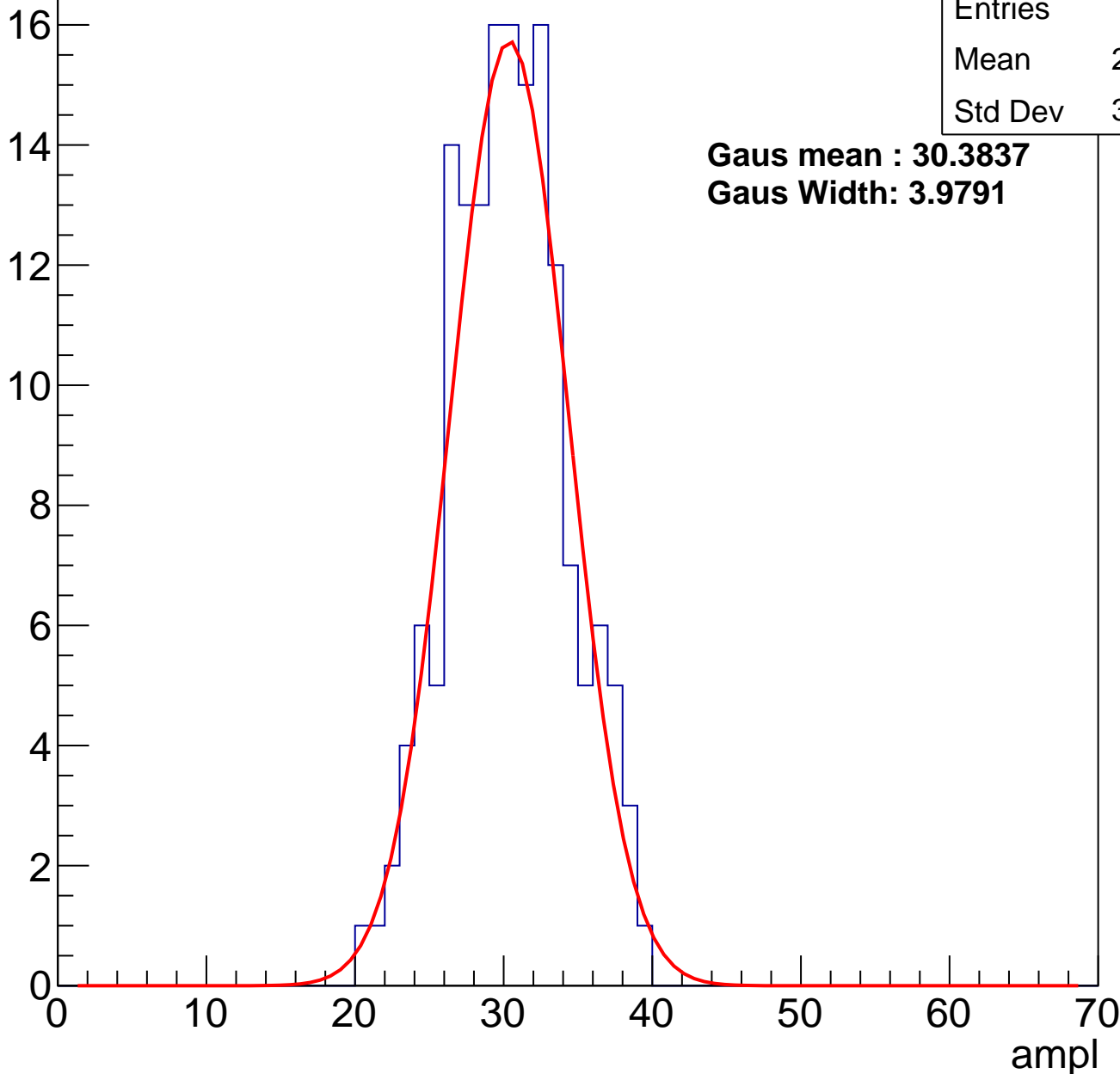
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	29.87
Std Dev	3.869

**Gaus mean : 30.3837**

**Gaus Width: 3.9791**

Entry



# B0L101S, U23-ch2, adc1

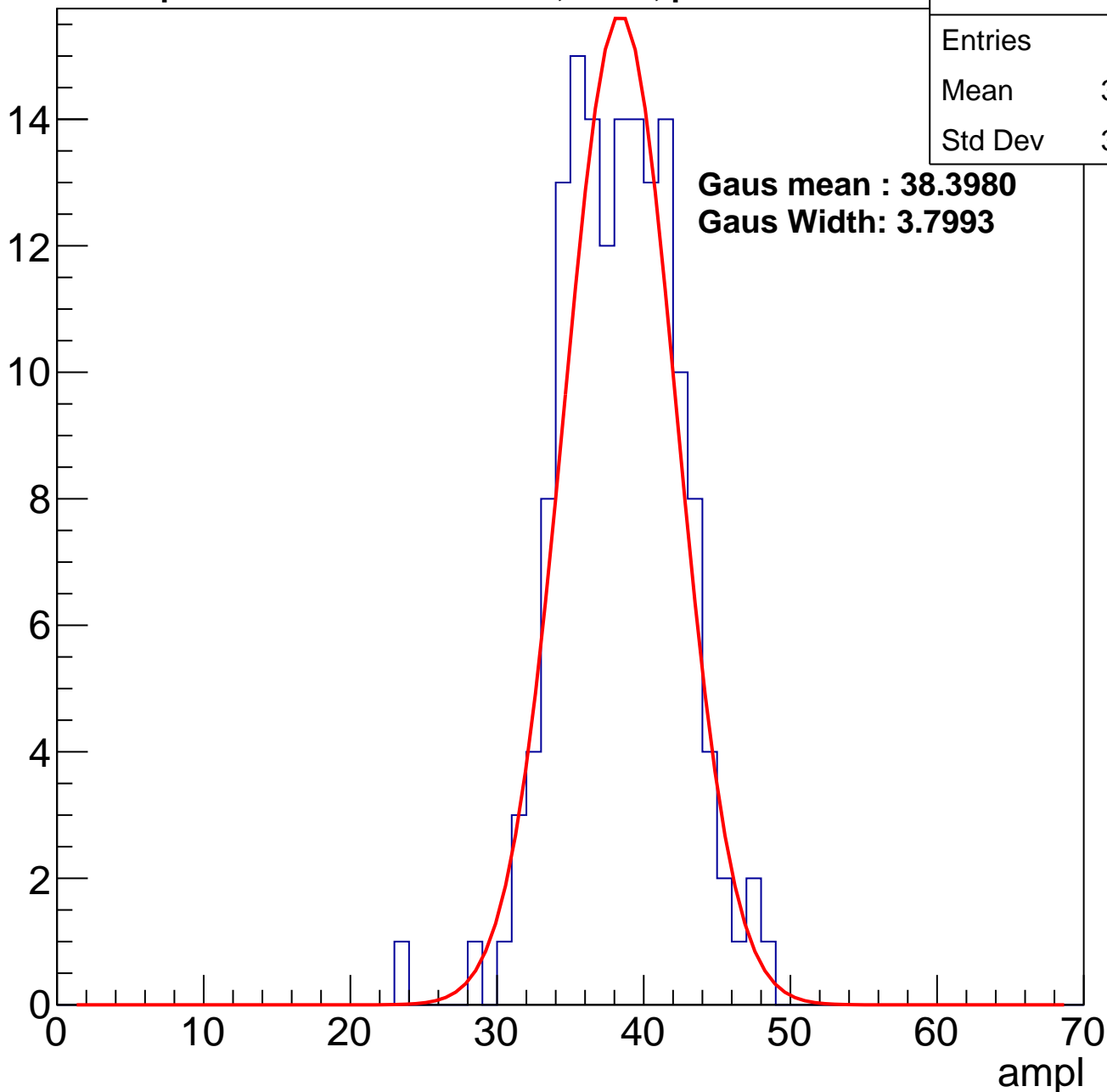
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	37.88
Std Dev	3.942

**Gaus mean : 38.3980**

**Gaus Width: 3.7993**

Entry



# B0L101S, U23-ch2, adc2

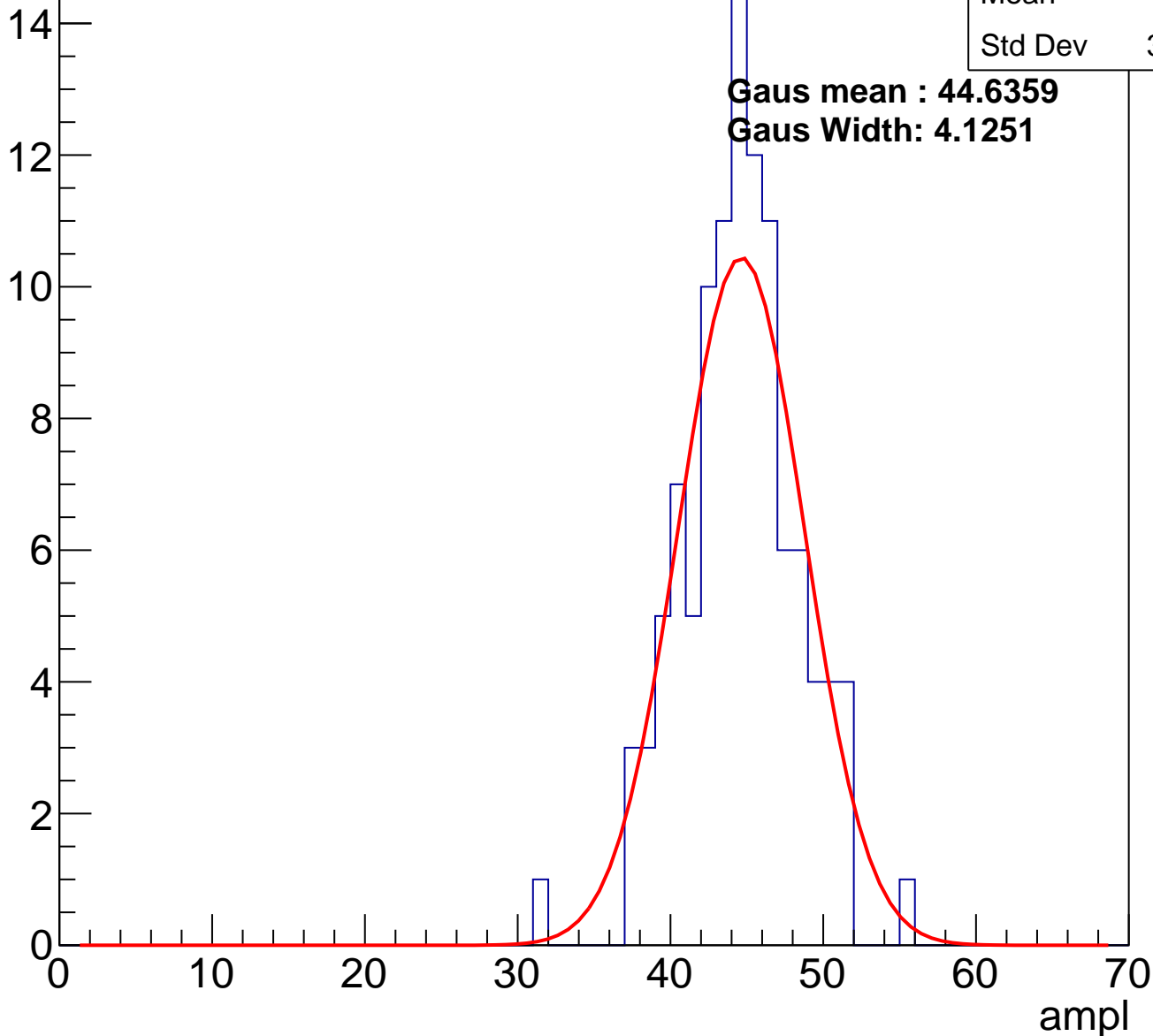
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	108
Mean	44.07
Std Dev	3.756

**Gaus mean : 44.6359**

**Gaus Width: 4.1251**

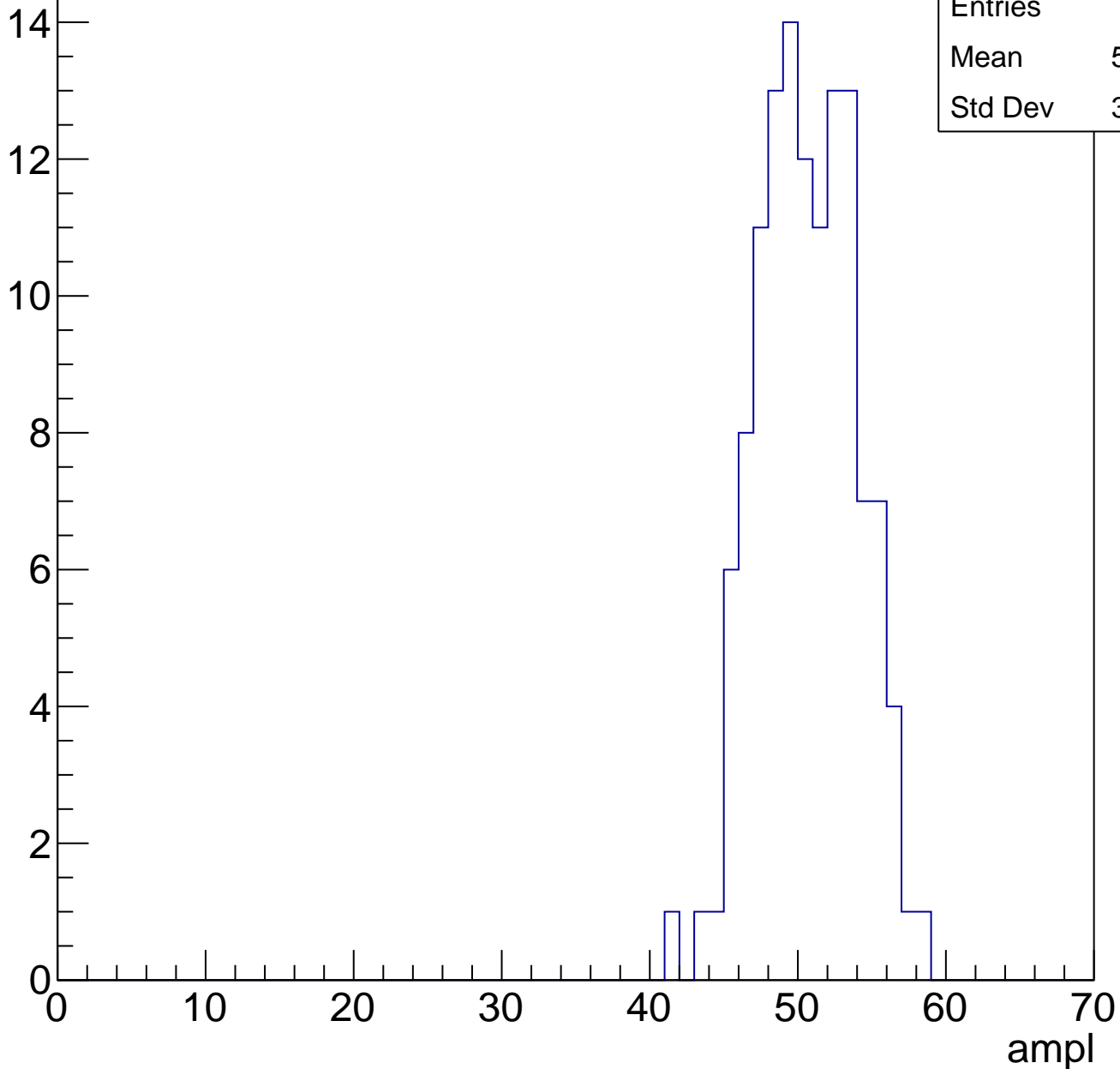


# B0L101S, U23-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	50.17
Std Dev	3.274

Entry



# B0L101S, U23-ch2, adc4

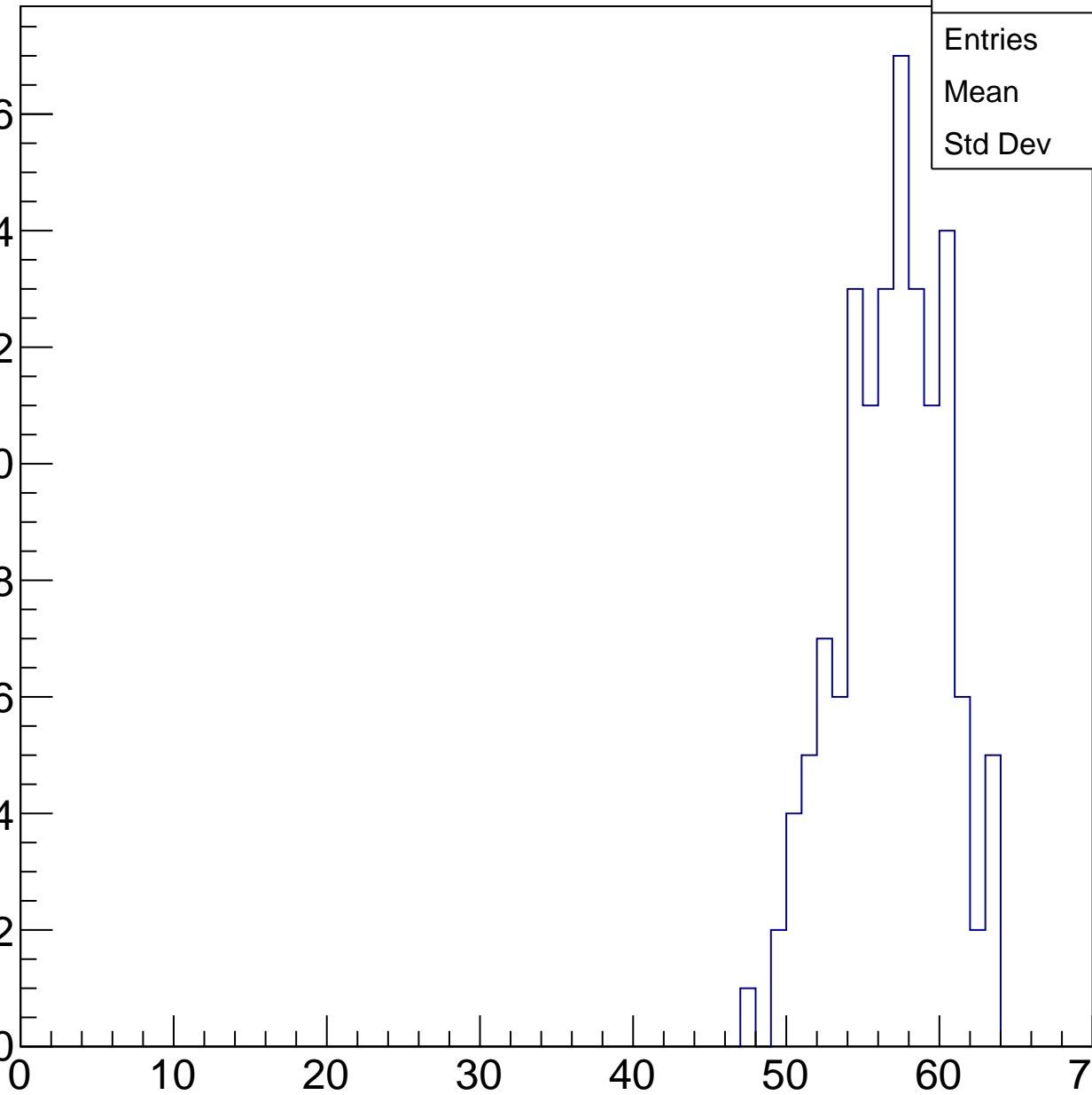
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	56.42
Std Dev	3.412

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch2, adc5

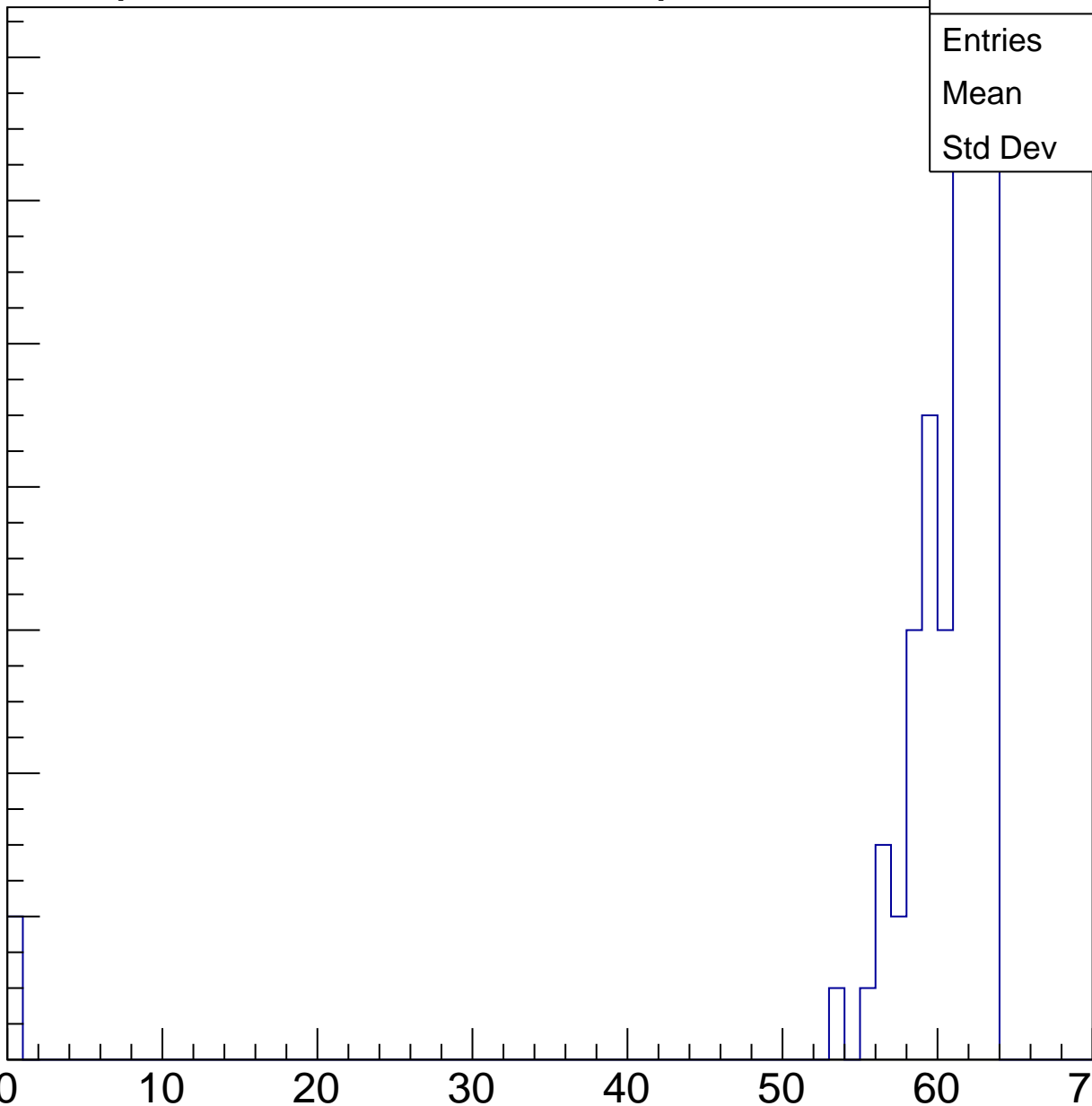
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	58.79
Std Dev	10.18

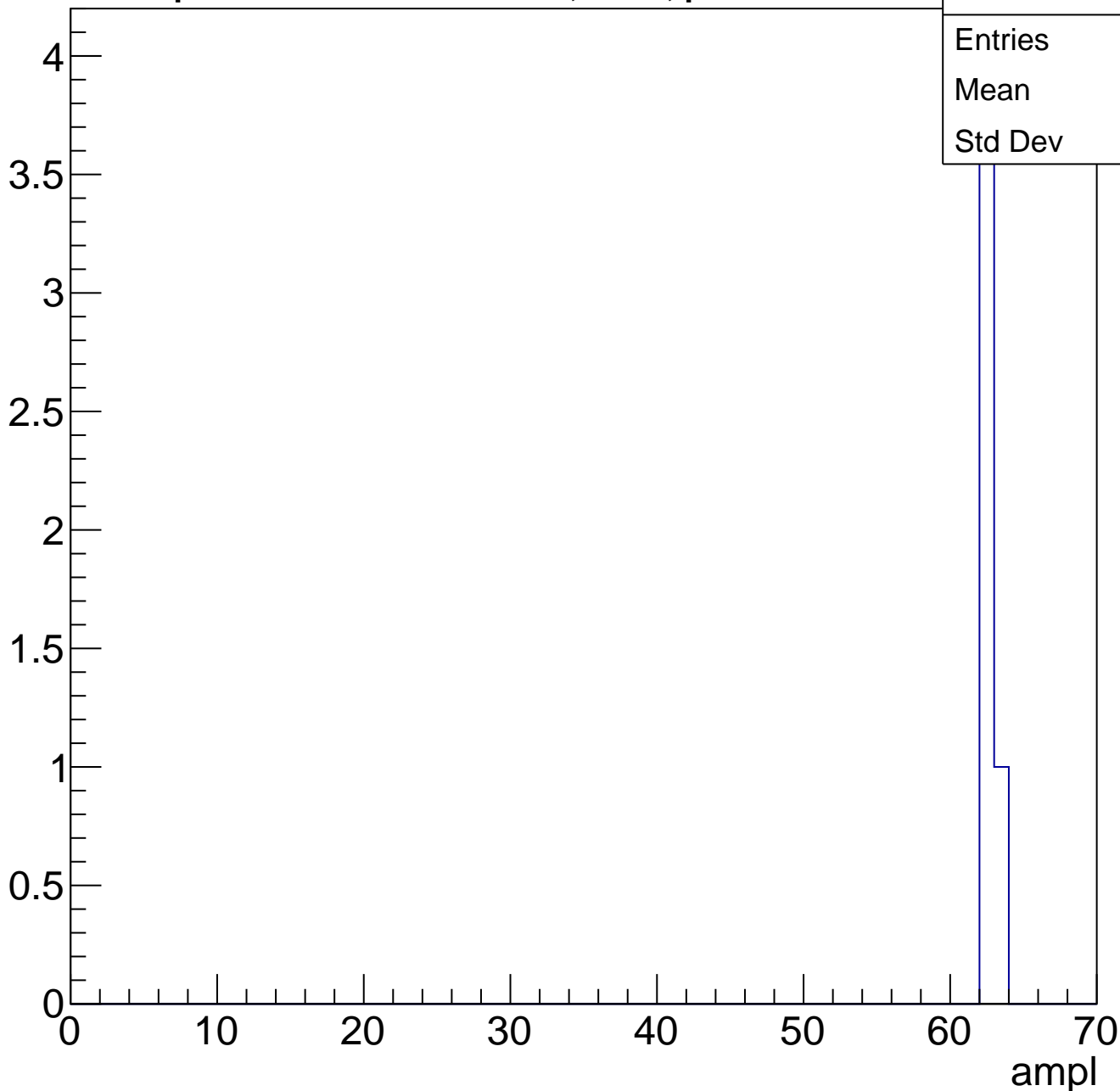
ampl



# B0L101S, U23-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch3, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

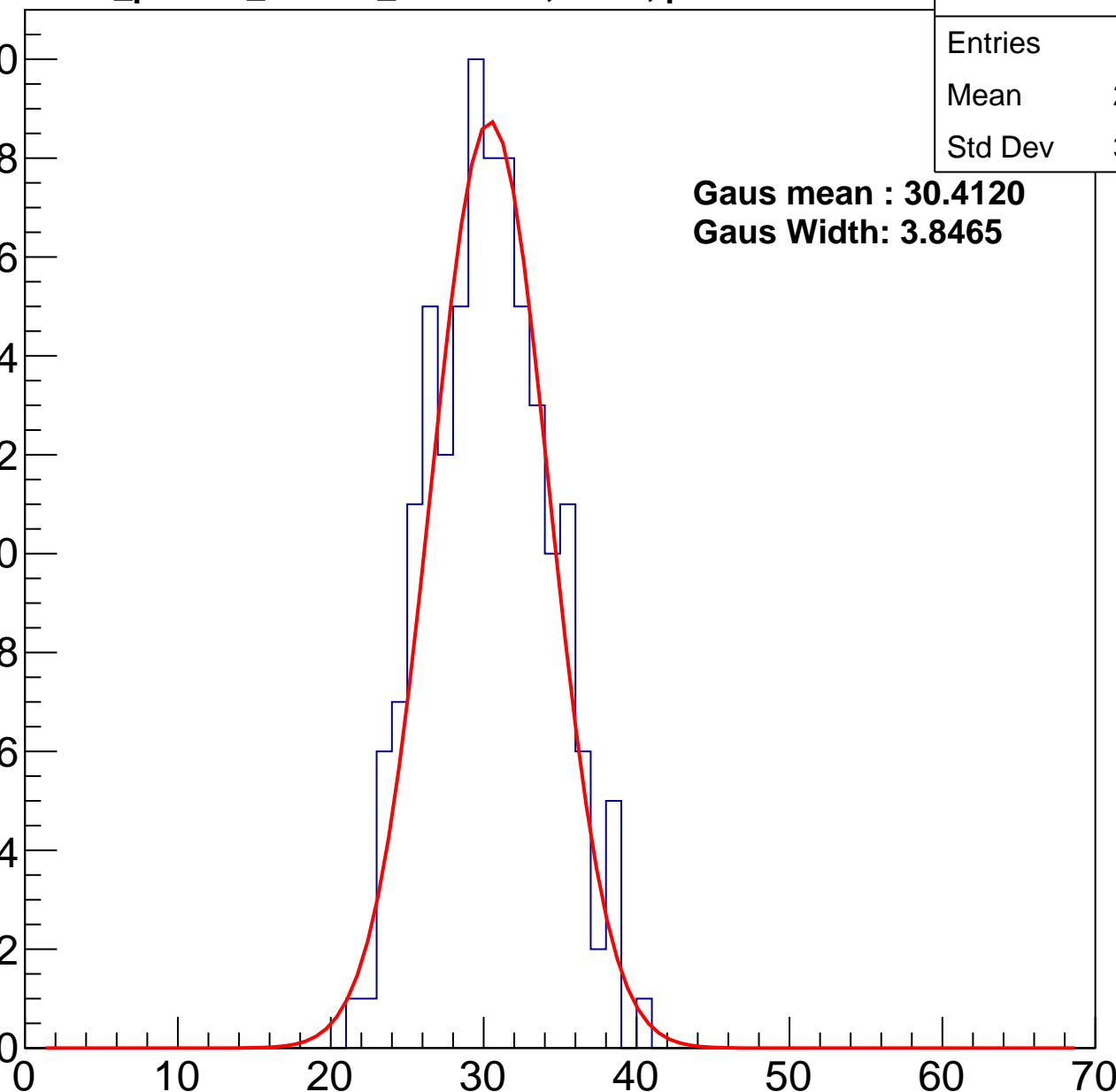
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	187
Mean	29.89
Std Dev	3.839

**Gaus mean : 30.4120**

**Gaus Width: 3.8465**



ampl

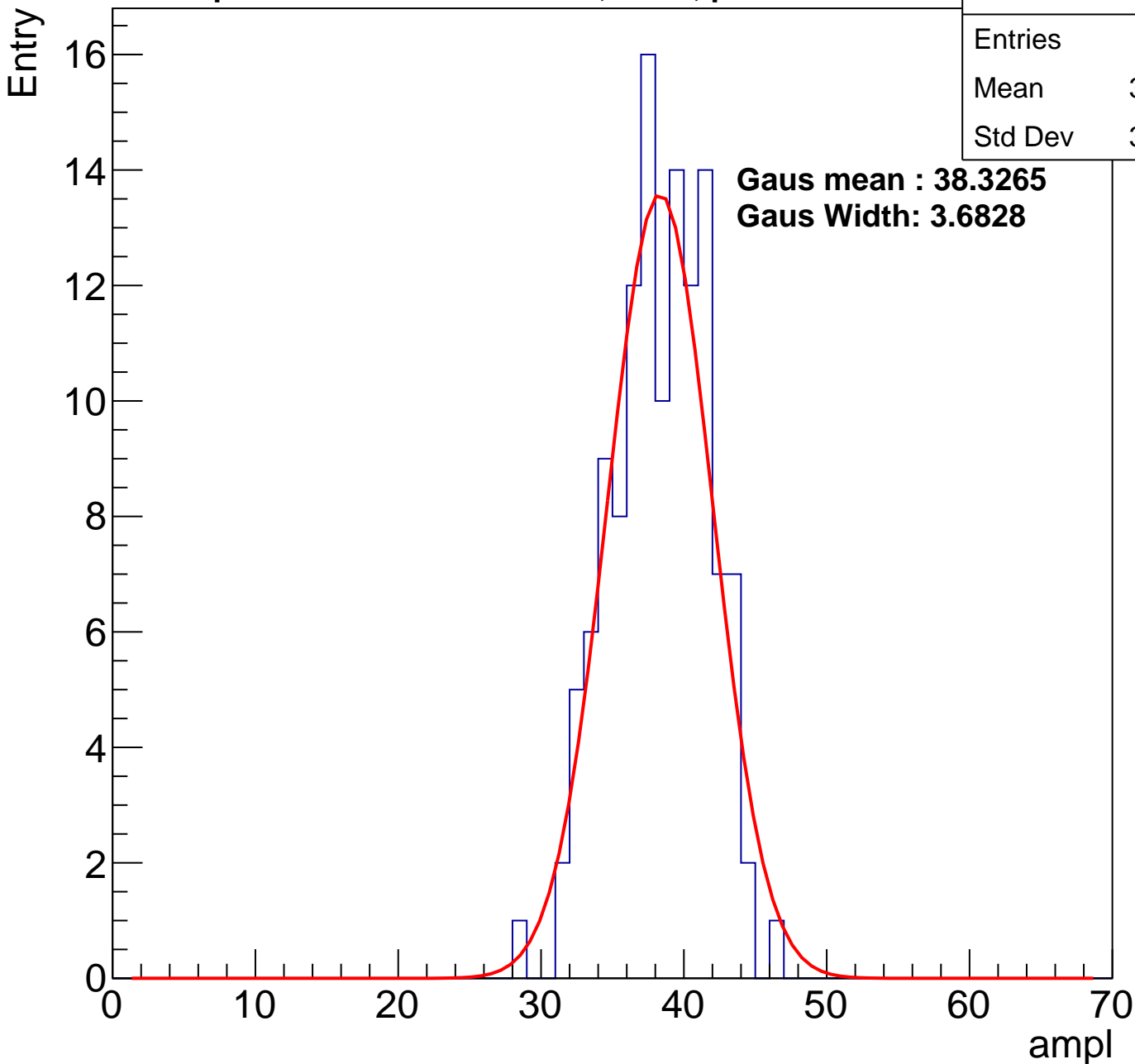
# B0L101S, U23-ch3, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	37.83
Std Dev	3.352

**Gaus mean : 38.3265**

**Gaus Width: 3.6828**



# B0L101S, U23-ch3, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

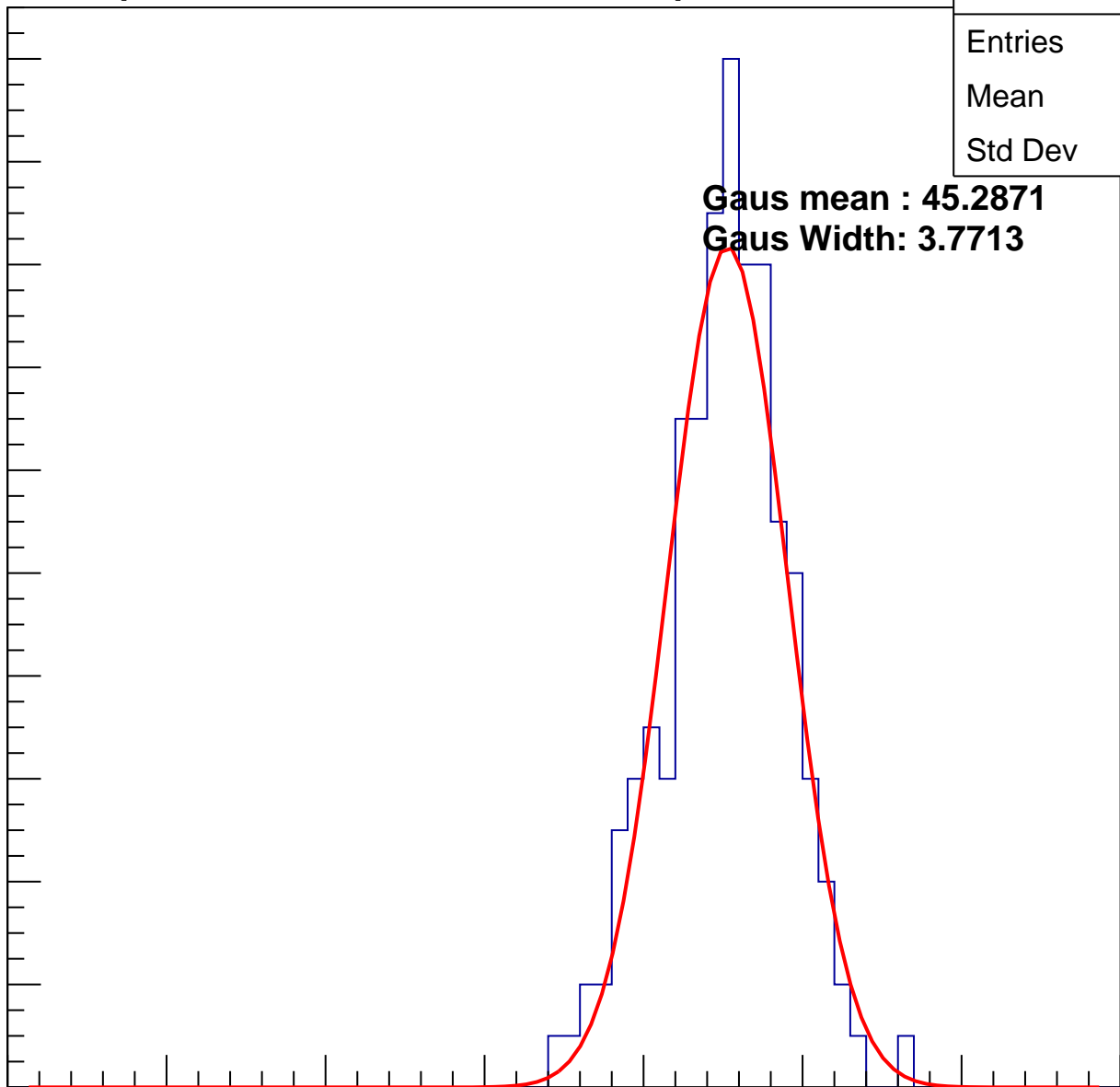
Entries	160
Mean	44.63
Std Dev	3.811

**Gaus mean : 45.2871**

**Gaus Width: 3.7713**

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	51.86
Std Dev	3.189

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

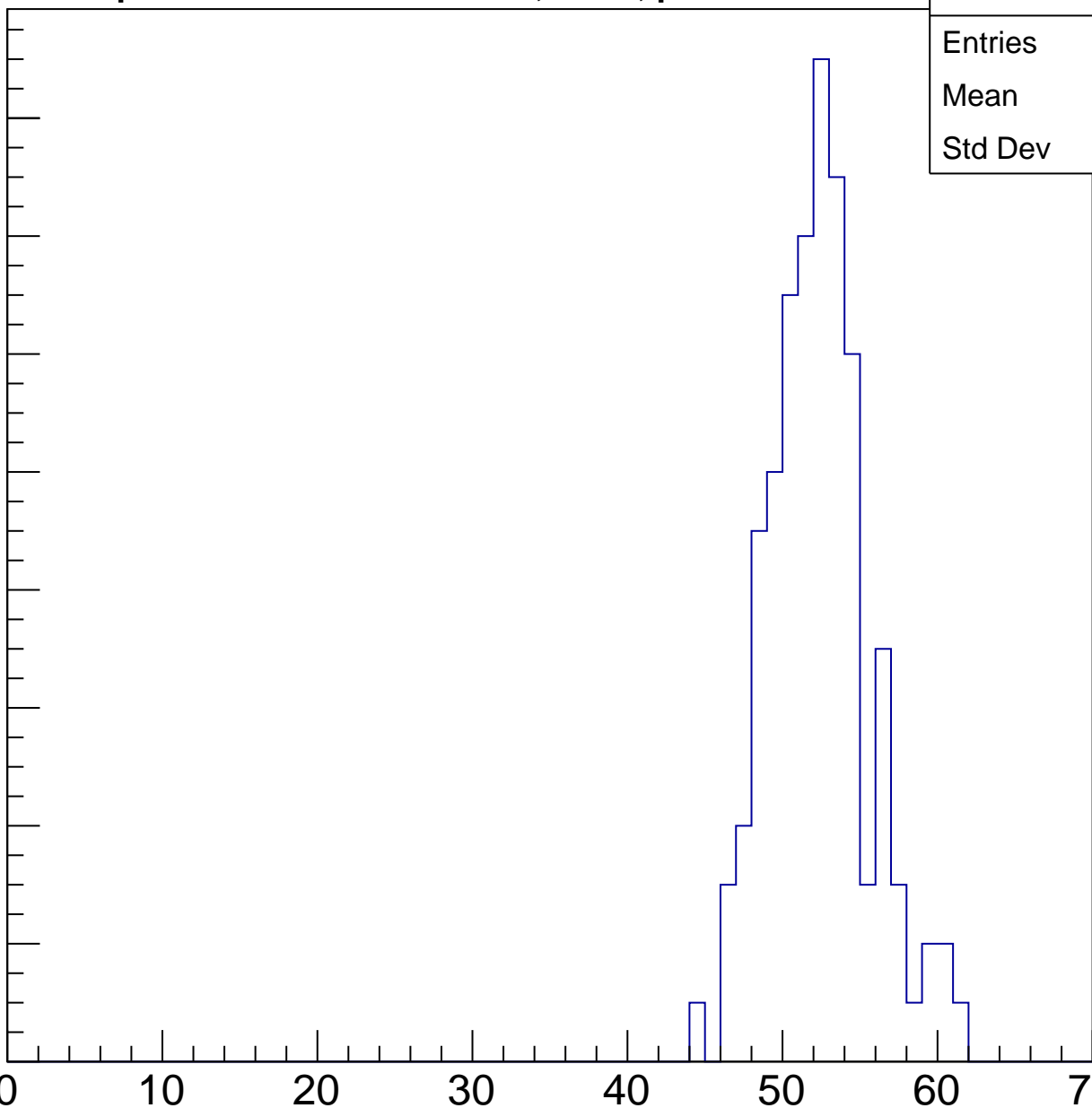
30

40

50

60

ampl



# B0L101S, U23-ch3, adc4

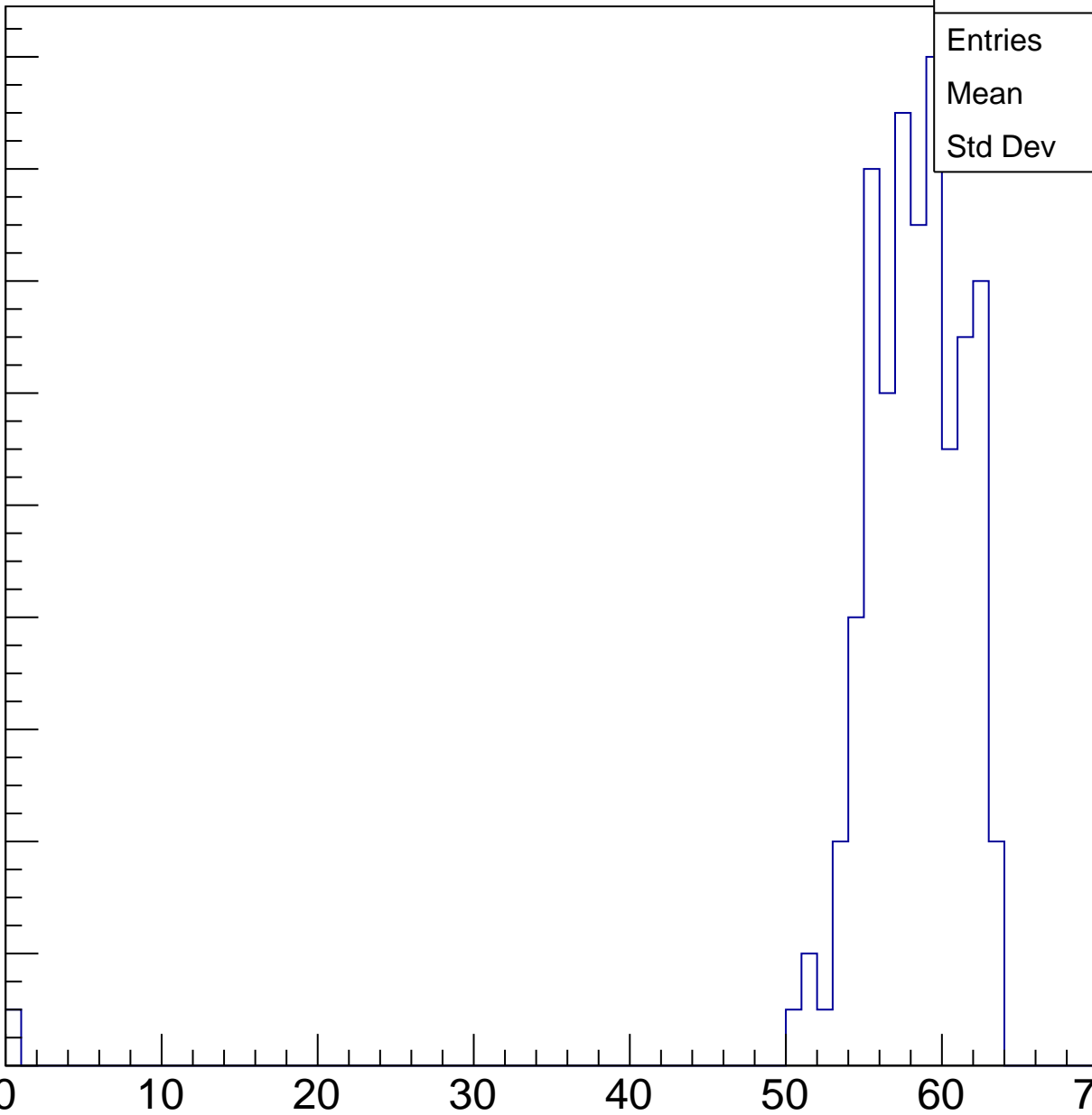
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	137
Mean	57.47
Std Dev	5.708

ampl



# B0L101S, U23-ch3, adc5

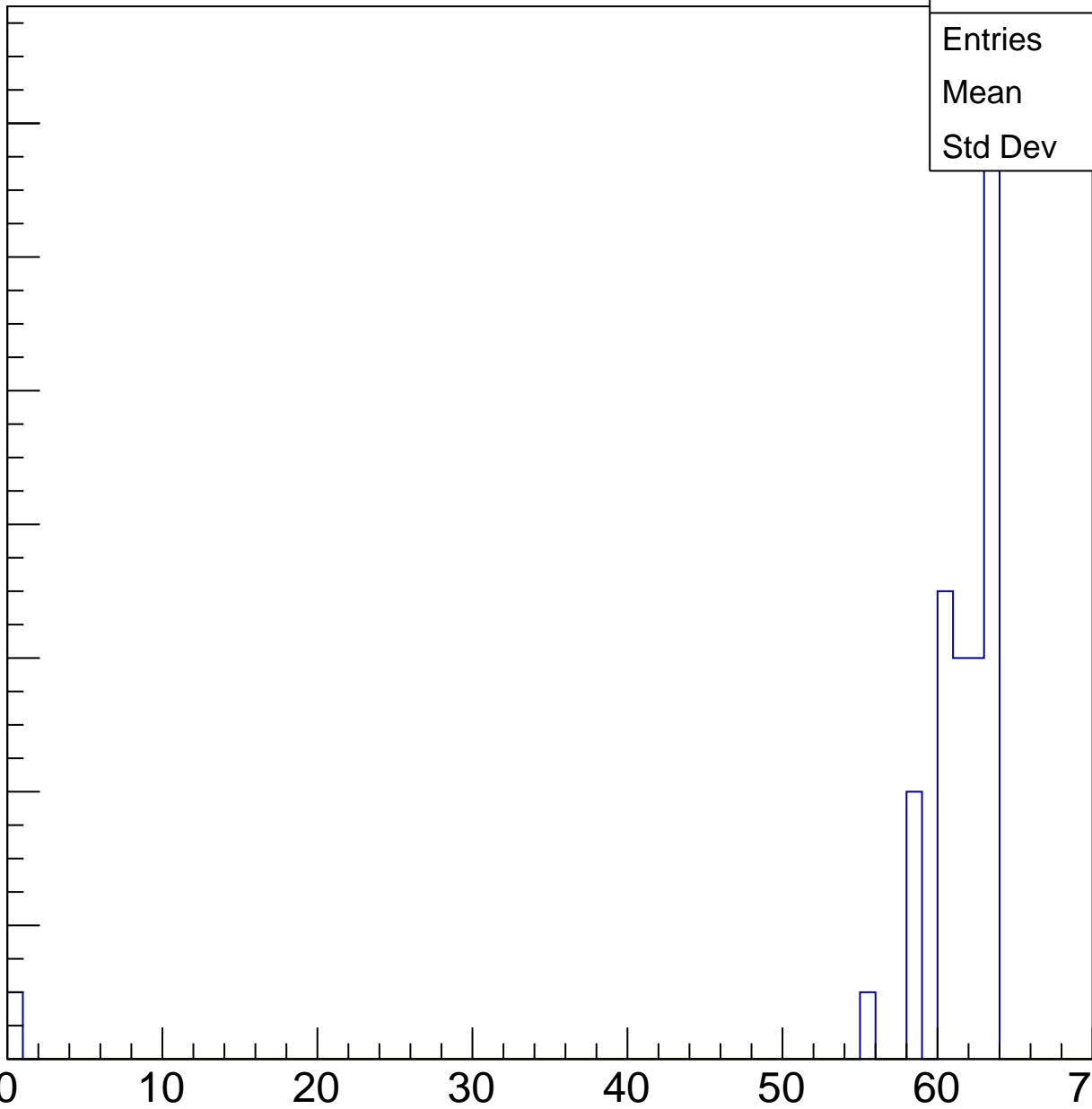
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	40
Mean	59.75
Std Dev	9.751

ampl



# B0L101S, U23-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

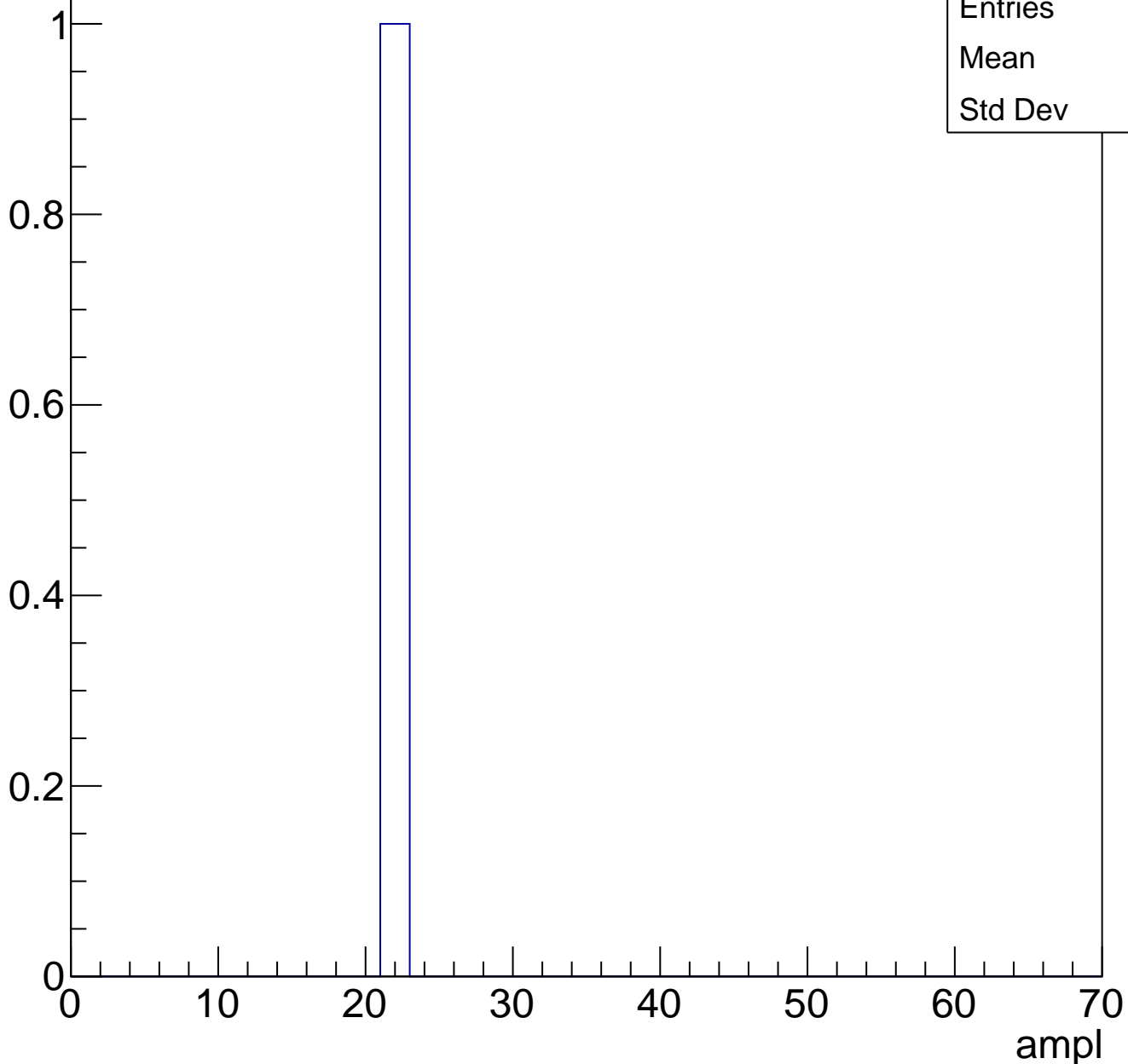




# B0L101S, U23-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

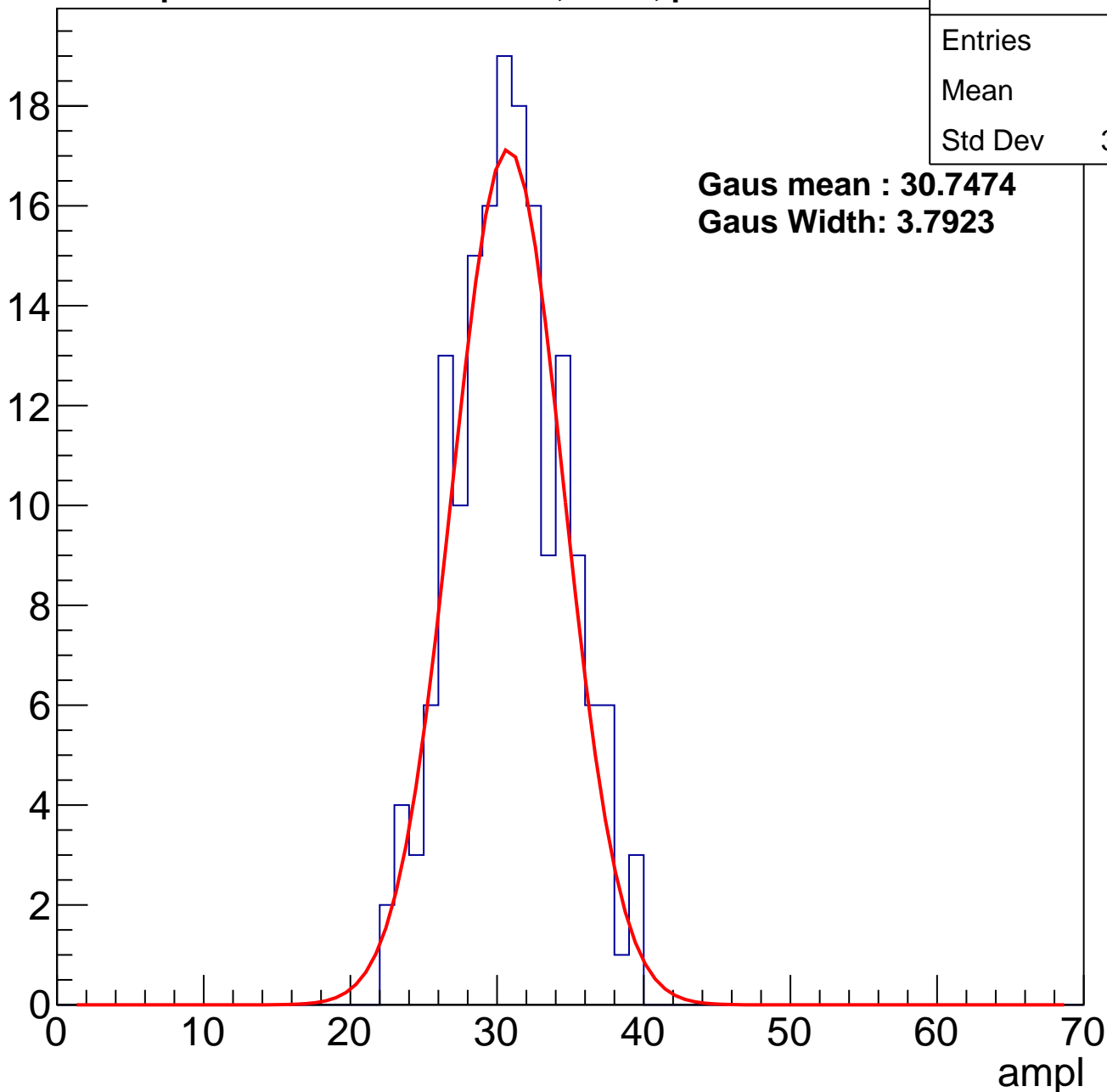


Entries	2
Mean	21.5
Std Dev	0.5

# B0L101S, U23-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



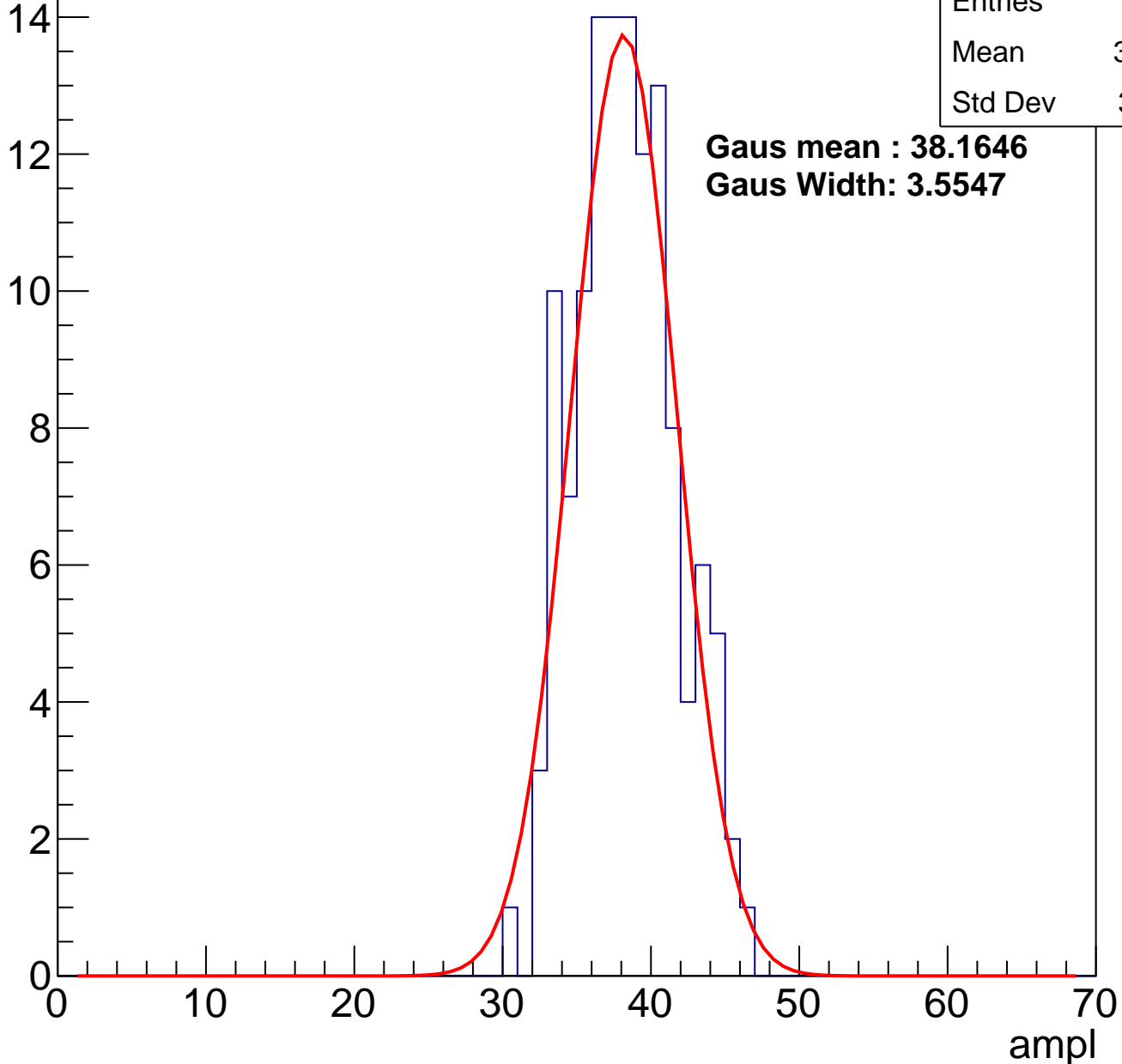
# B0L101S, U23-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	37.87
Std Dev	3.341

**Gaus mean : 38.1646**  
**Gaus Width: 3.5547**

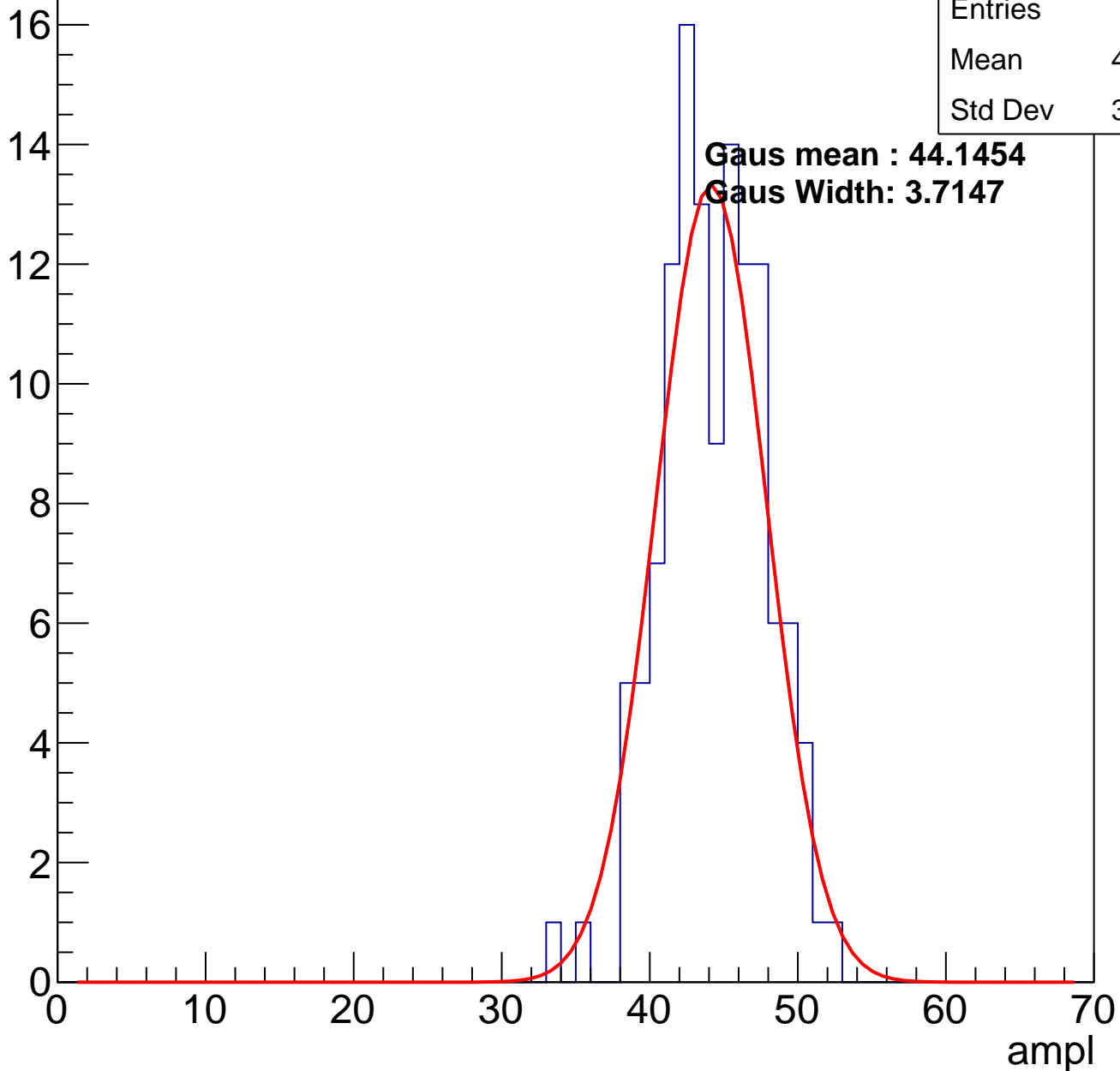
Entry



# B0L101S, U23-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

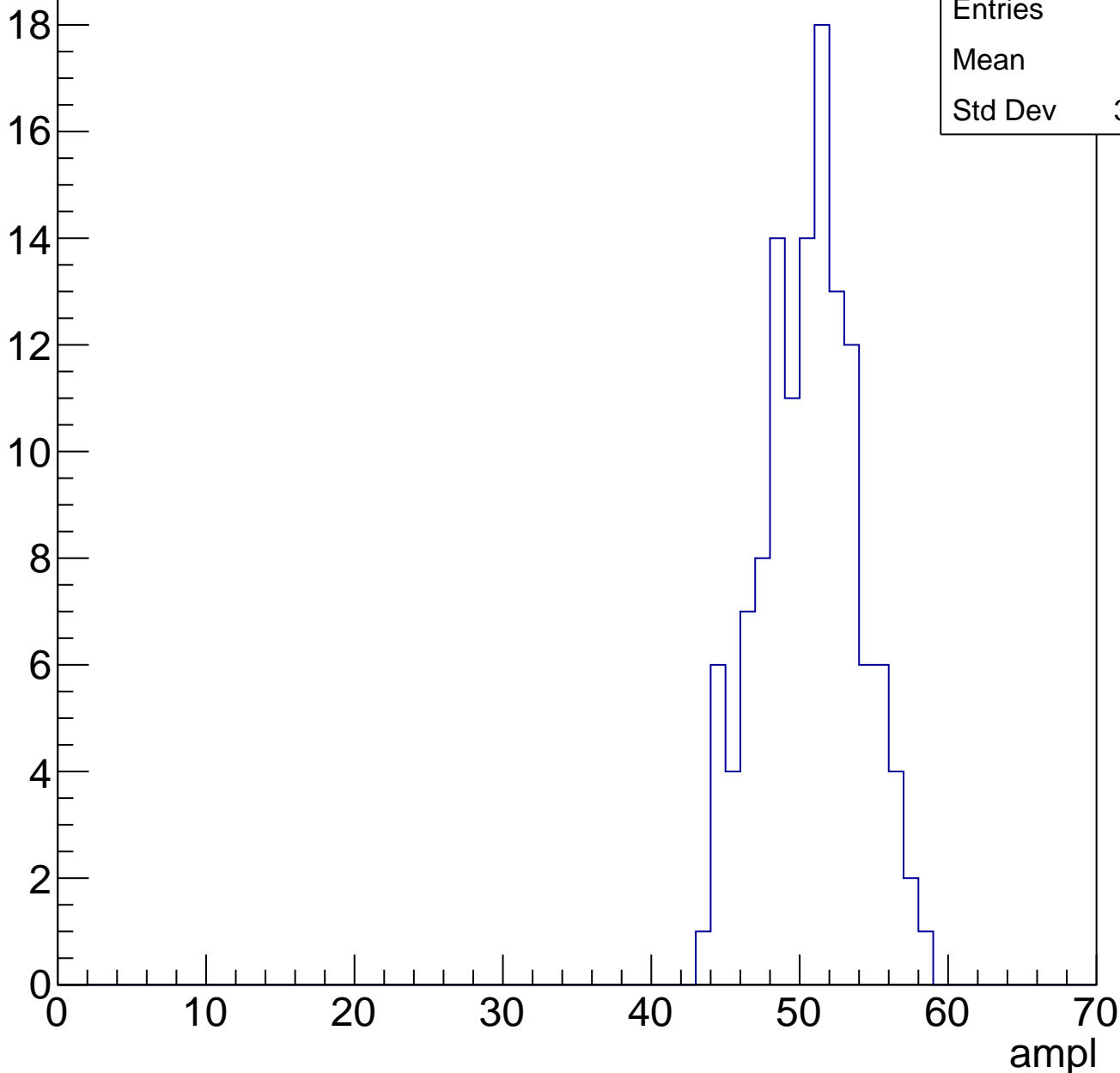
Entry



# B0L101S, U23-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch4, adc4

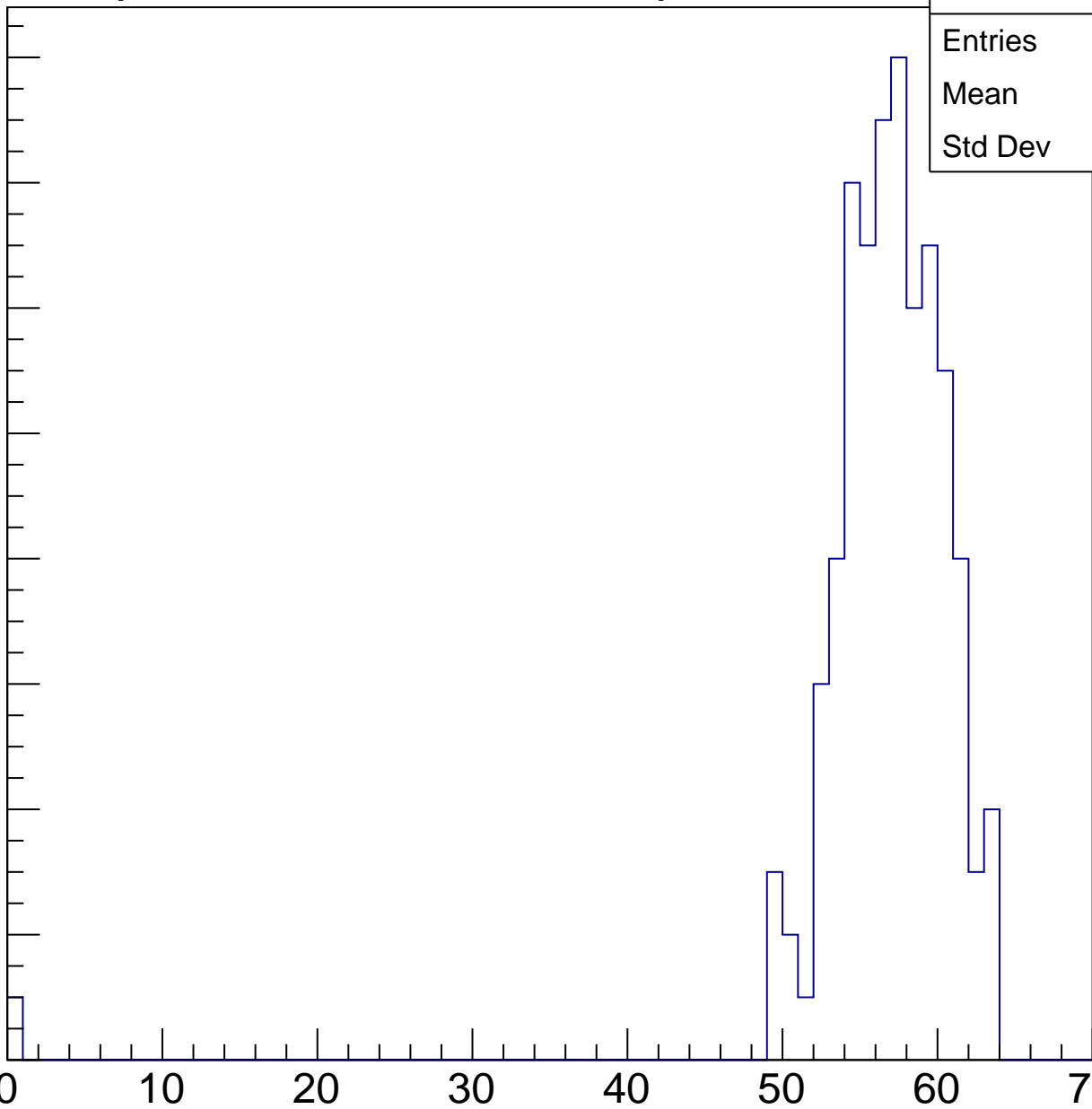
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	130
Mean	56.2
Std Dev	5.866

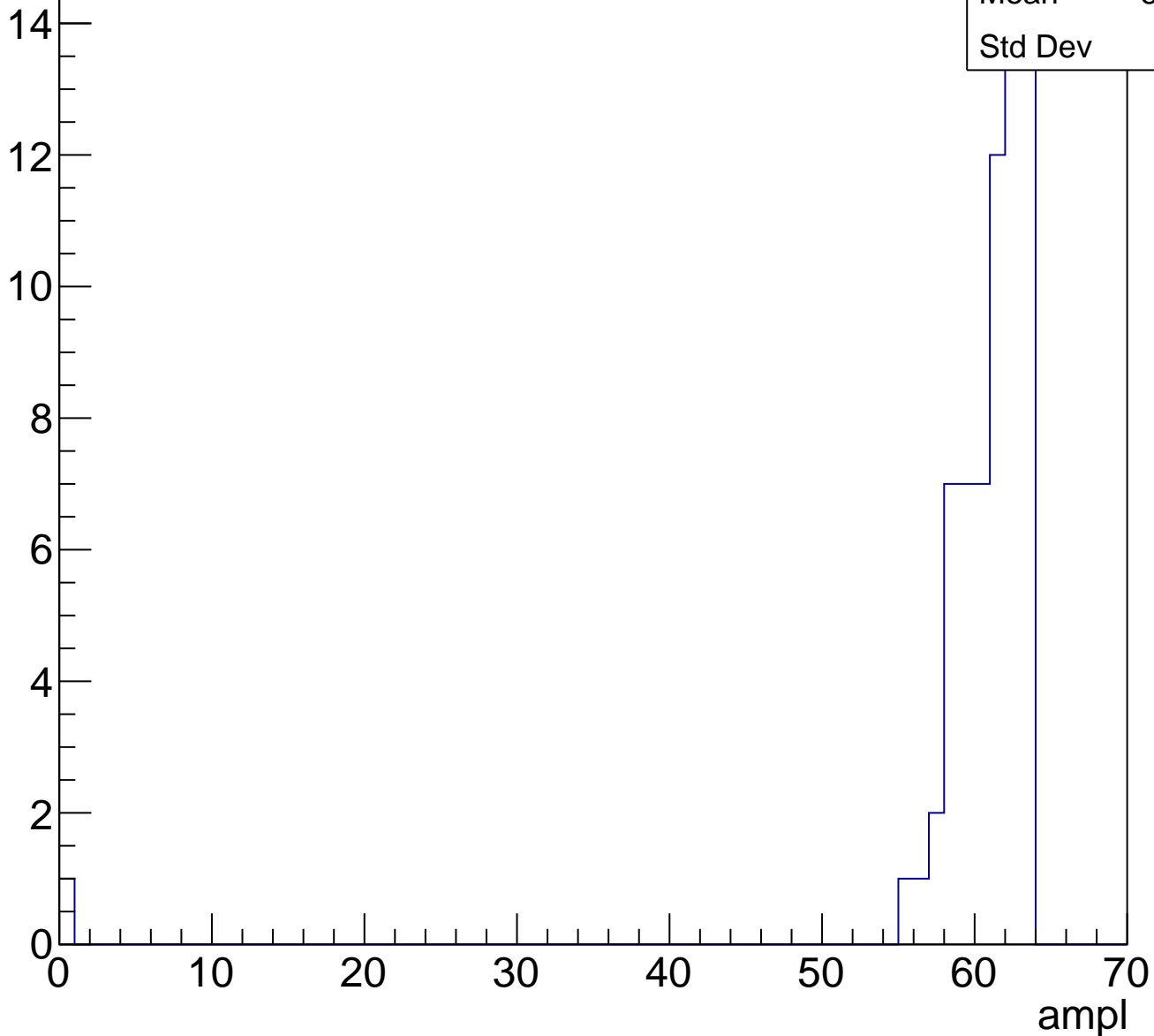
ampl



# B0L101S, U23-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

0

10

20

30

40

50

60

70

ampl

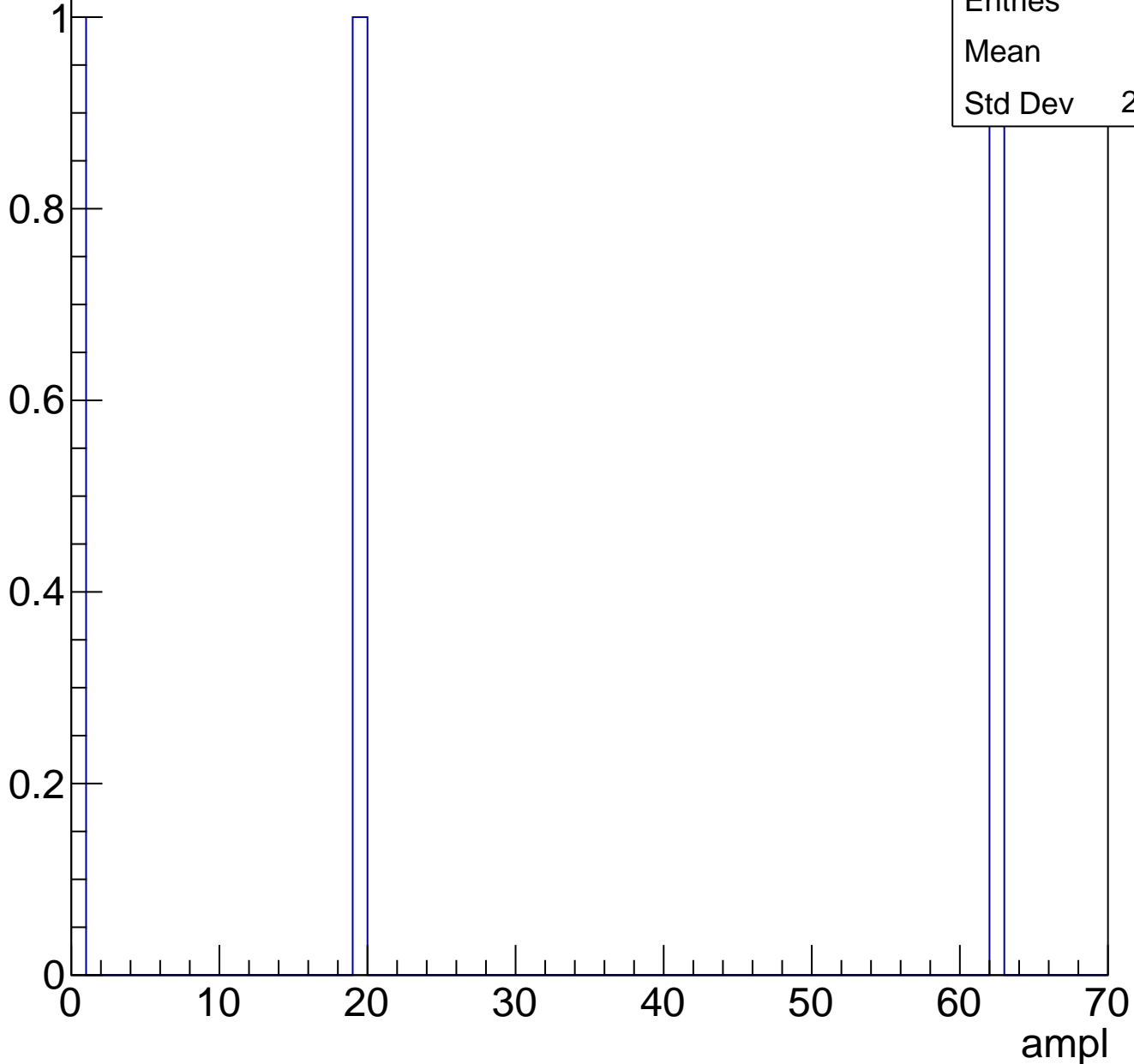
Entries	5
Mean	61.6
Std Dev	1.356



# B0L101S, U23-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	27
Std Dev	25.94

# B0L101S, U23-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

29.92

Std Dev

4.259

**Gaus mean : 30.5891**

**Gaus Width: 3.5010**

0

10

20

30

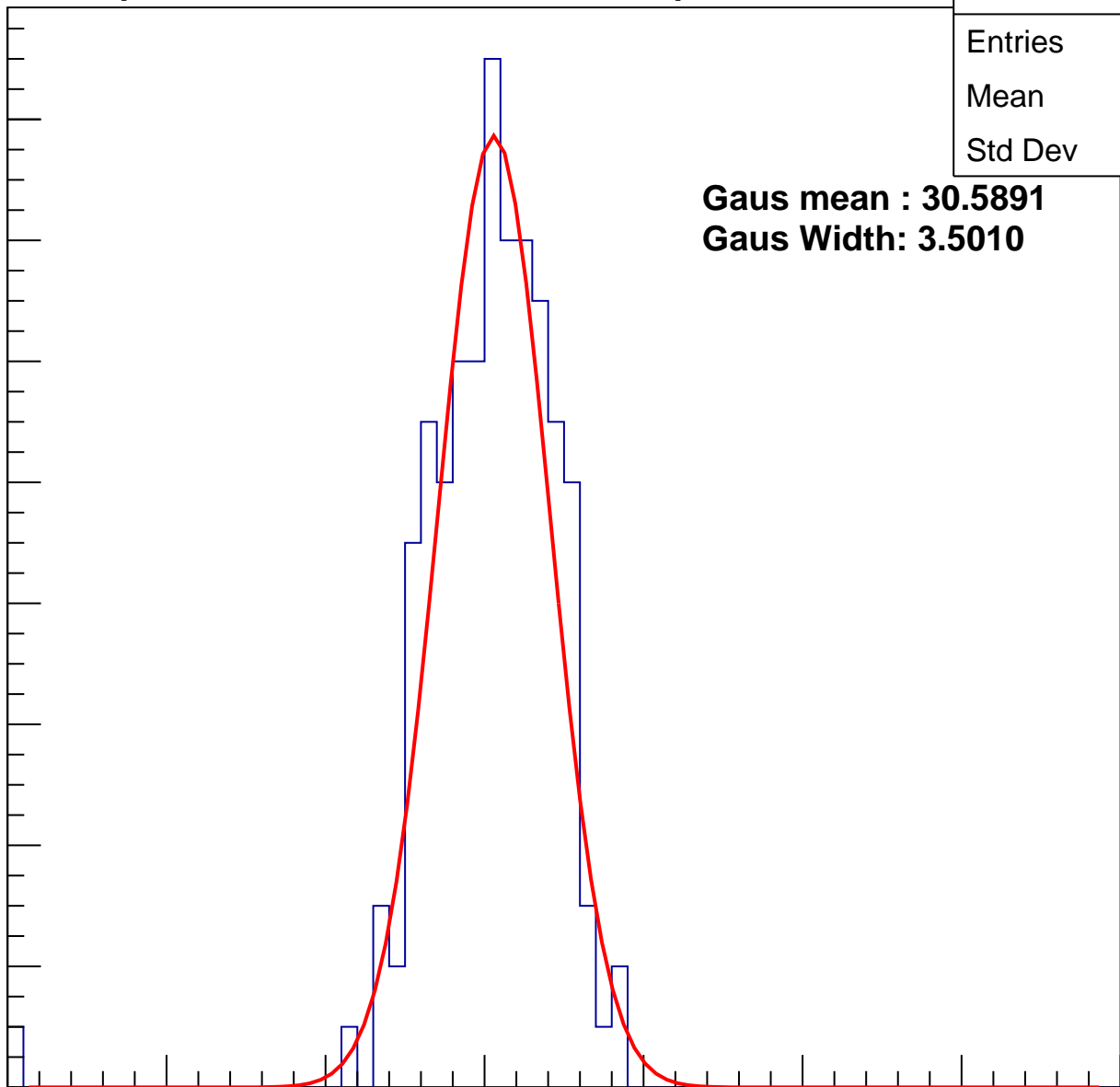
40

50

60

70

ampl



# B0L101S, U23-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	37.23
Std Dev	4.786

**Gaus mean : 38.0303**

**Gaus Width: 3.7629**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

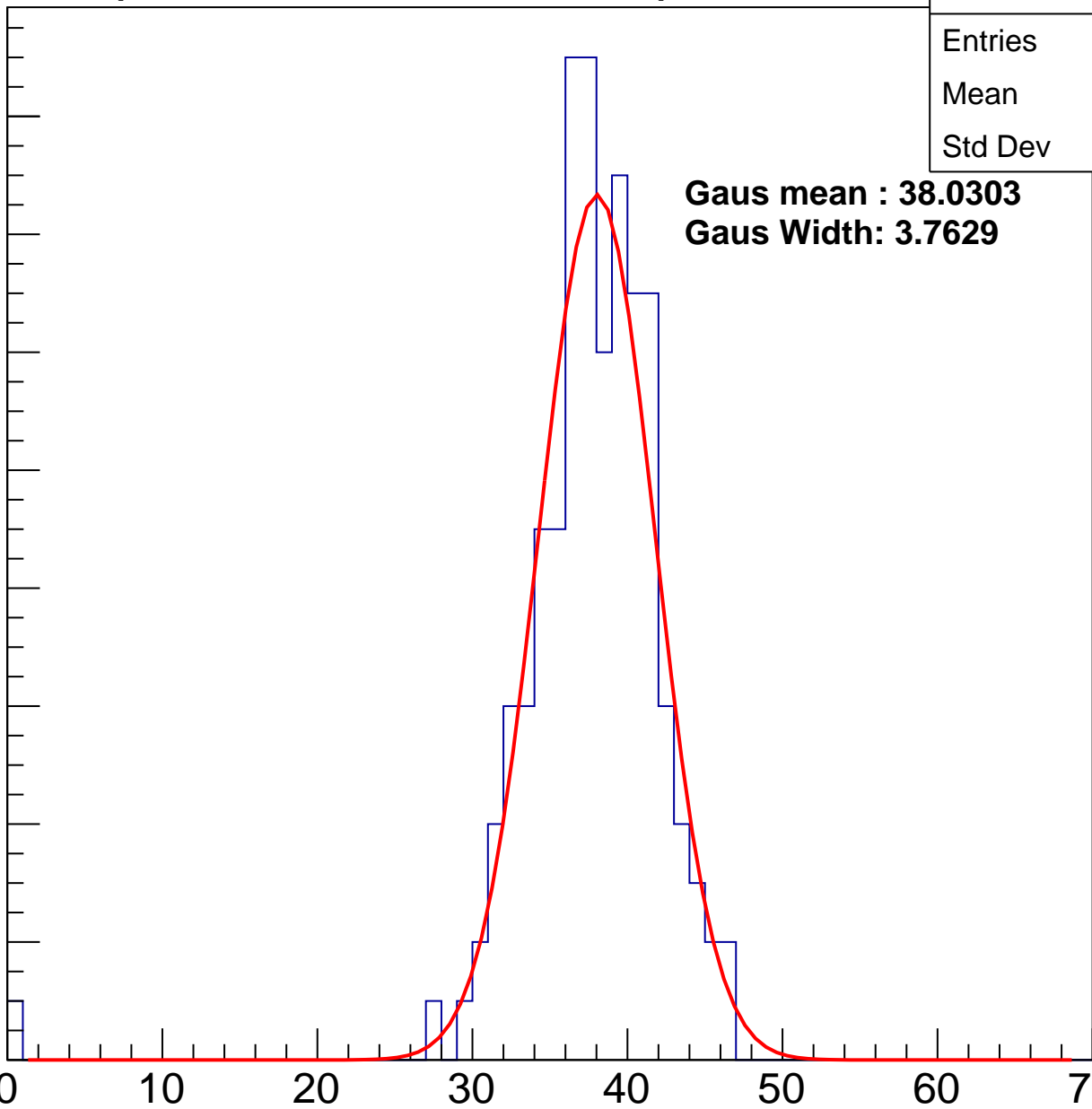
40

50

60

70

ampl



# B0L101S, U23-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	44.38
Std Dev	3.726

**Gaus mean : 44.8019**

**Gaus Width: 3.4916**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

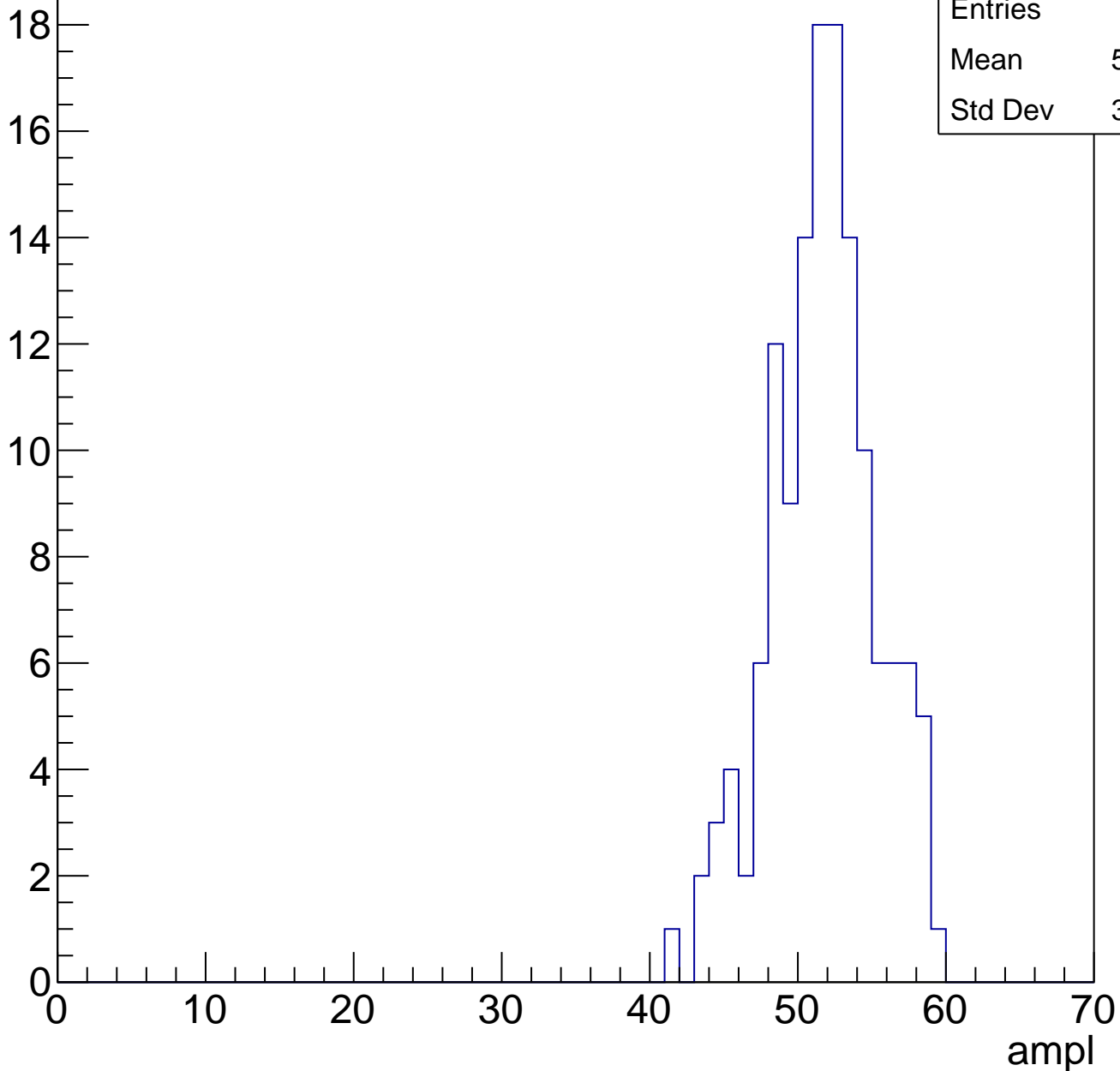
60

70

# B0L101S, U23-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

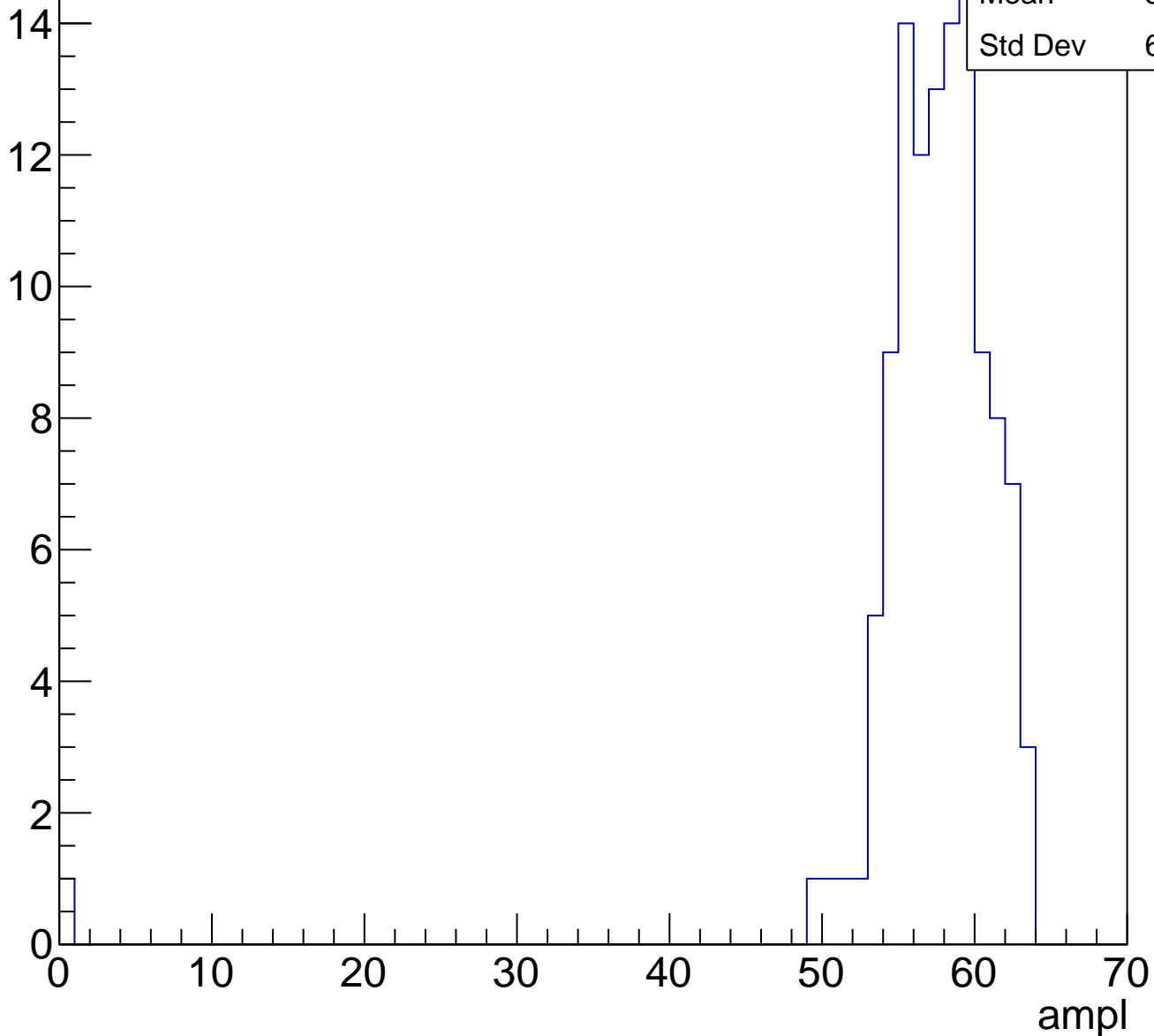


Entries	137
Mean	51.26
Std Dev	3.573

# B0L101S, U23-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch5, adc5

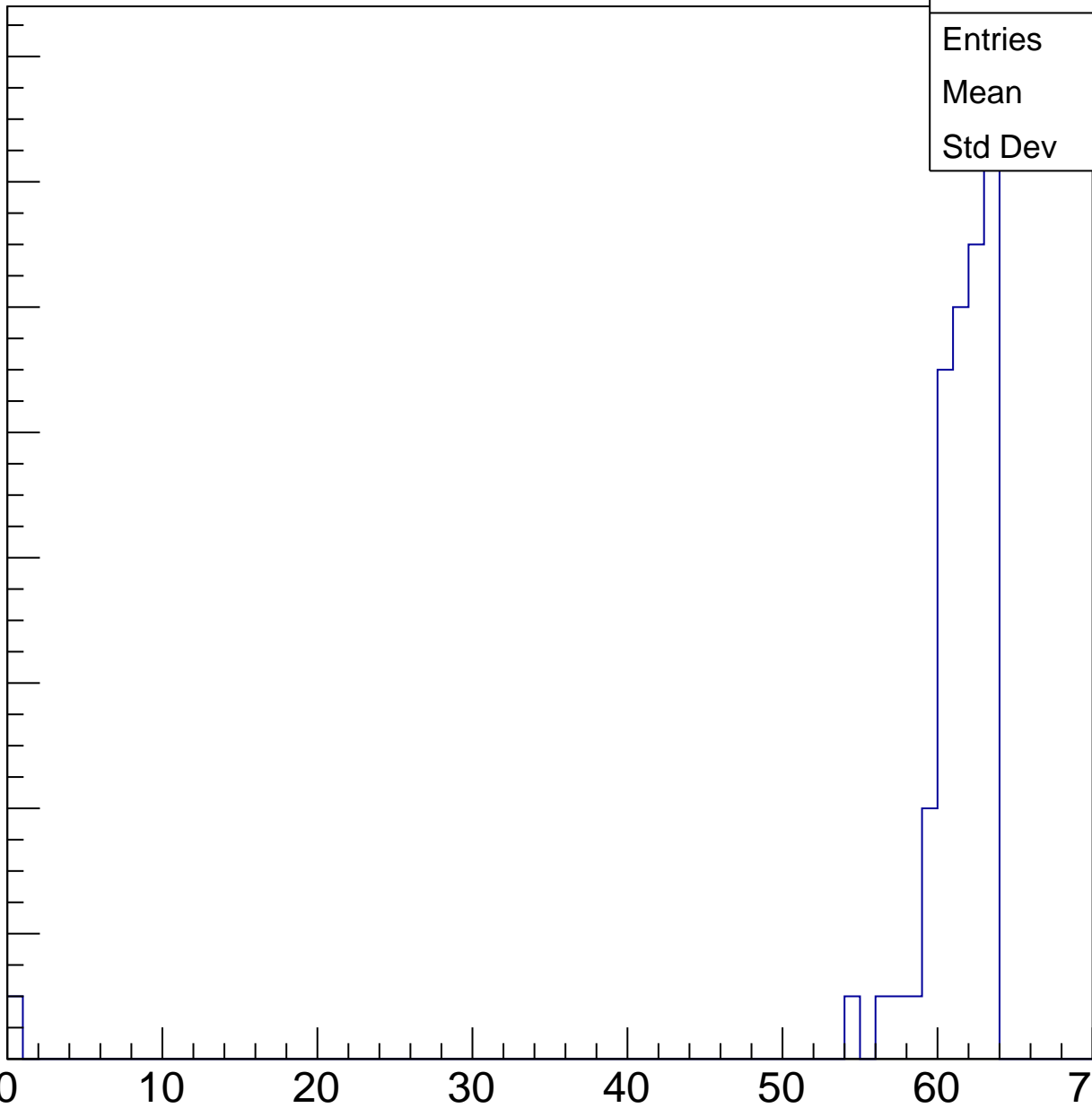
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	61
Mean	60.11
Std Dev	7.971

ampl



# B0L101S, U23-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch6, adc0

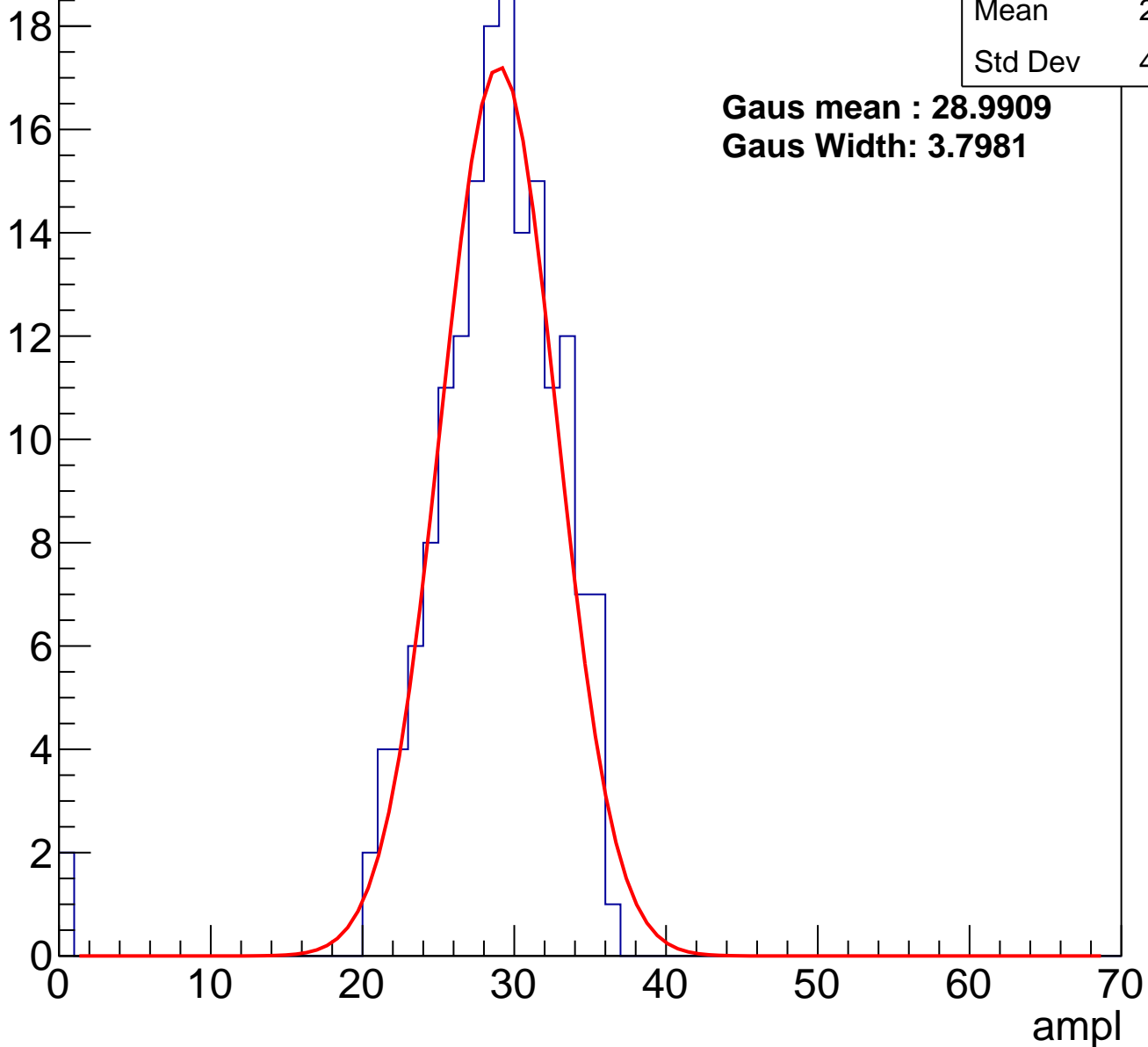
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	168
Mean	28.22
Std Dev	4.764

**Gaus mean : 28.9909**

**Gaus Width: 3.7981**

Entry



# B0L101S, U23-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	36.02
Std Dev	3.381

**Gaus mean : 36.0882**

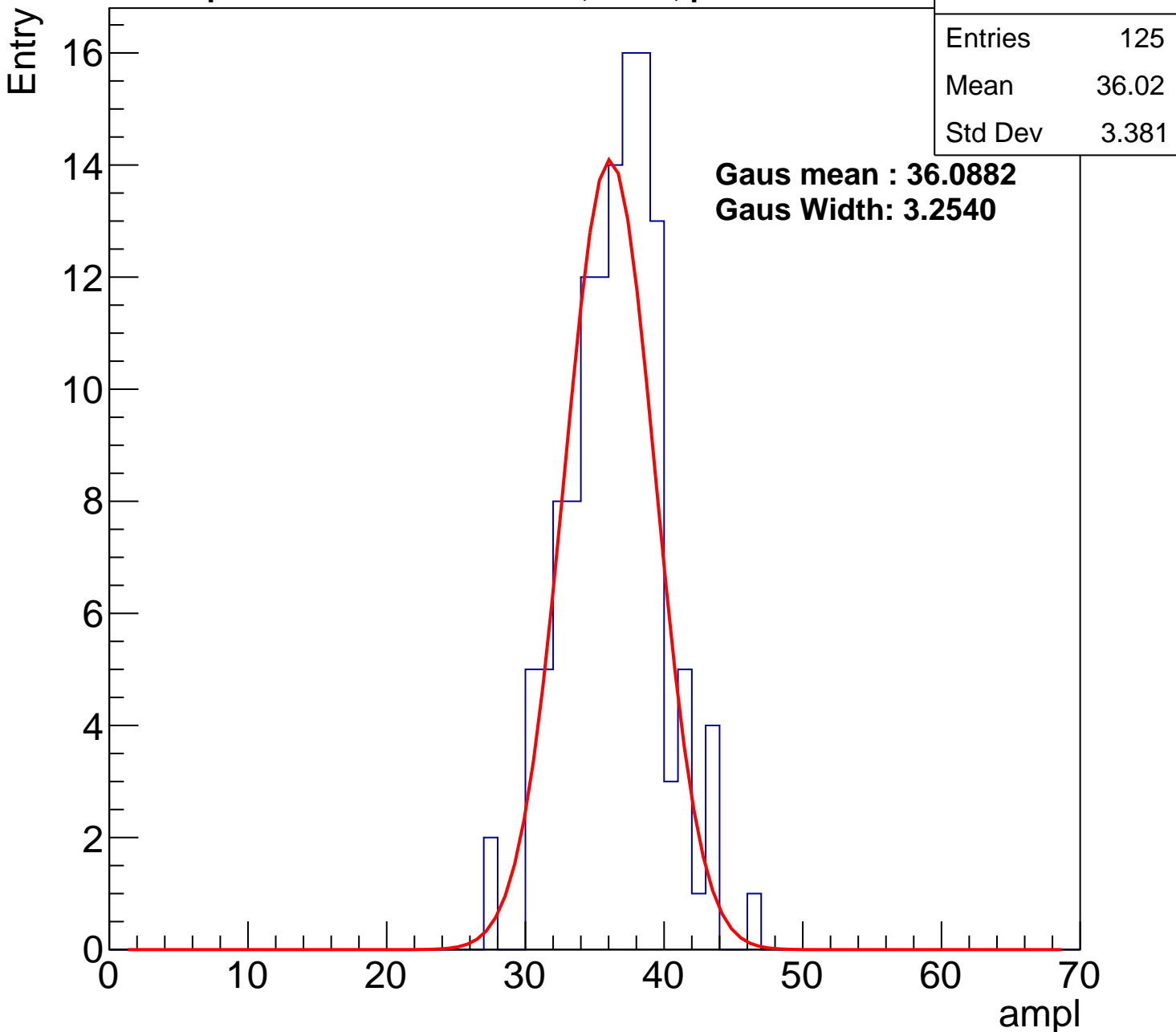
**Gaus Width: 3.2540**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	41.97
Std Dev	3.224

**Gaus mean : 42.6700**

**Gaus Width: 3.1282**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

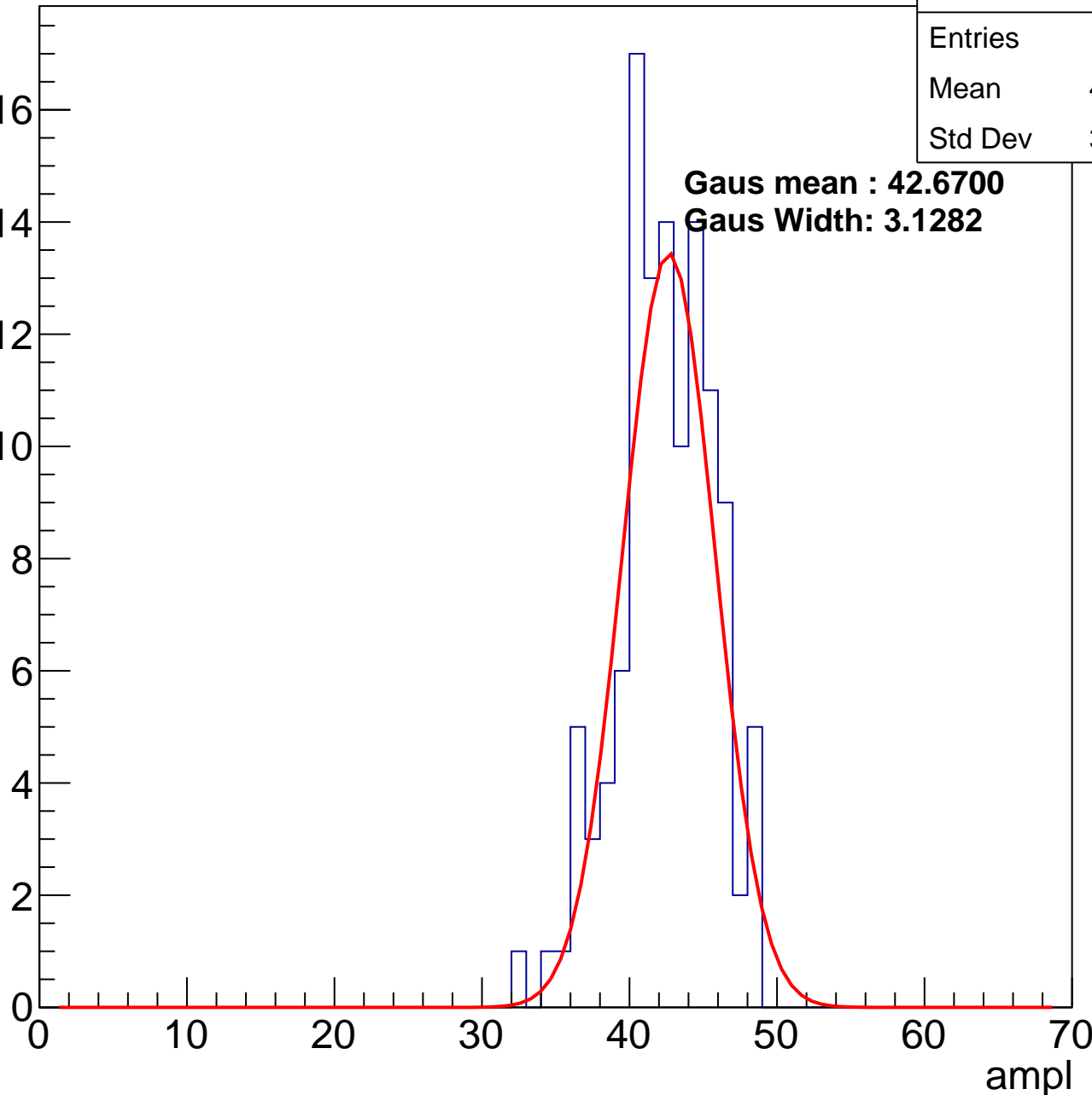
40

50

60

70

ampl



# B0L101S, U23-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	48.92
Std Dev	4.043

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

ampl

# B0L101S, U23-ch6, adc4

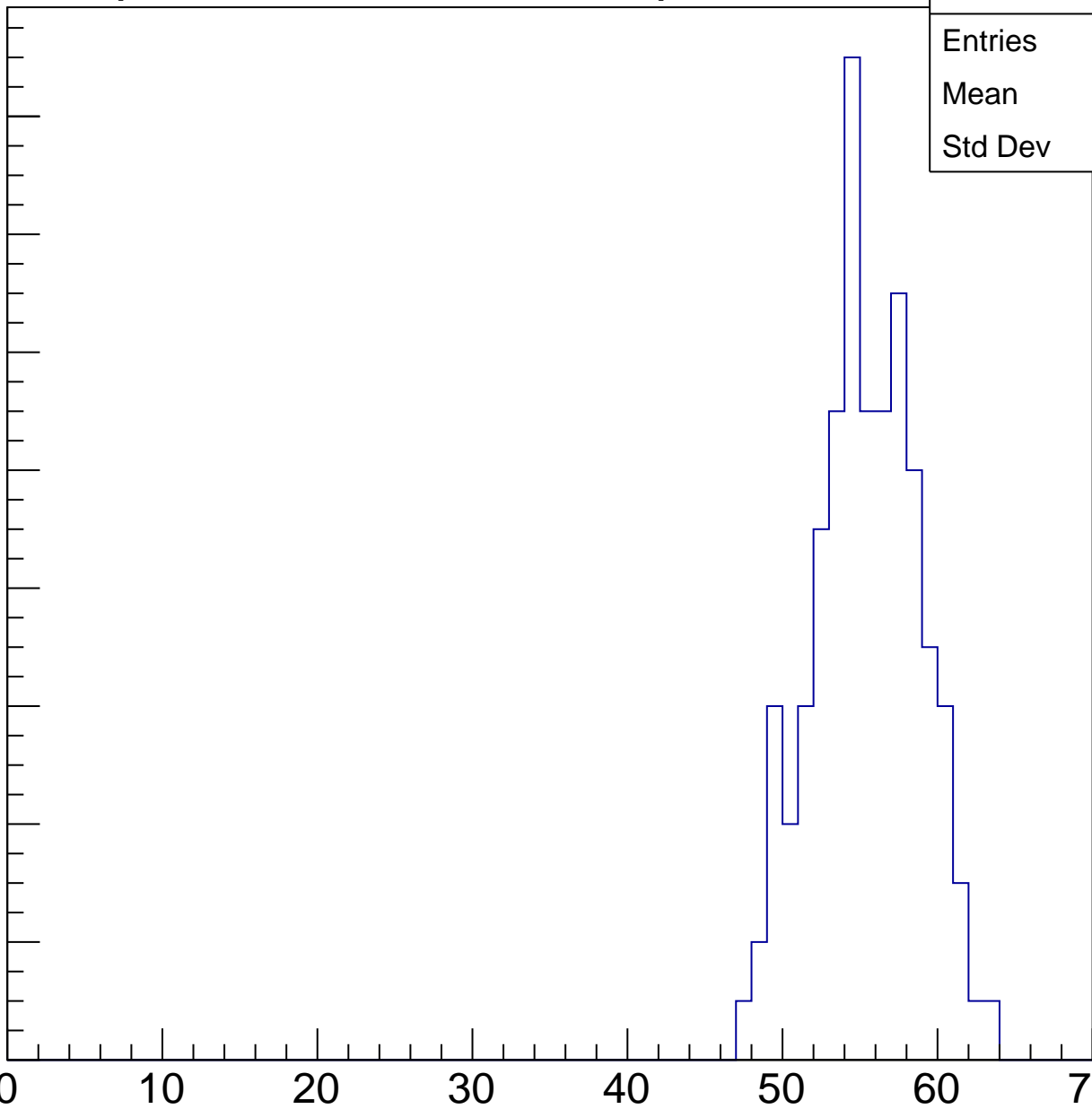
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	54.92
Std Dev	3.37

ampl



# B0L101S, U23-ch6, adc5

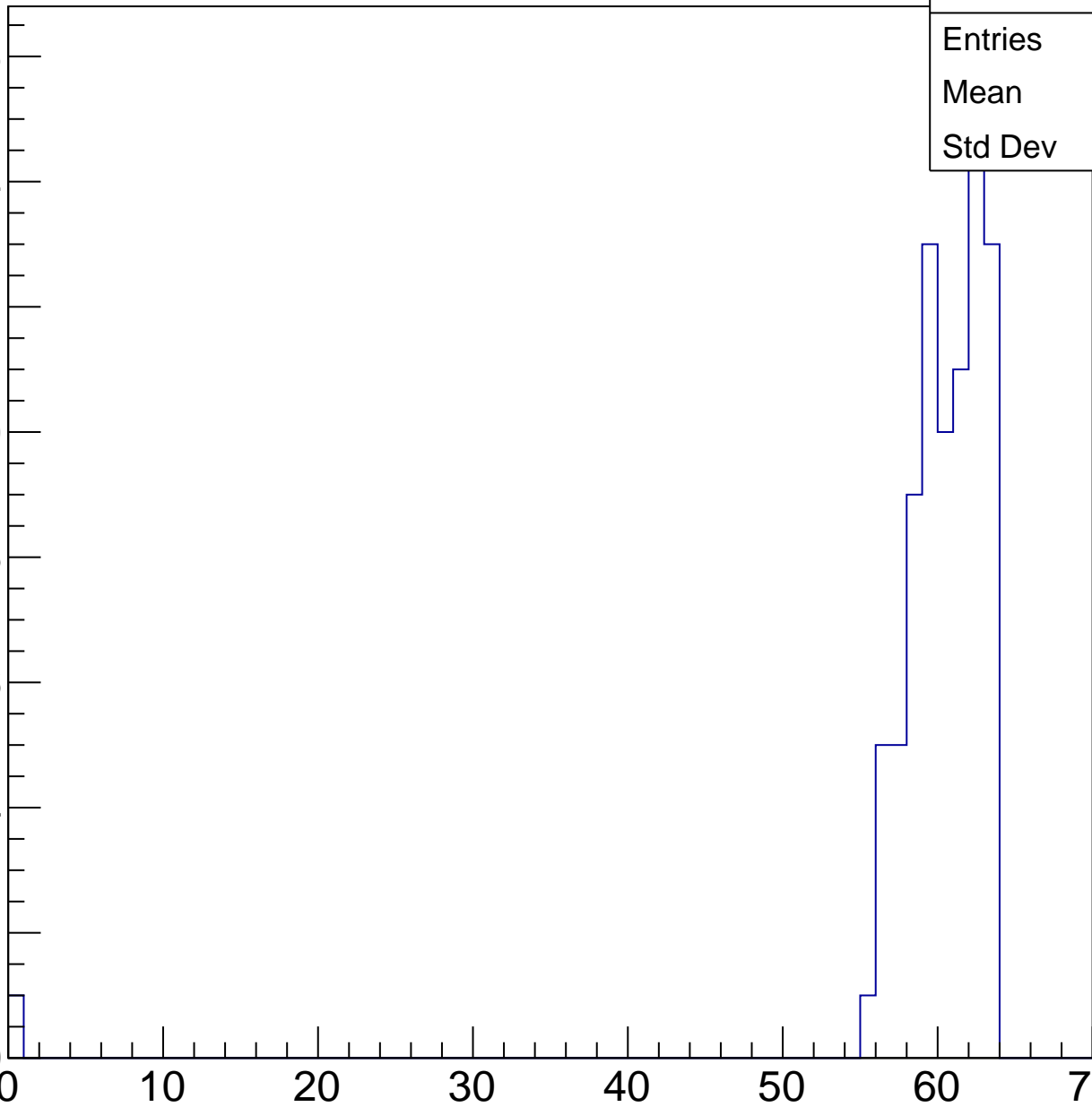
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	84
Mean	59.42
Std Dev	6.868

ampl

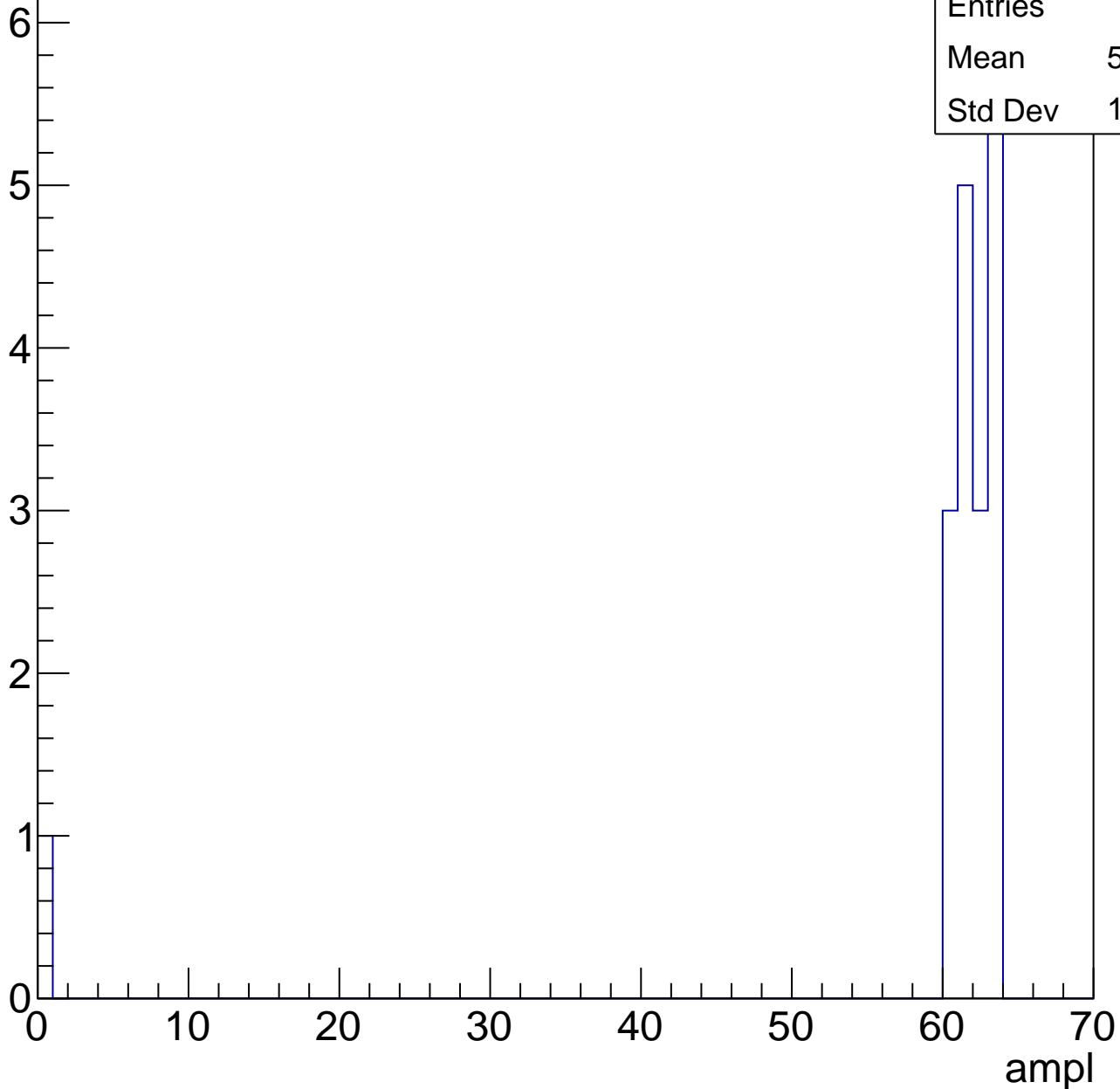


# B0L101S, U23-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	18
Mean	58.28
Std Dev	14.18





# B0L101S, U23-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

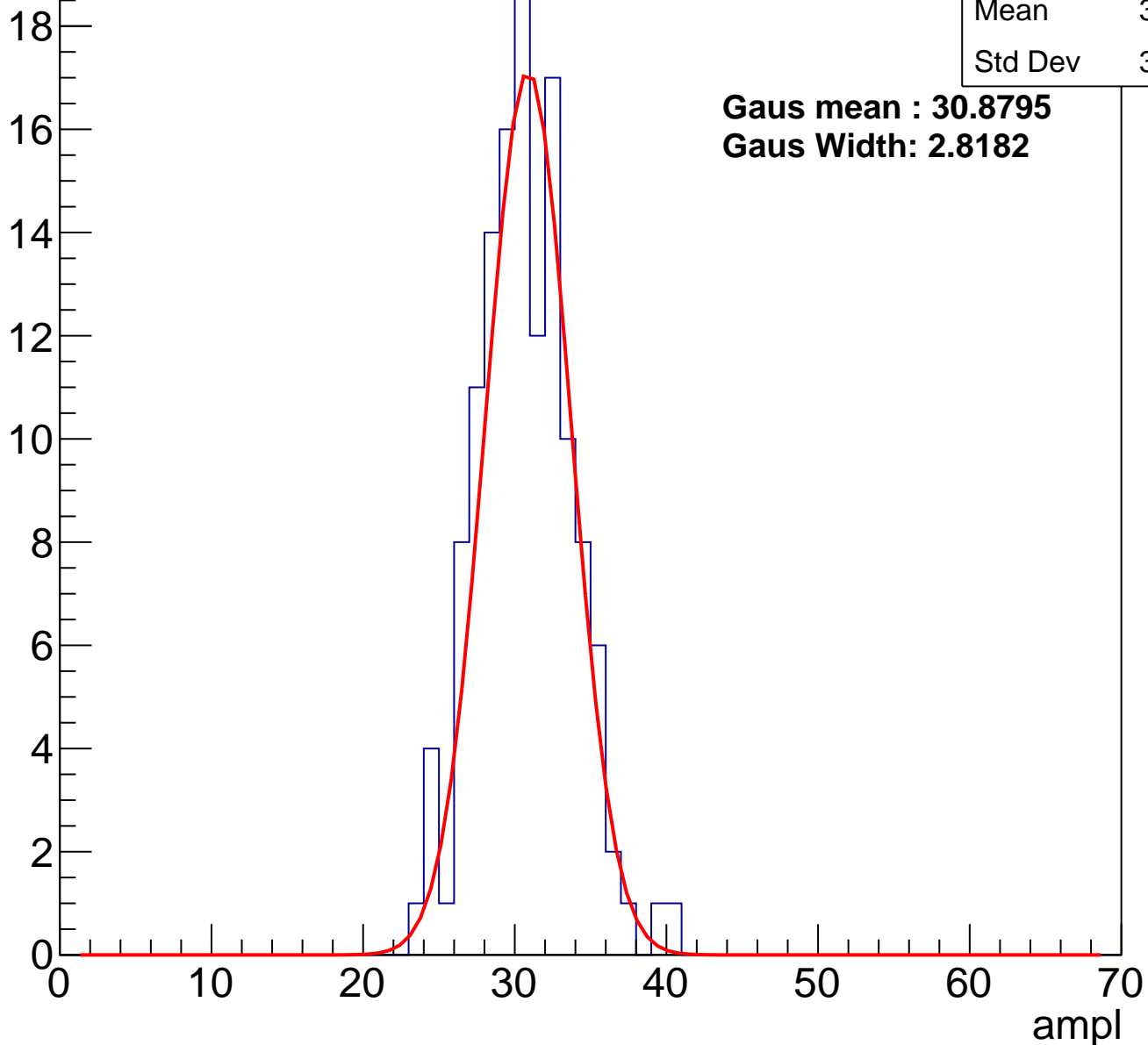
Entry



# B0L101S, U23-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



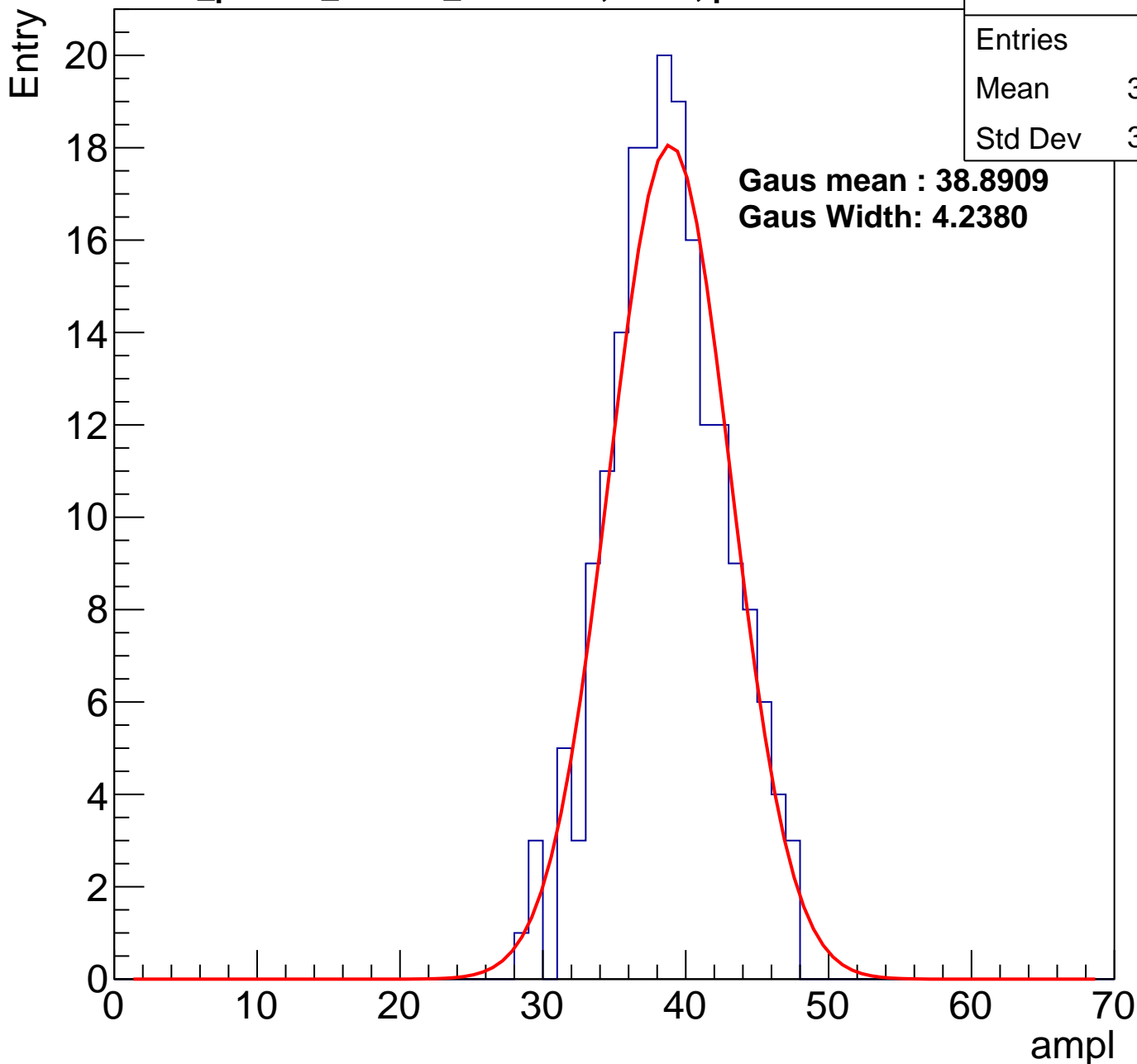
# B0L101S, U23-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	191
Mean	38.28
Std Dev	3.954

**Gaus mean : 38.8909**

**Gaus Width: 4.2380**



# B0L101S, U23-ch7, adc2

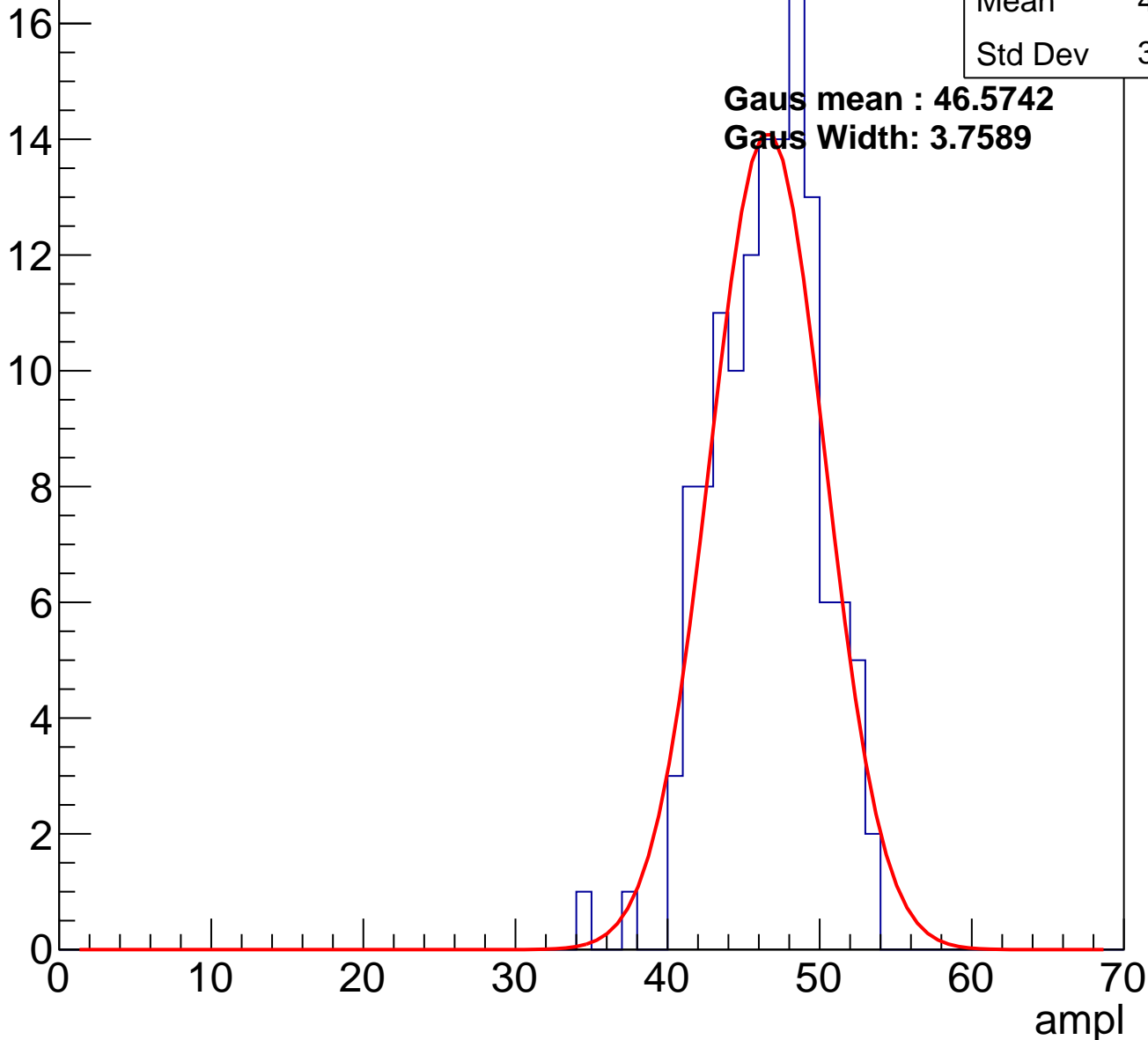
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	131
Mean	46.07
Std Dev	3.422

**Gaus mean : 46.5742**

**Gaus Width: 3.7589**



# B0L101S, U23-ch7, adc3

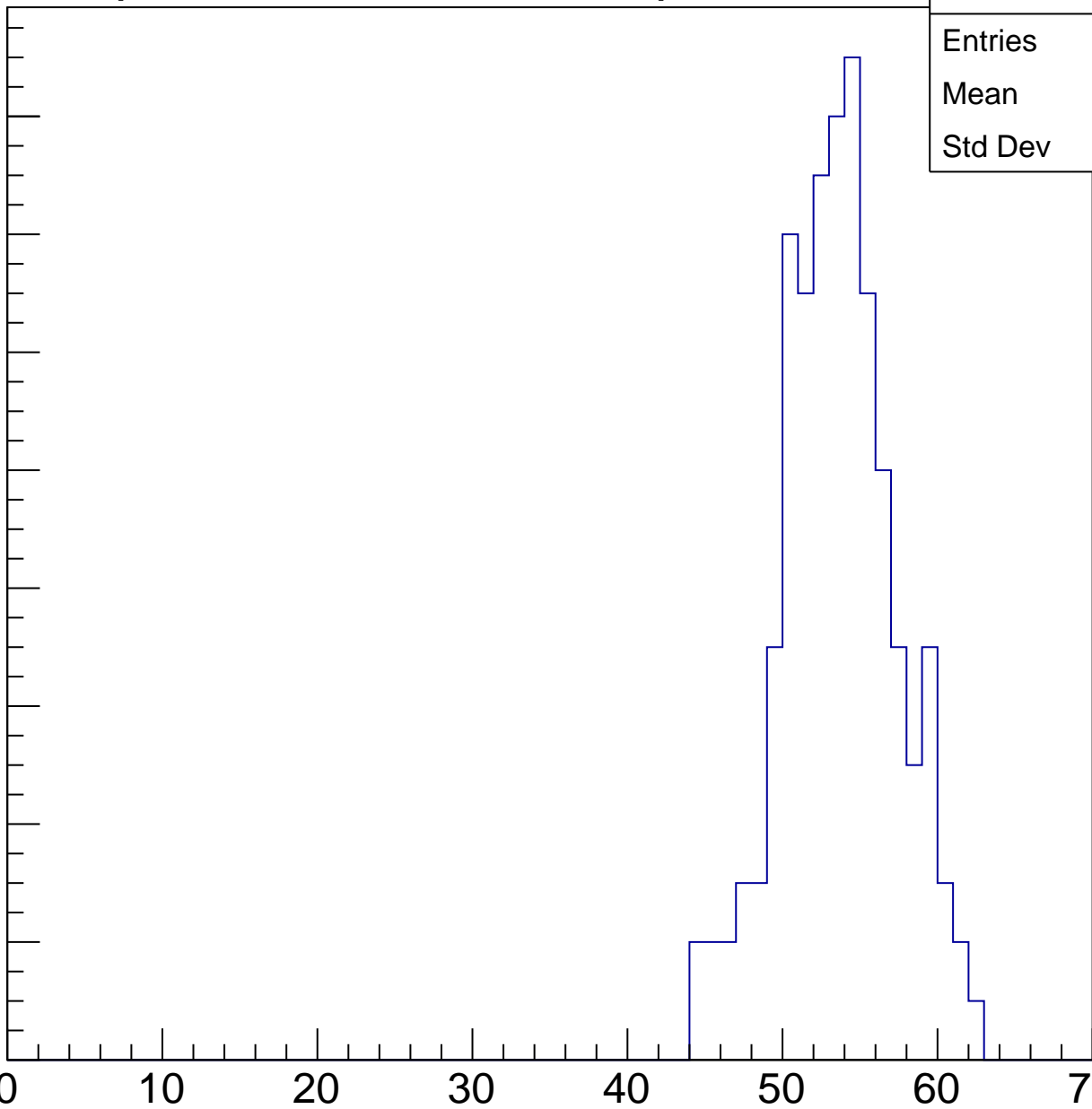
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	142
Mean	53.15
Std Dev	3.635

ampl



# B0L101S, U23-ch7, adc4

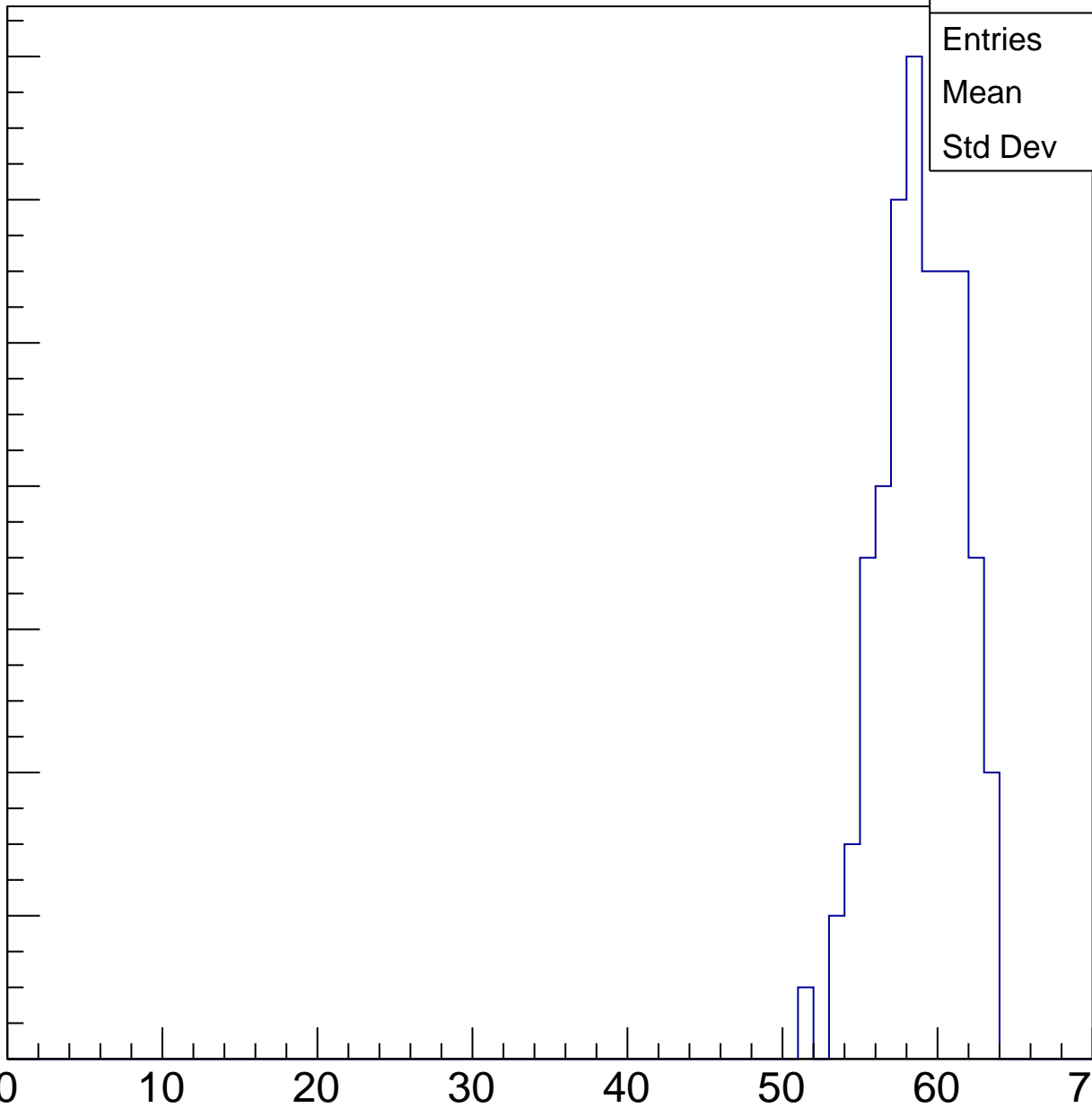
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	91
Mean	58.4
Std Dev	2.58

ampl



# B0L101S, U23-ch7, adc5

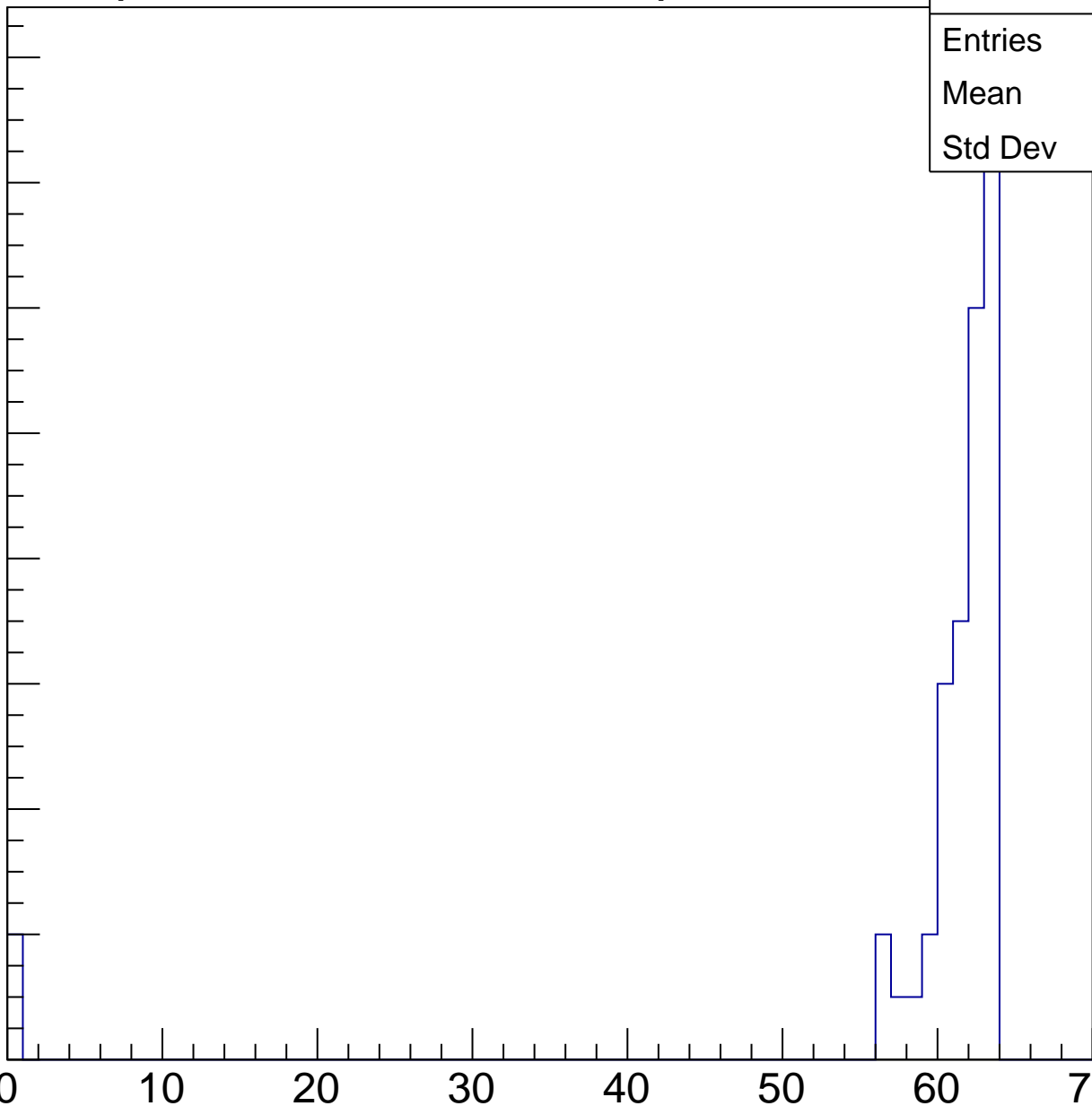
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	49
Mean	58.86
Std Dev	12.27

ampl



# B0L101S, U23-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

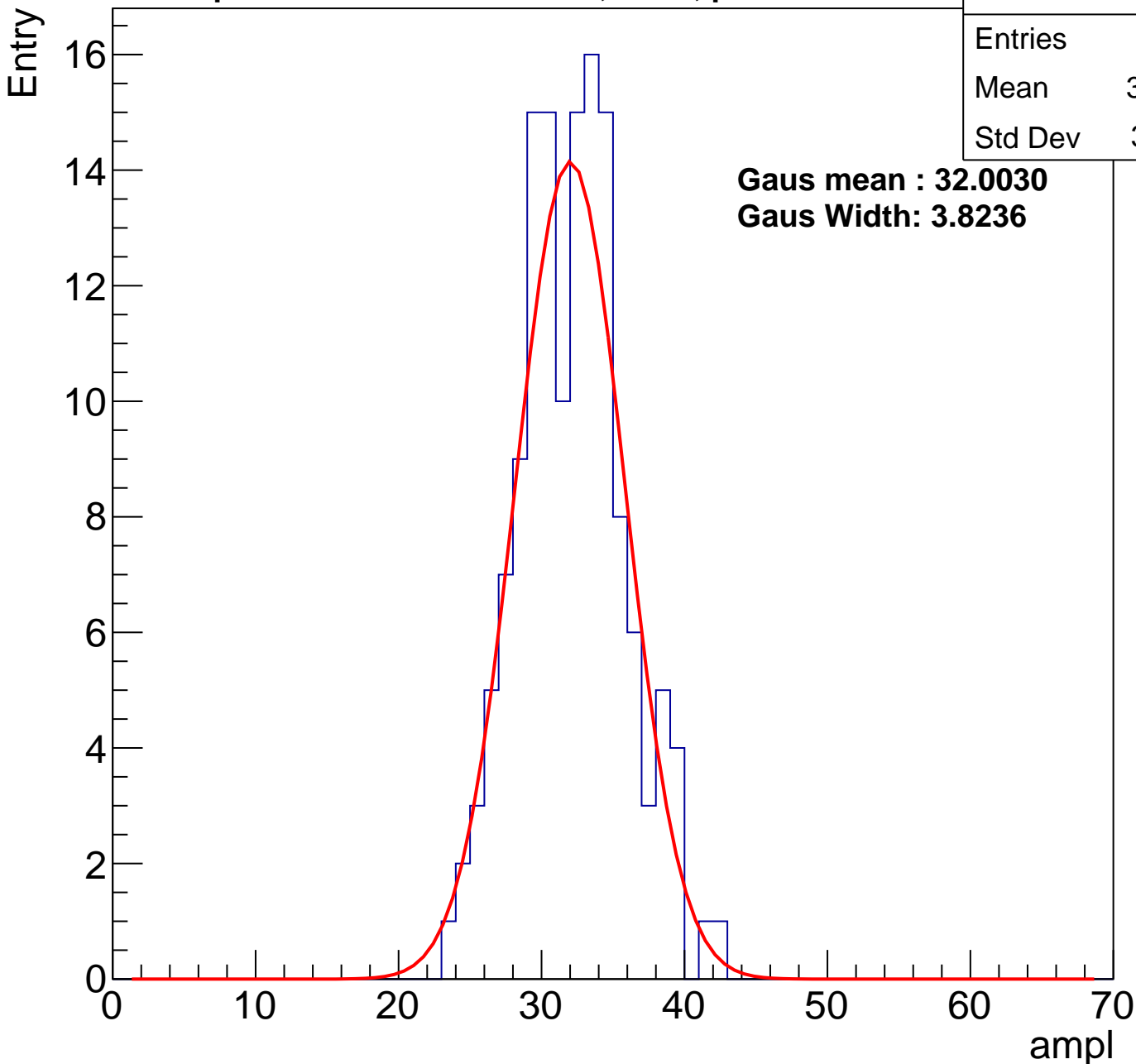
# B0L101S, U23-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	31.67
Std Dev	3.691

**Gaus mean : 32.0030**

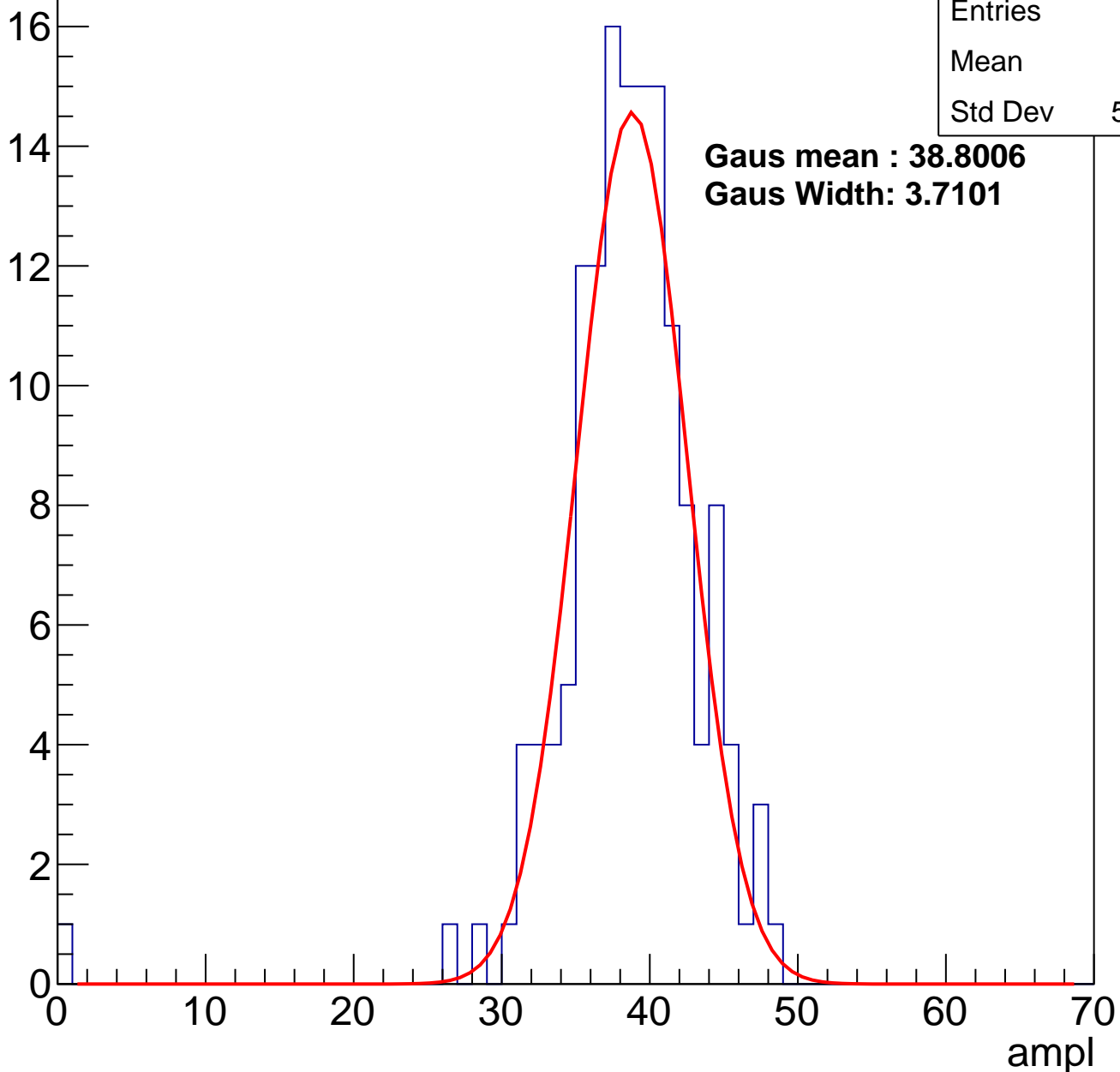
**Gaus Width: 3.8236**



# B0L101S, U23-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

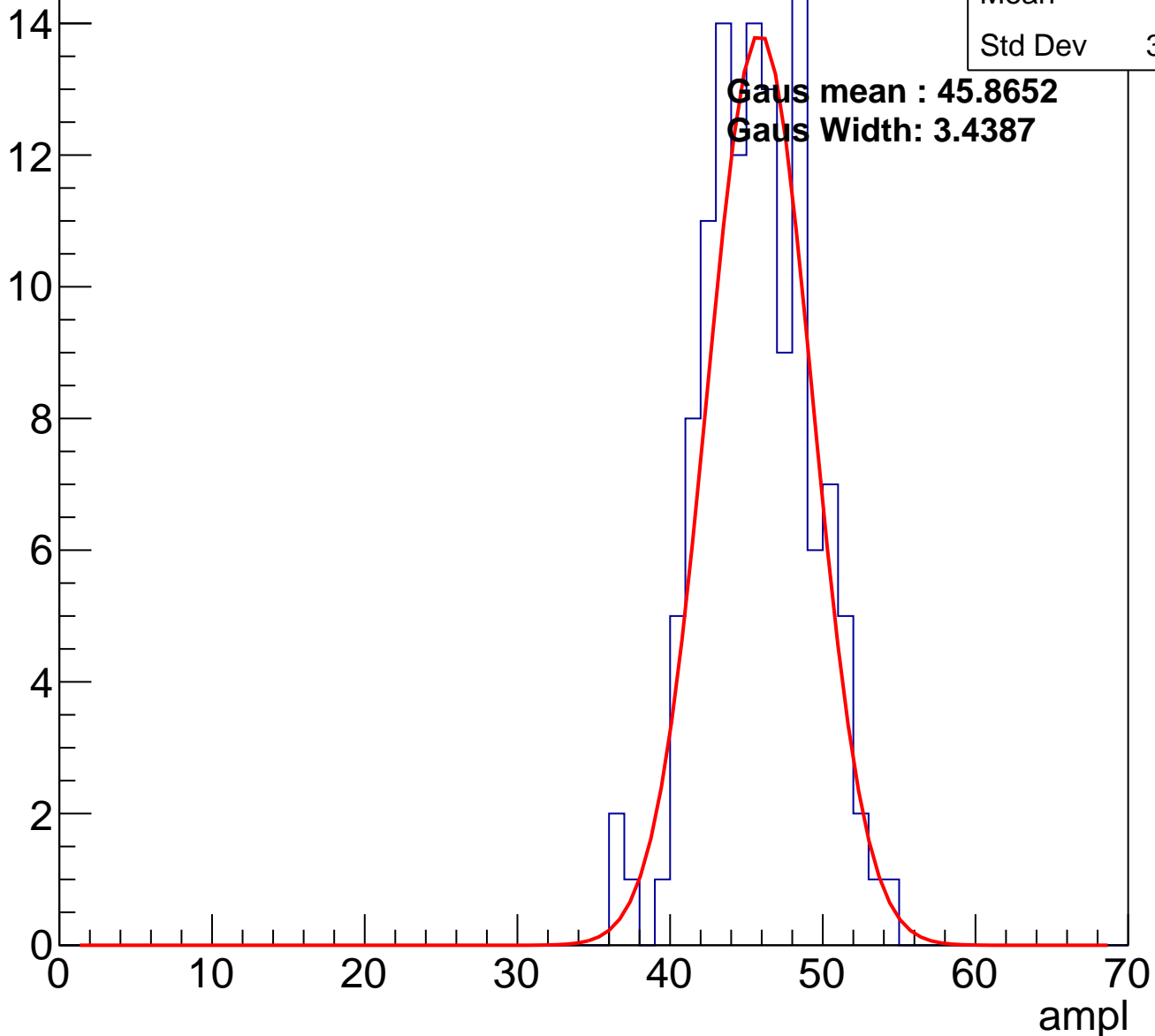
Entry



# B0L101S, U23-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

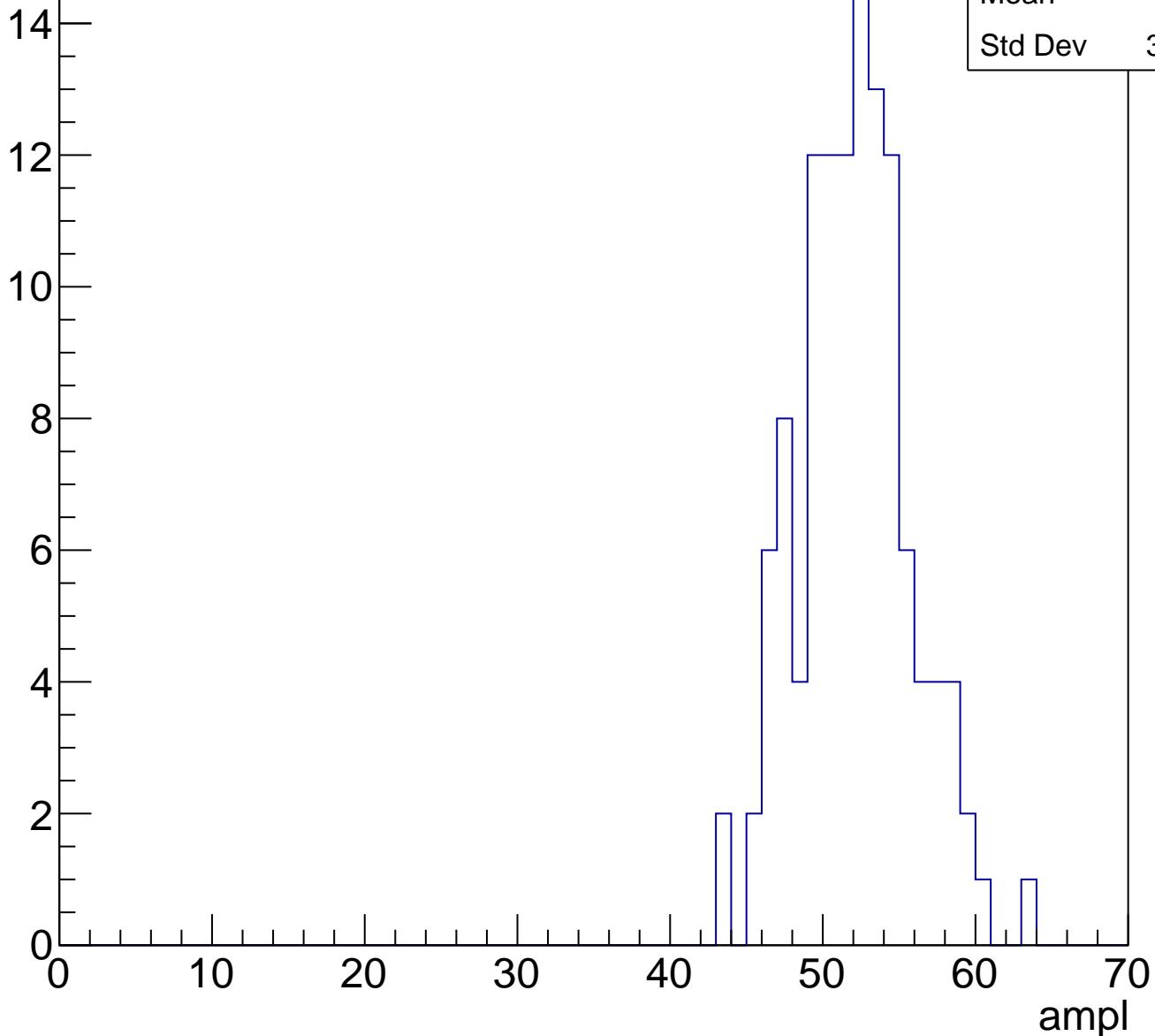


# B0L101S, U23-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	51.6
Std Dev	3.646

Entry



# B0L101S, U23-ch8, adc4

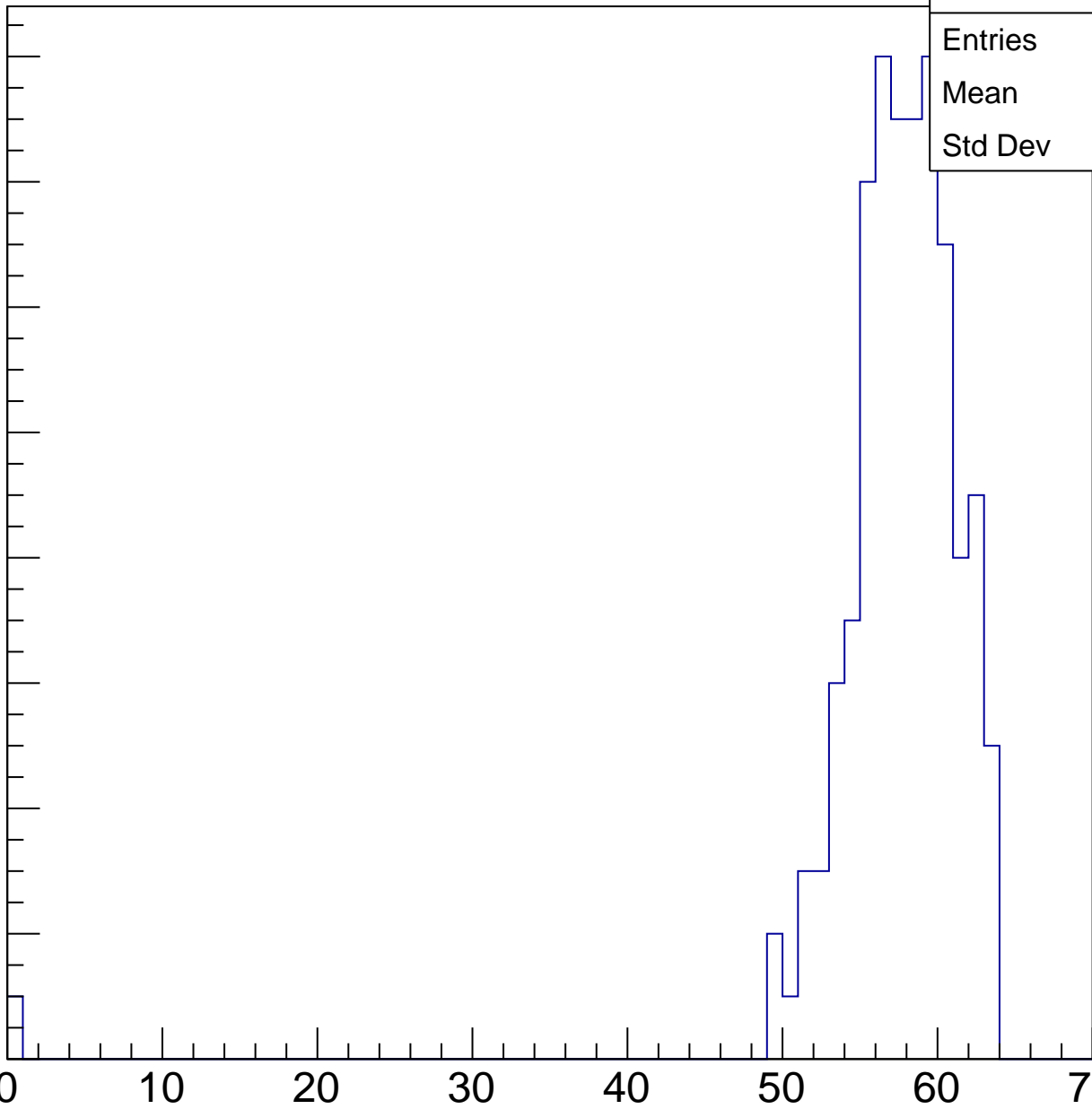
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	134
Mean	56.93
Std Dev	5.839

ampl



# B0L101S, U23-ch8, adc5

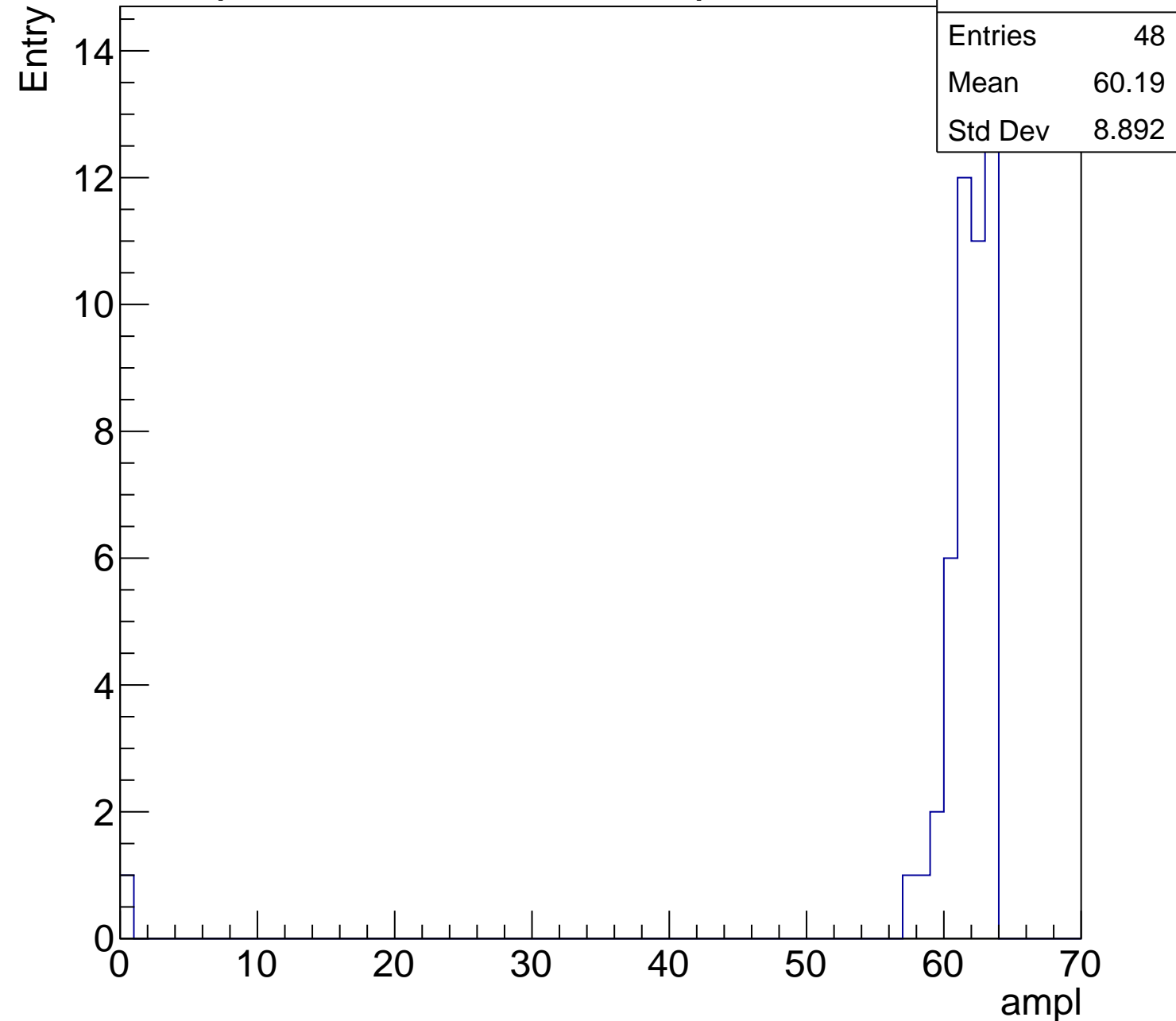
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	48
Mean	60.19
Std Dev	8.892

ampl



# B0L101S, U23-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch9, adc0

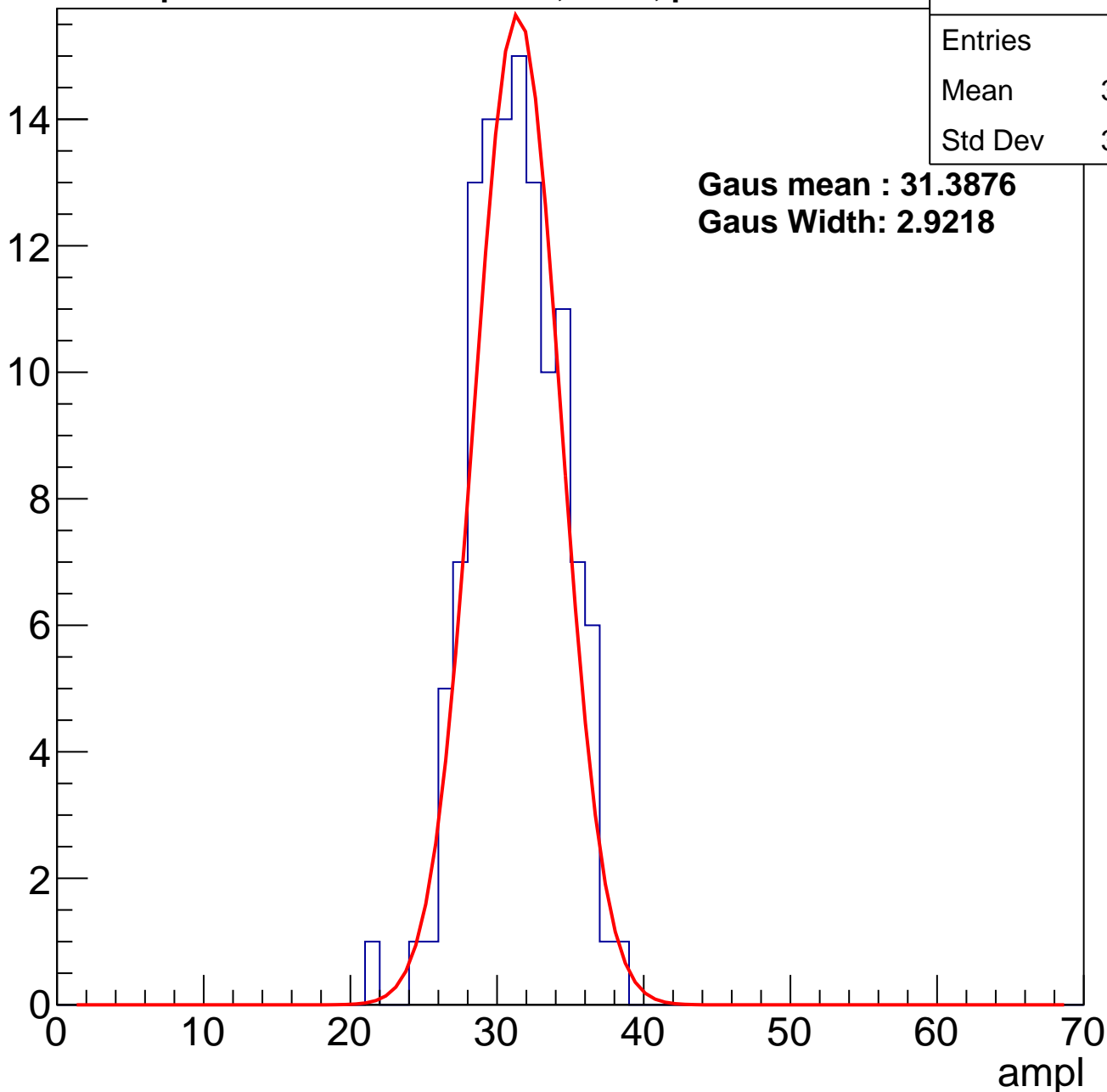
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	30.83
Std Dev	3.034

**Gaus mean : 31.3876**

**Gaus Width: 2.9218**

Entry



# B0L101S, U23-ch9, adc1

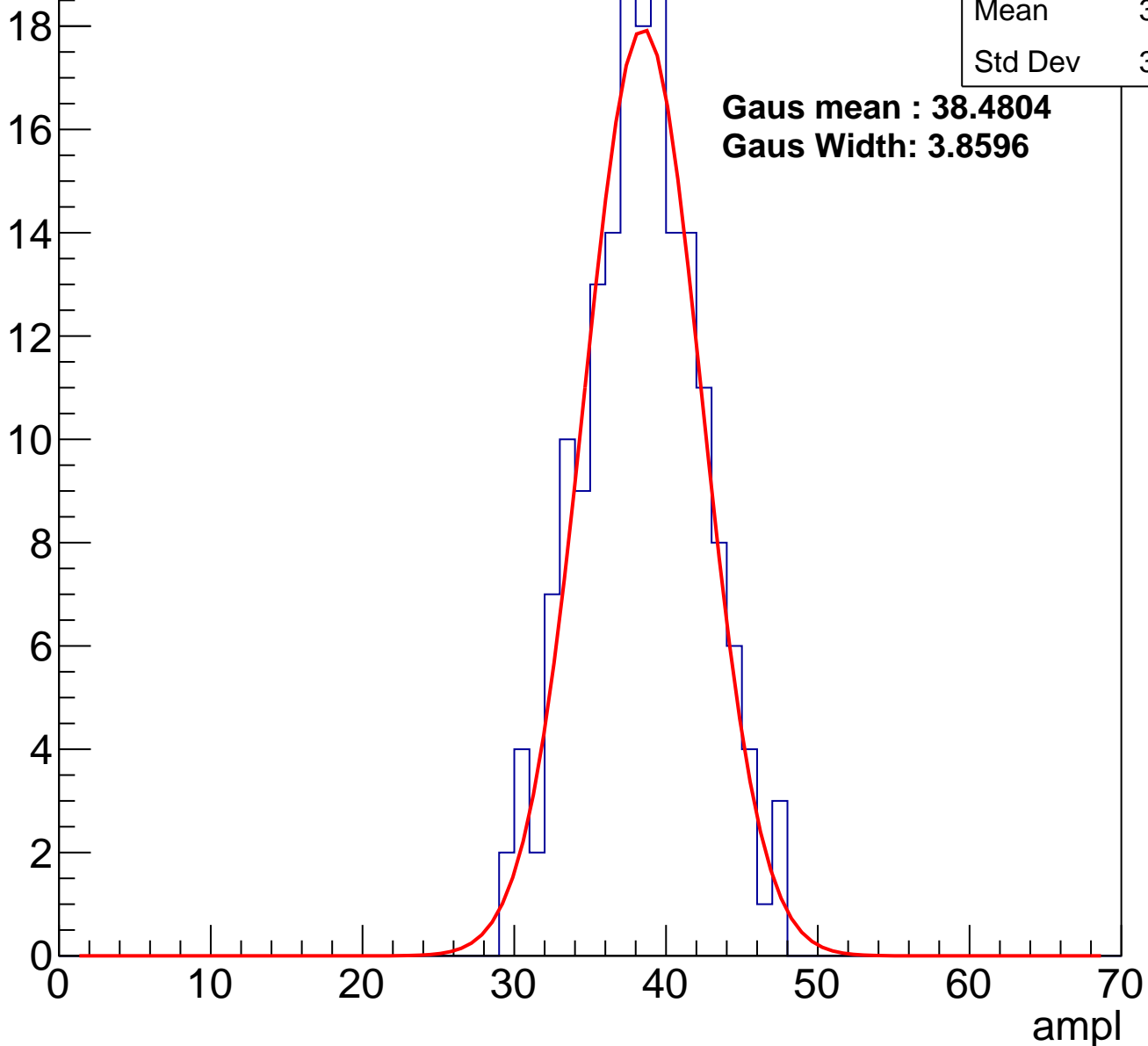
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	178
Mean	37.97
Std Dev	3.858

**Gaus mean : 38.4804**

**Gaus Width: 3.8596**

Entry



# B0L101S, U23-ch9, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

100

Mean

44.84

Std Dev

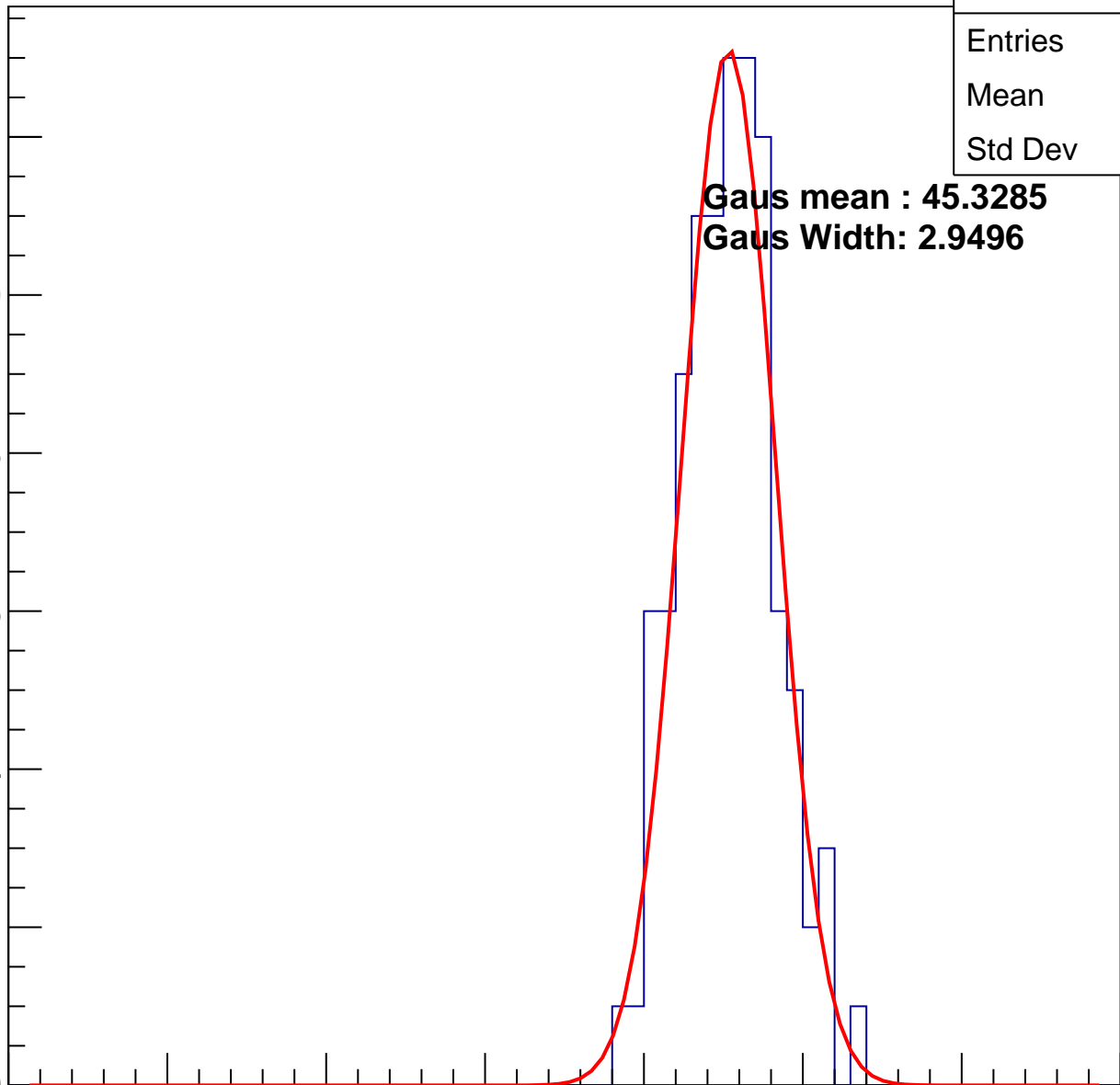
2.969

**Gaus mean : 45.3285**

**Gaus Width: 2.9496**

0 10 20 30 40 50 60 70

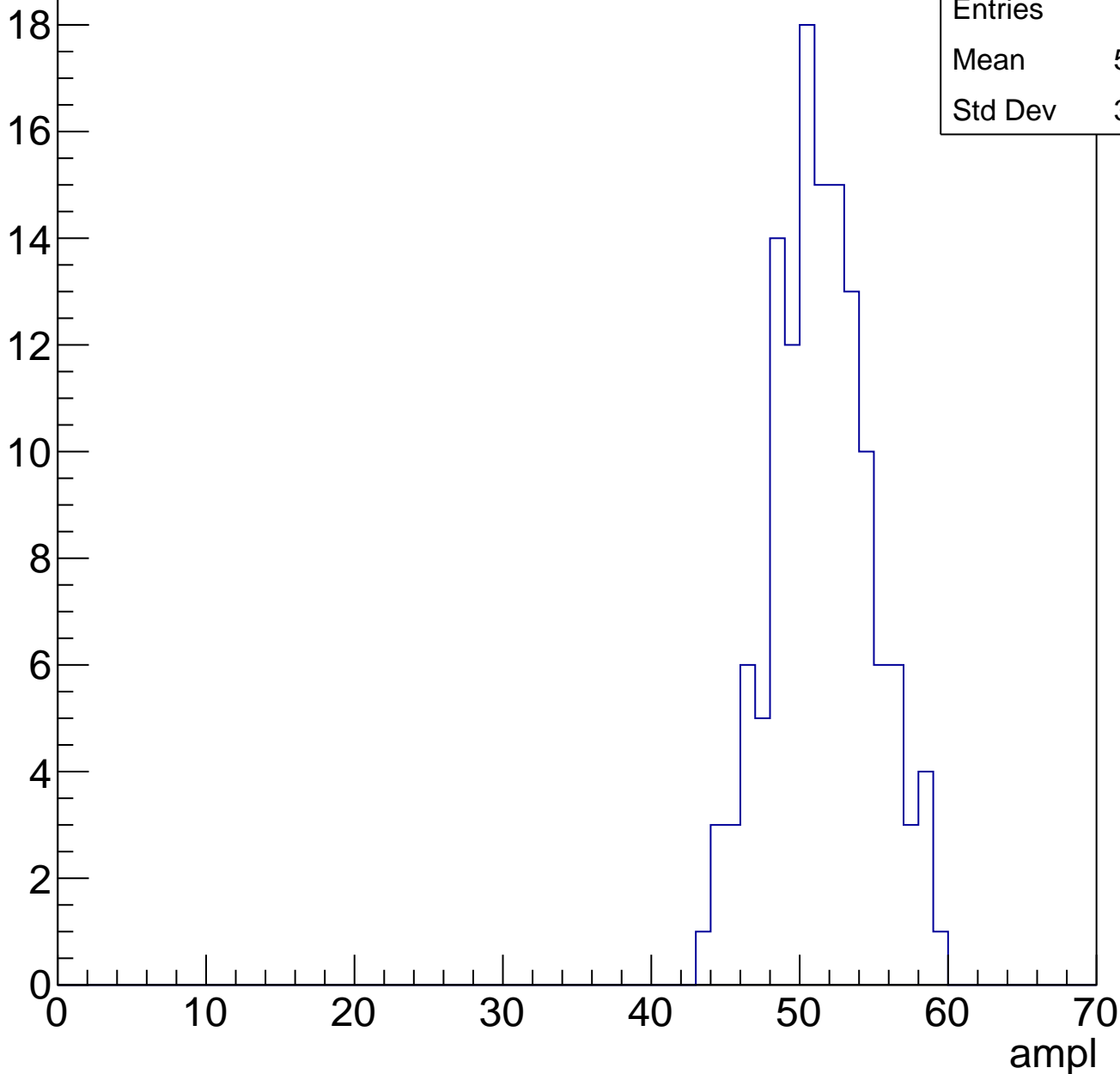
ampl



# B0L101S, U23-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	135
Mean	50.99
Std Dev	3.347

# B0L101S, U23-ch9, adc4

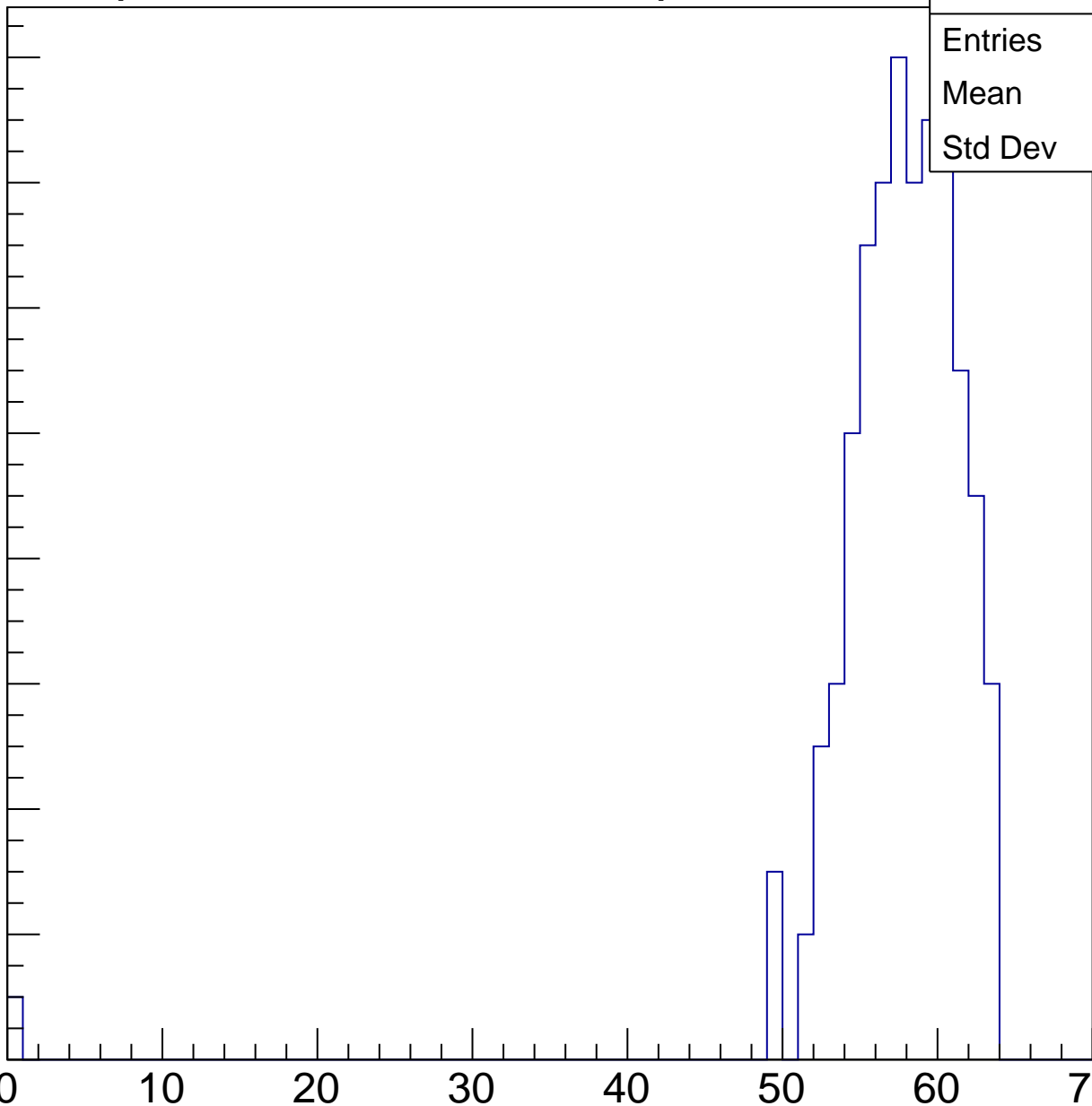
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	141
Mean	57.04
Std Dev	5.789

ampl



# B0L101S, U23-ch9, adc5

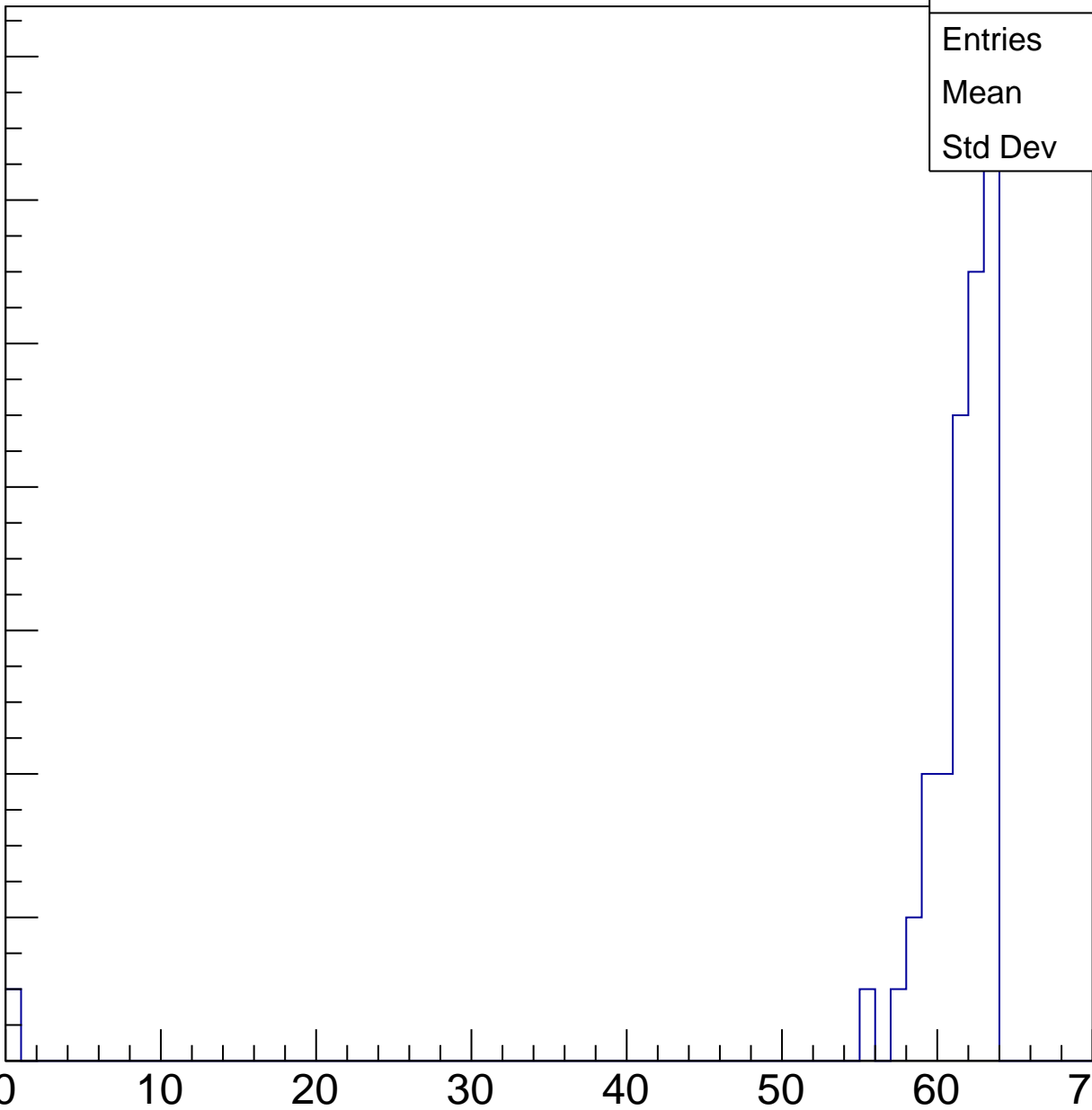
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	47
Mean	59.94
Std Dev	9.021

ampl



# B0L101S, U23-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch10, adc0

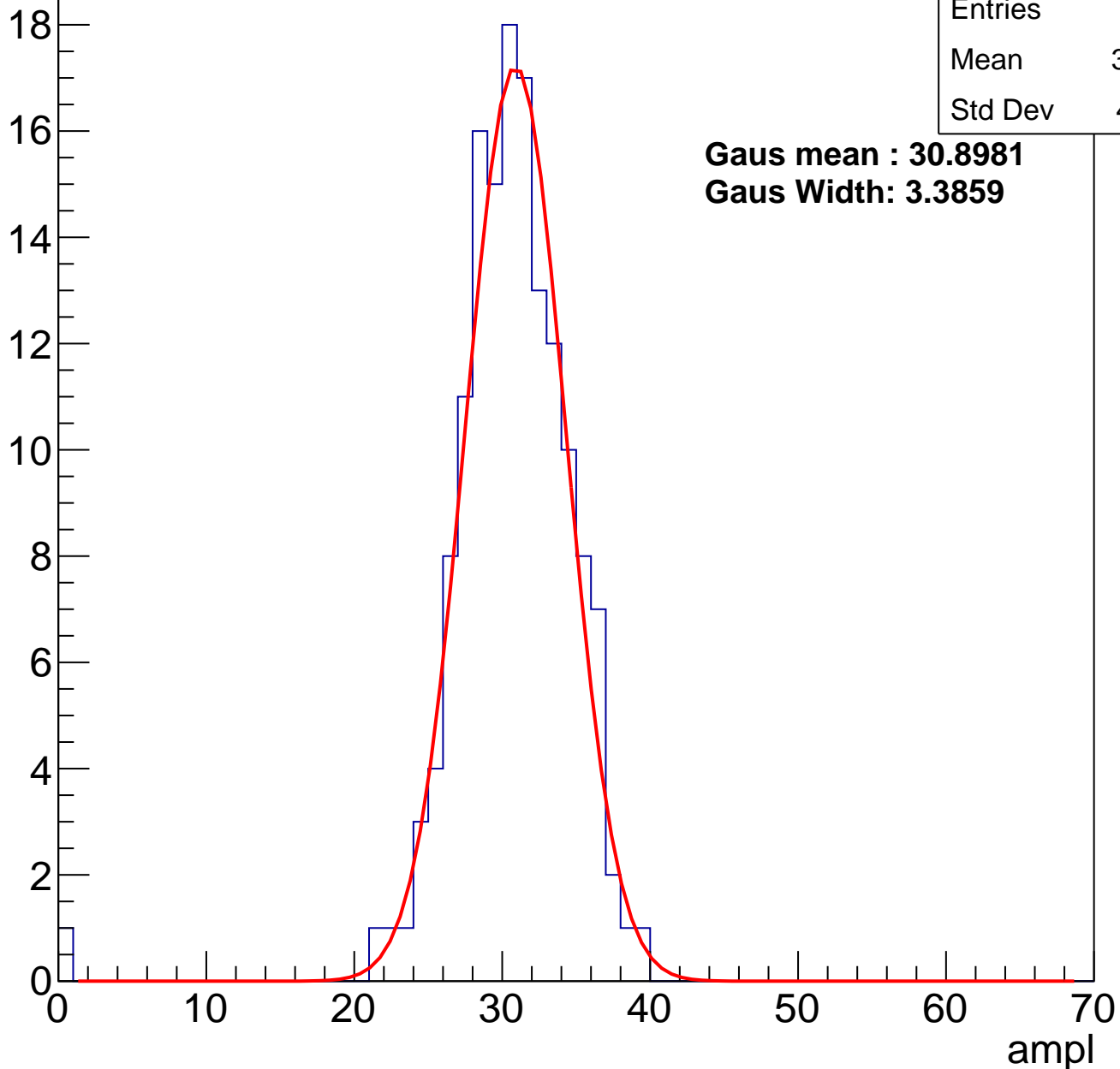
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	30.19
Std Dev	4.191

**Gaus mean : 30.8981**

**Gaus Width: 3.3859**

Entry



# B0L101S, U23-ch10, adc1

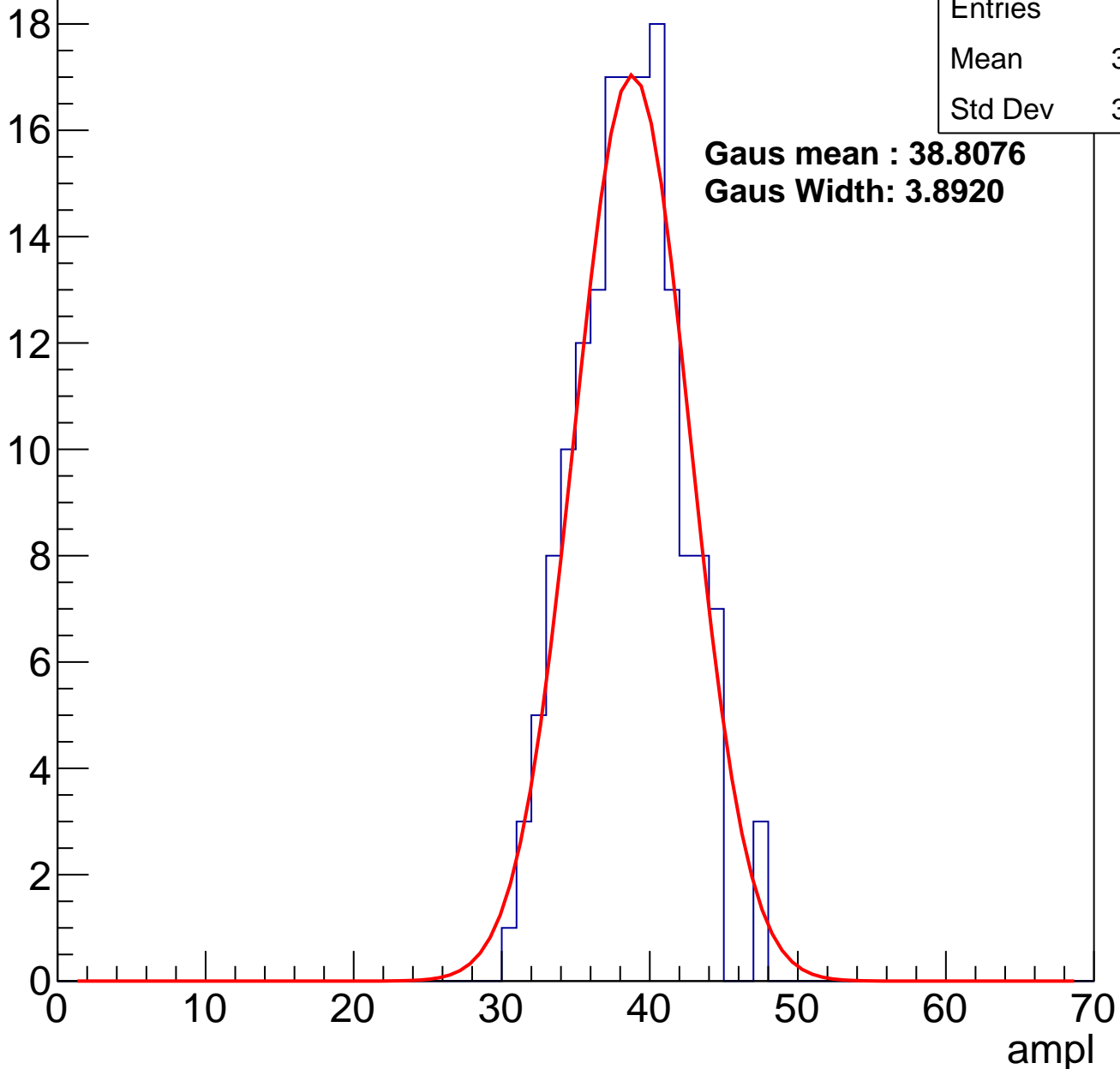
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	38.09
Std Dev	3.496

**Gaus mean : 38.8076**

**Gaus Width: 3.8920**

Entry



# B0L101S, U23-ch10, adc2

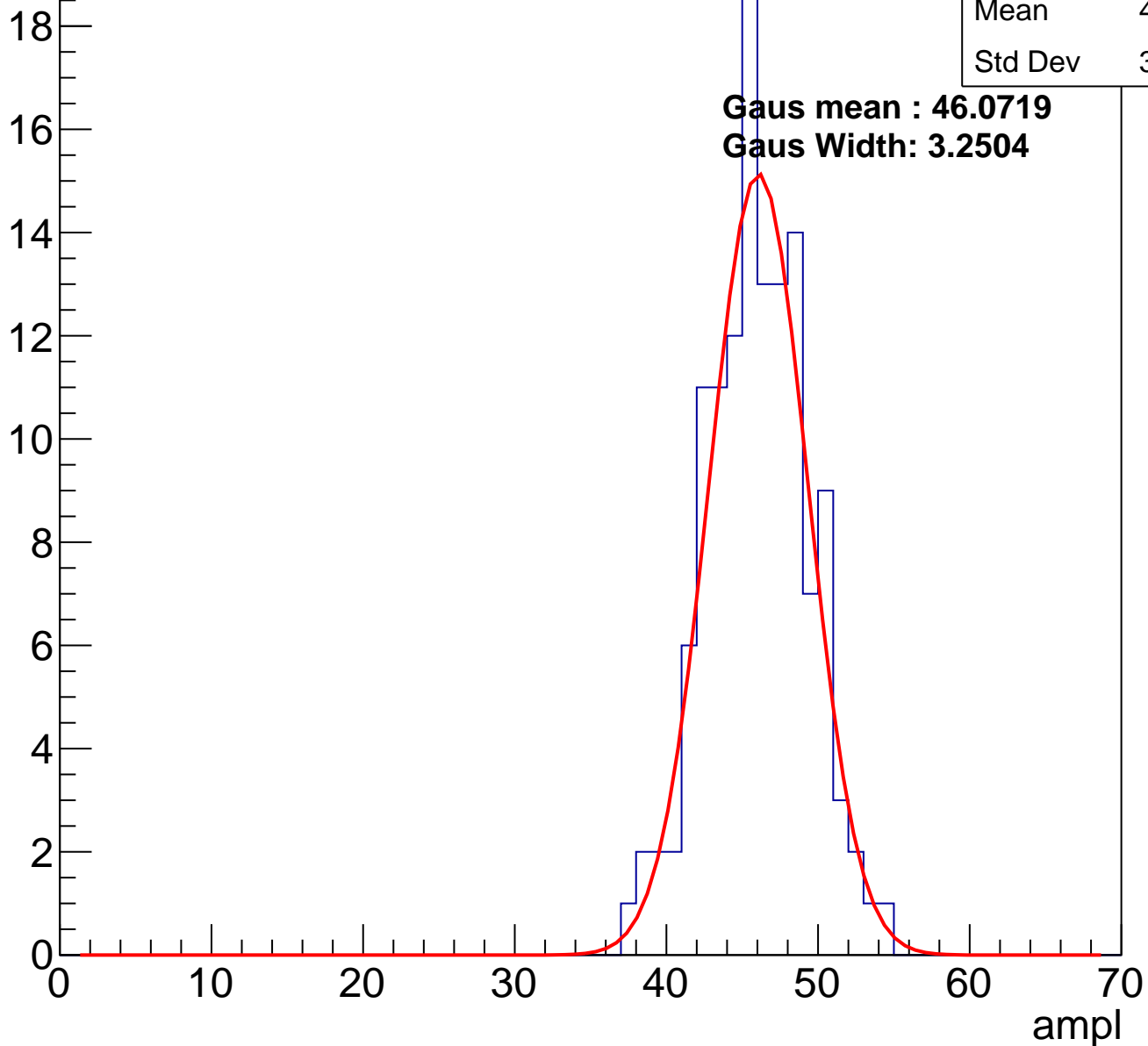
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	45.53
Std Dev	3.269

**Gaus mean : 46.0719**

**Gaus Width: 3.2504**

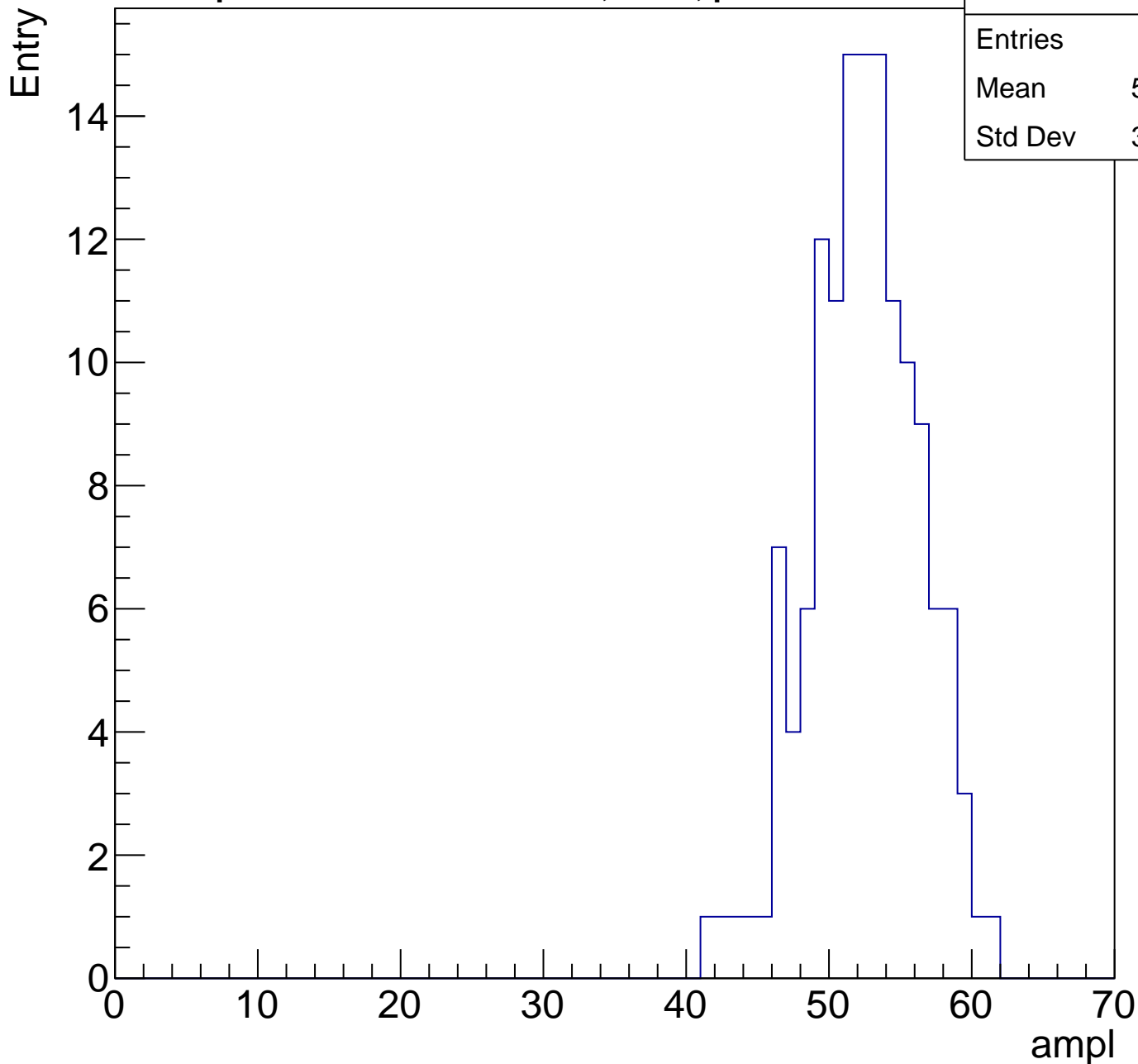
Entry



# B0L101S, U23-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	52.02
Std Dev	3.803



# B0L101S, U23-ch10, adc4

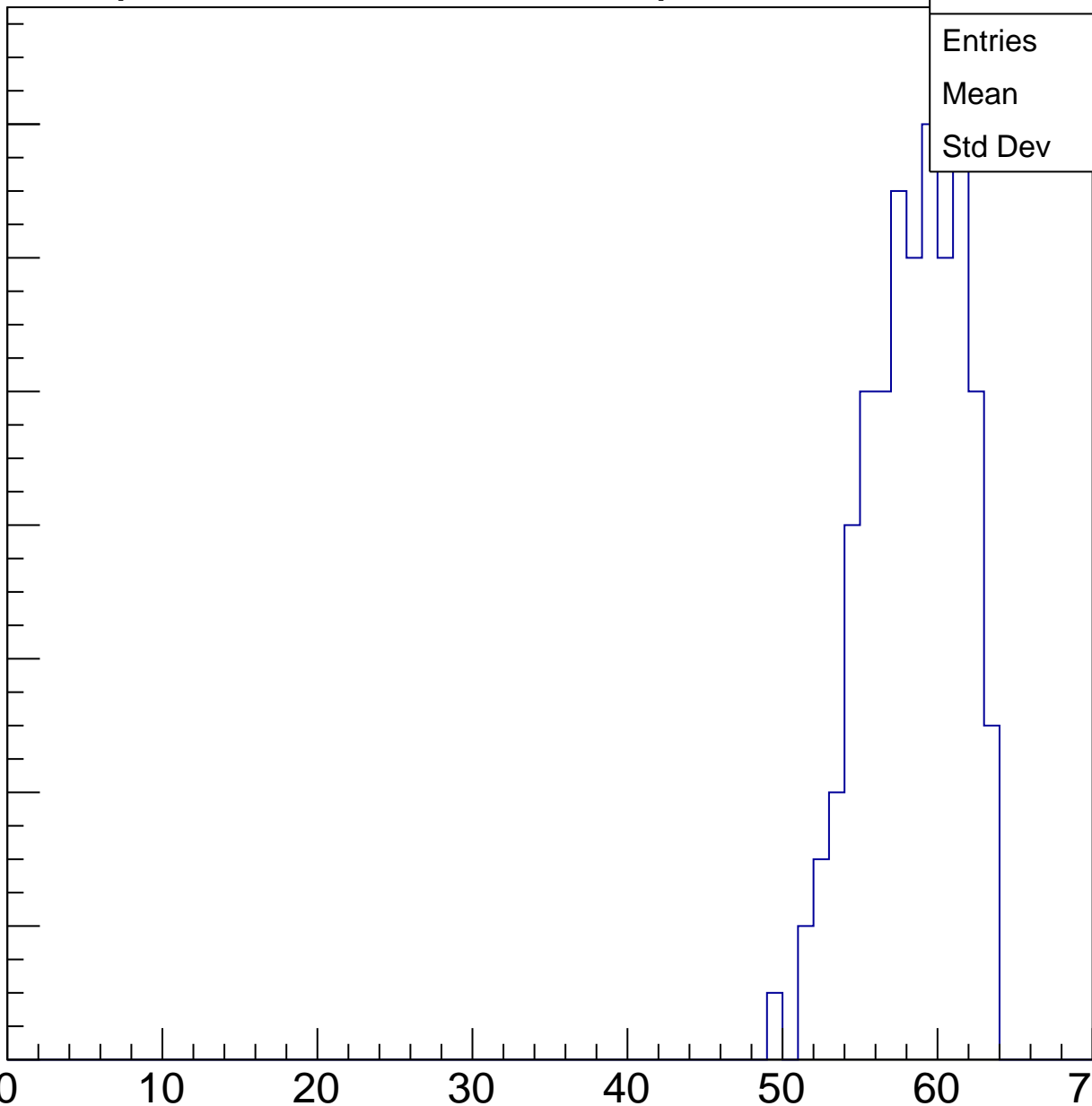
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	57.93
Std Dev	3.094

ampl



# B0L101S, U23-ch10, adc5

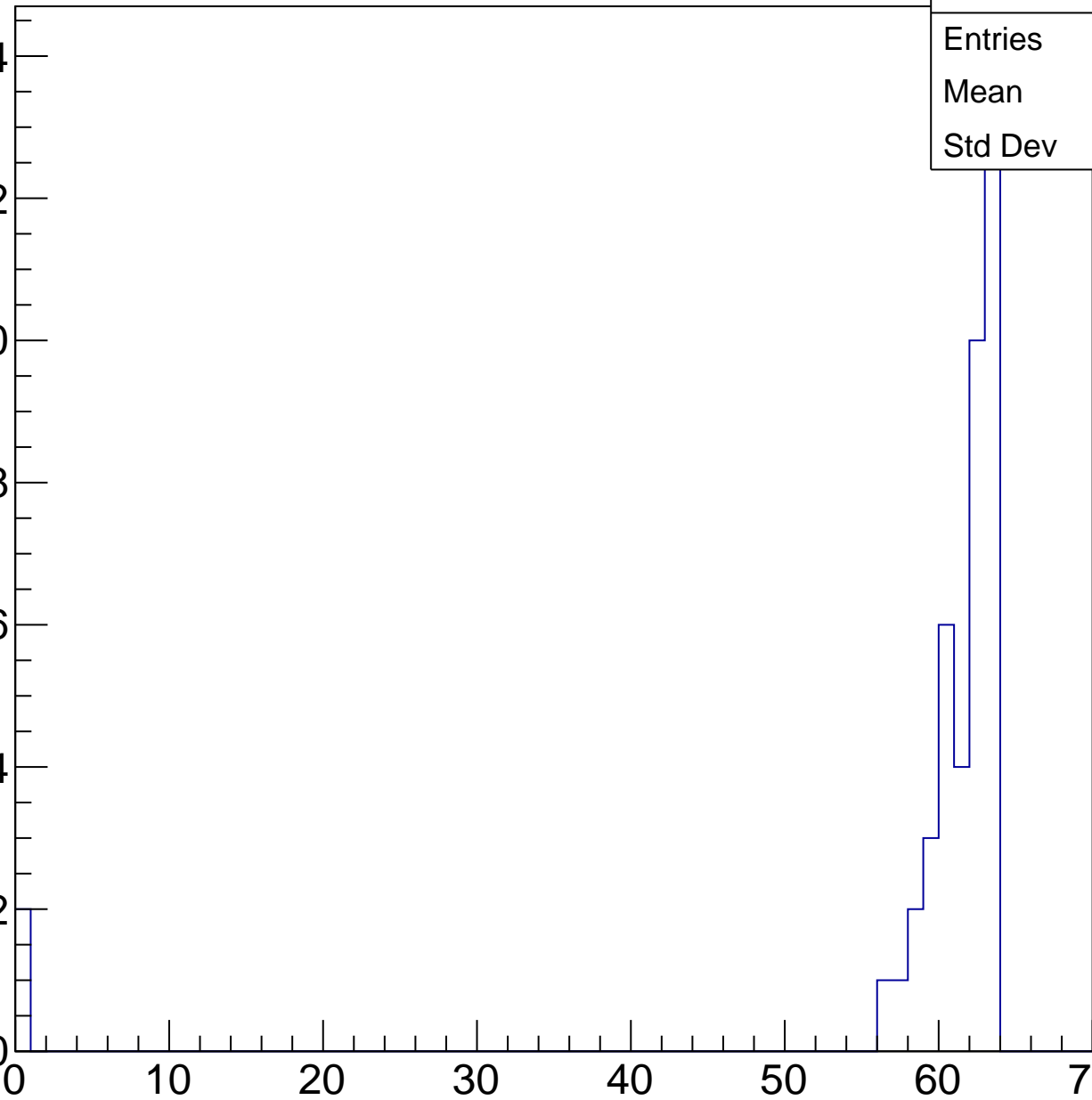
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	43
Mean	58.42
Std Dev	13.03

ampl



# B0L101S, U23-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch11, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	28.41
Std Dev	4.279

**Gaus mean : 28.8918**

**Gaus Width: 3.4072**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

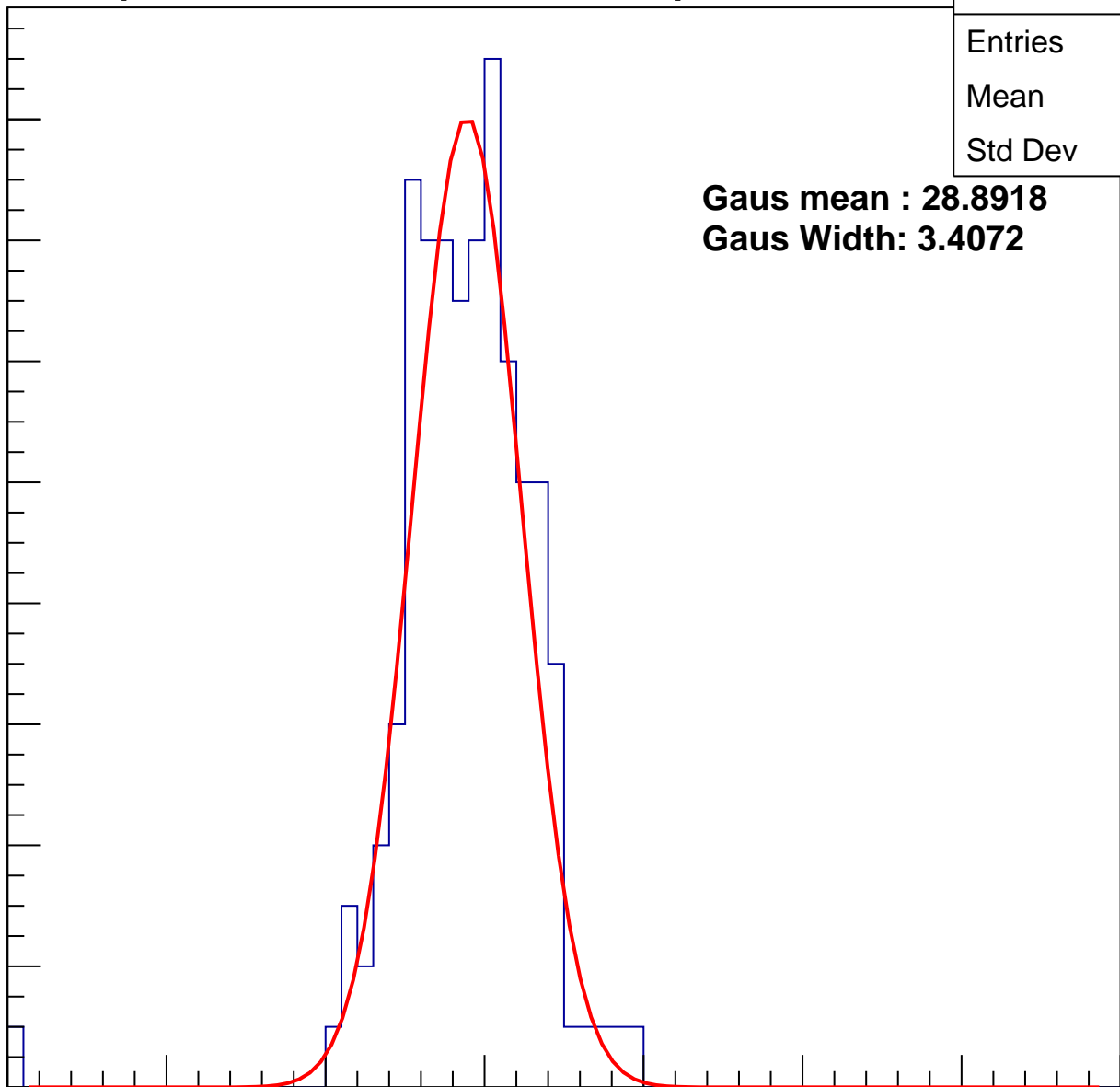
40

50

60

70

ampl



# B0L101S, U23-ch11, adc1

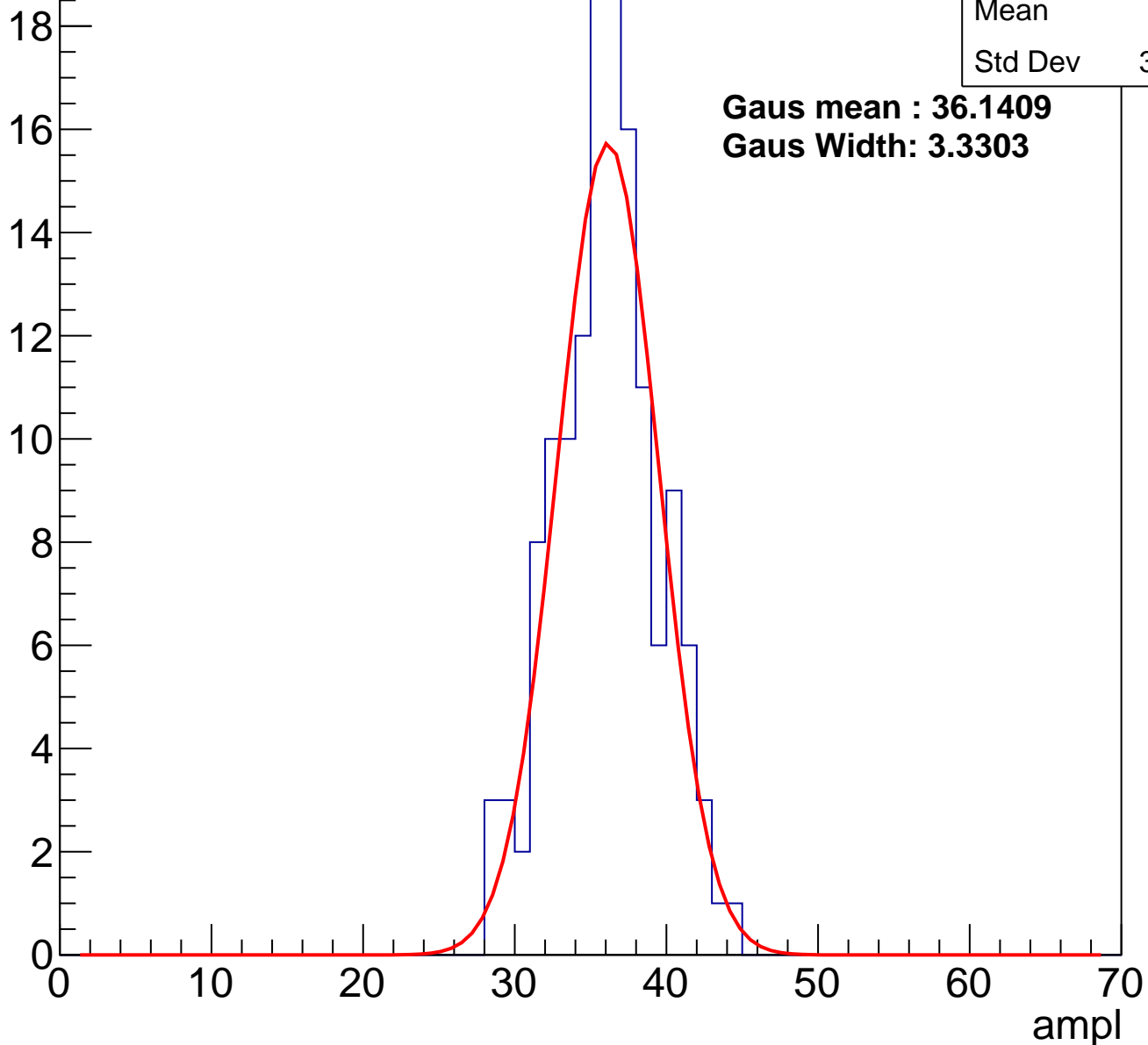
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	35.6
Std Dev	3.323

**Gaus mean : 36.1409**

**Gaus Width: 3.3303**

Entry



# B0L101S, U23-ch11, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

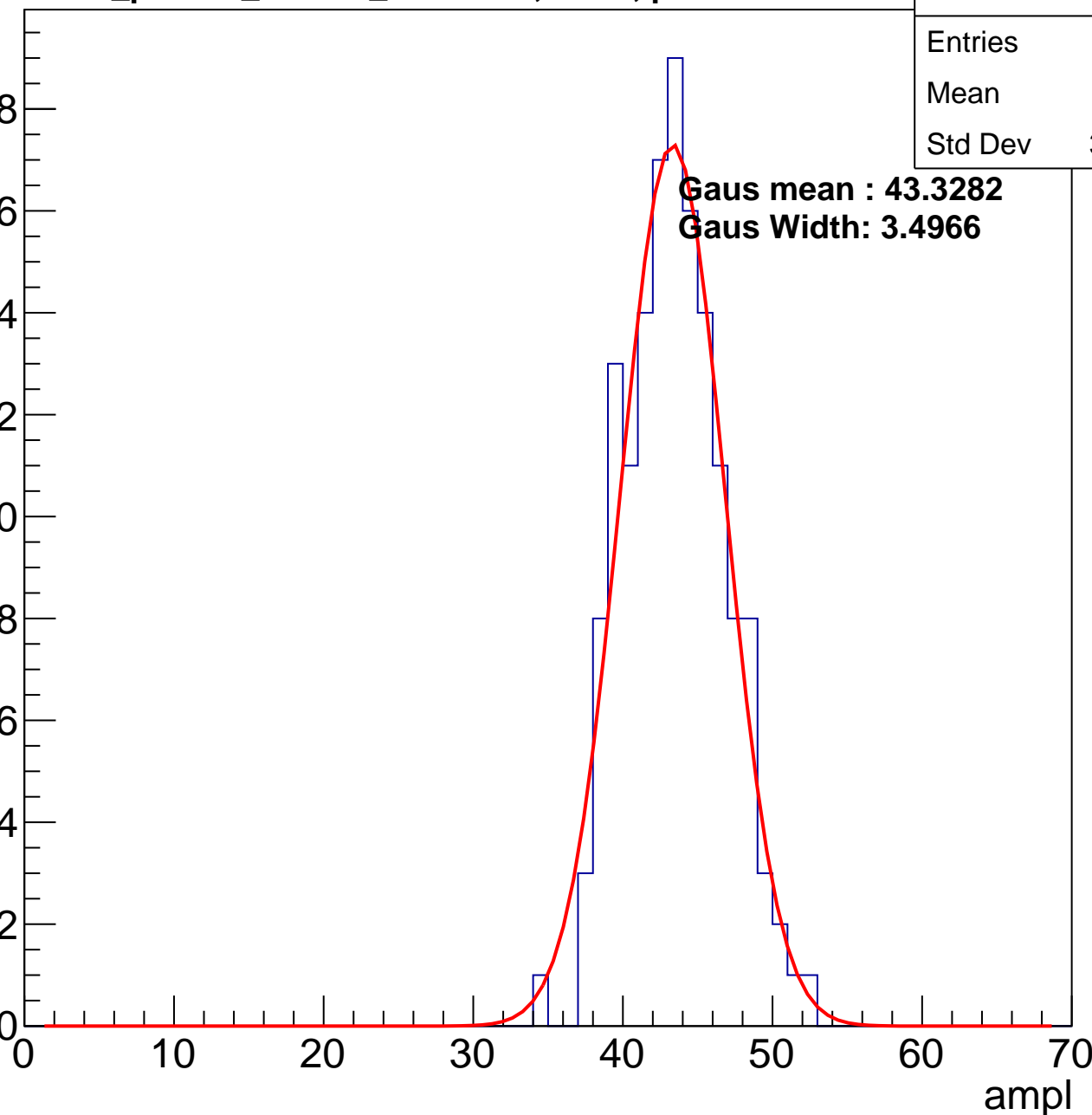
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	150
Mean	43.01
Std Dev	3.285

**Gaus mean : 43.3282**

**Gaus Width: 3.4966**

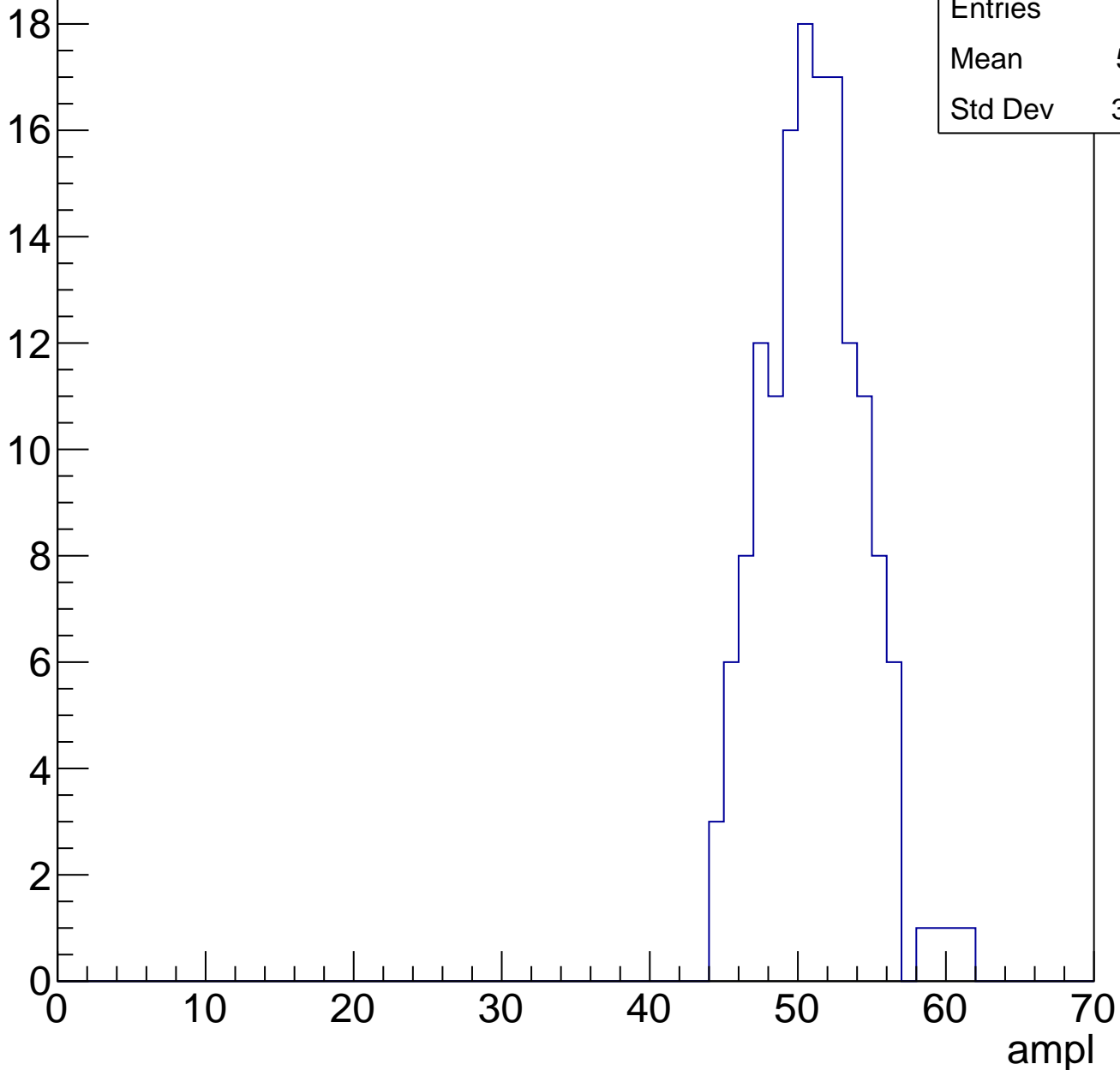


# B0L101S, U23-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	50.61
Std Dev	3.327

Entry

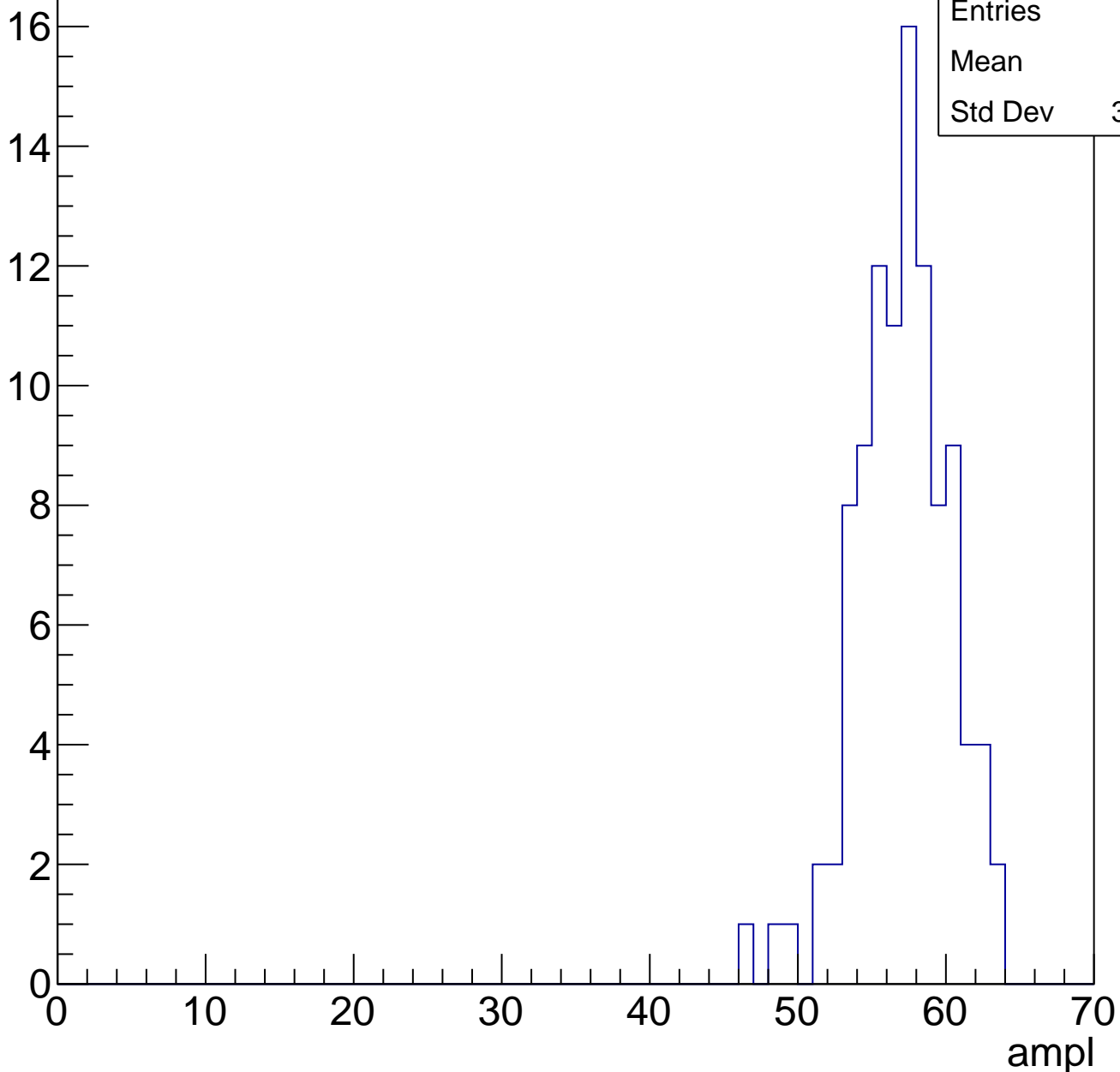


# B0L101S, U23-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	102
Mean	56.6
Std Dev	3.132

Entry



# B0L101S, U23-ch11, adc5

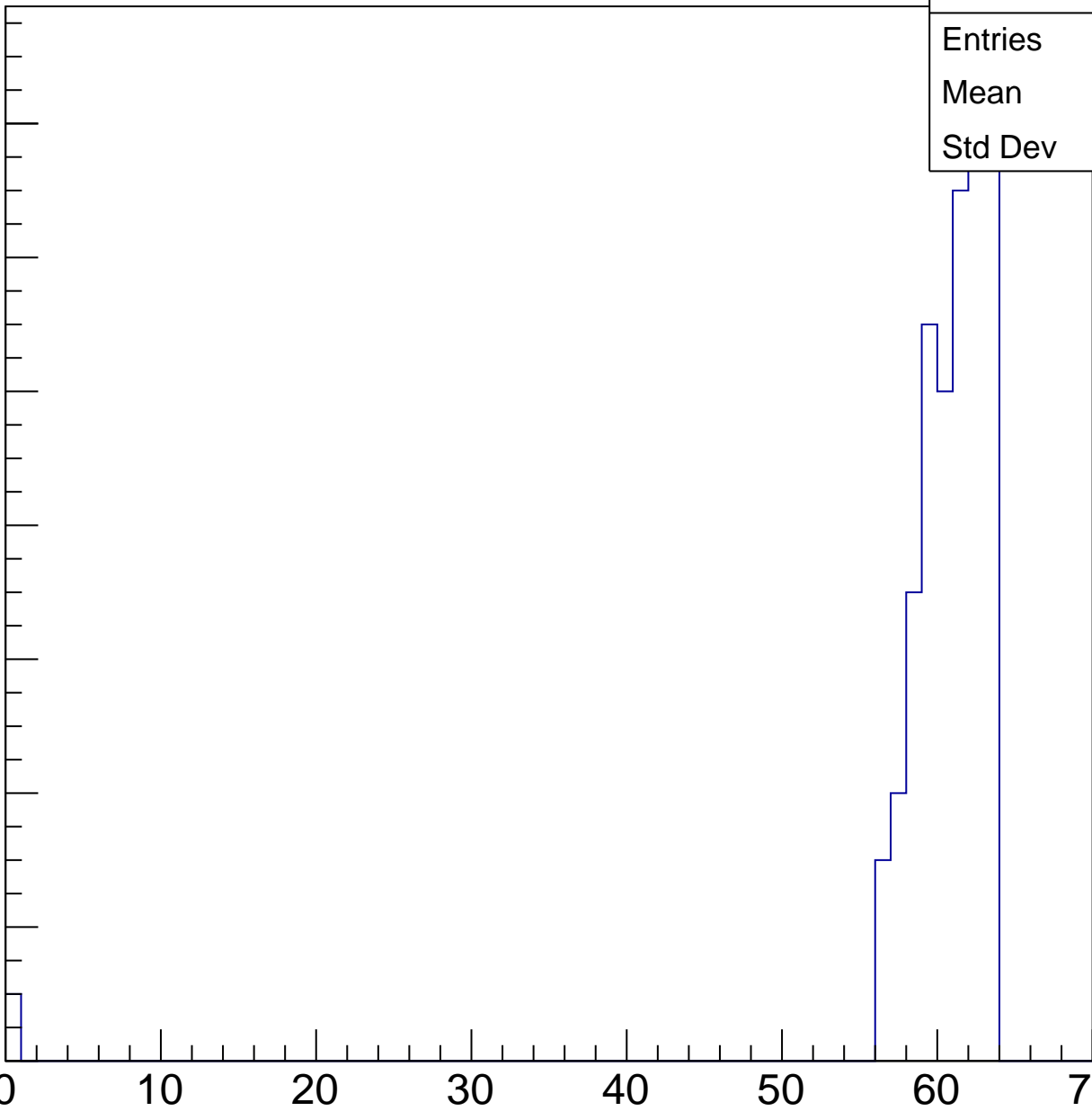
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	78
Mean	59.71
Std Dev	7.089

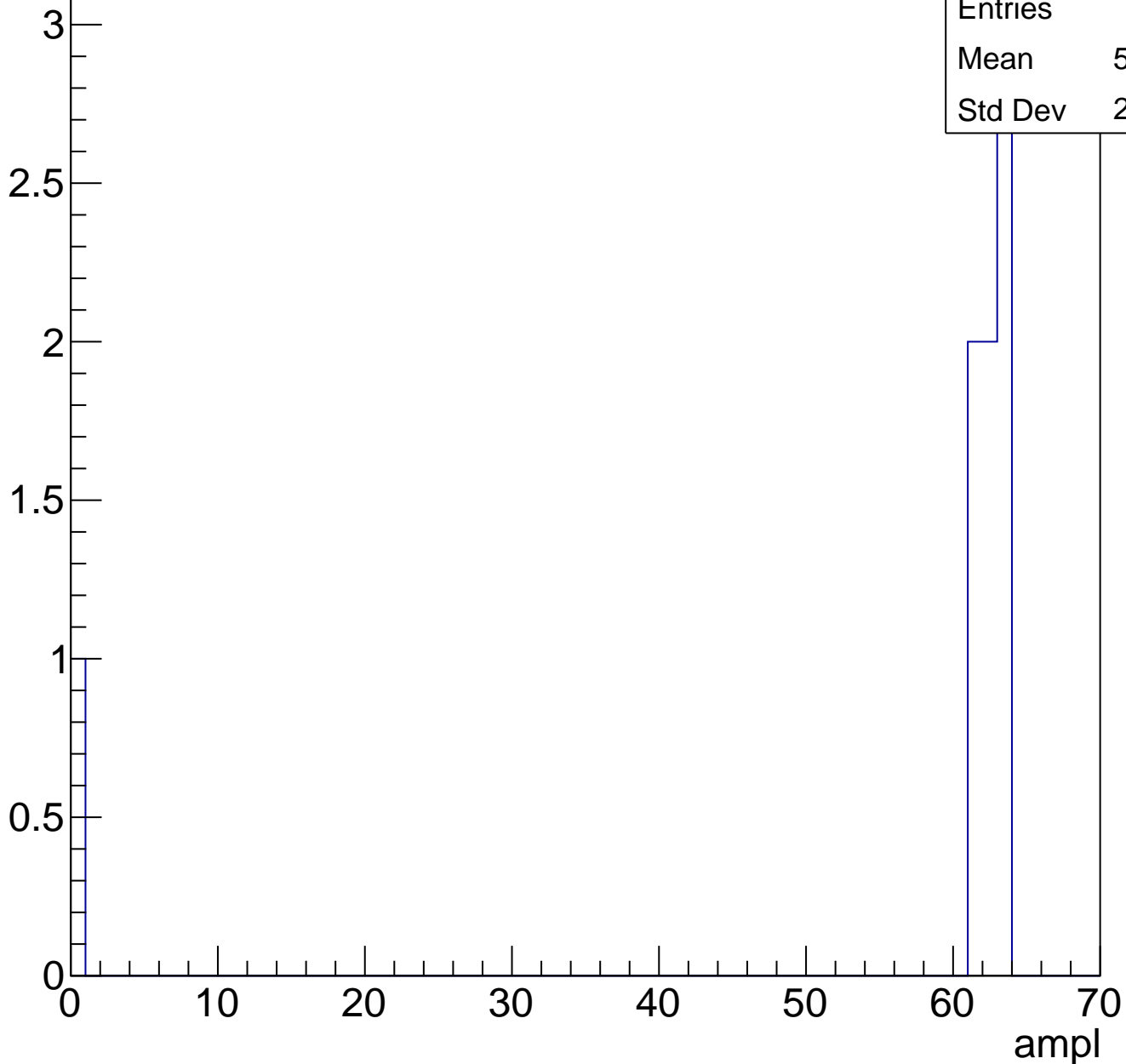
ampl



# B0L101S, U23-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

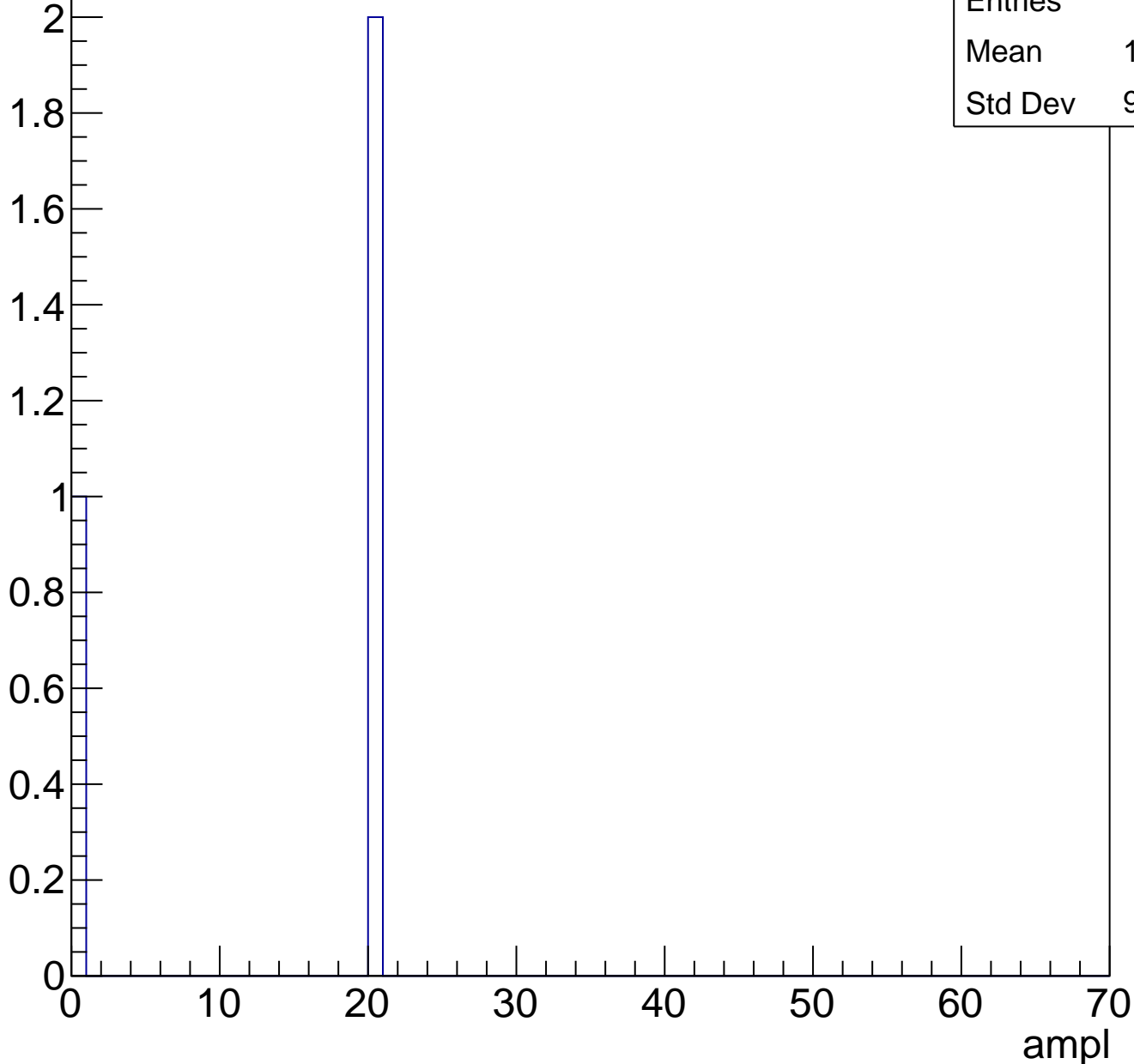




# B0L101S, U23-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



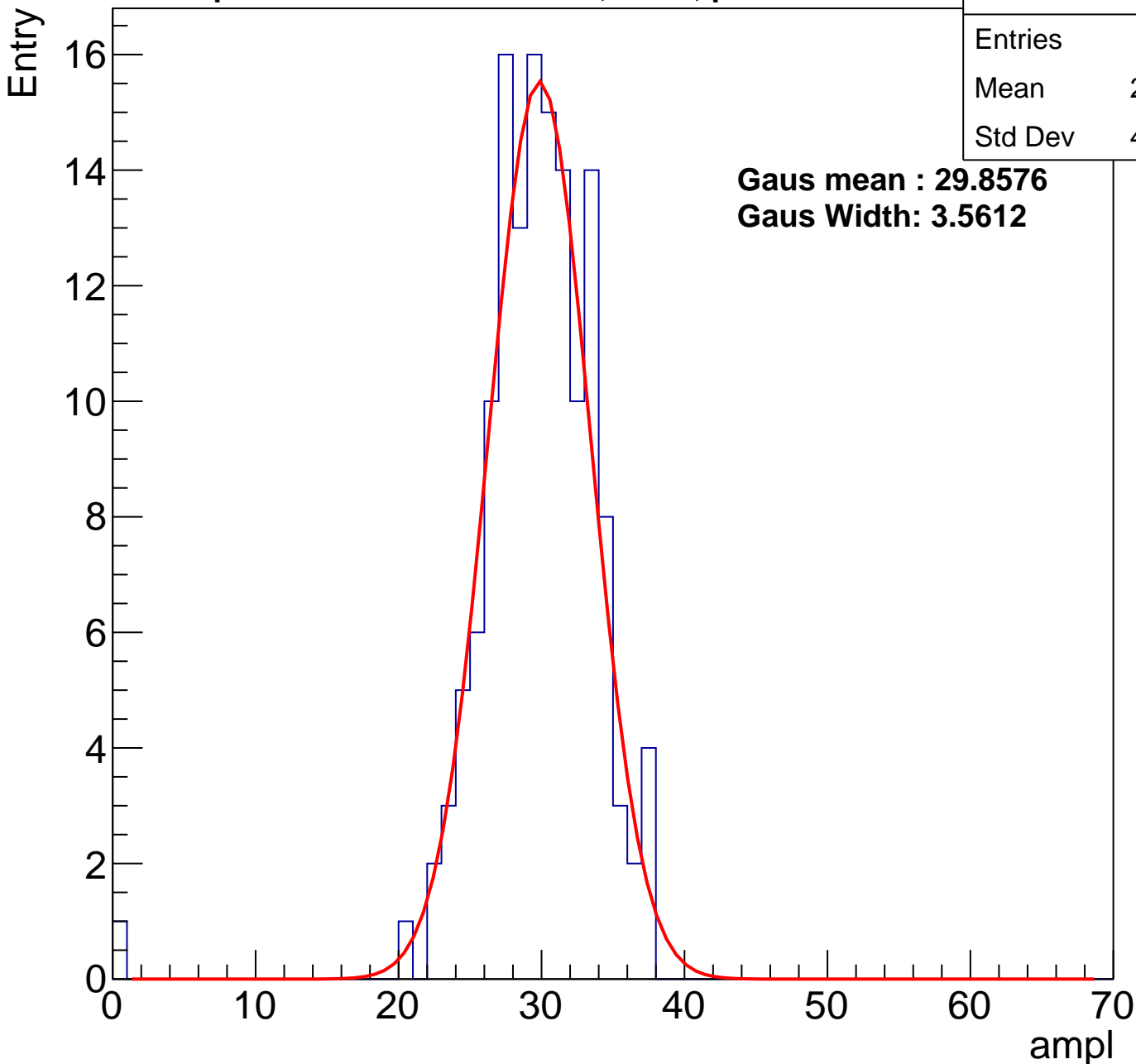
# B0L101S, U23-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	29.27
Std Dev	4.225

**Gaus mean : 29.8576**

**Gaus Width: 3.5612**



# B0L101S, U23-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

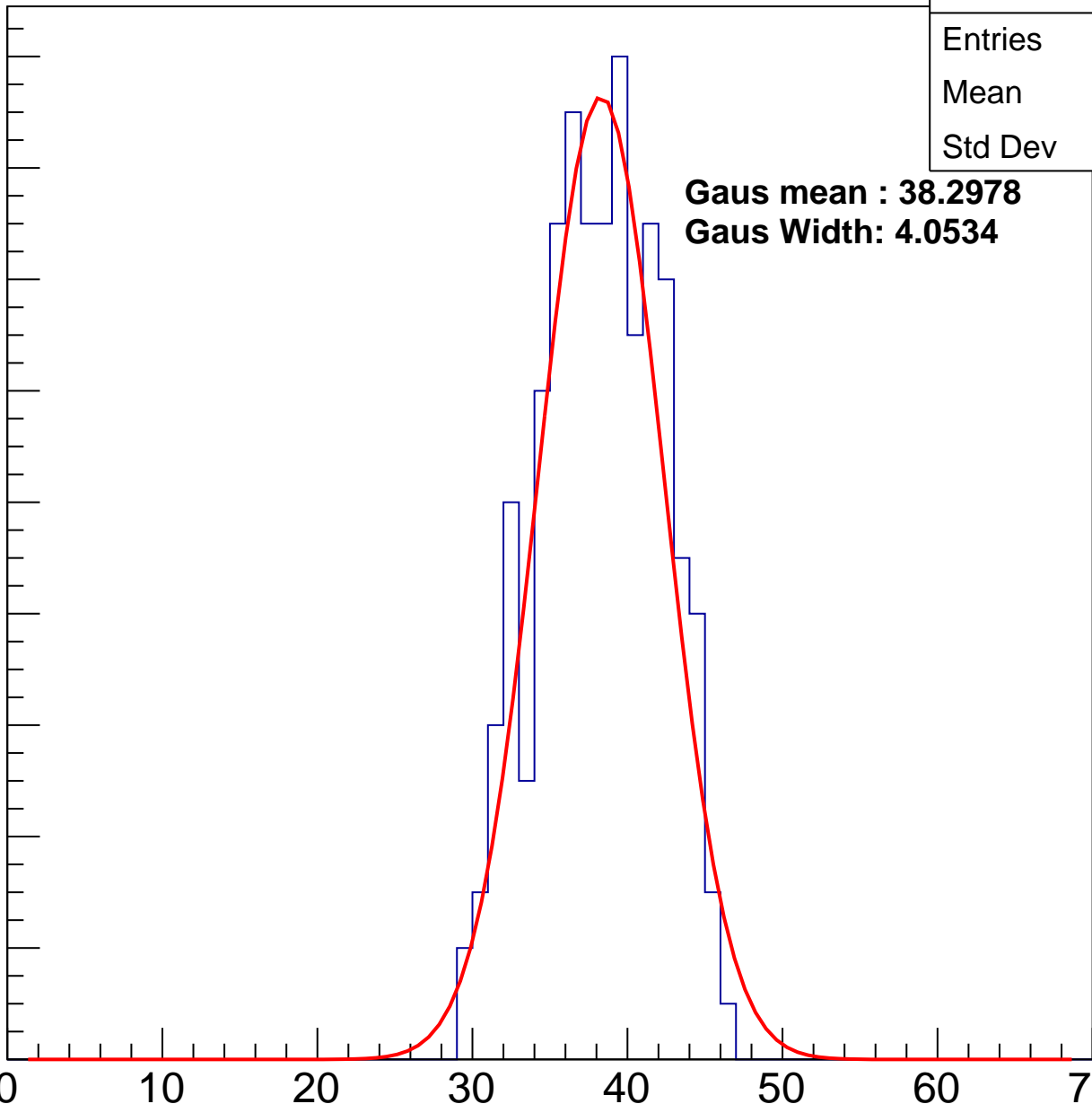
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	181
Mean	37.76
Std Dev	3.852

**Gaus mean : 38.2978**

**Gaus Width: 4.0534**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch12, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

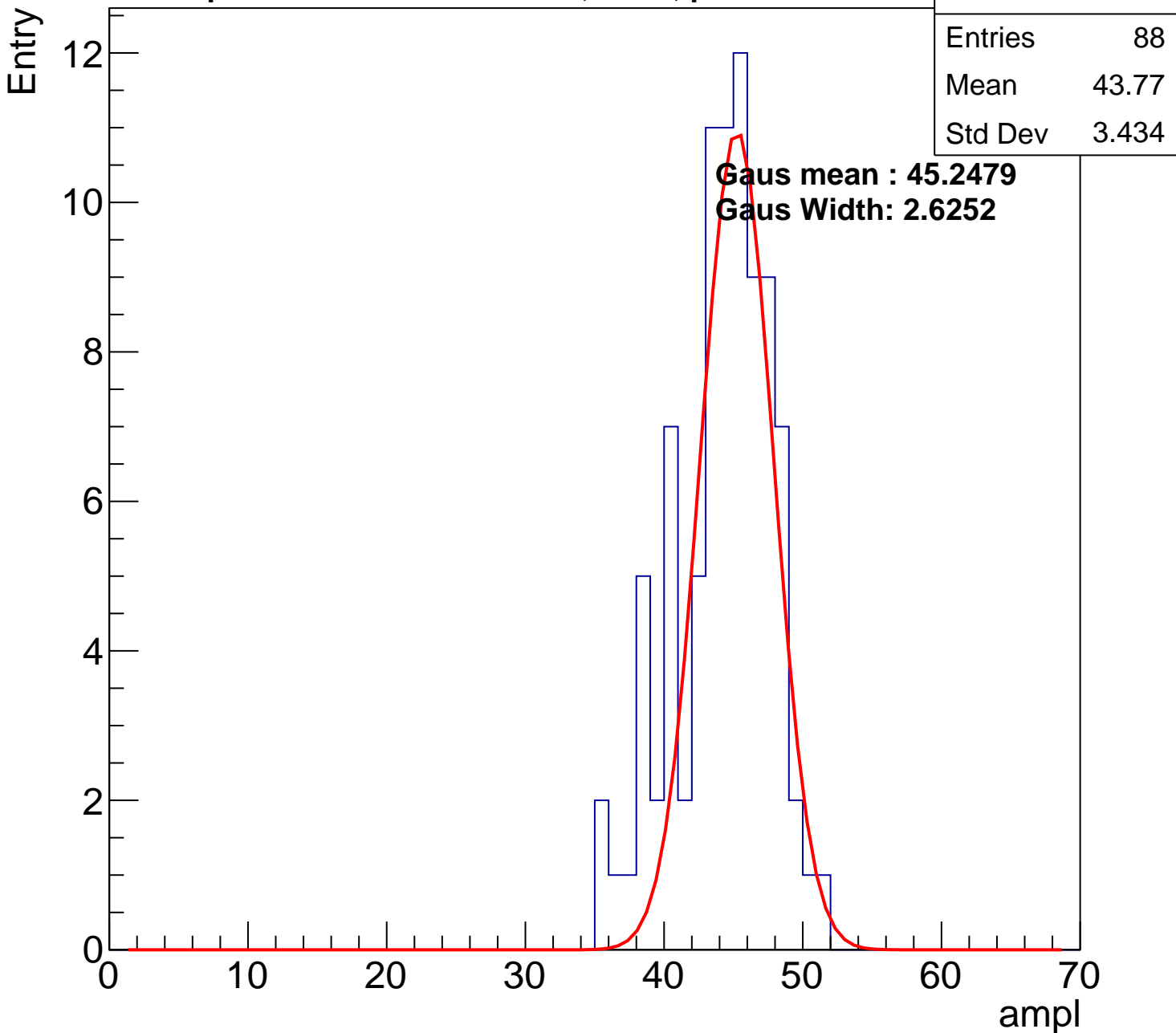
Entries	88
Mean	43.77
Std Dev	3.434

**Gaus mean : 45.2479**

**Gaus Width: 2.6252**

ampl

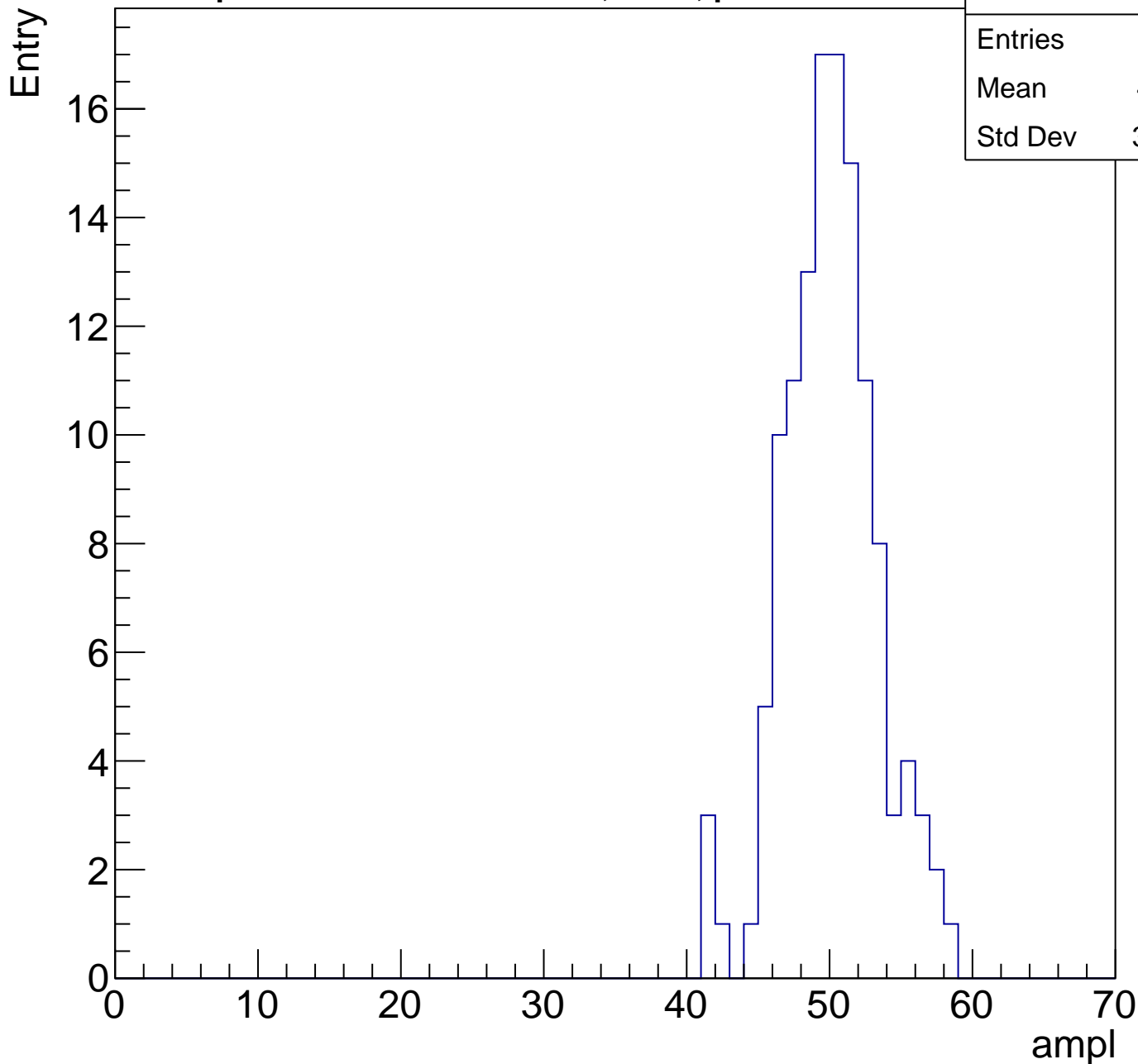
0 10 20 30 40 50 60 70



# B0L101S, U23-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	49.61
Std Dev	3.257



# B0L101S, U23-ch12, adc4

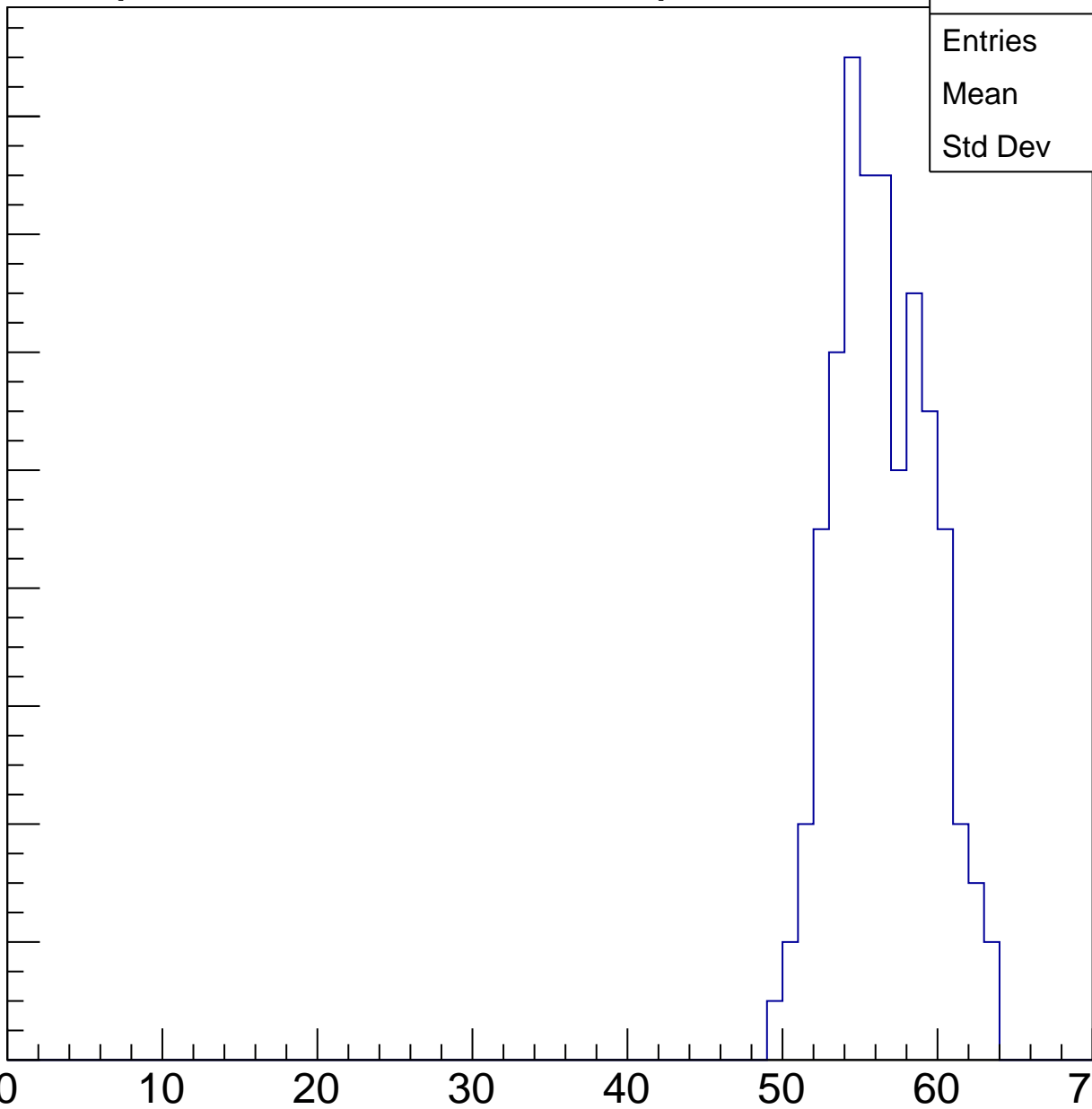
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	127
Mean	55.98
Std Dev	3.031

ampl



# B0L101S, U23-ch12, adc5

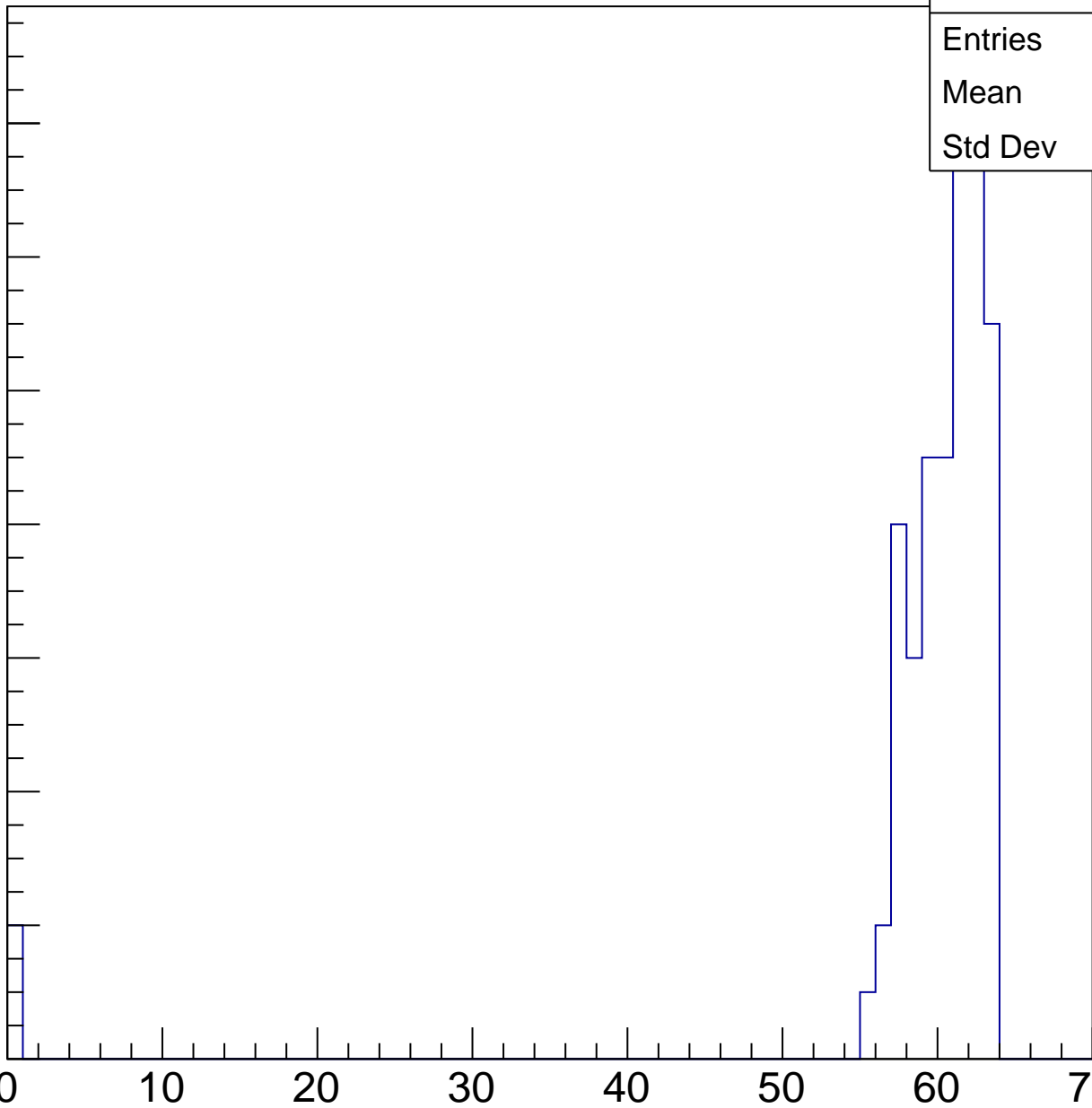
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	78
Mean	58.72
Std Dev	9.745

ampl

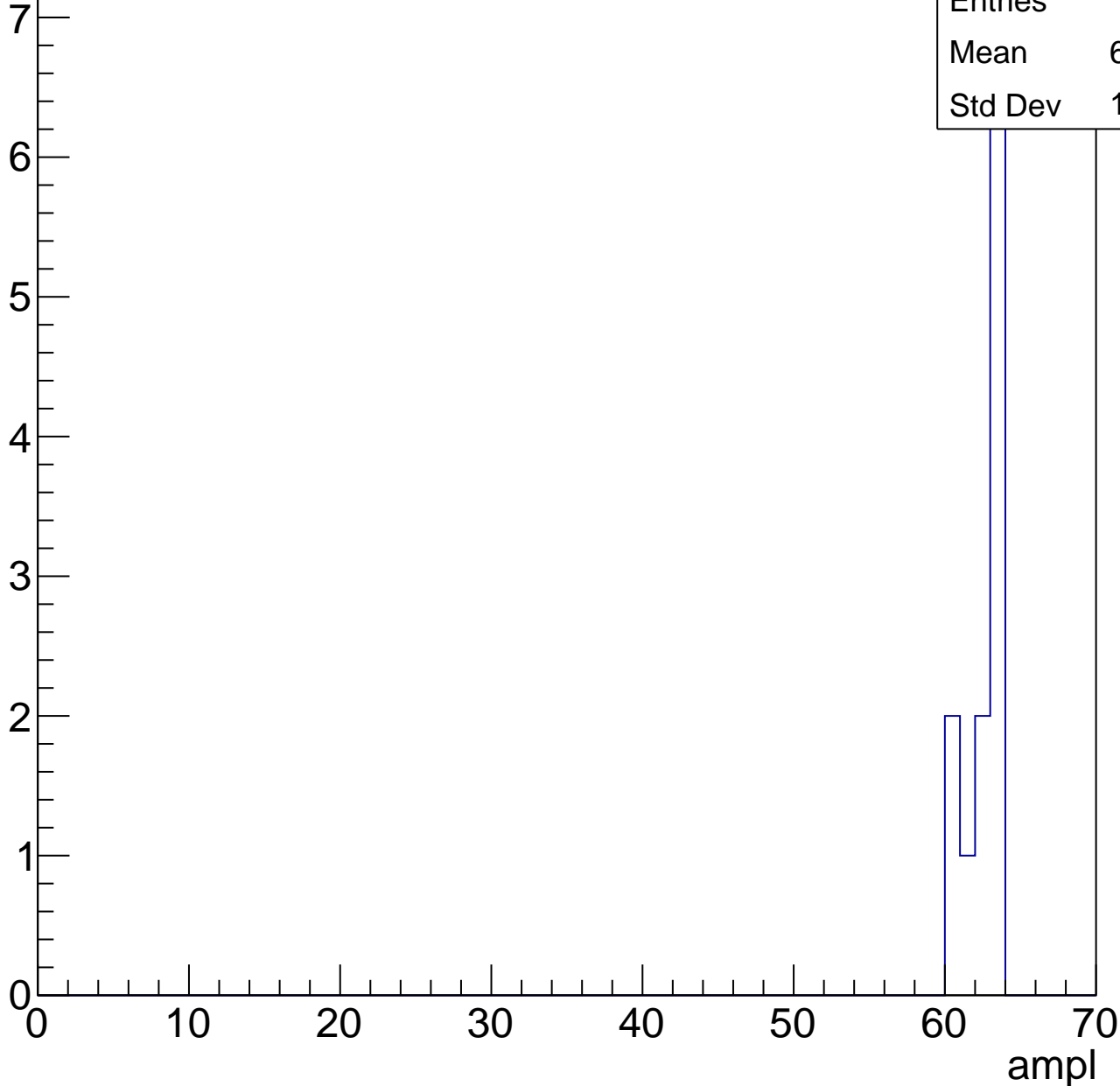


# B0L101S, U23-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62.17
Std Dev	1.143





# B0L101S, U23-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch13, adc0

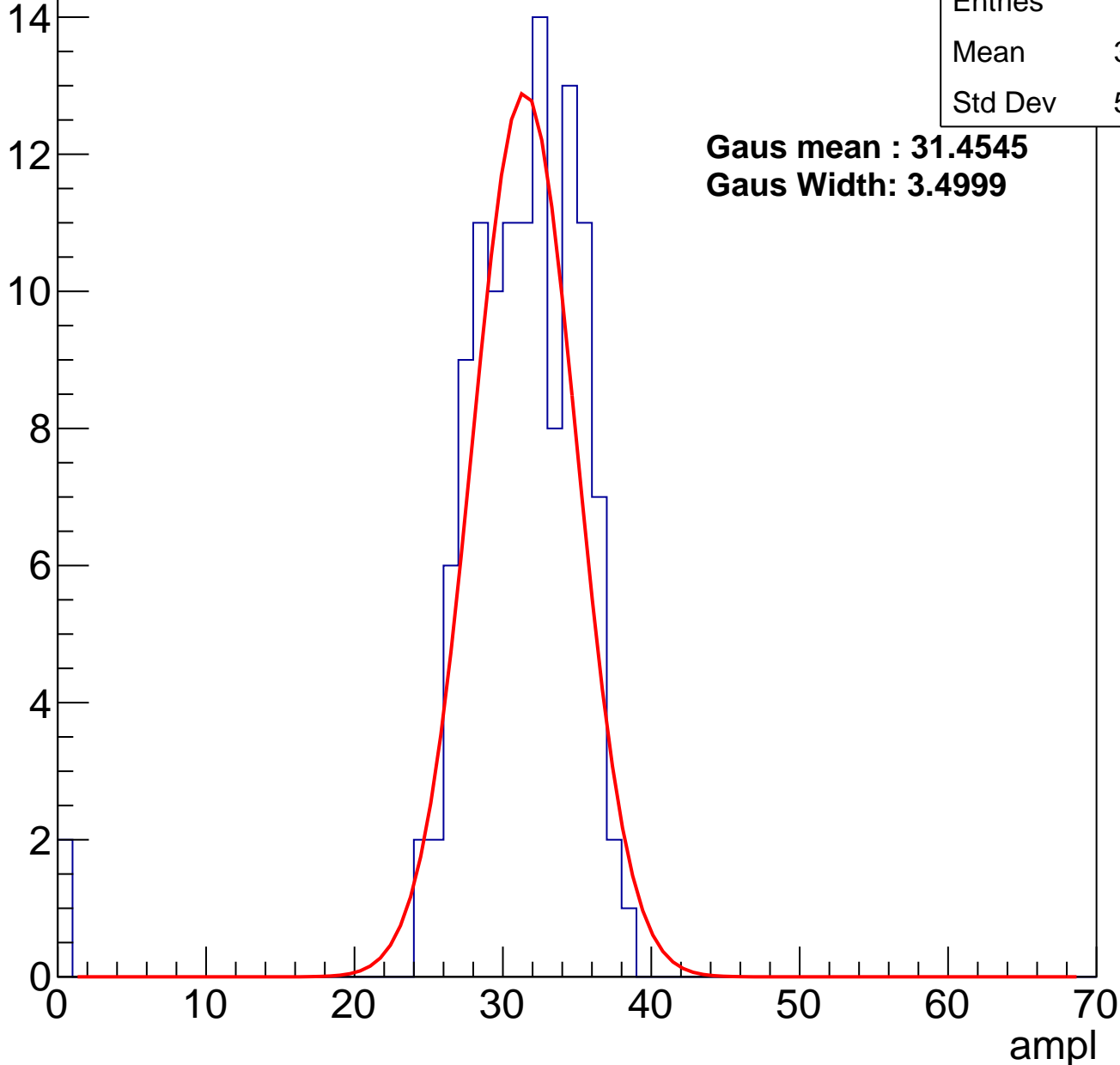
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	30.57
Std Dev	5.123

**Gaus mean : 31.4545**

**Gaus Width: 3.4999**

Entry



# B0L101S, U23-ch13, adc1

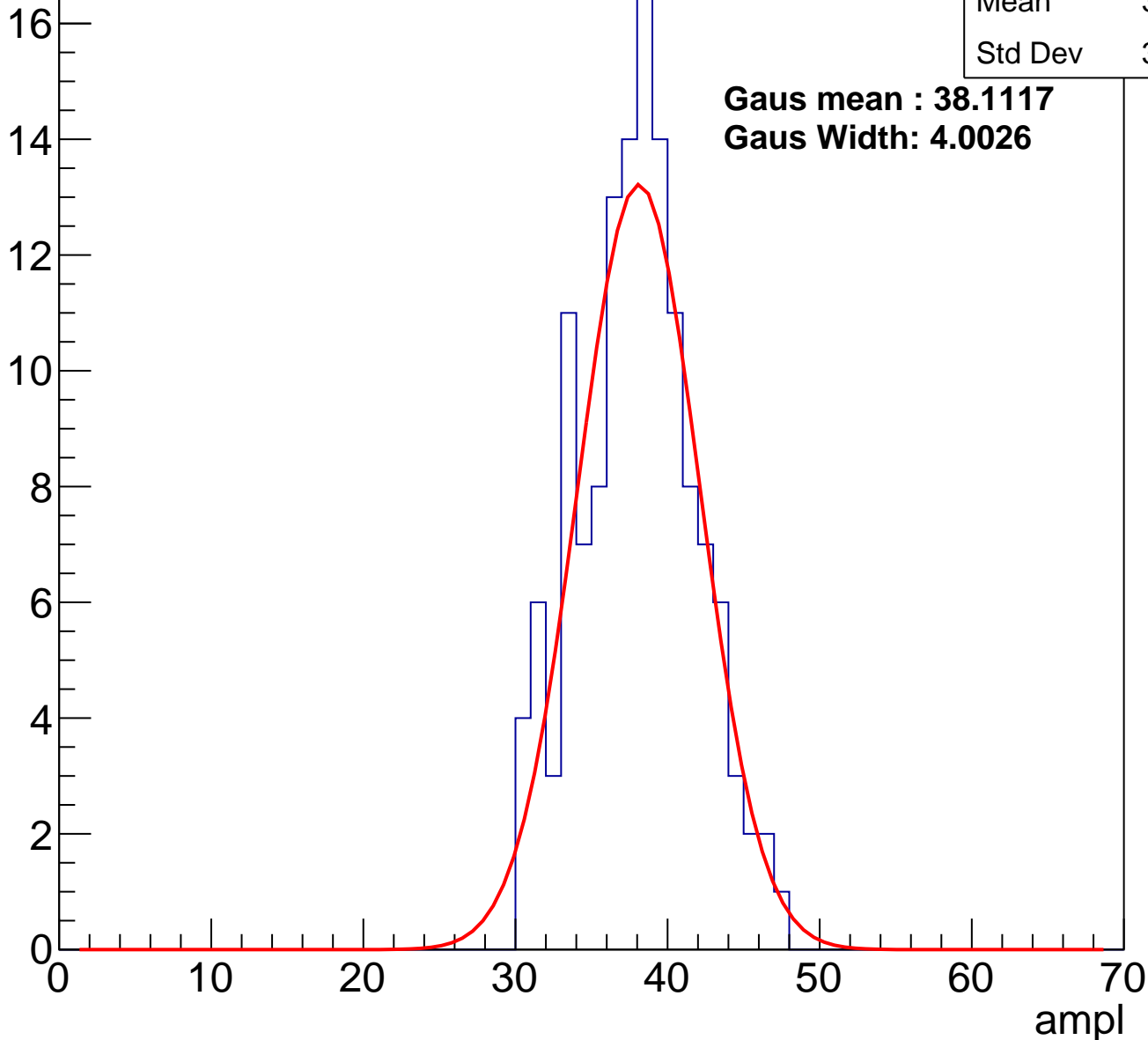
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	37.53
Std Dev	3.783

**Gaus mean : 38.1117**

**Gaus Width: 4.0026**

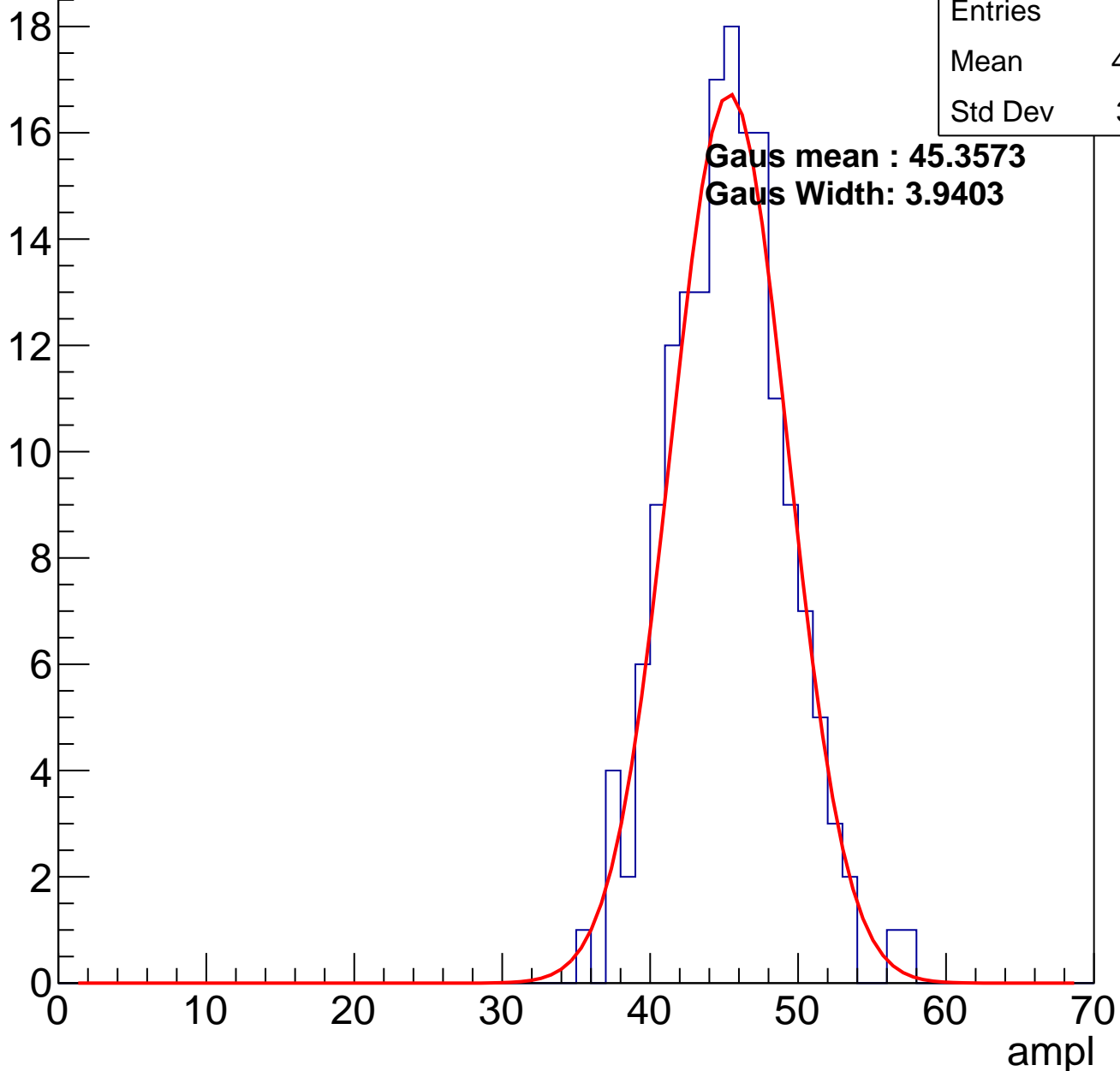
Entry



# B0L101S, U23-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

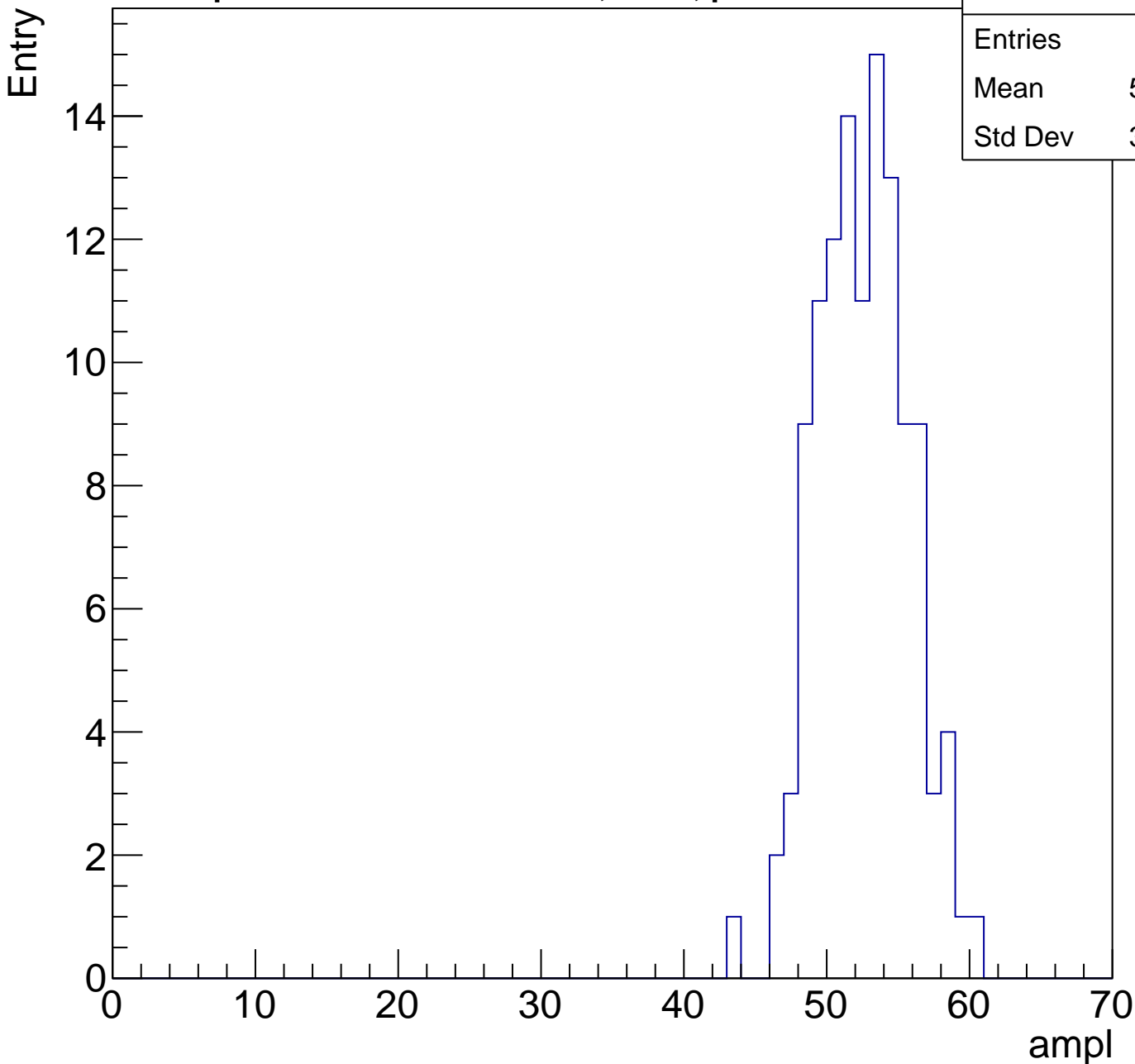
Entry



# B0L101S, U23-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	52.13
Std Dev	3.129



# B0L101S, U23-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	57.47
Std Dev	5.968

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

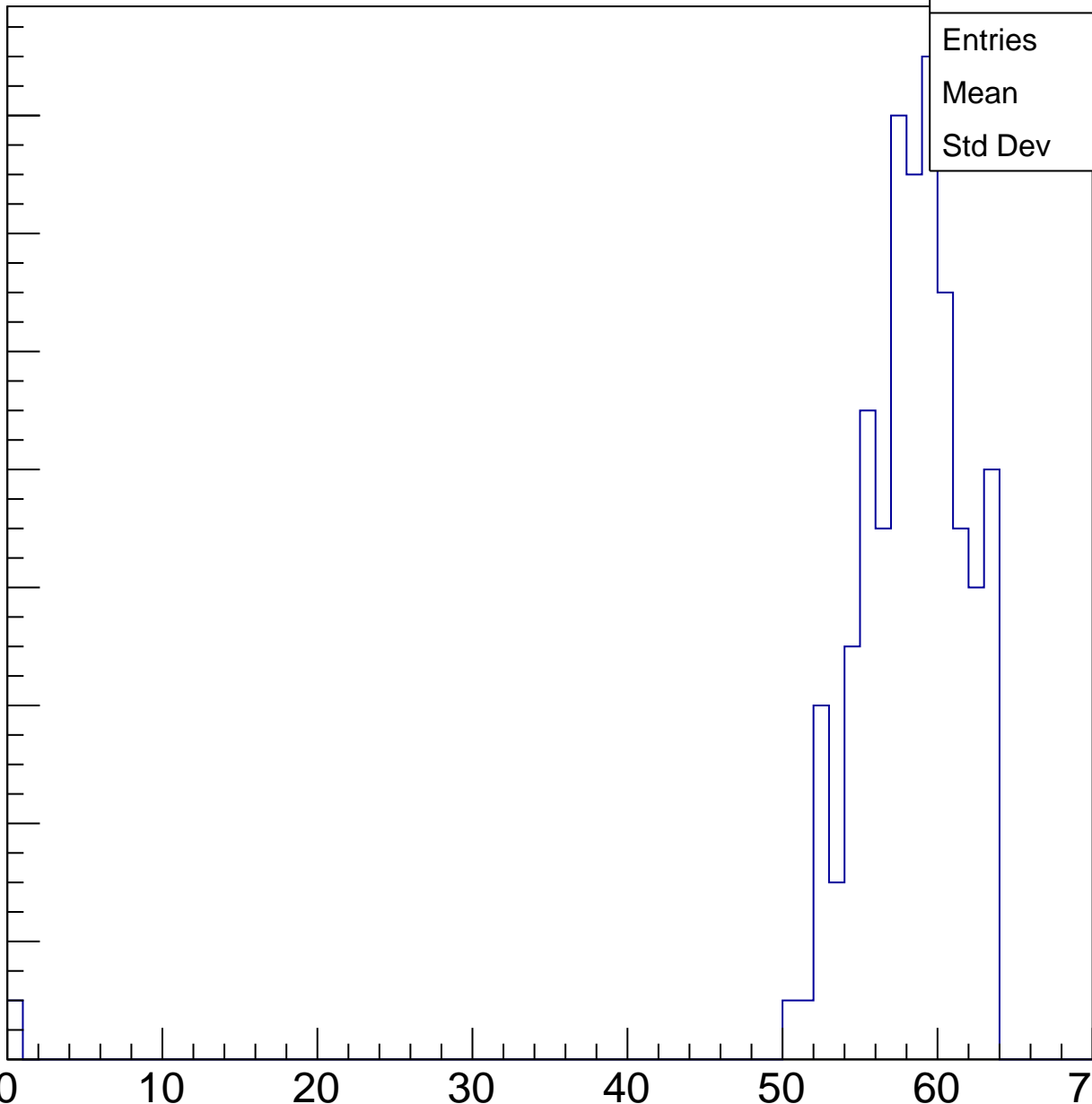
40

50

60

70

ampl



# B0L101S, U23-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	42
Mean	59.83
Std Dev	9.464

# B0L101S, U23-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



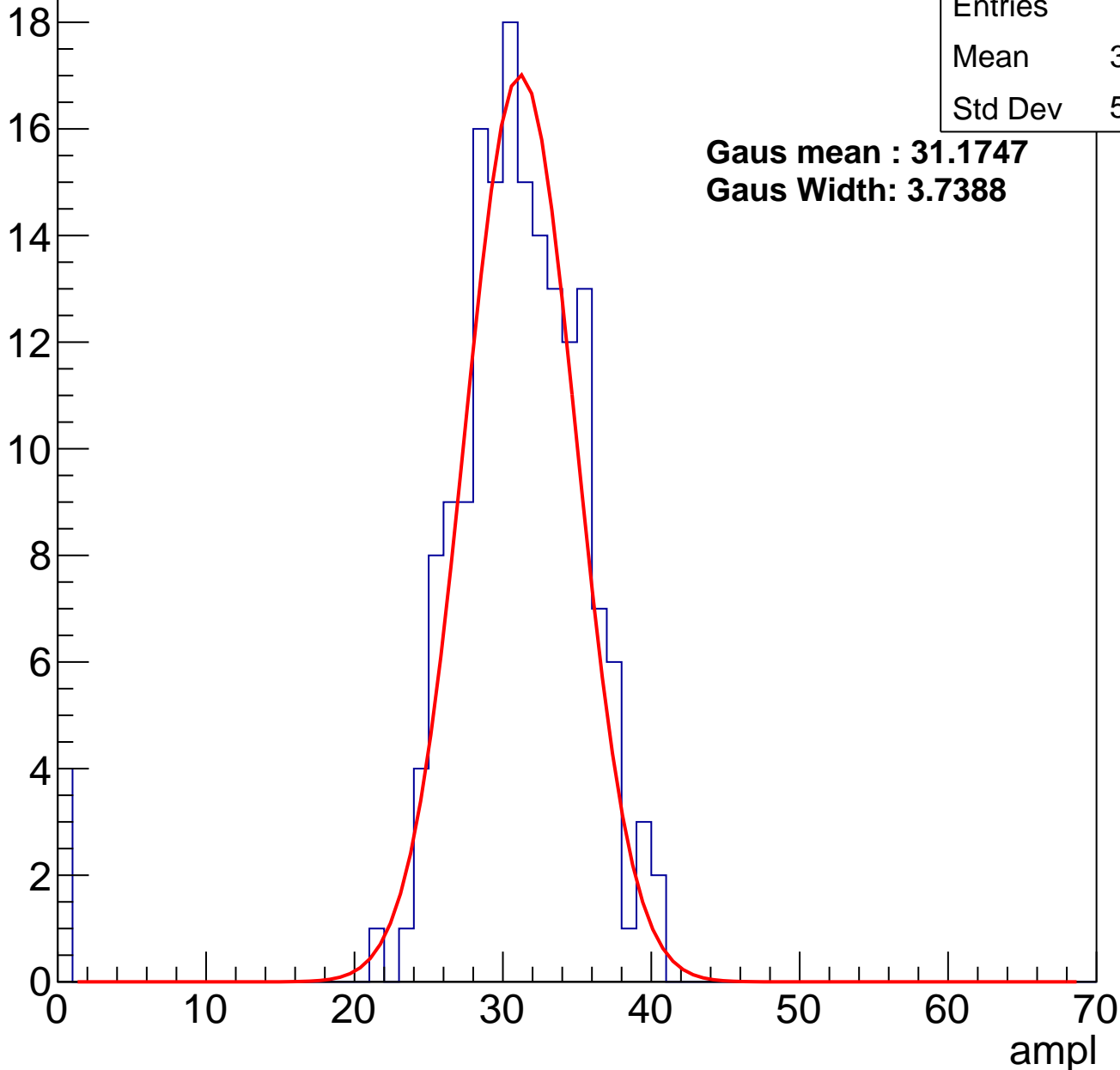
# B0L101S, U23-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	30.14
Std Dev	5.989

**Gaus mean : 31.1747**  
**Gaus Width: 3.7388**

Entry



# B0L101S, U23-ch14, adc1

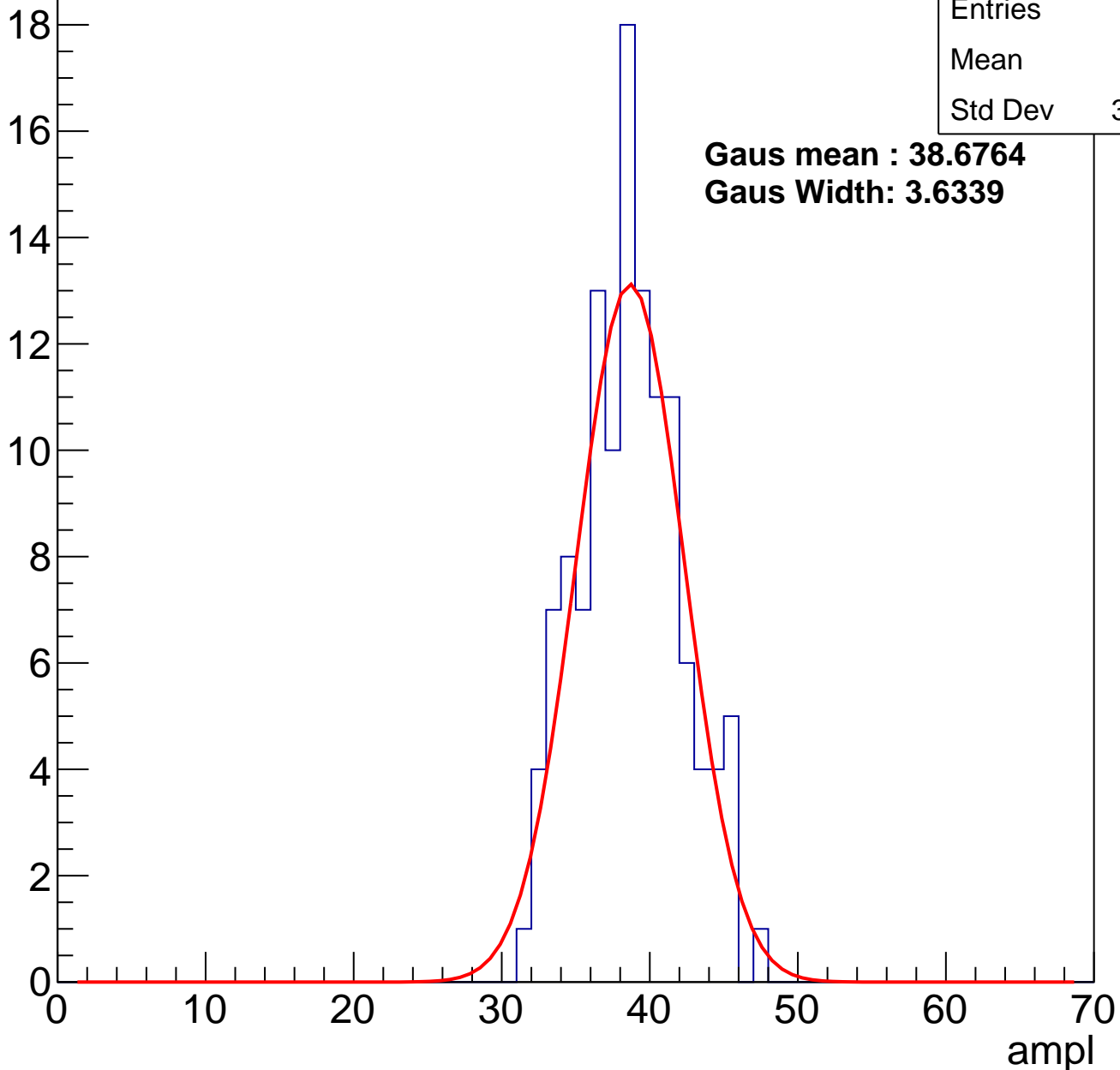
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	38.2
Std Dev	3.419

**Gaus mean : 38.6764**

**Gaus Width: 3.6339**

Entry



# B0L101S, U23-ch14, adc2

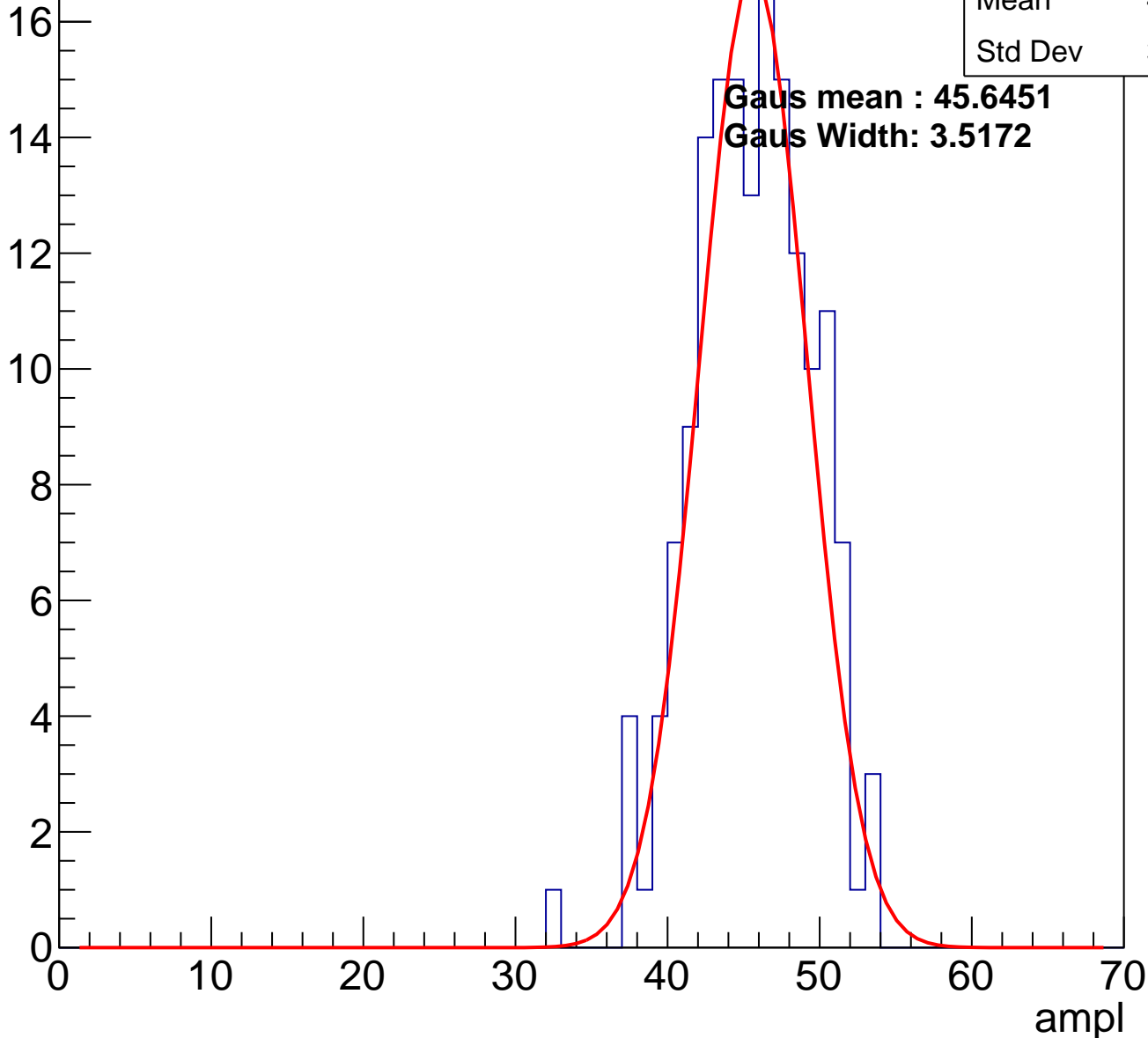
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	45.11
Std Dev	3.761

**Gaus mean : 45.6451**

**Gaus Width: 3.5172**

Entry

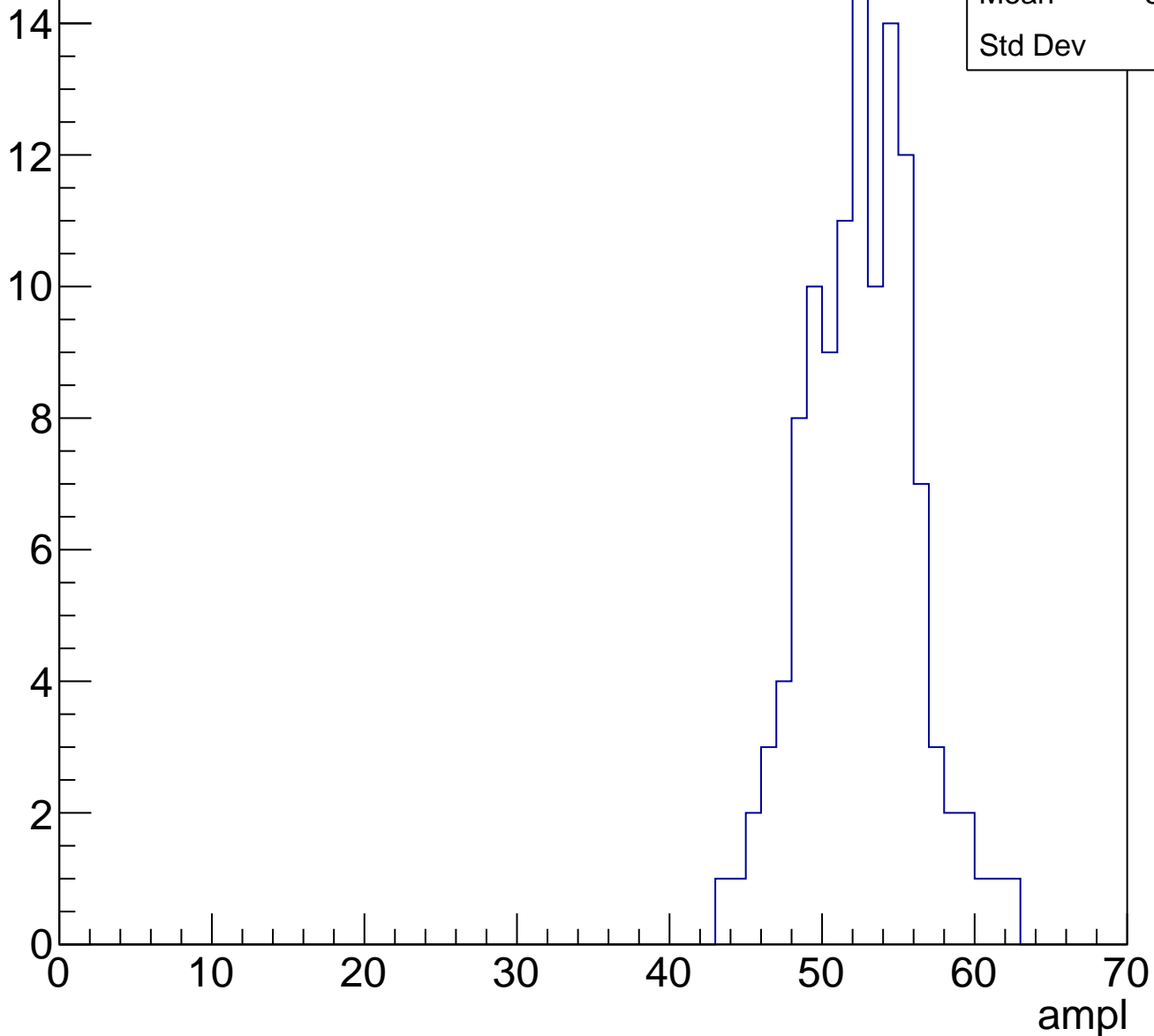


# B0L101S, U23-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	52.09
Std Dev	3.57

Entry



# B0L101S, U23-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

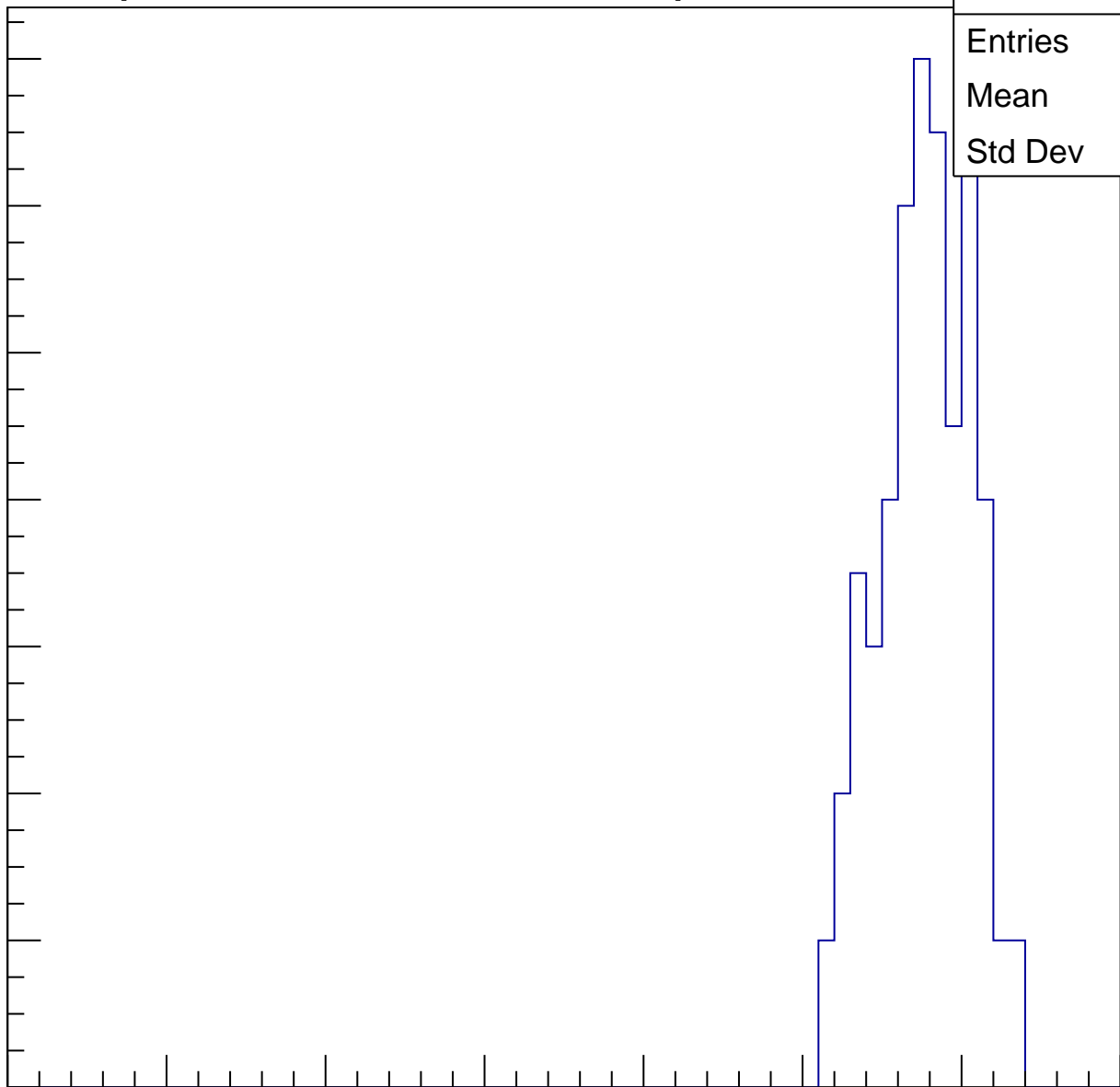
Entries	101
Mean	57.21
Std Dev	2.819

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch14, adc5

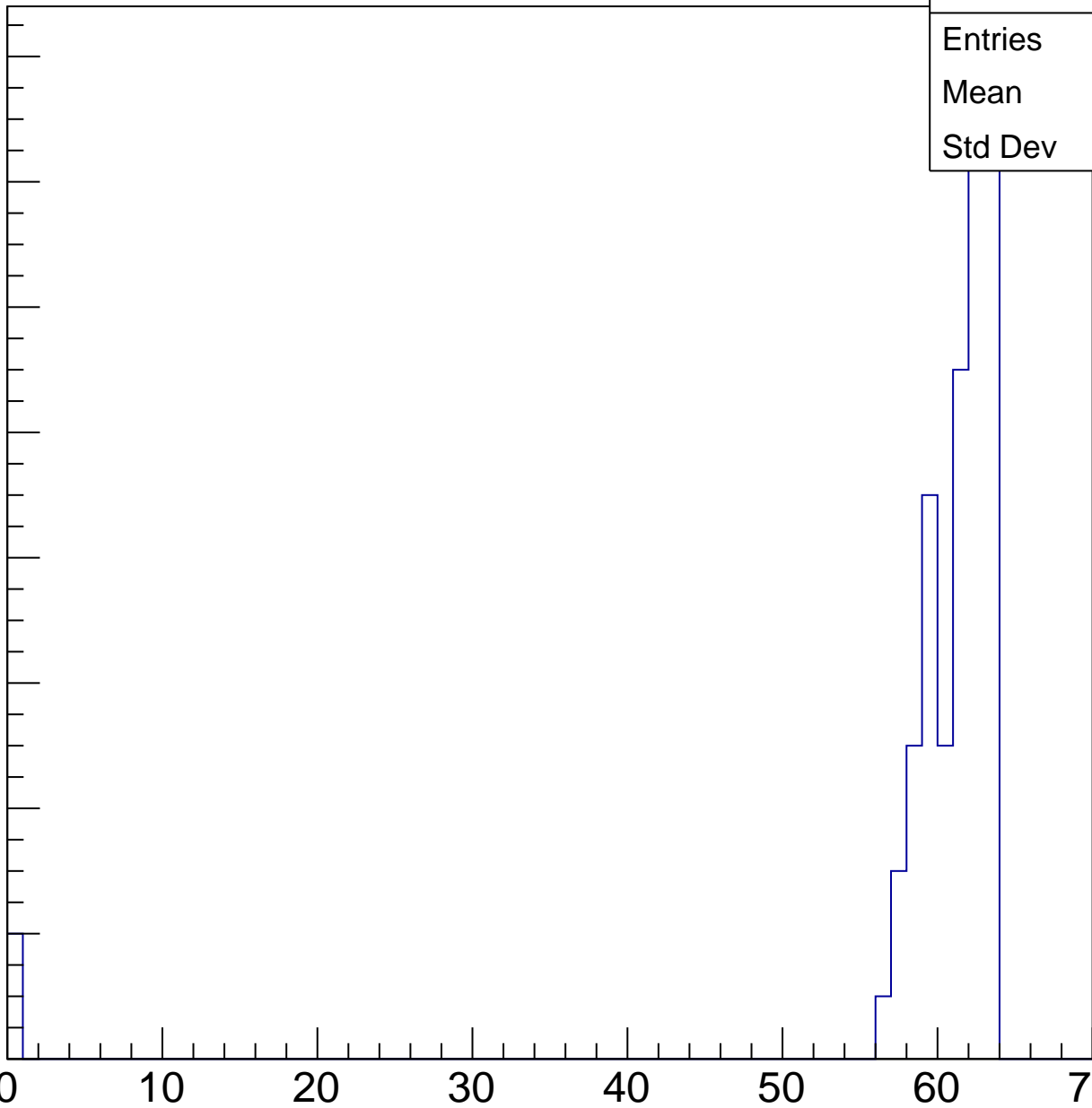
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	67
Mean	59.06
Std Dev	10.53

ampl



# B0L101S, U23-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch15, adc0

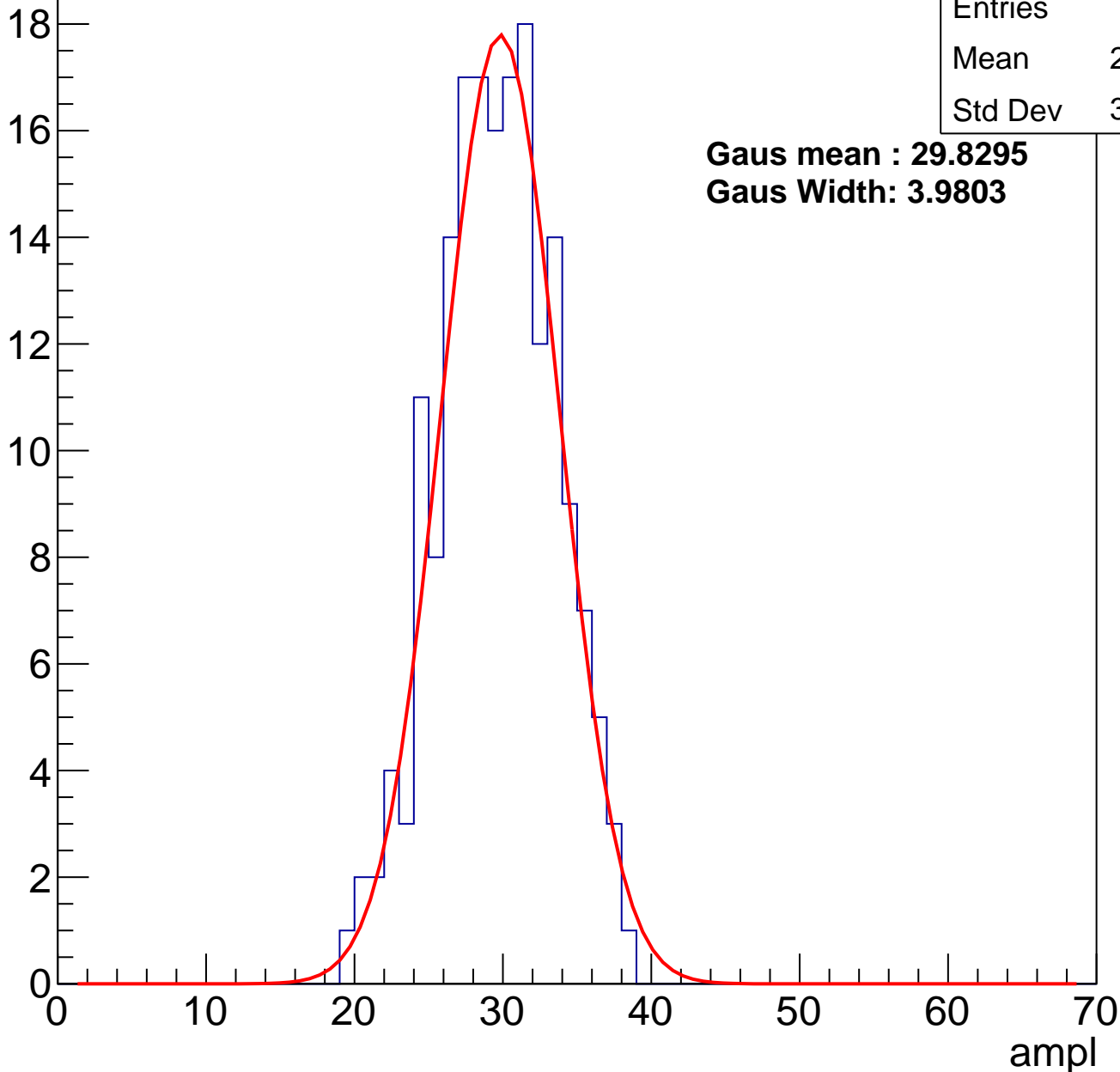
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	181
Mean	29.17
Std Dev	3.876

**Gaus mean : 29.8295**

**Gaus Width: 3.9803**

Entry



# B0L101S, U23-ch15, adc1

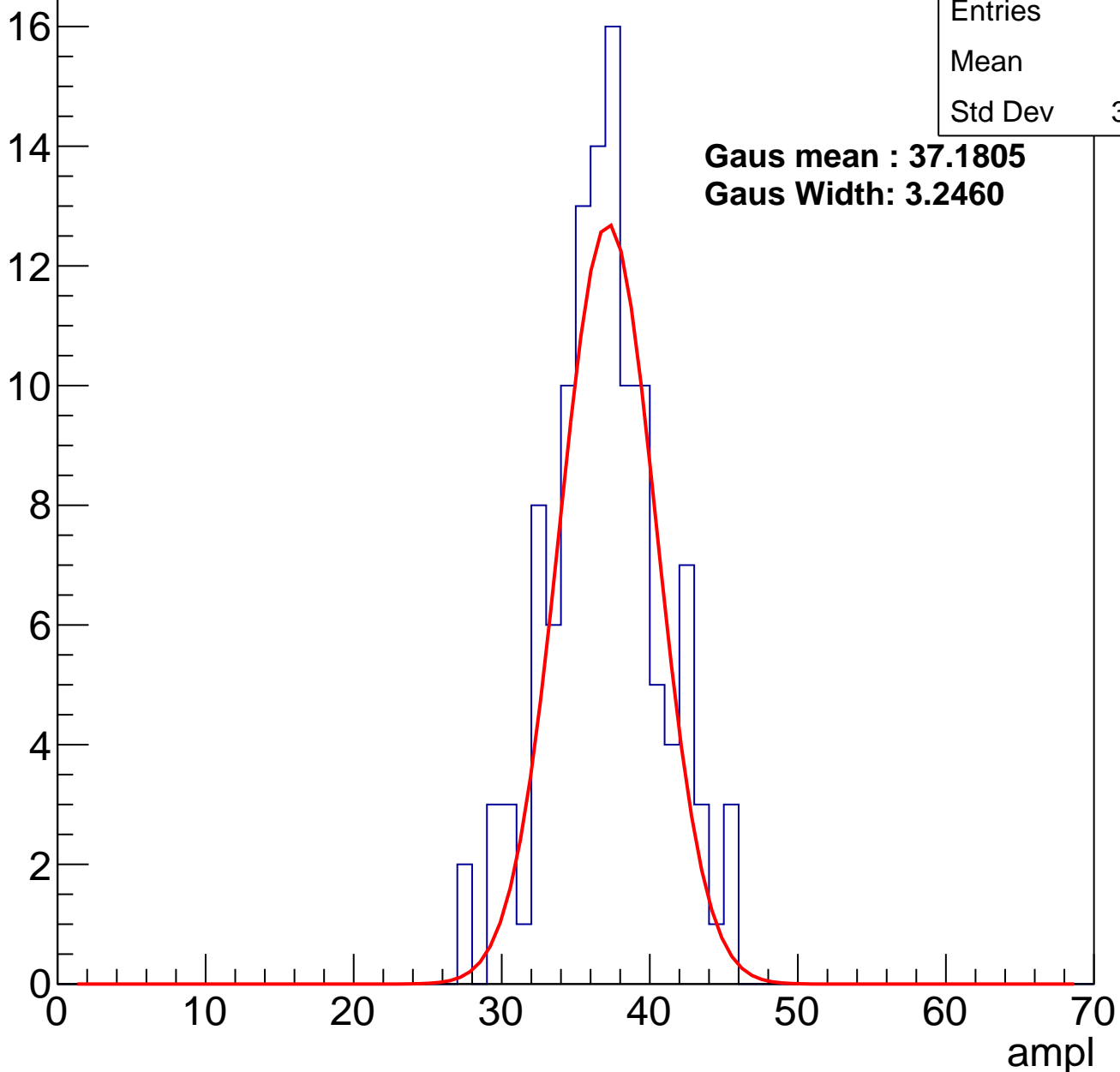
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	36.5
Std Dev	3.757

**Gaus mean : 37.1805**

**Gaus Width: 3.2460**

Entry



# B0L101S, U23-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

149

Mean

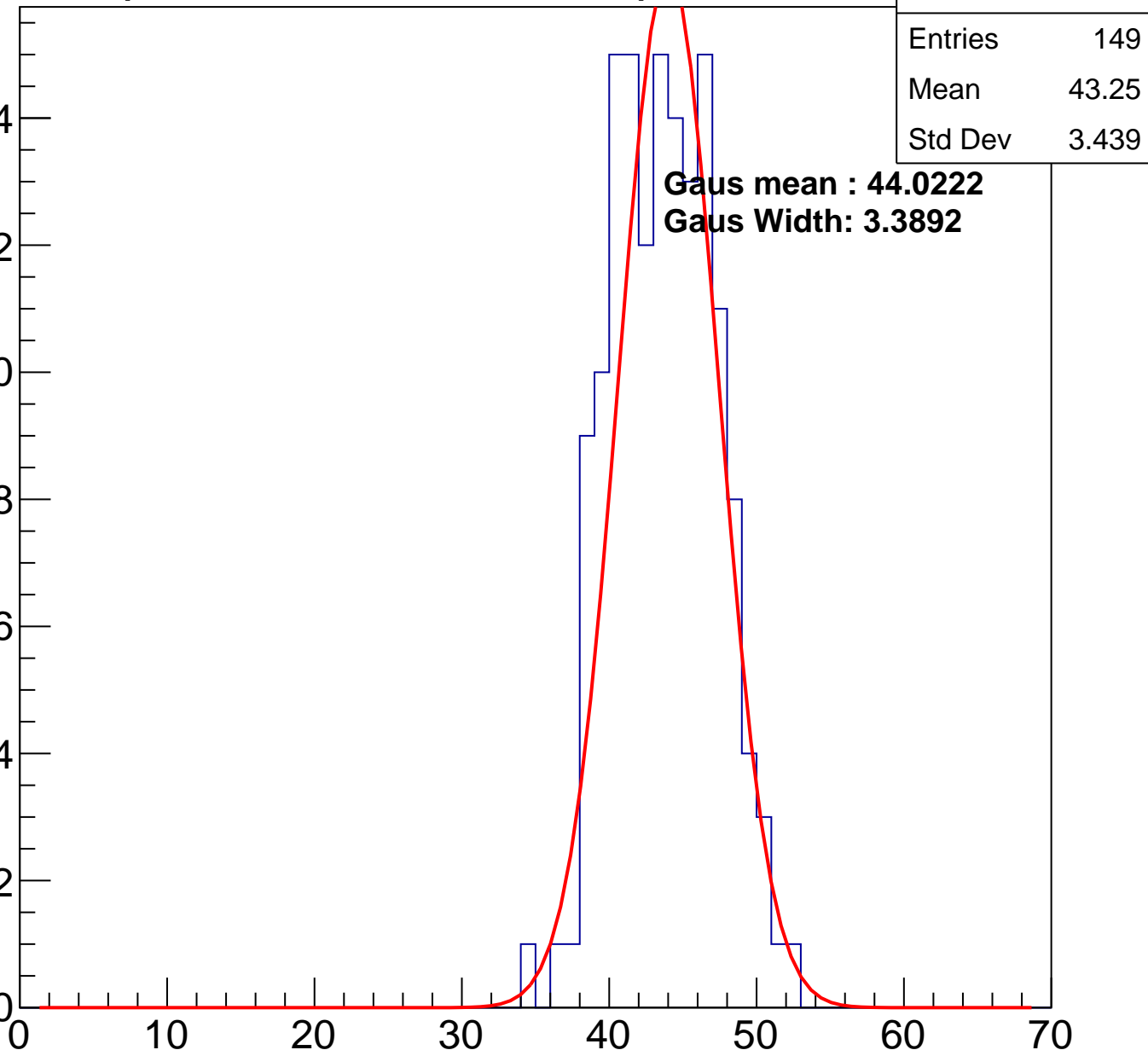
43.25

Std Dev

3.439

**Gaus mean : 44.0222**

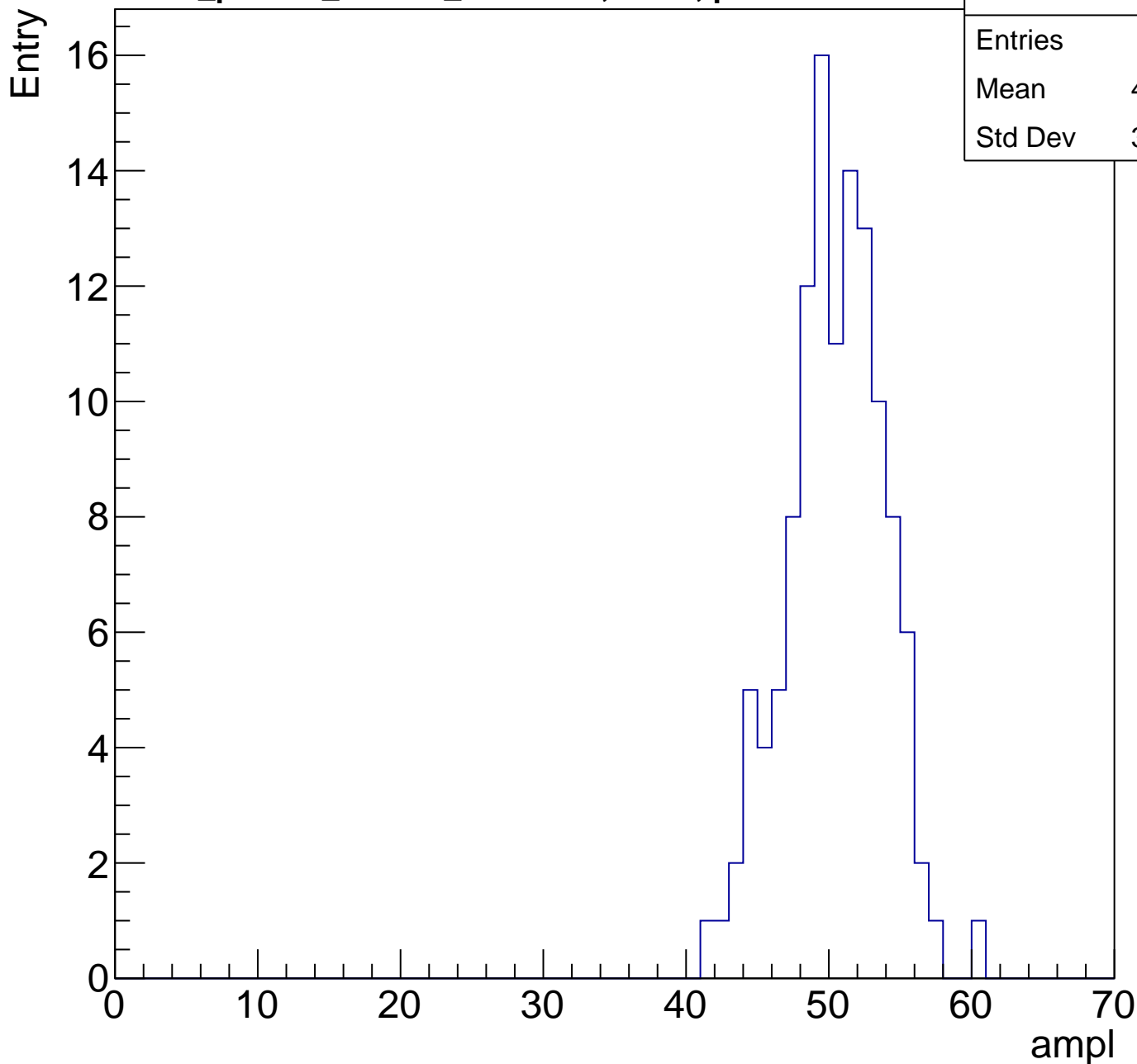
**Gaus Width: 3.3892**



# B0L101S, U23-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

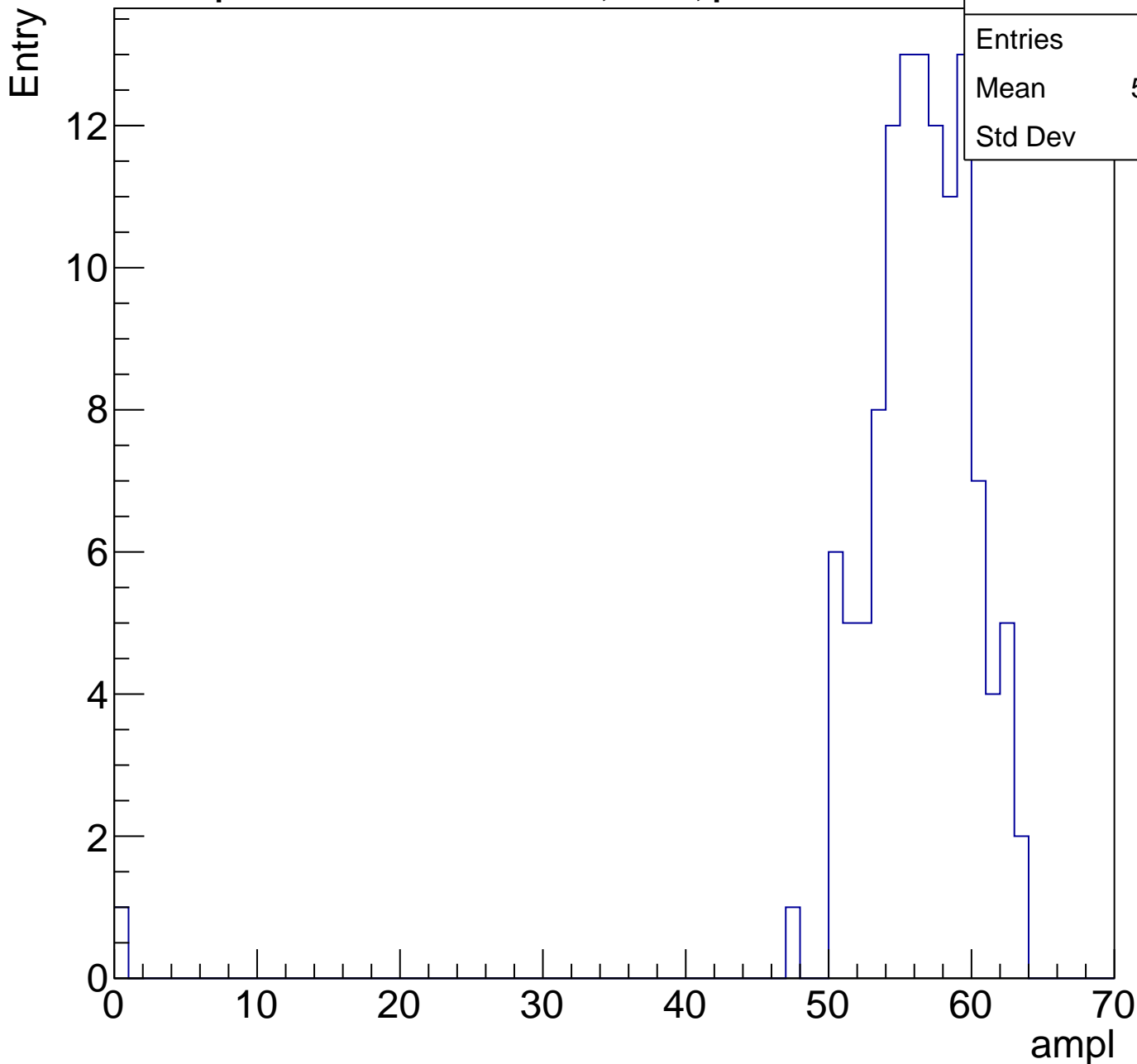
Entries	120
Mean	49.97
Std Dev	3.408



# B0L101S, U23-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	55.64
Std Dev	6.12



# B0L101S, U23-ch15, adc5

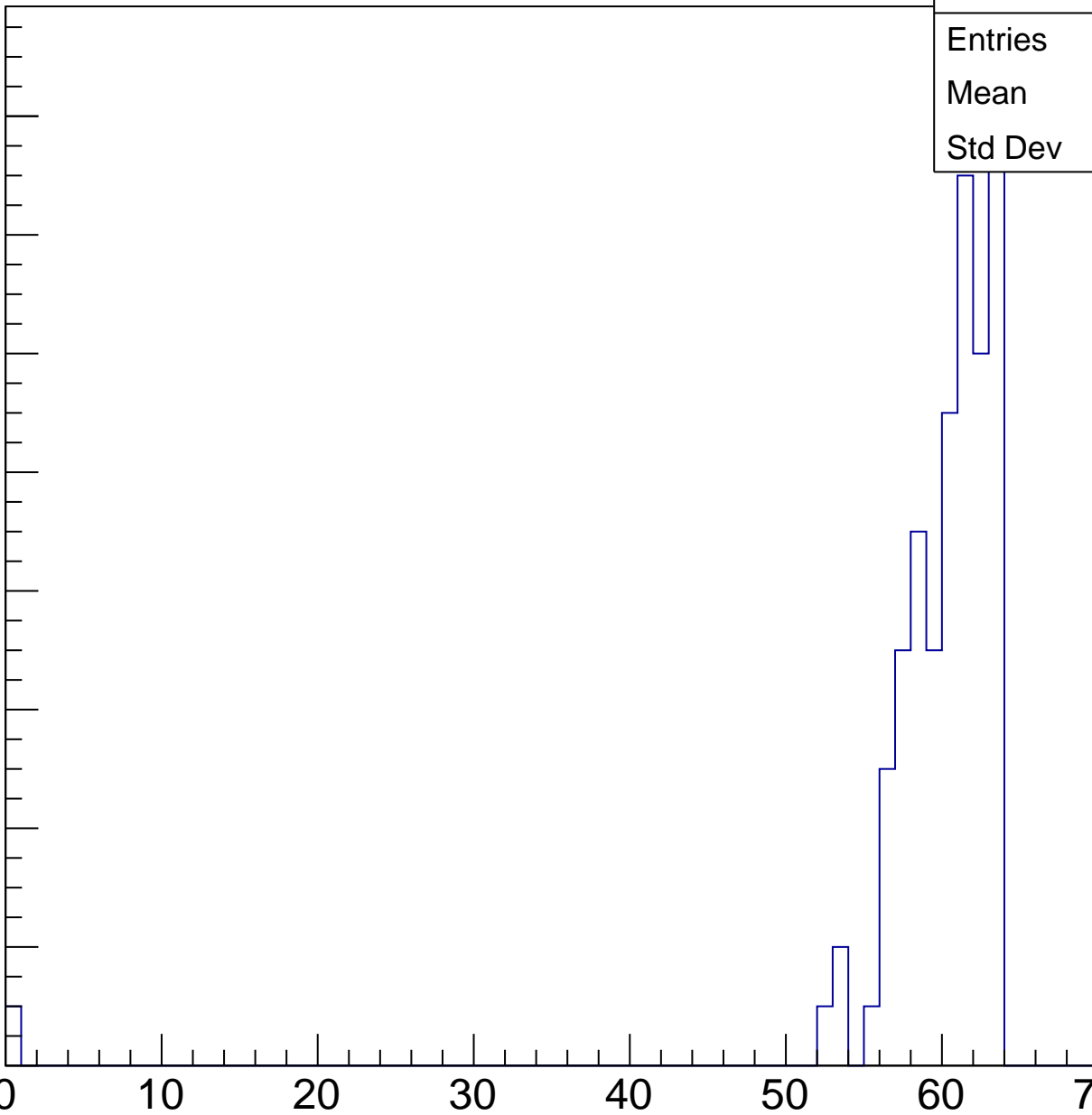
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	59.28
Std Dev	6.862

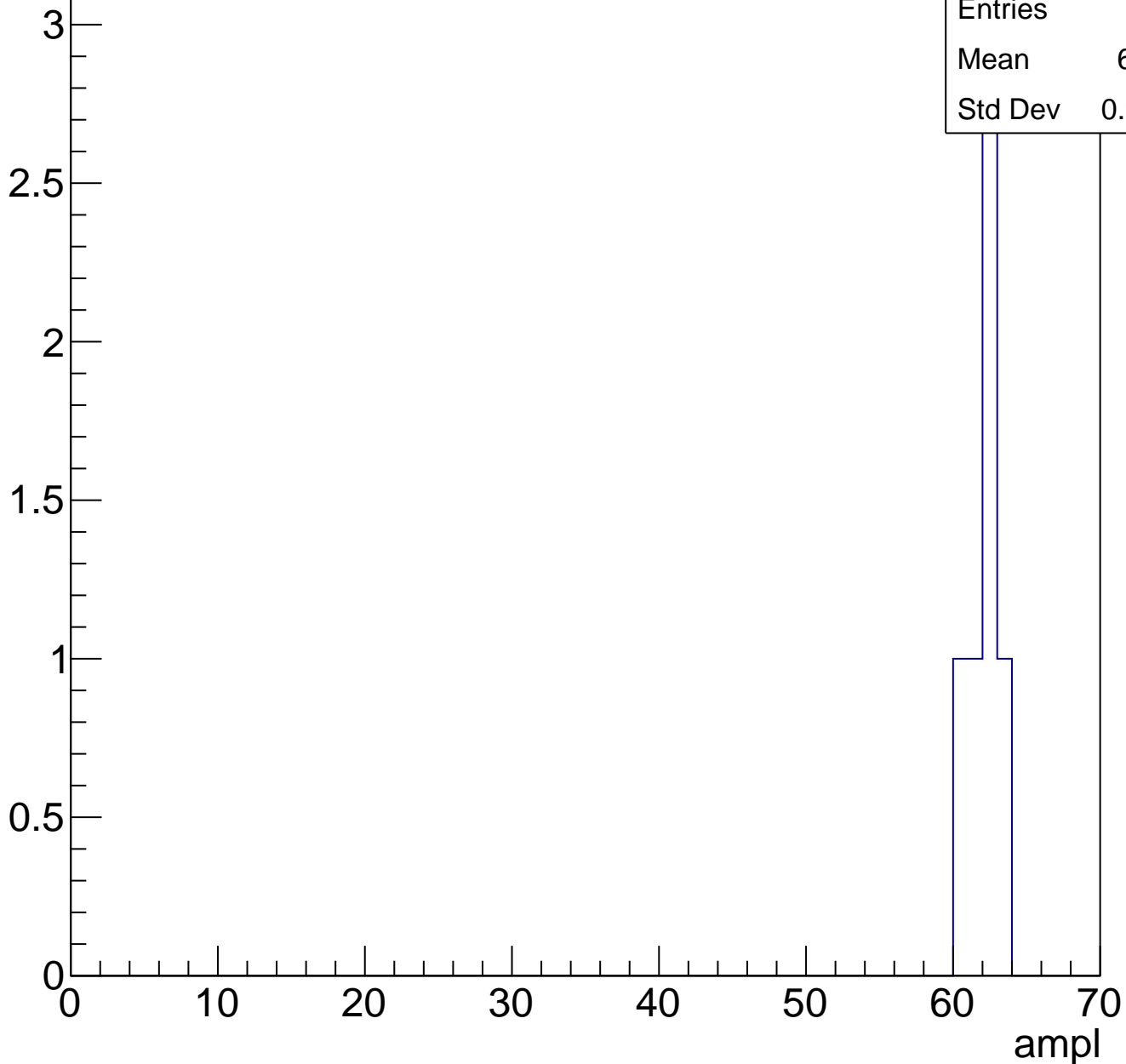
ampl



# B0L101S, U23-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch16, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	30.67
Std Dev	4.382

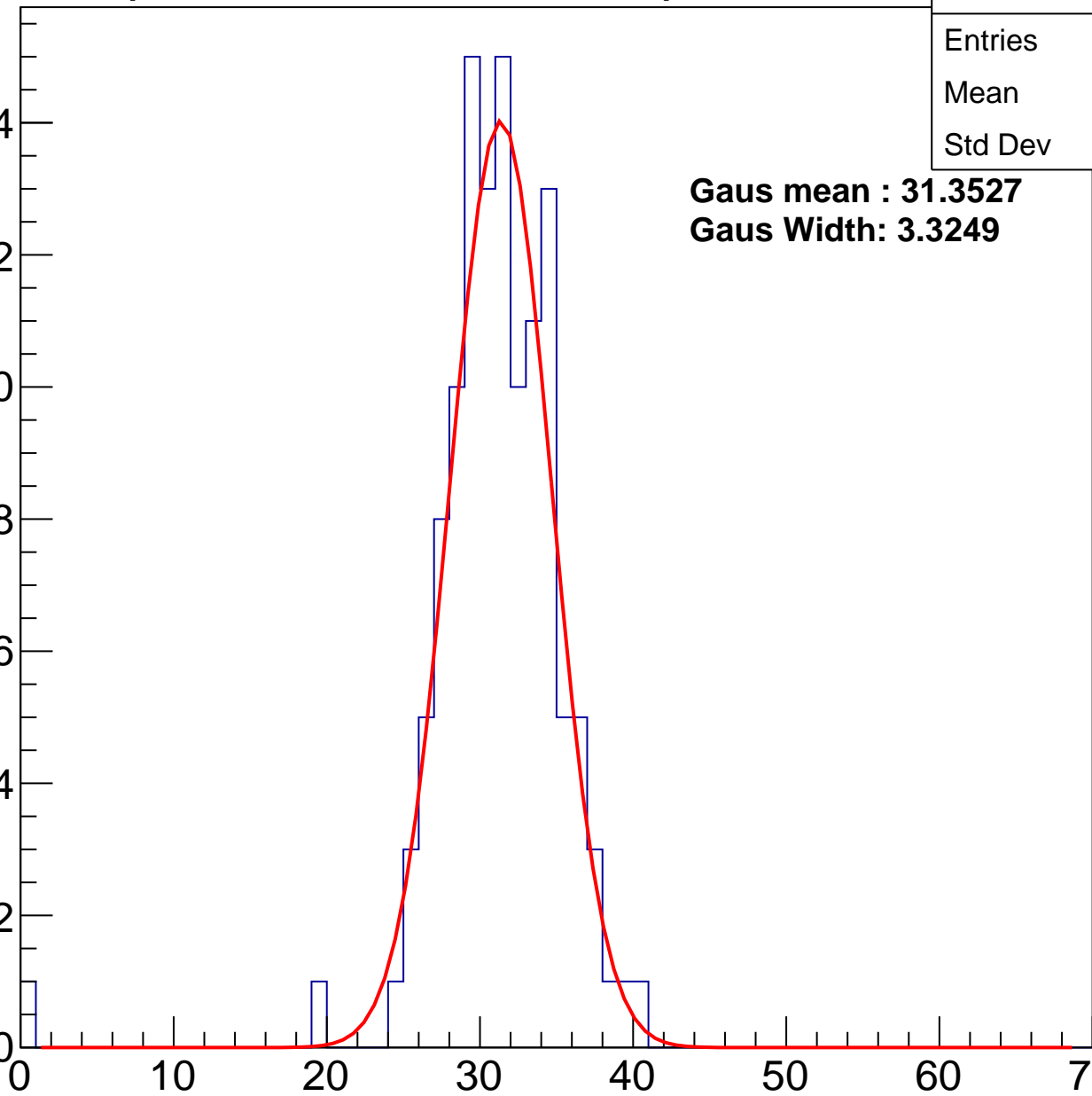
**Gaus mean : 31.3527**

**Gaus Width: 3.3249**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

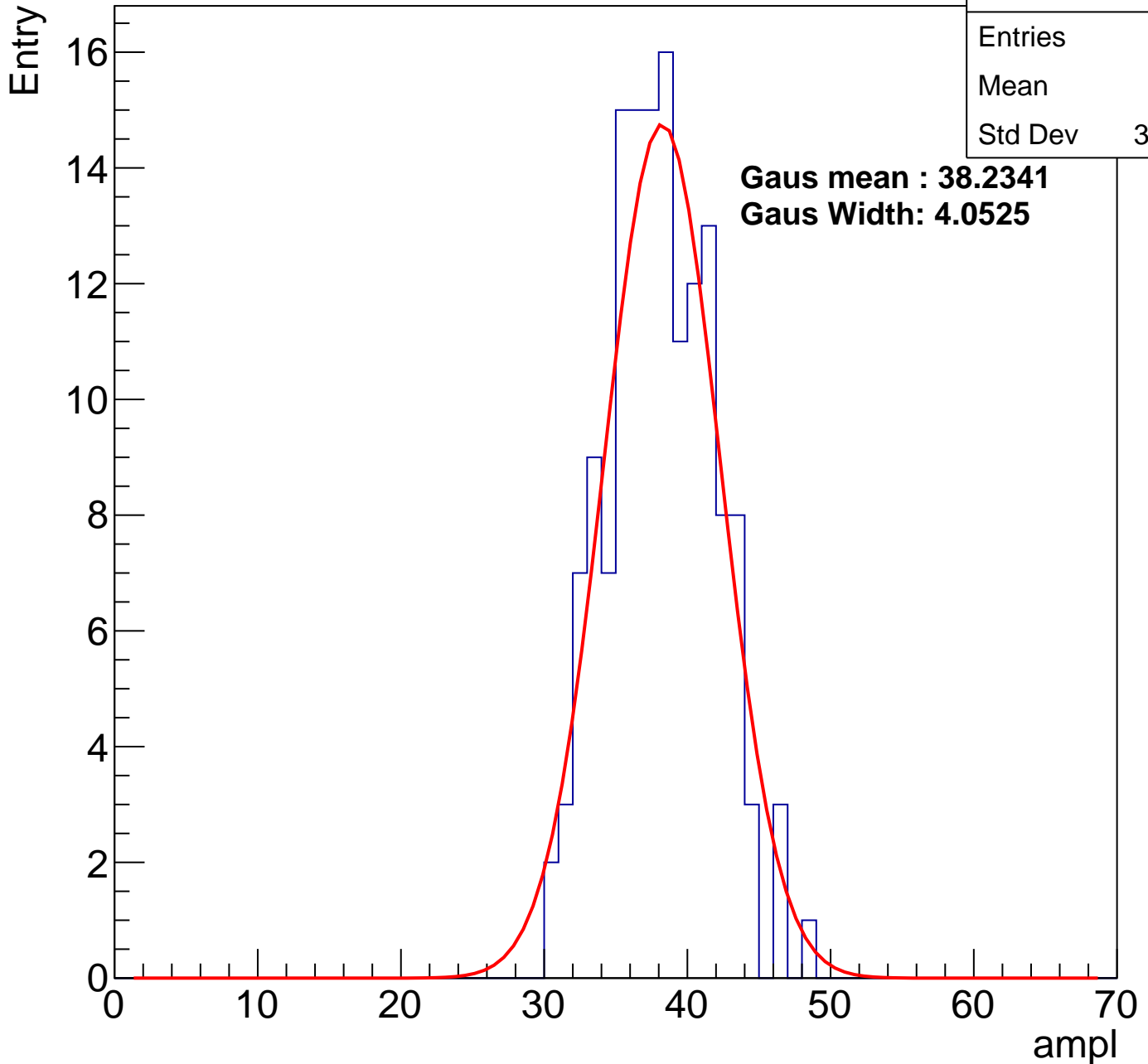


# B0L101S, U23-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	37.7
Std Dev	3.644

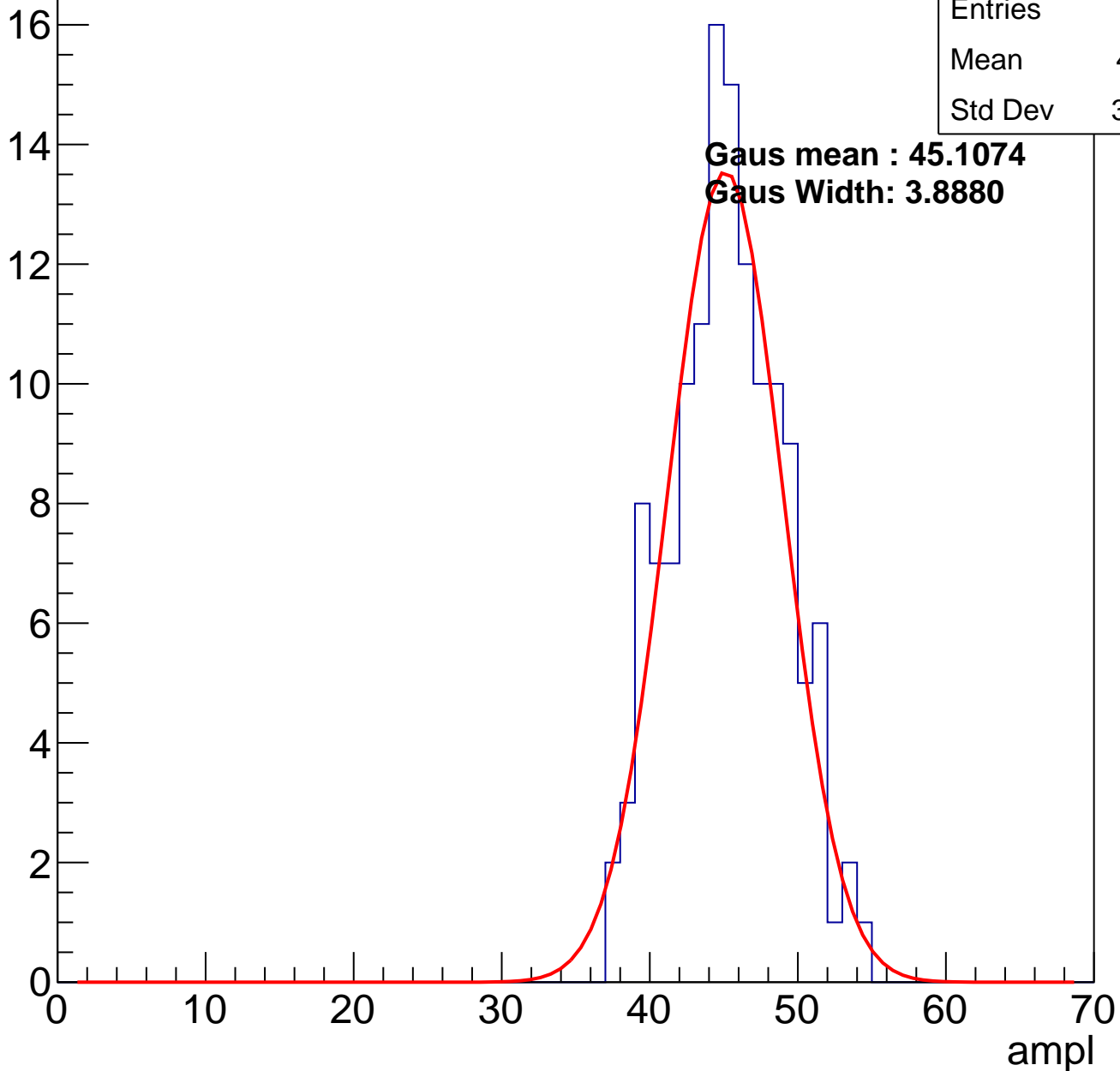
**Gaus mean : 38.2341**  
**Gaus Width: 4.0525**



# B0L101S, U23-ch16, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

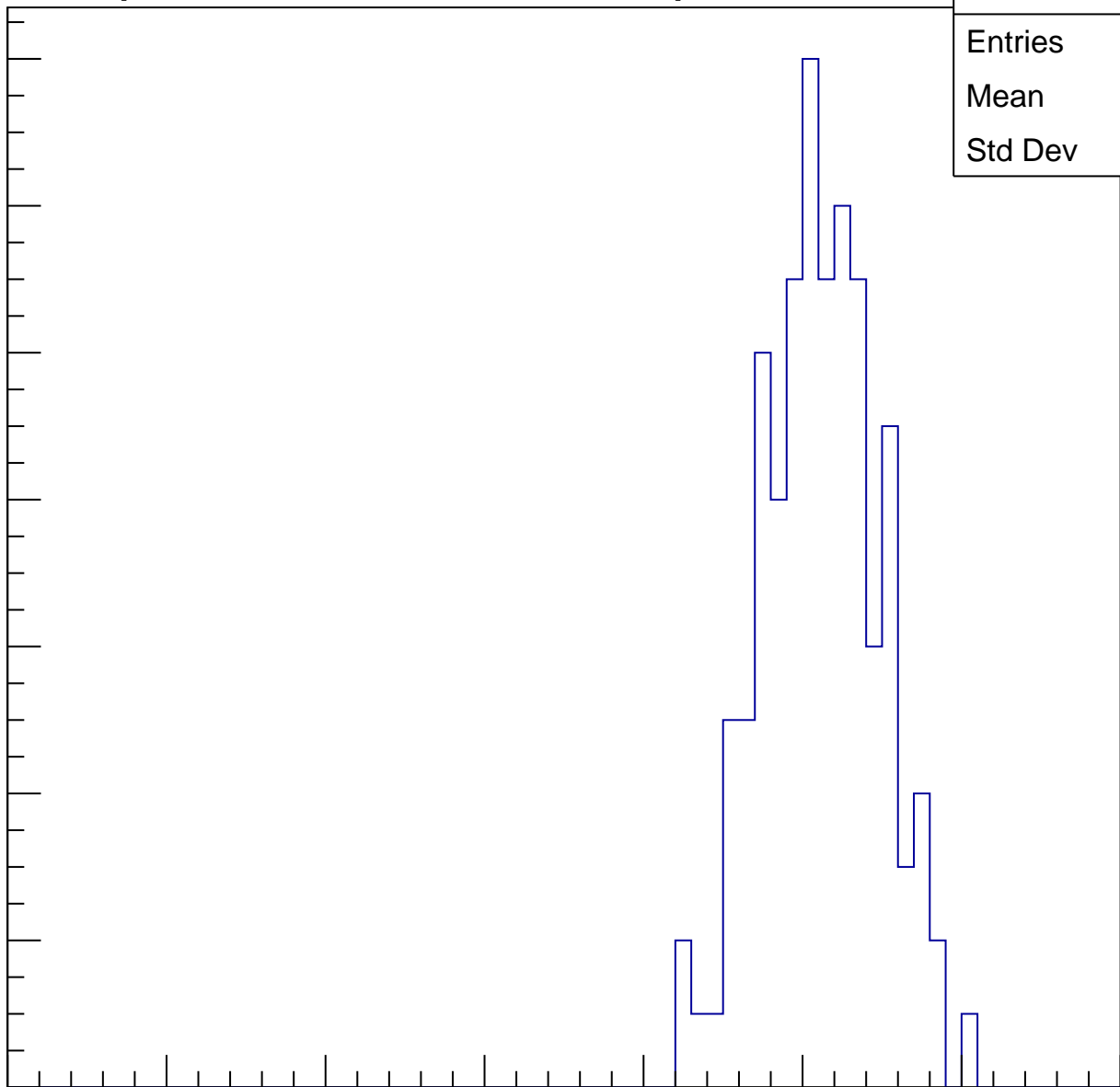
Entries	116
Mean	50.67
Std Dev	3.607

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch16, adc4

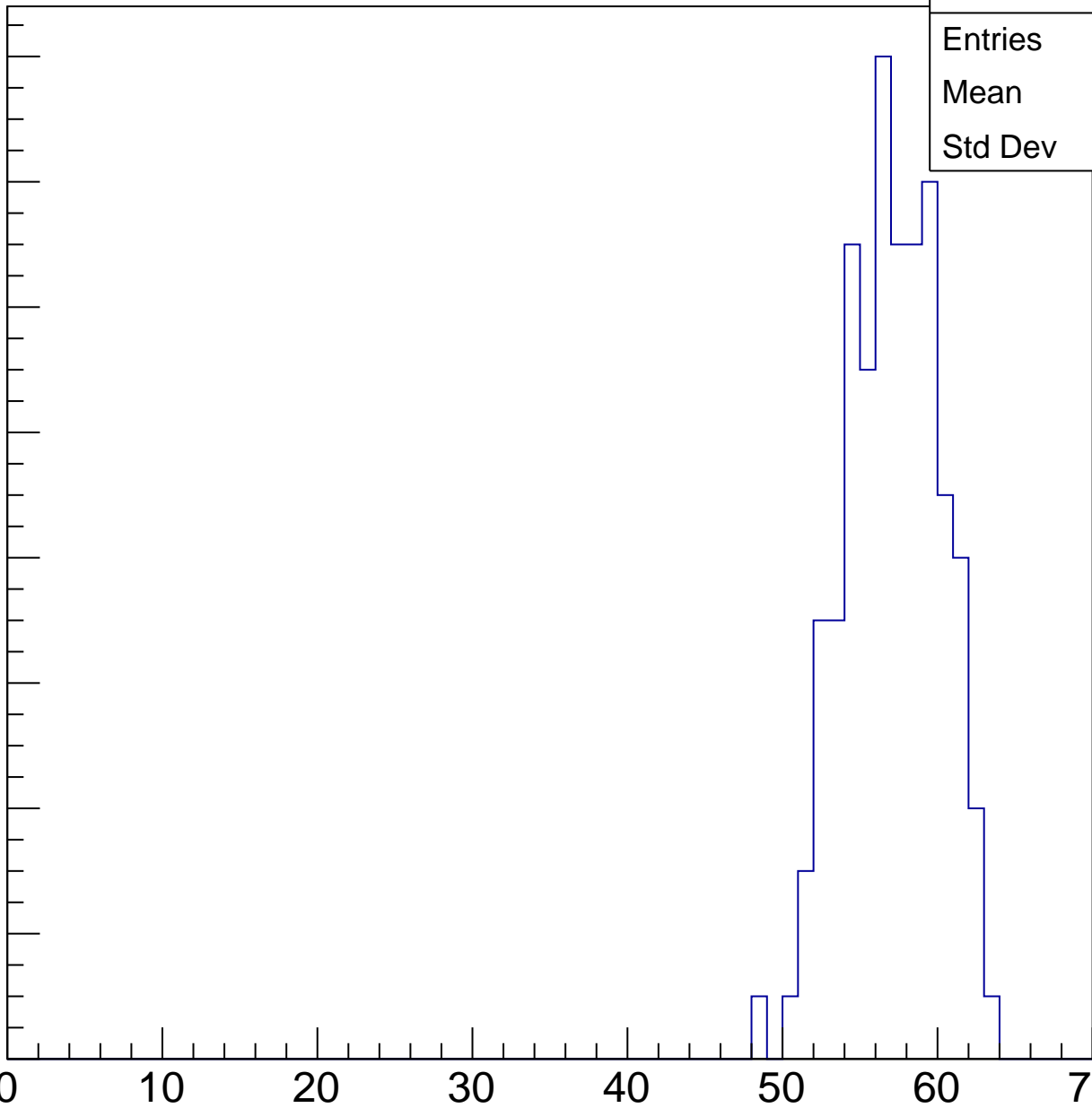
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	121
Mean	56.6
Std Dev	3.011

ampl



# B0L101S, U23-ch16, adc5

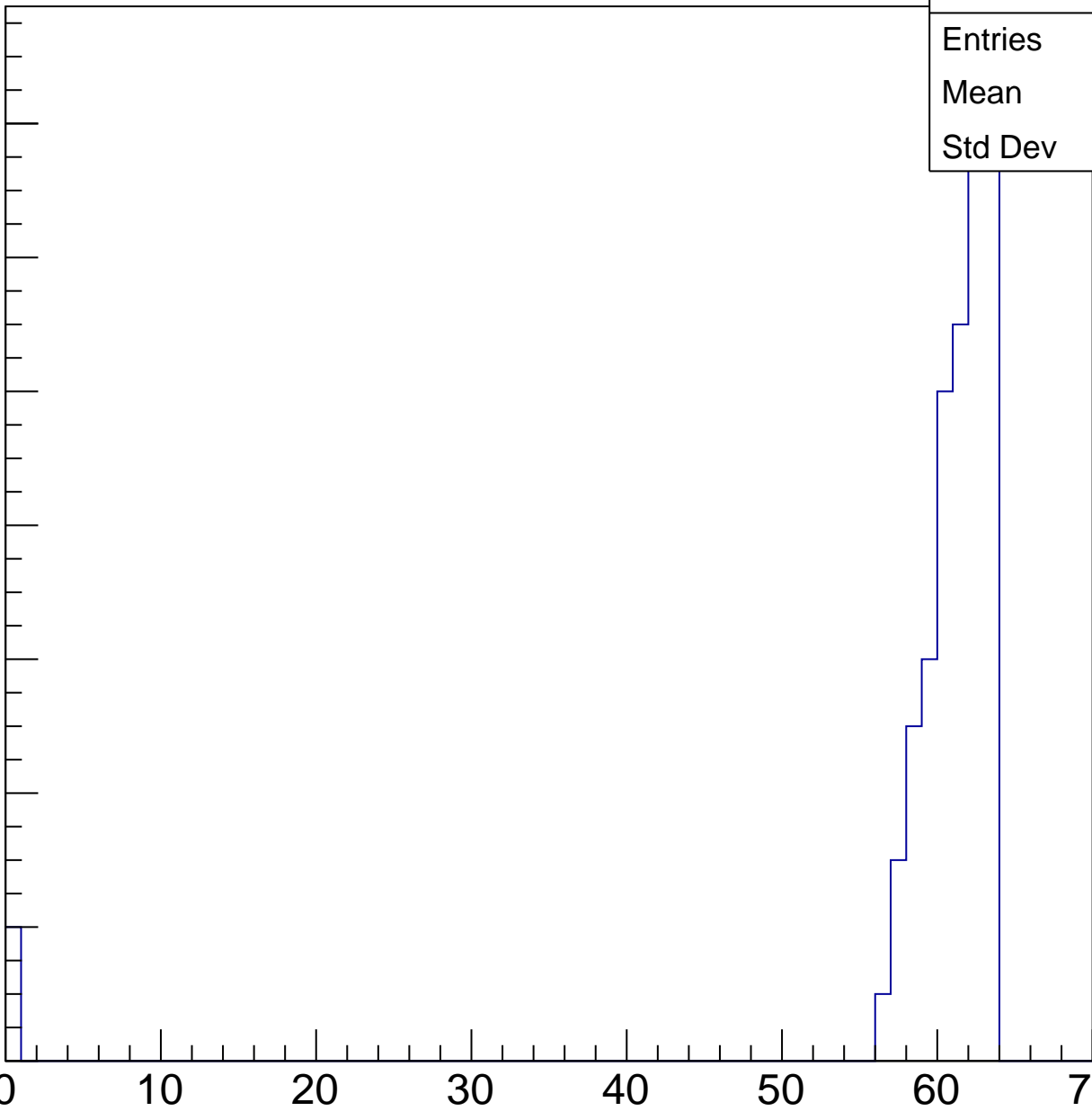
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	67
Mean	59.03
Std Dev	10.52

ampl



# B0L101S, U23-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L101S, U23-ch17, adc0

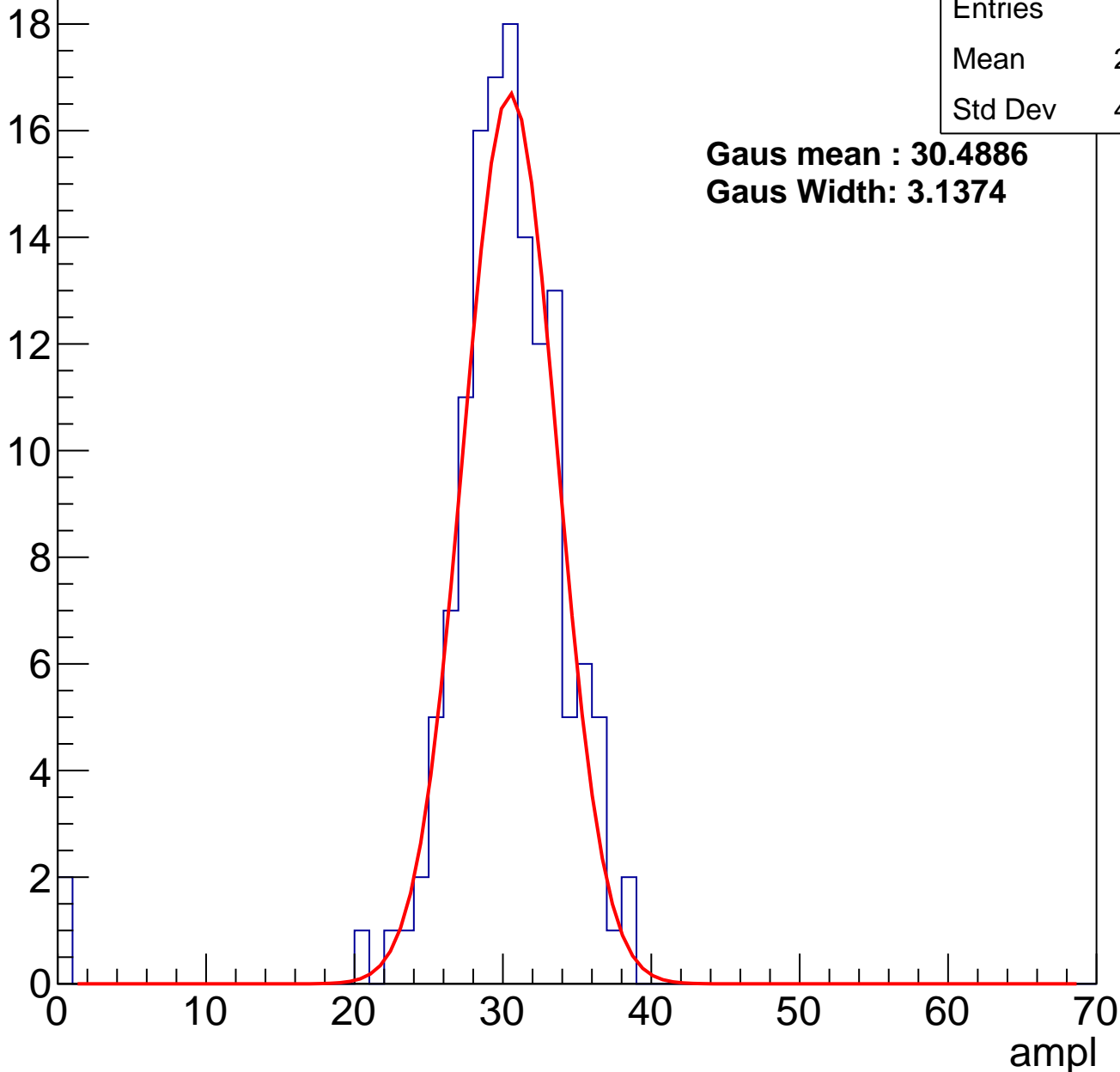
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	29.63
Std Dev	4.834

**Gaus mean : 30.4886**

**Gaus Width: 3.1374**

Entry



# B0L101S, U23-ch17, adc1

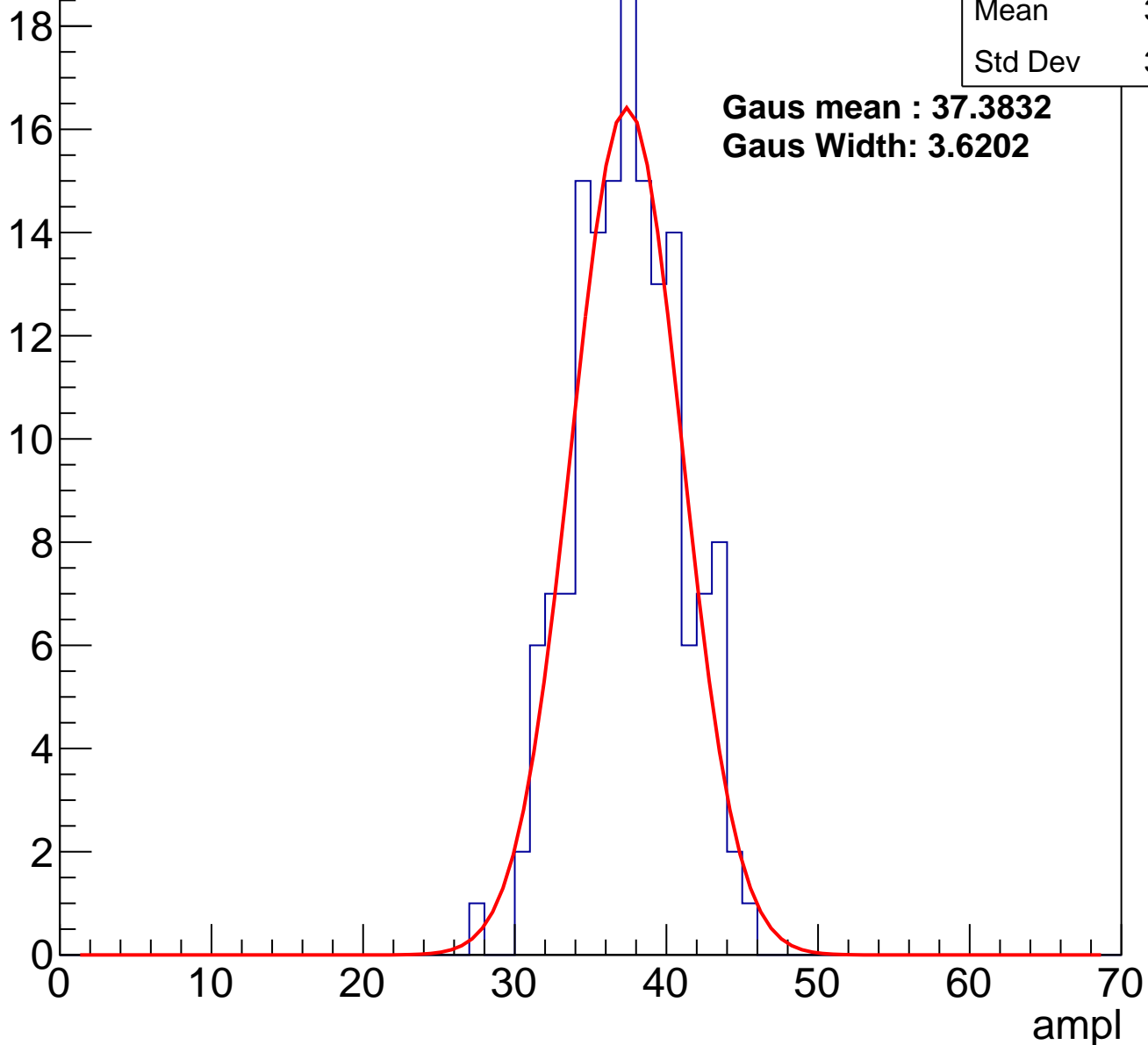
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	37.01
Std Dev	3.461

**Gaus mean : 37.3832**

**Gaus Width: 3.6202**

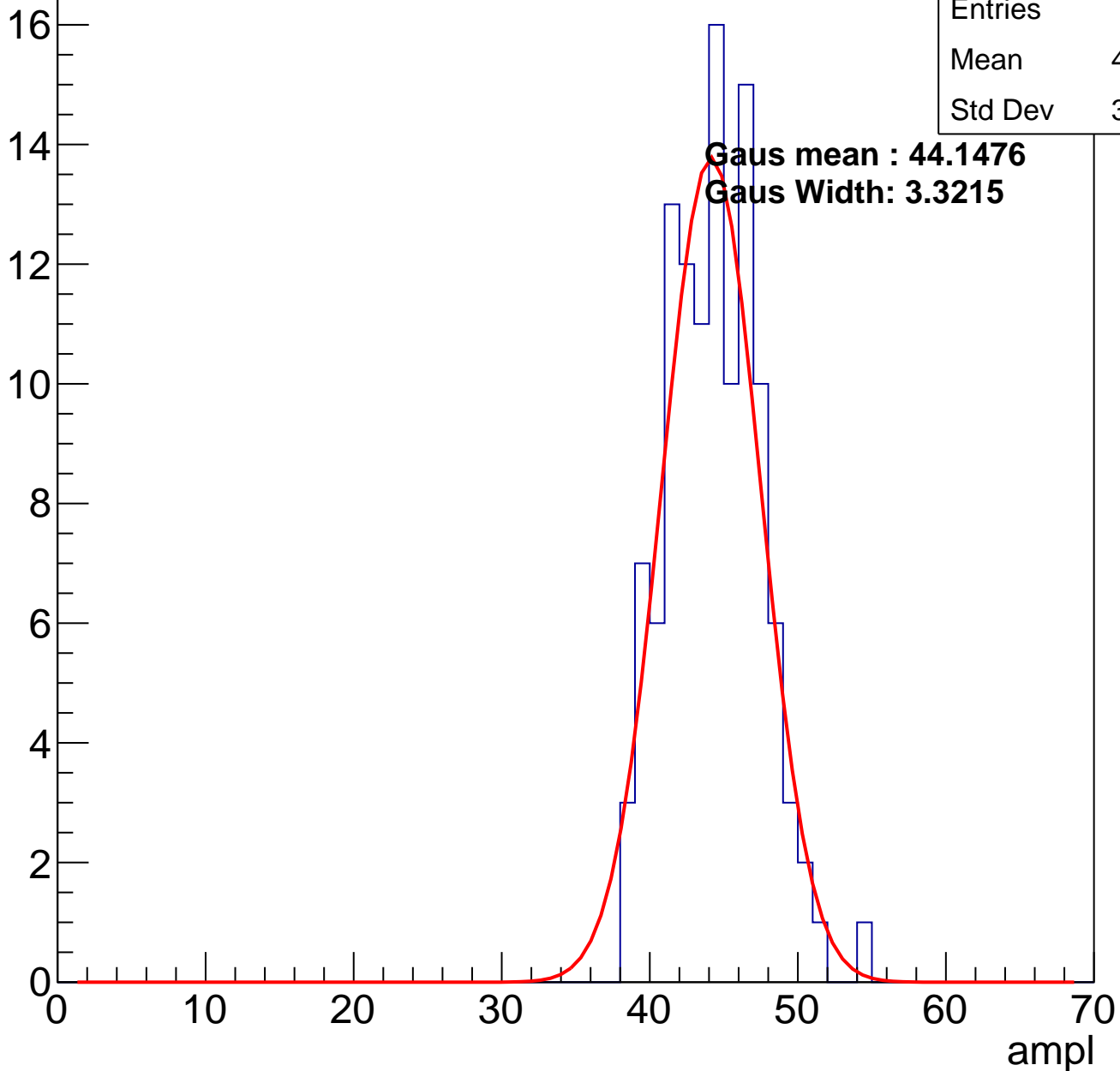
Entry



# B0L101S, U23-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

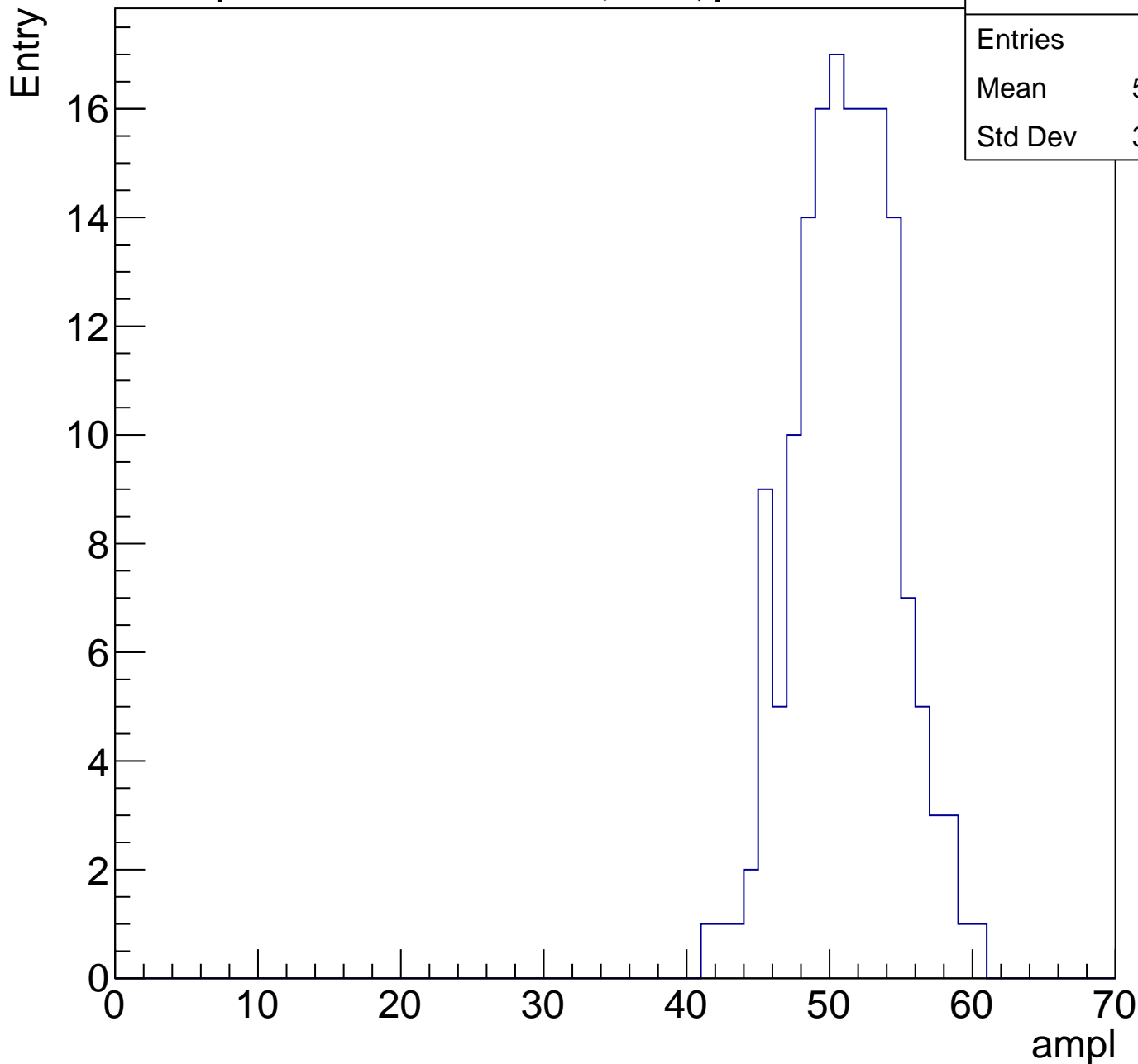
Entry



# B0L101S, U23-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	50.67
Std Dev	3.544



# B0L101S, U23-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

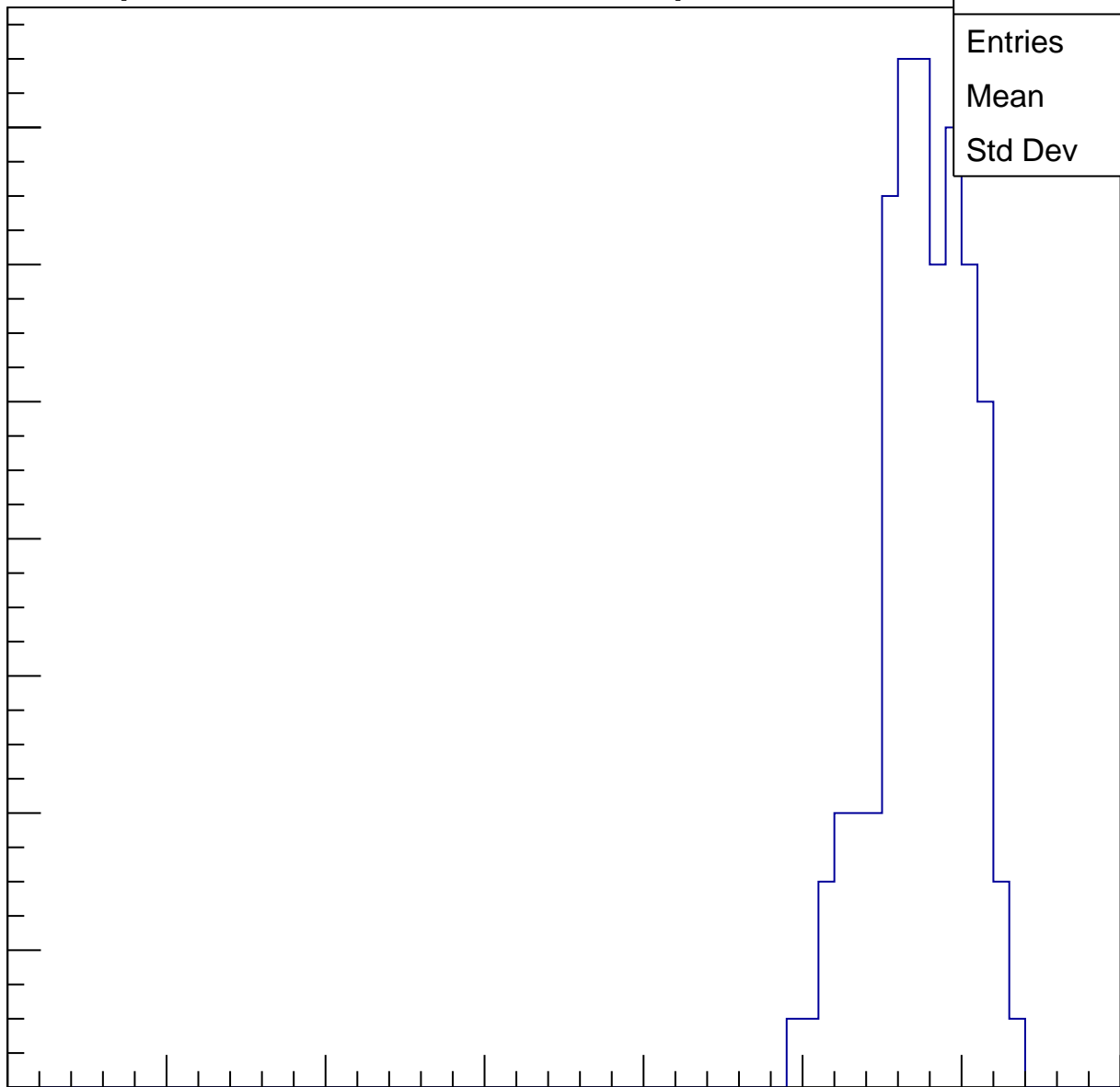
50

60

70

ampl

Entries	112
Mean	57.13
Std Dev	2.886



# B0L101S, U23-ch17, adc5

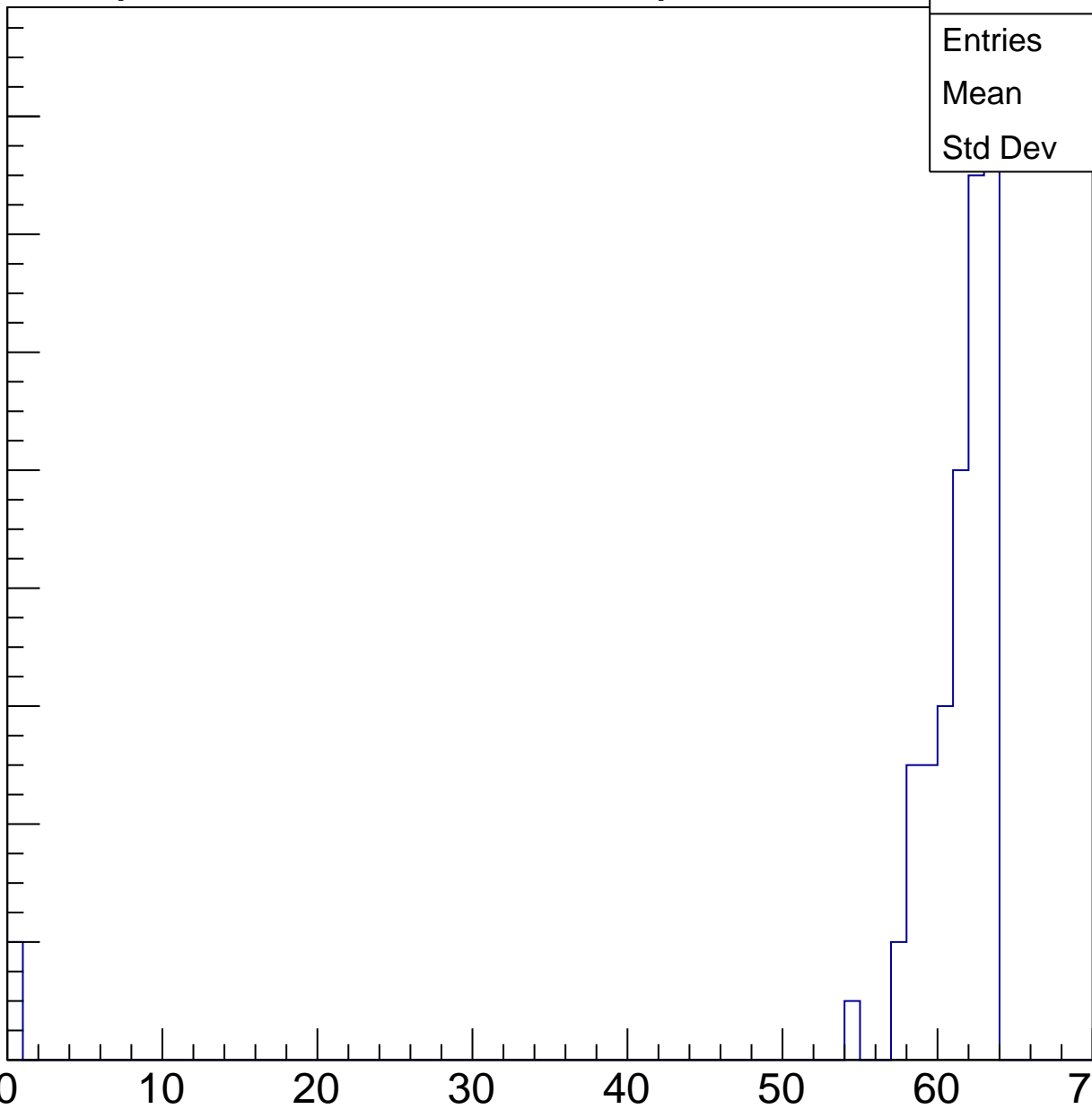
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	63
Mean	59.11
Std Dev	10.88

ampl



# B0L101S, U23-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

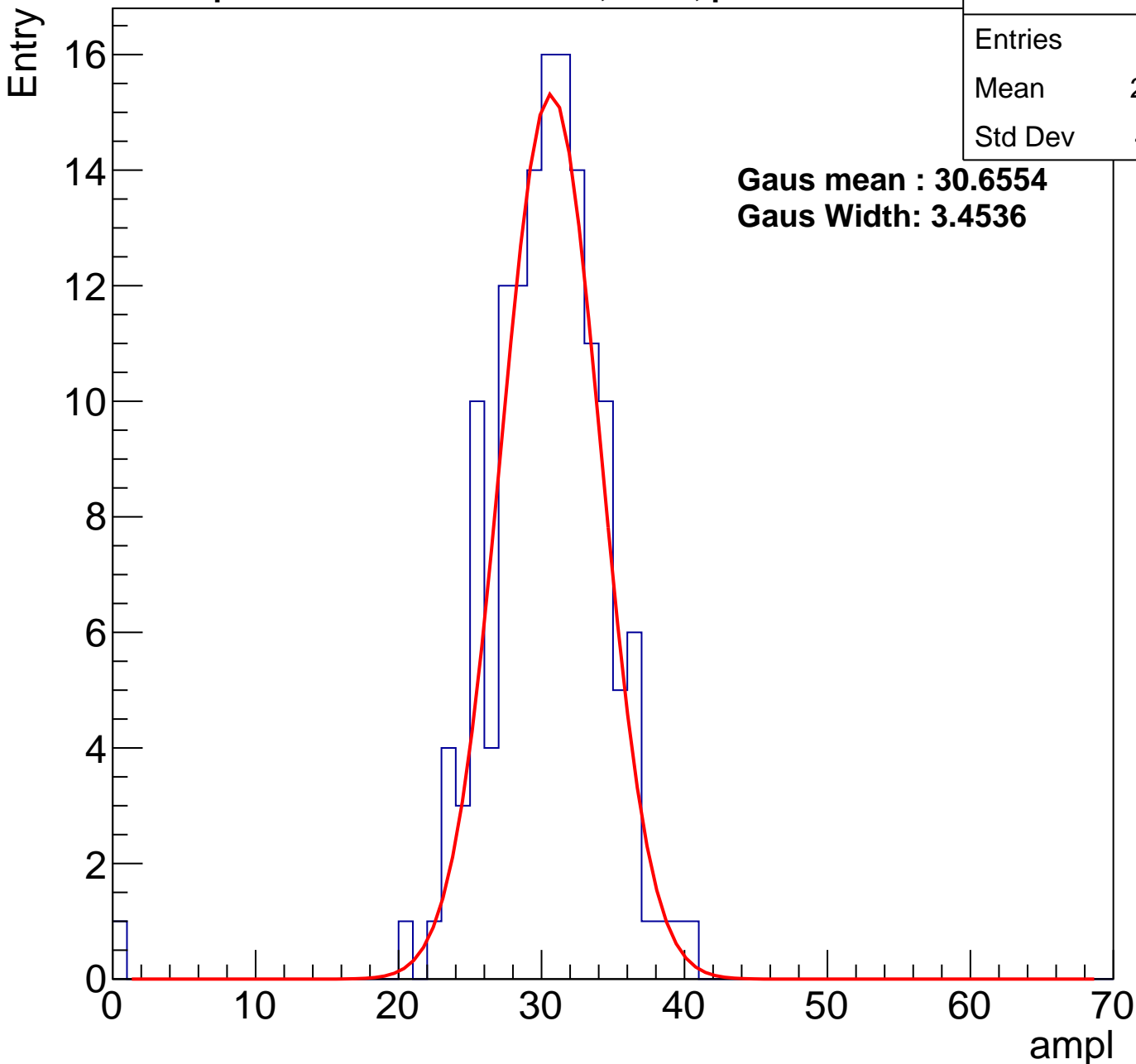
# B0L101S, U23-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	29.85
Std Dev	4.421

**Gaus mean : 30.6554**

**Gaus Width: 3.4536**

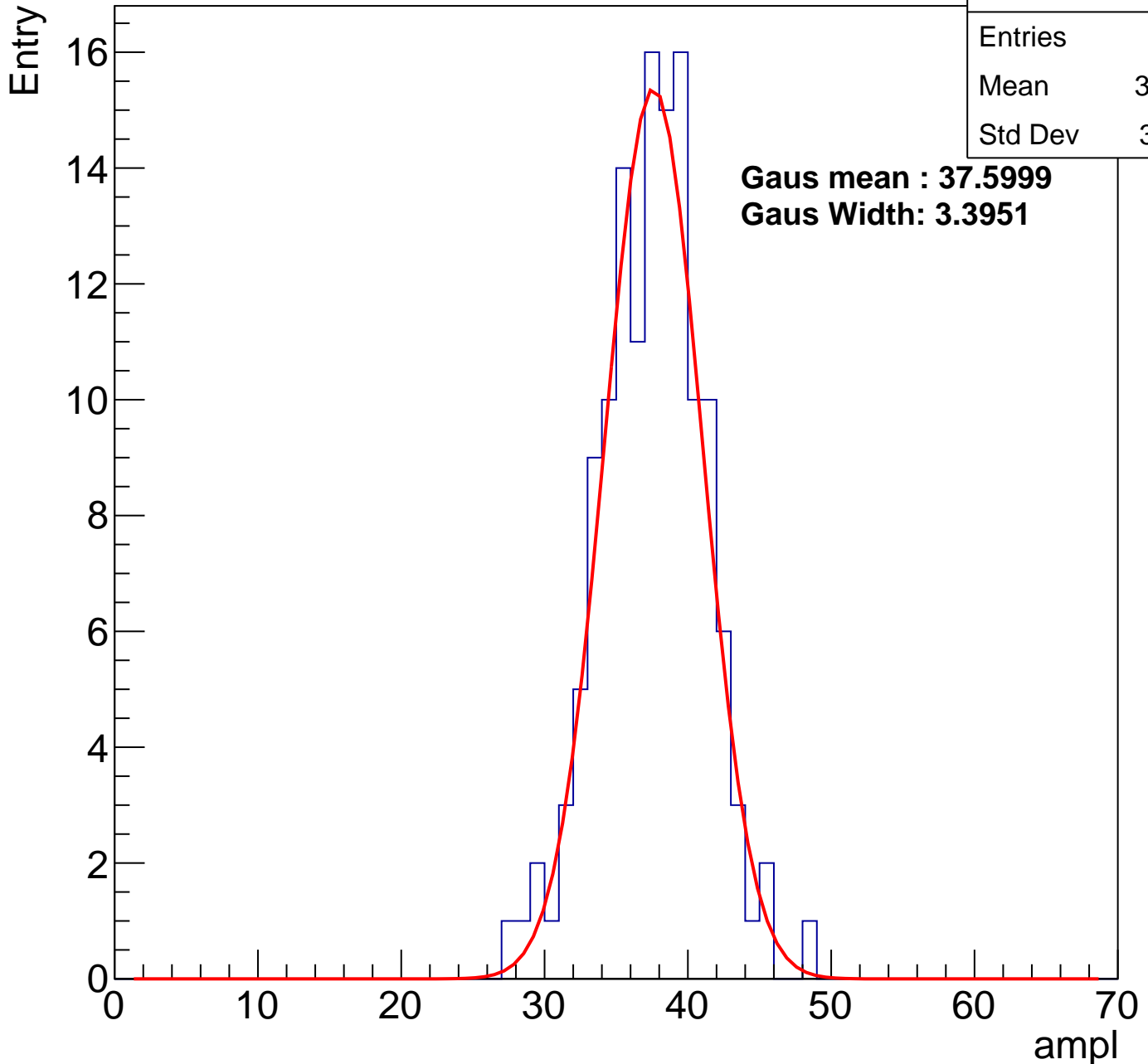


# B0L101S, U23-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	37.07
Std Dev	3.601

**Gaus mean : 37.5999**  
**Gaus Width: 3.3951**



# B0L101S, U23-ch18, adc2

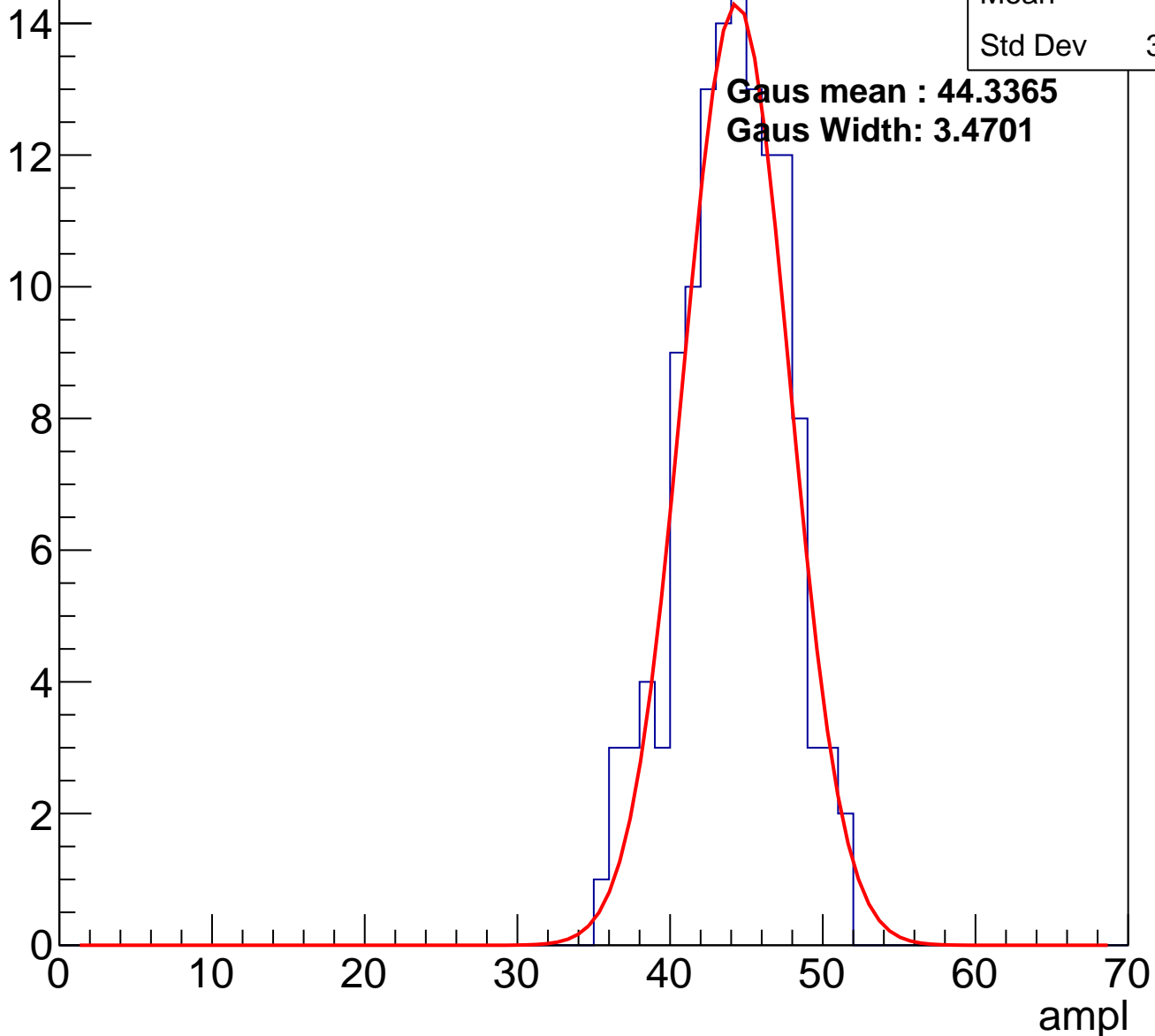
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	43.63
Std Dev	3.423

**Gaus mean : 44.3365**

**Gaus Width: 3.4701**

Entry



# B0L101S, U23-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

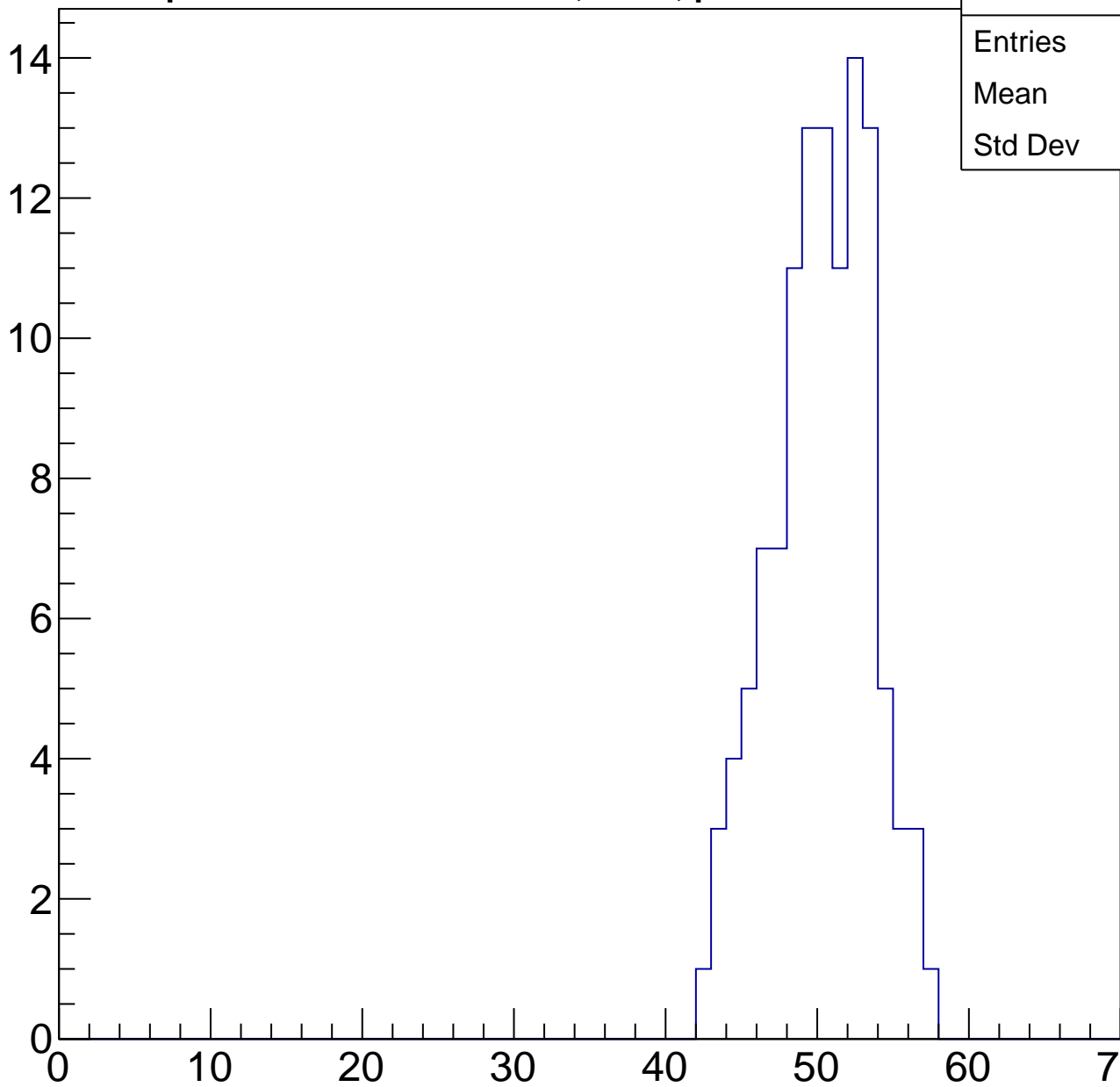
Entries	114
Mean	49.79
Std Dev	3.248

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	55.14
Std Dev	6.218

Entry

14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

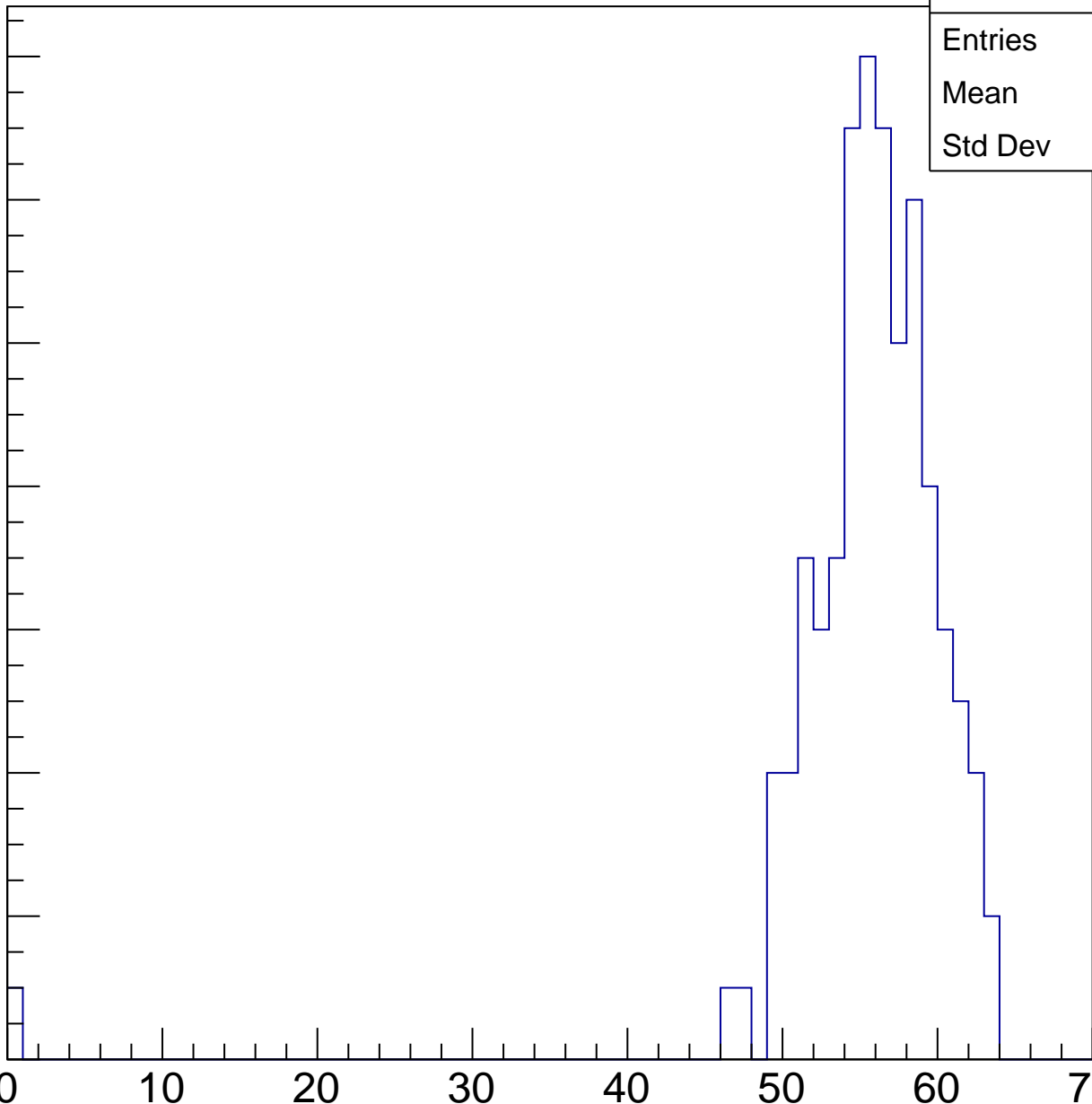
40

50

60

70

ampl



# B0L101S, U23-ch18, adc5

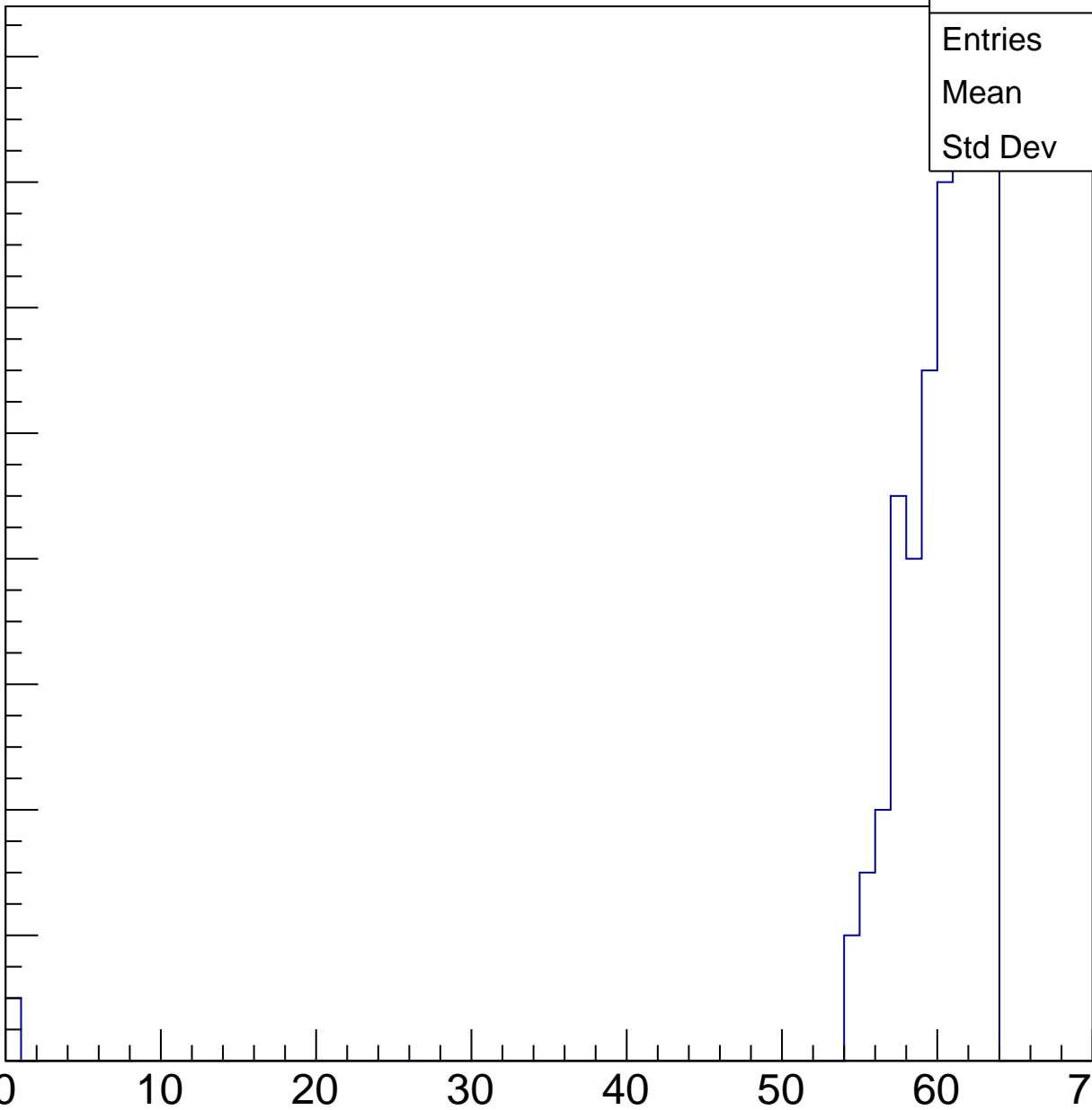
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	98
Mean	59.34
Std Dev	6.473

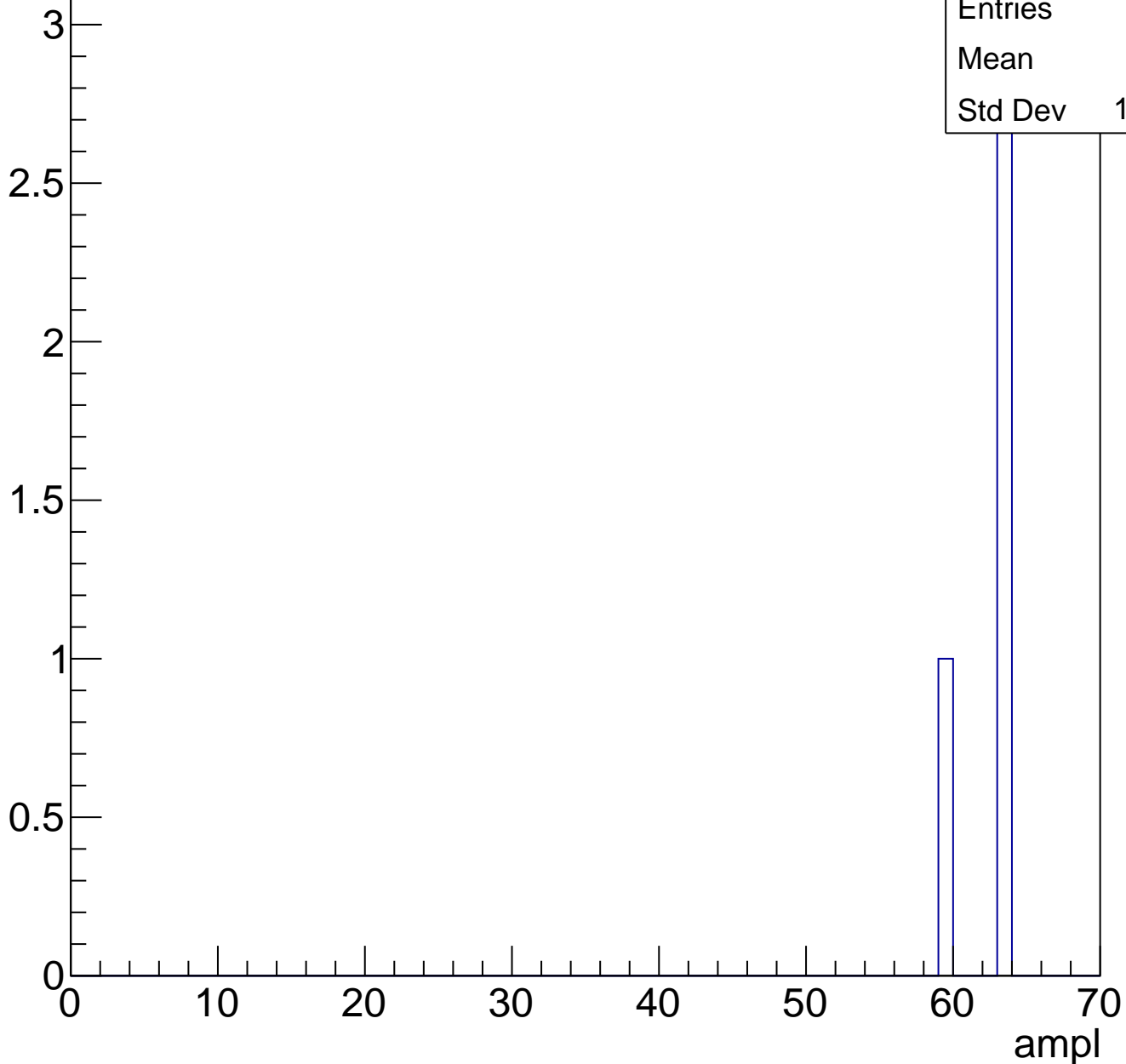
ampl



# B0L101S, U23-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L101S, U23-ch19, adc0

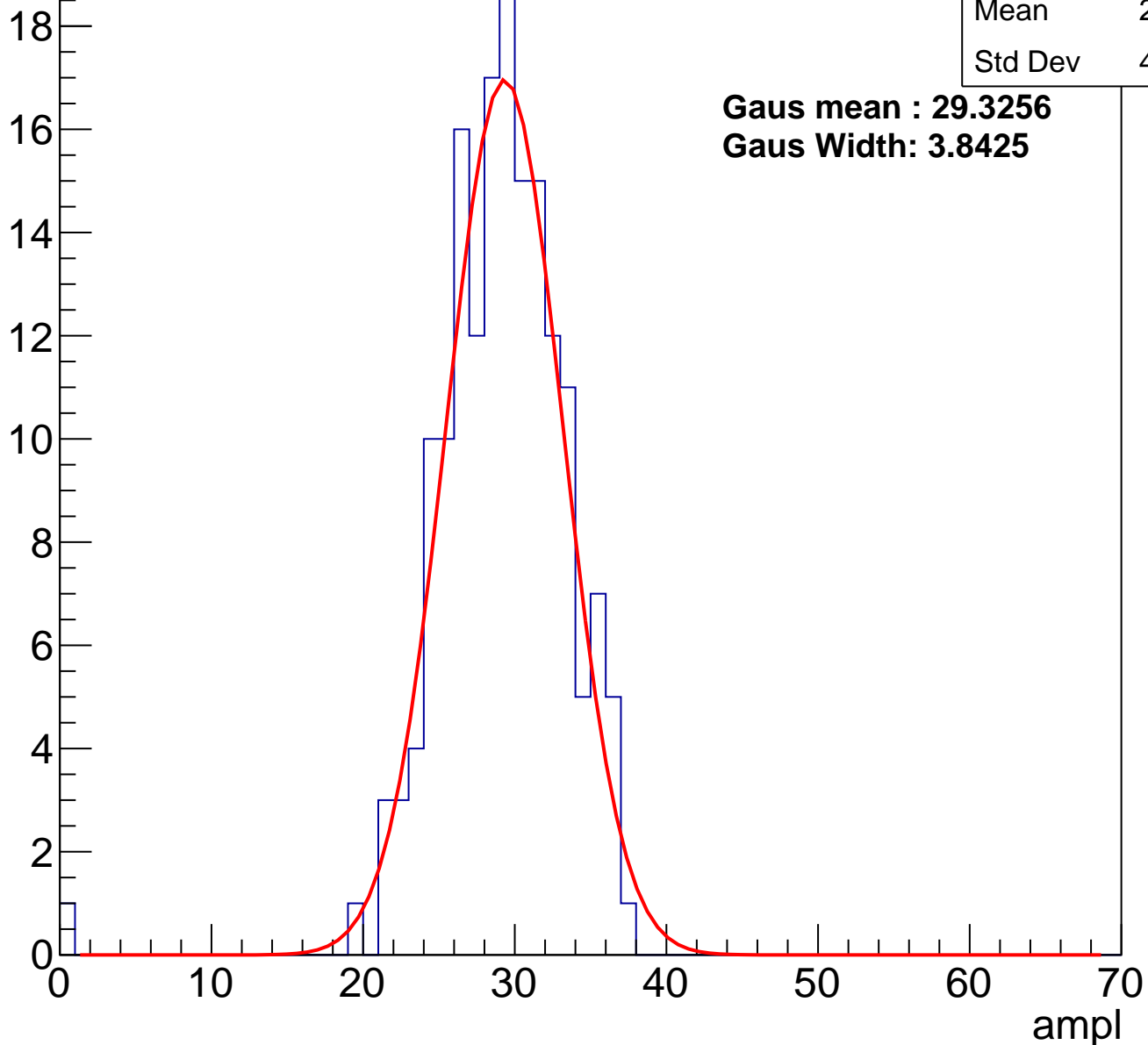
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	167
Mean	28.69
Std Dev	4.289

**Gaus mean : 29.3256**

**Gaus Width: 3.8425**

Entry



# B0L101S, U23-ch19, adc1

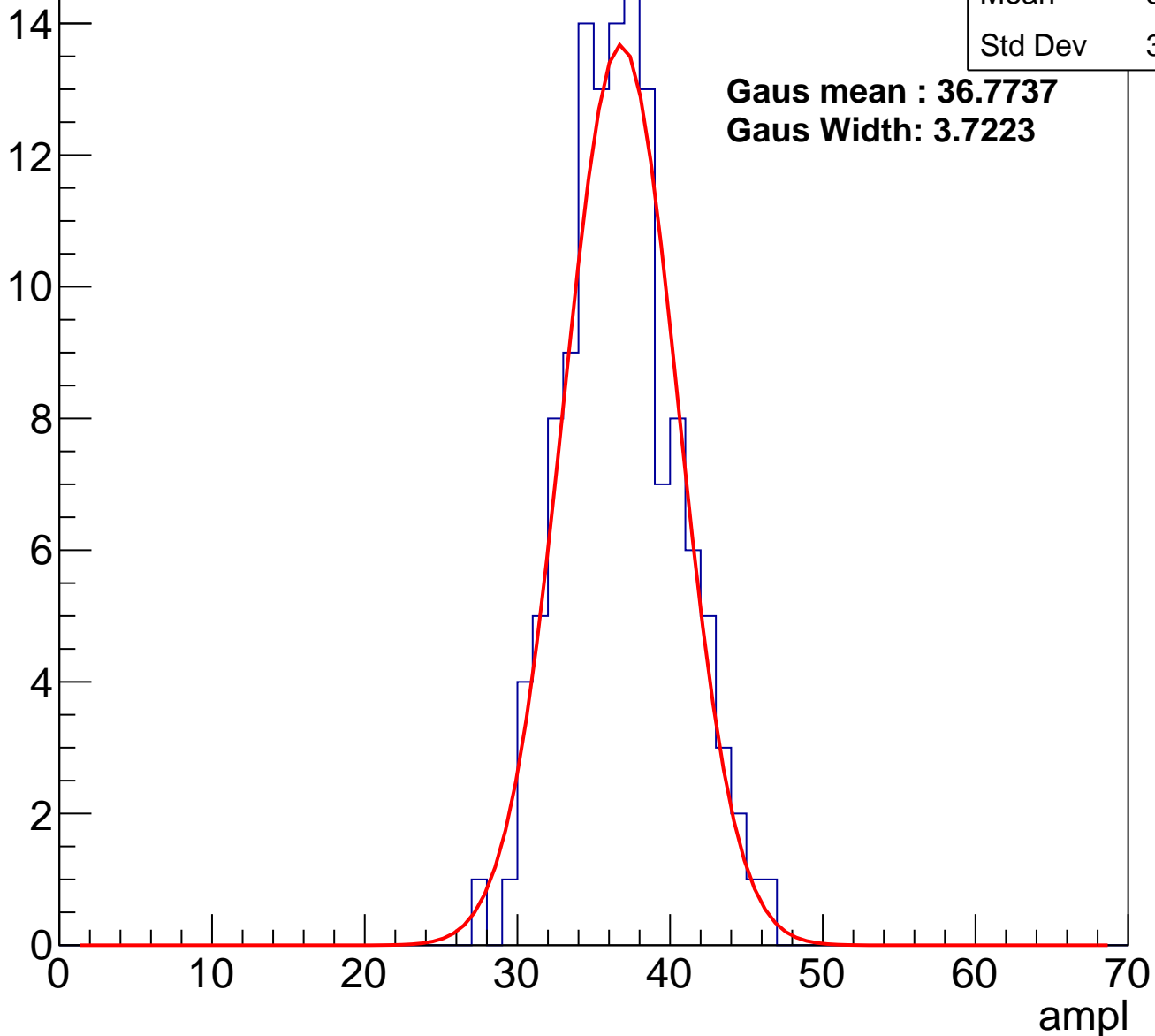
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	36.35
Std Dev	3.622

**Gaus mean : 36.7737**

**Gaus Width: 3.7223**

Entry



# B0L101S, U23-ch19, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

156

Mean

43.22

Std Dev

3.443

**Gaus mean : 43.7371**

**Gaus Width: 3.5148**

0

10

20

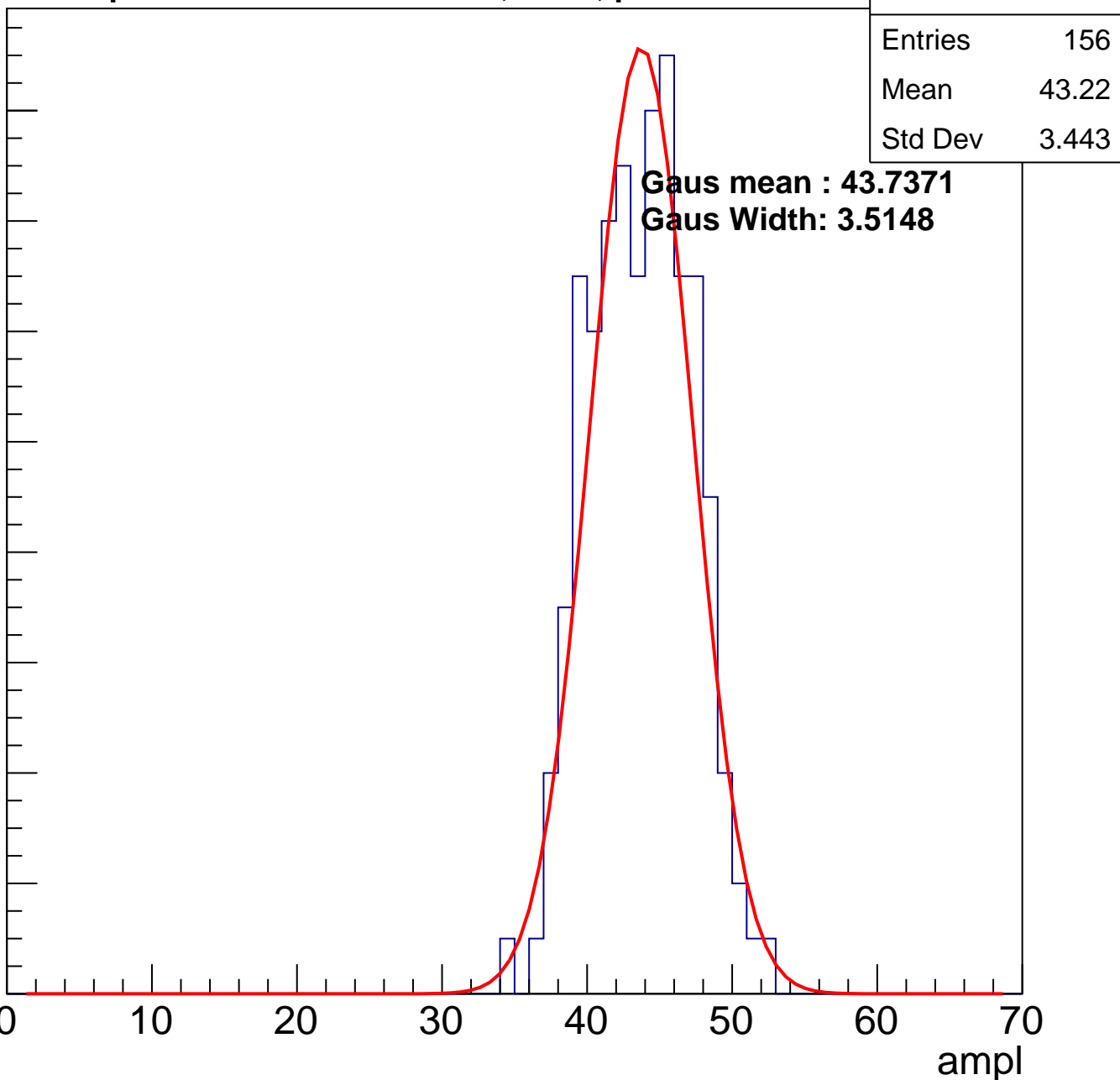
30

40

50

60

ampl

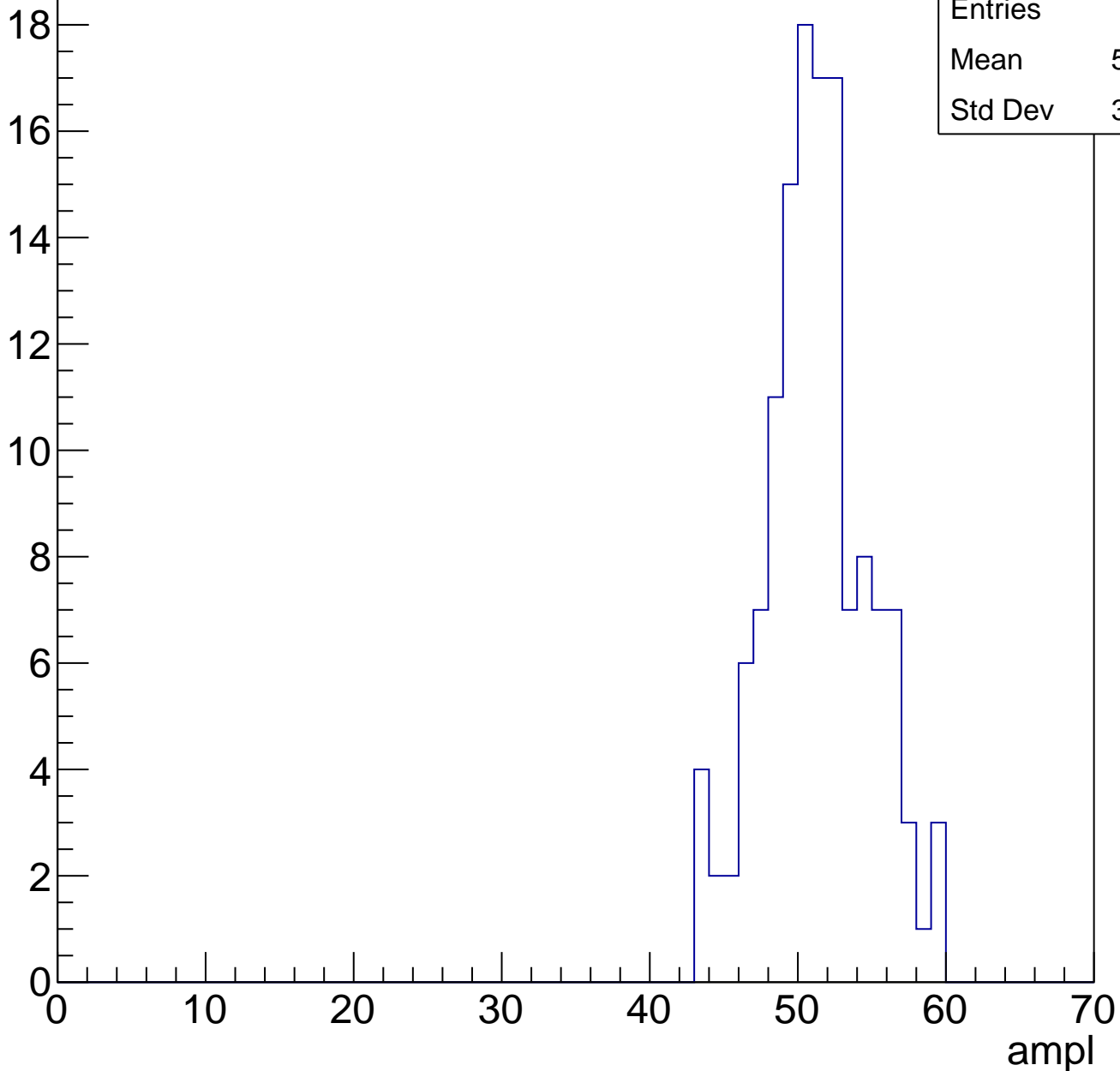


# B0L101S, U23-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	50.78
Std Dev	3.455

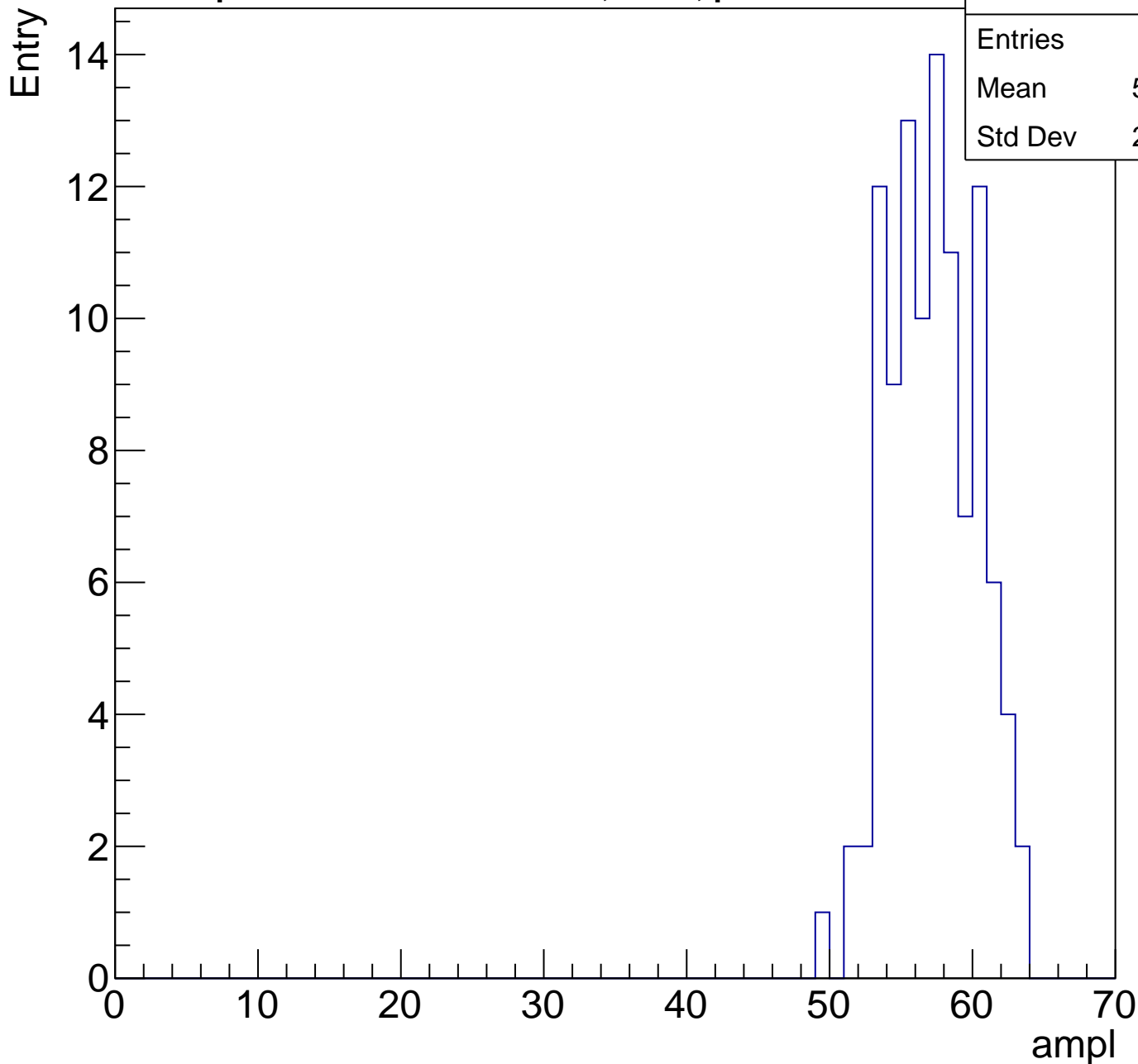
Entry



# B0L101S, U23-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	56.77
Std Dev	2.977



# B0L101S, U23-ch19, adc5

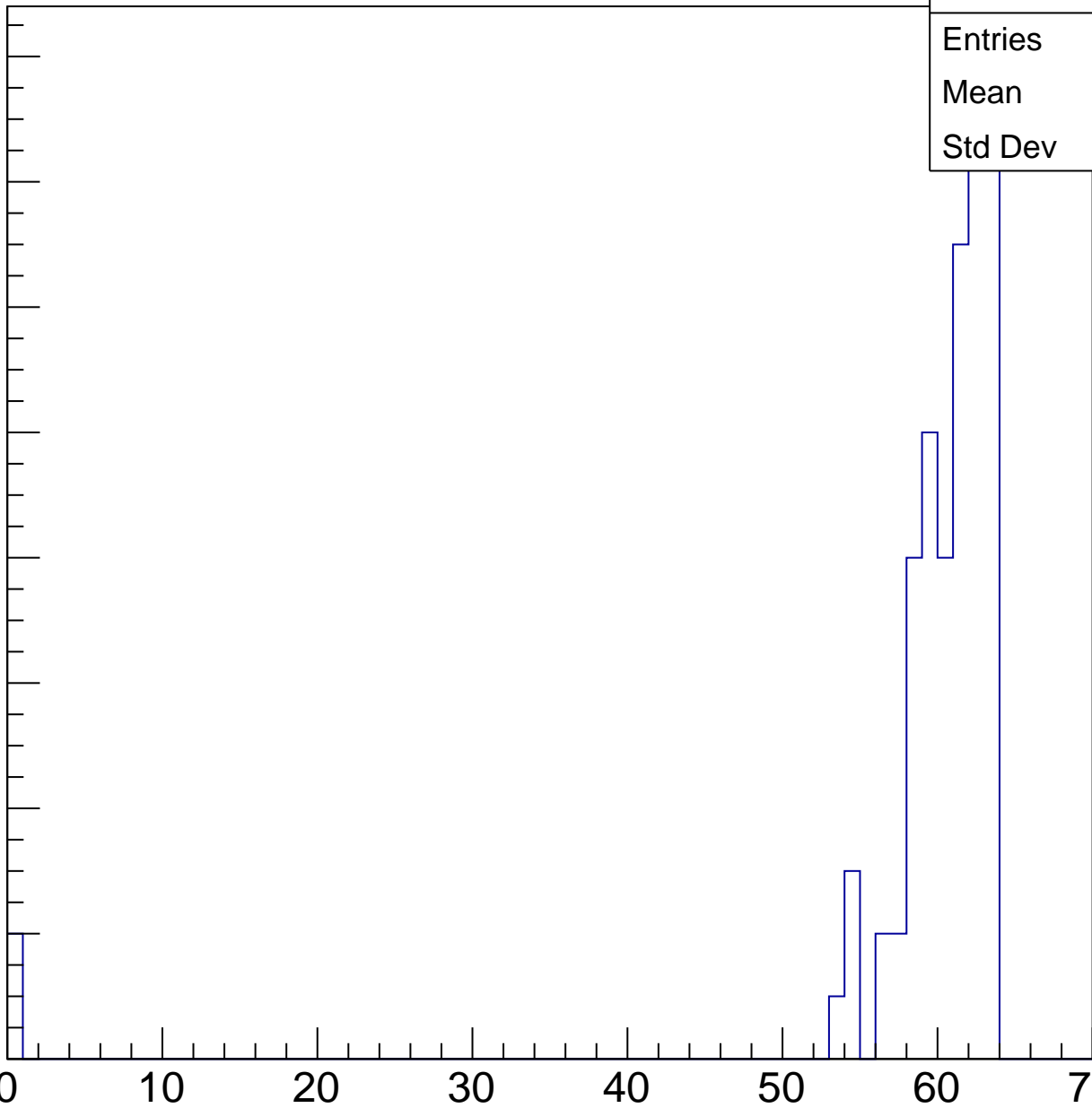
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	58.86
Std Dev	9.664

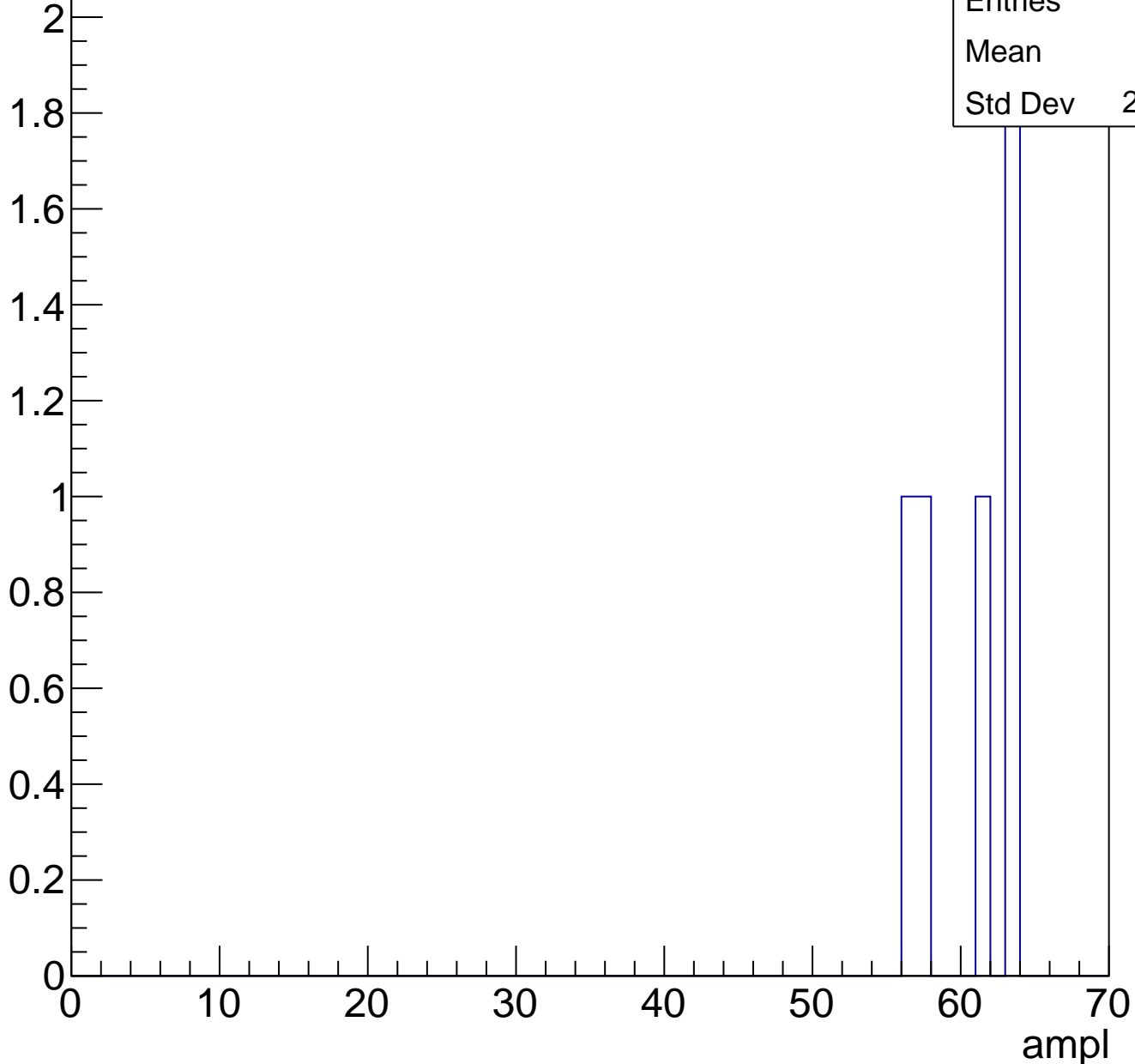
ampl



# B0L101S, U23-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	30.23
Std Dev	3.237

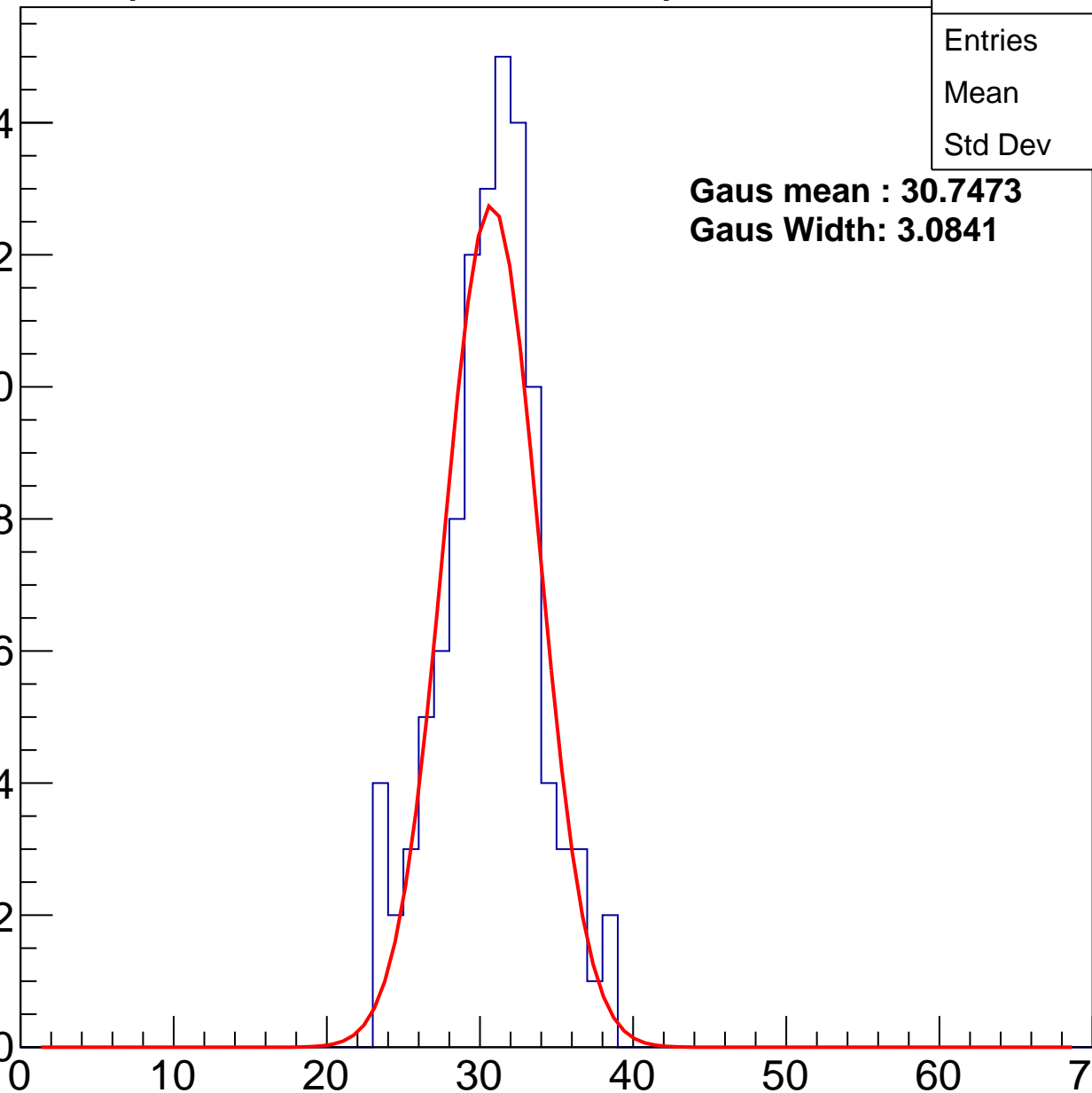
**Gaus mean : 30.7473**

**Gaus Width: 3.0841**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

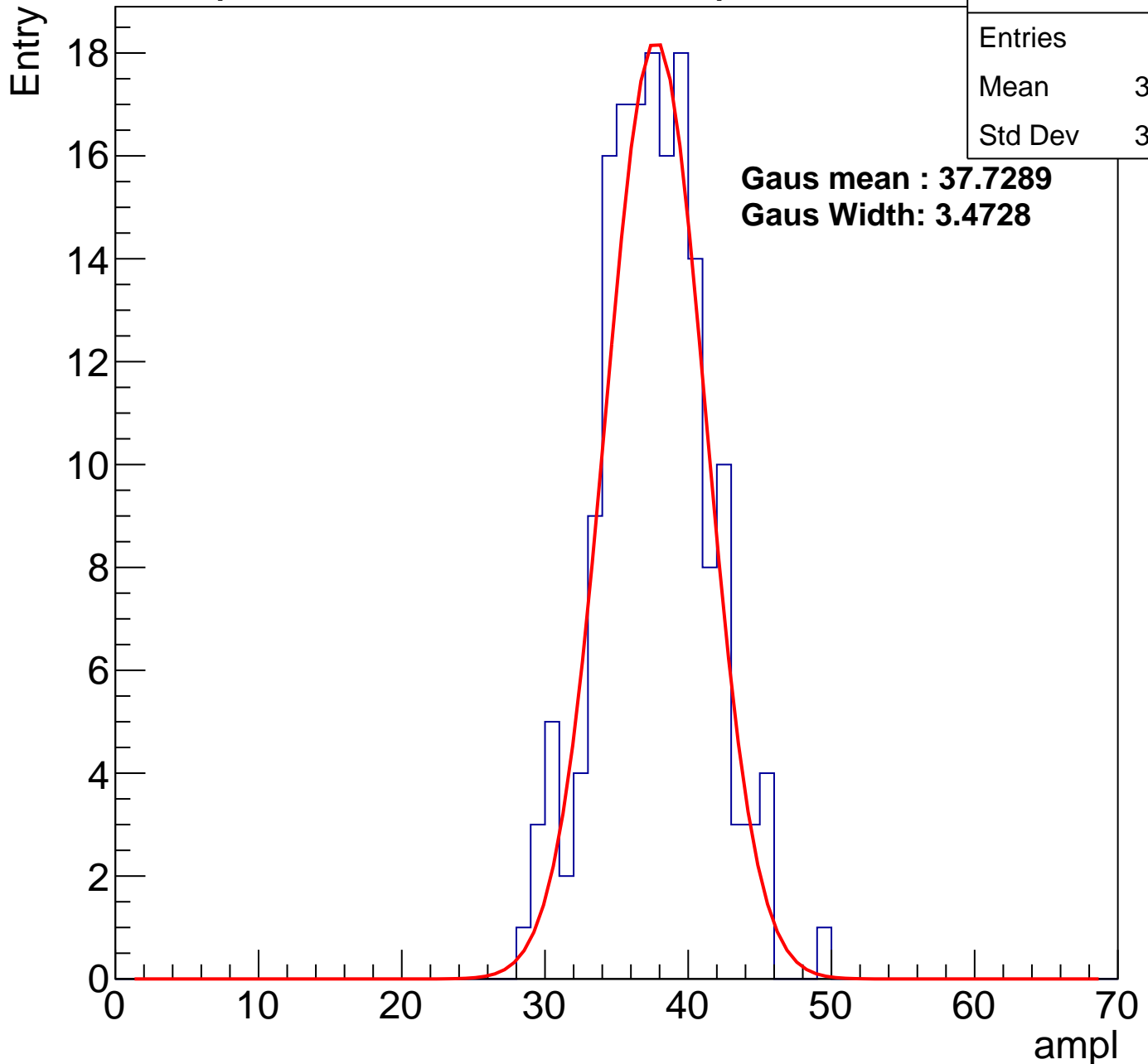


# B0L101S, U23-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	37.14
Std Dev	3.694

**Gaus mean : 37.7289**  
**Gaus Width: 3.4728**



# B0L101S, U23-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

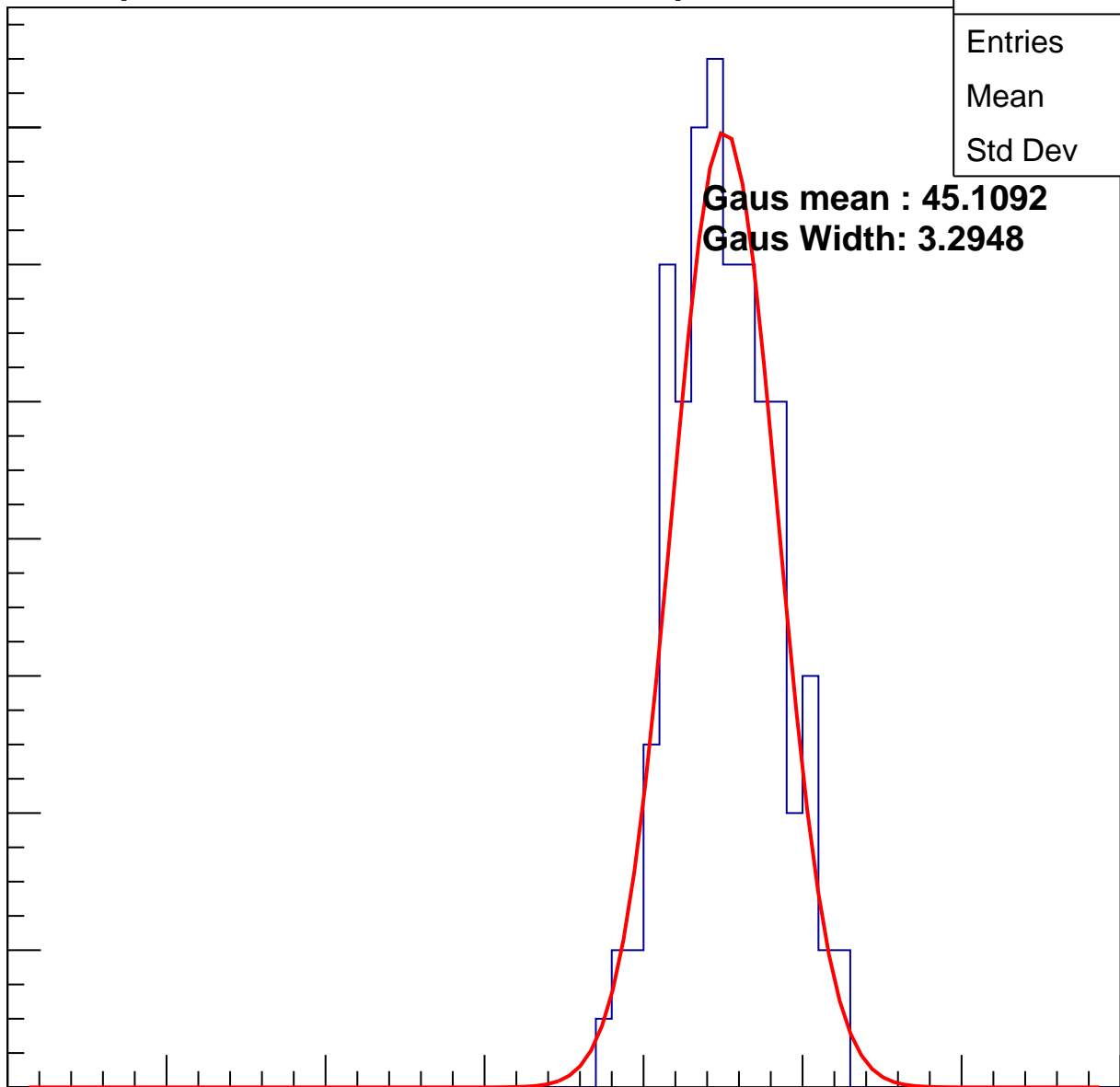
Entries	119
Mean	44.61
Std Dev	3.202

**Gaus mean : 45.1092**

**Gaus Width: 3.2948**

0 10 20 30 40 50 60 70

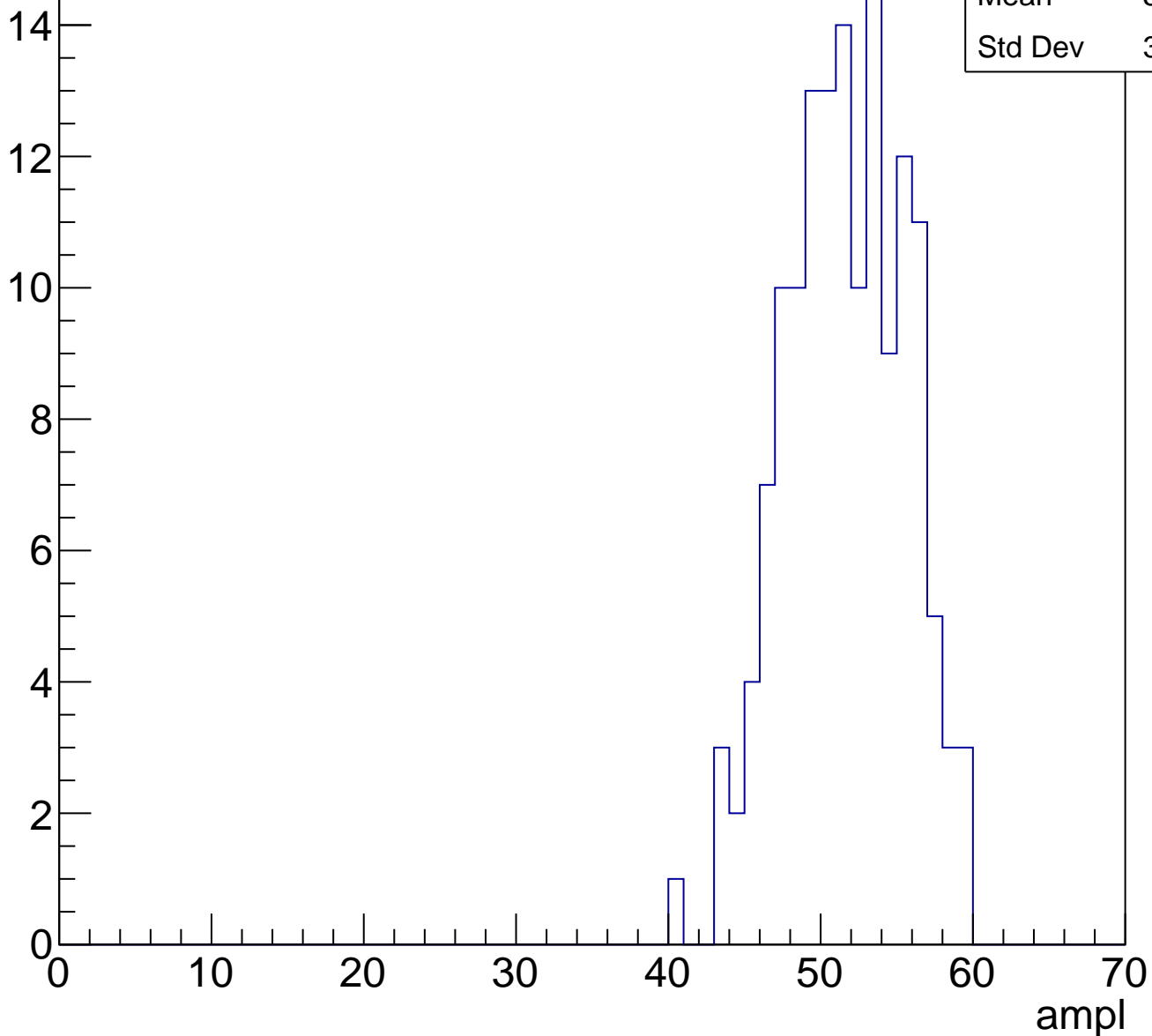
ampl



# B0L101S, U23-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	145
Mean	51.19
Std Dev	3.874

# B0L101S, U23-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

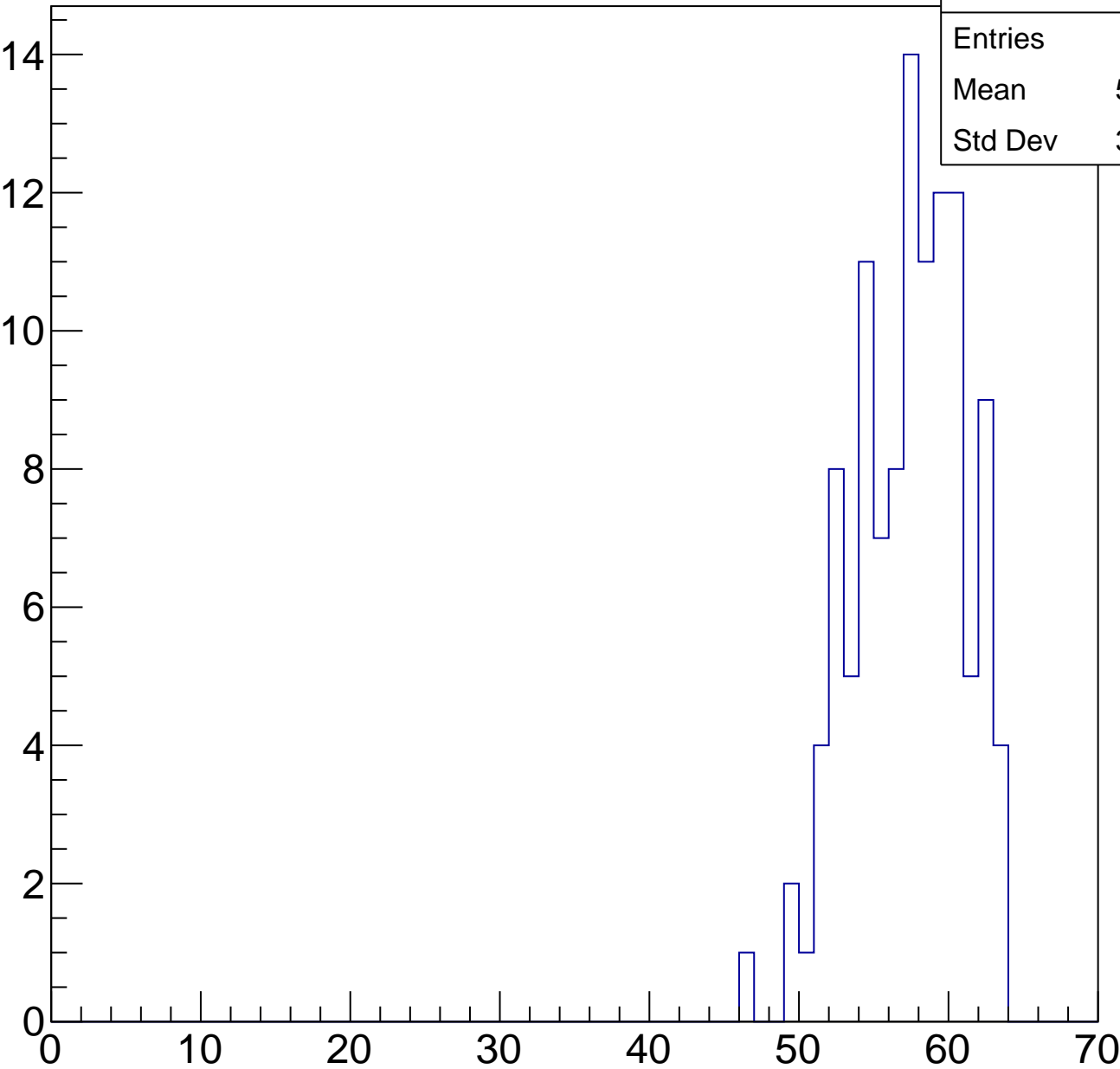
Entries	114
Mean	56.89
Std Dev	3.593

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch20, adc5

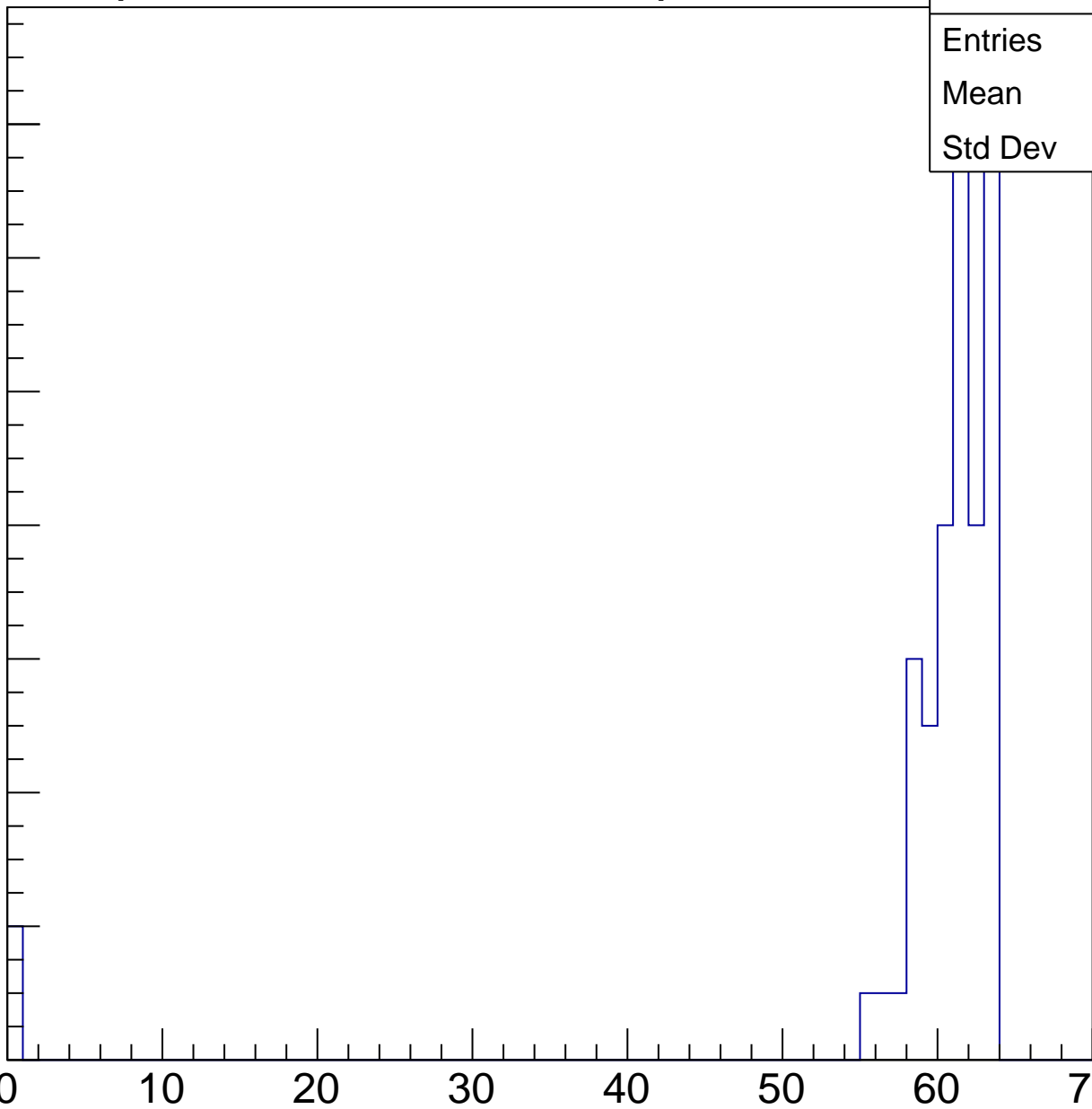
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	61
Mean	58.75
Std Dev	10.98

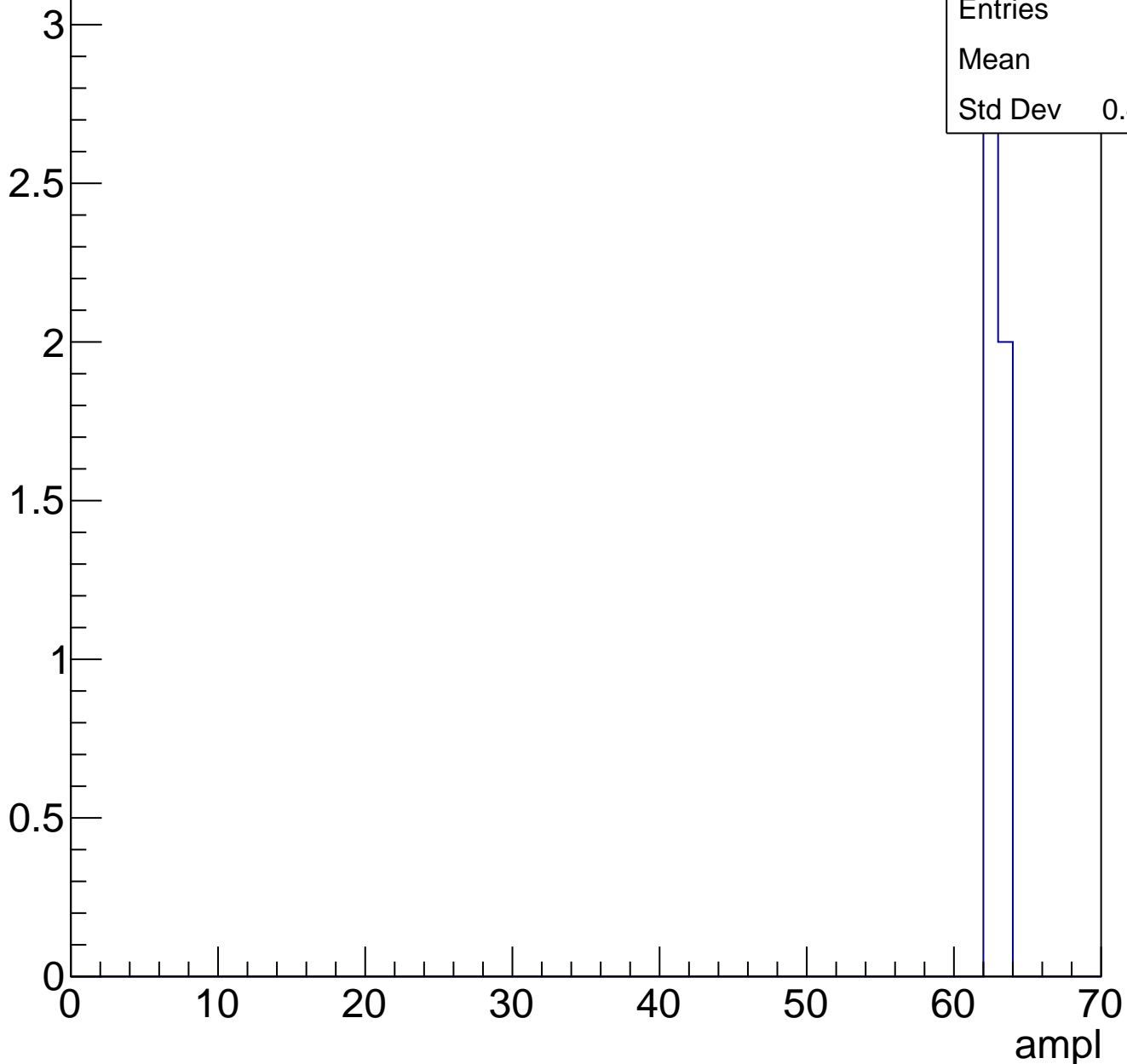
ampl



# B0L101S, U23-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

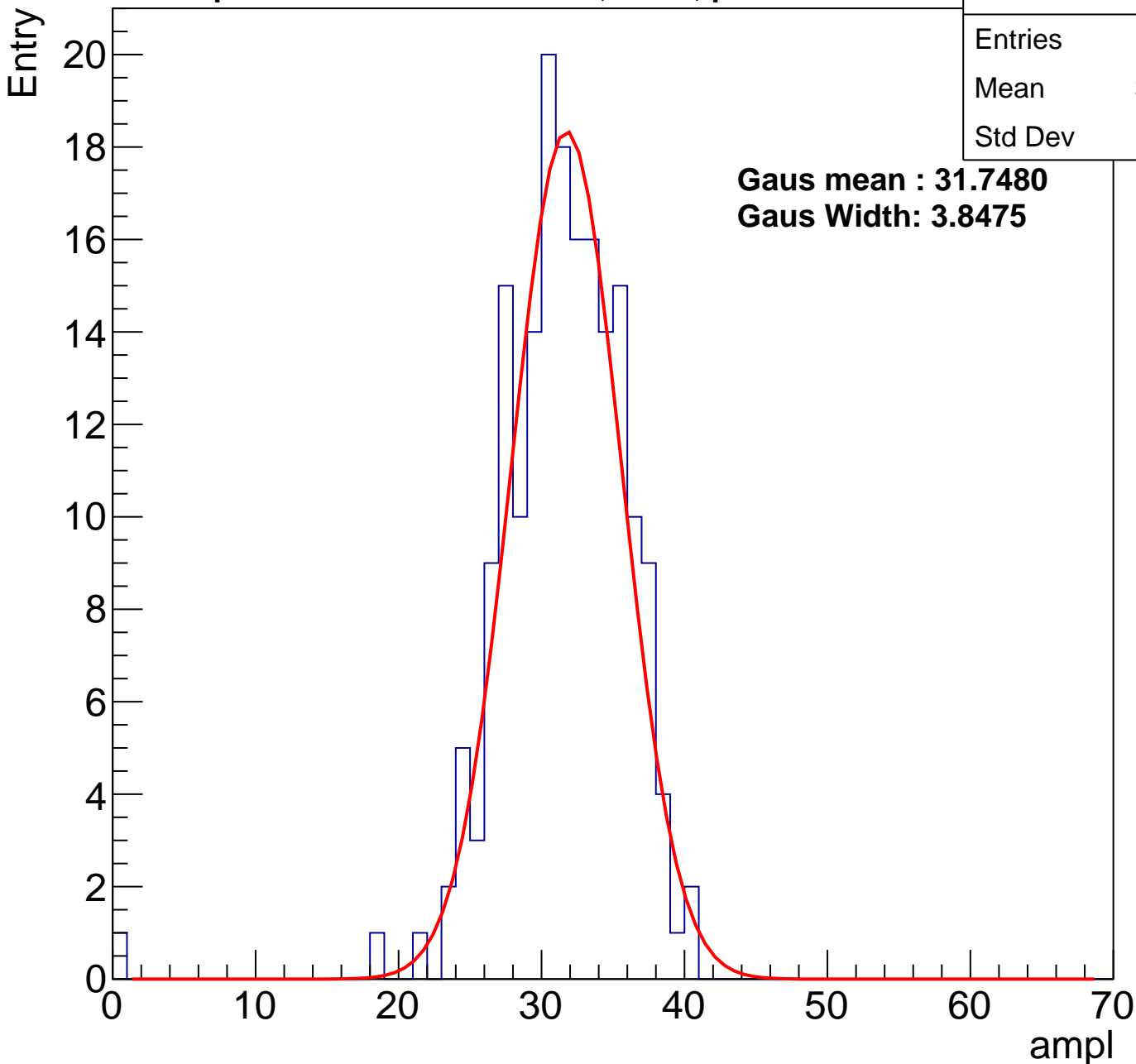
# B0L101S, U23-ch21, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	186
Mean	31.01
Std Dev	4.51

**Gaus mean : 31.7480**

**Gaus Width: 3.8475**



# B0L101S, U23-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	39.22
Std Dev	3.506

**Gaus mean : 39.8230**

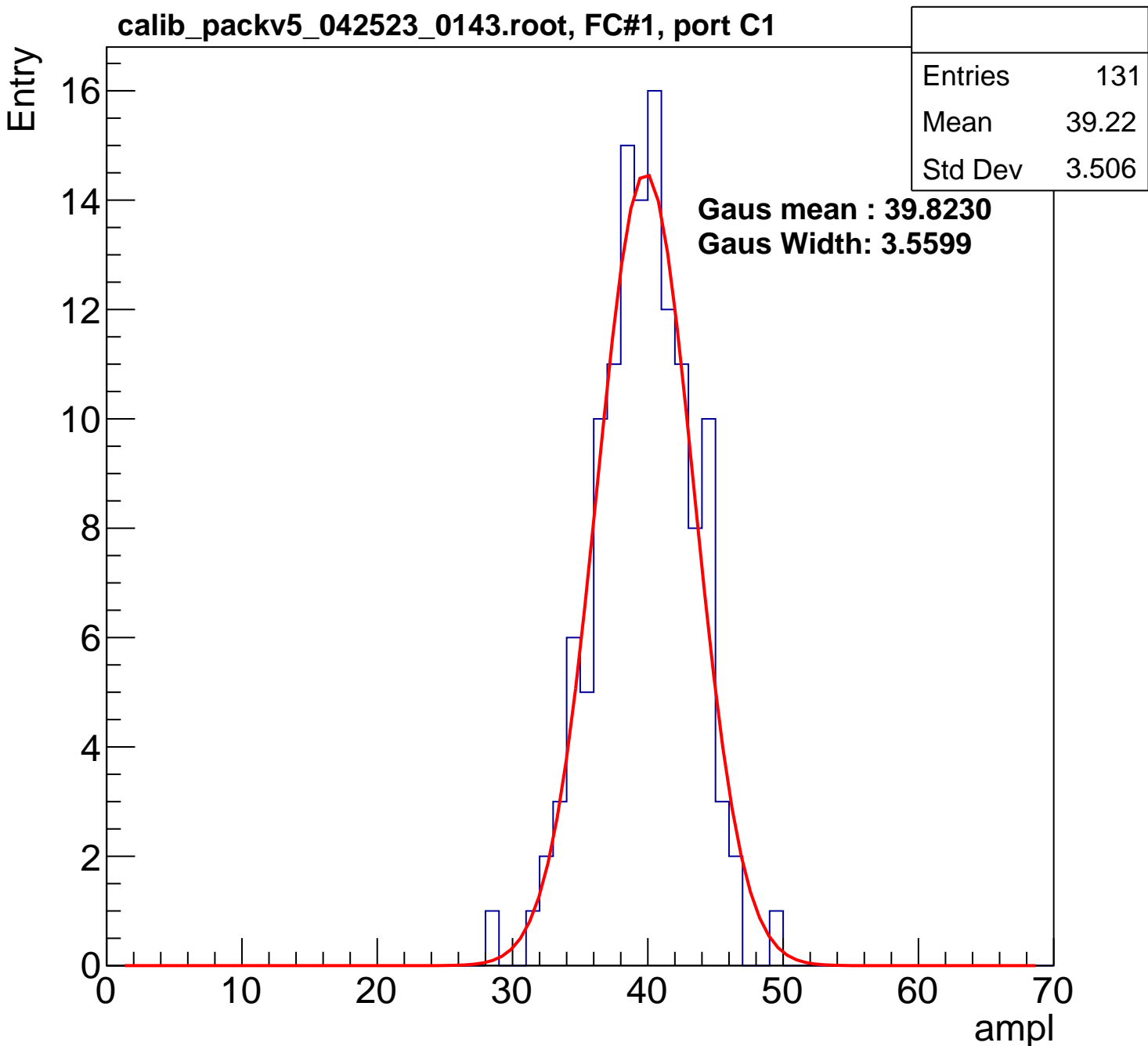
**Gaus Width: 3.5599**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

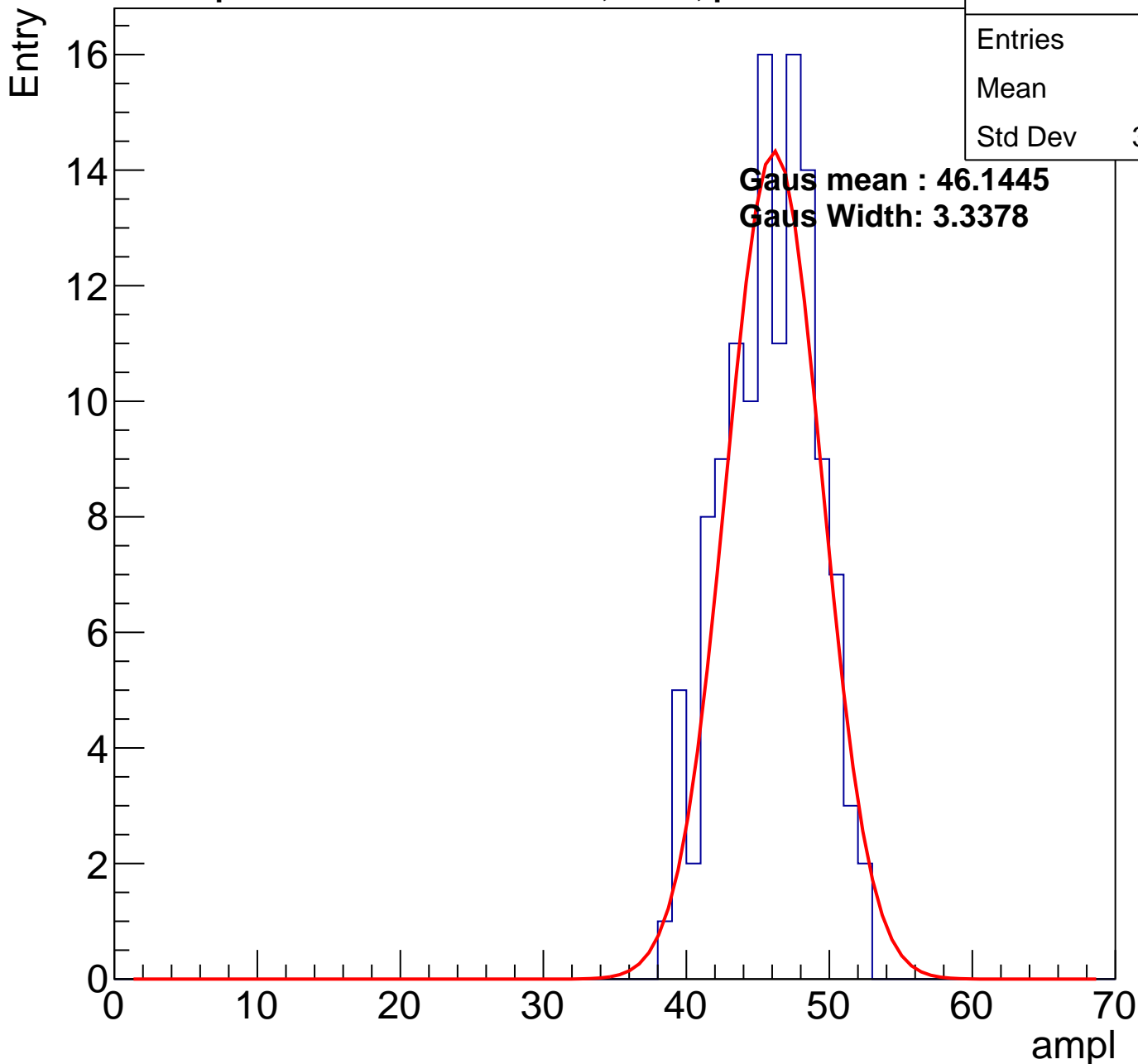
0 10 20 30 40 50 60 70



# B0L101S, U23-ch21, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

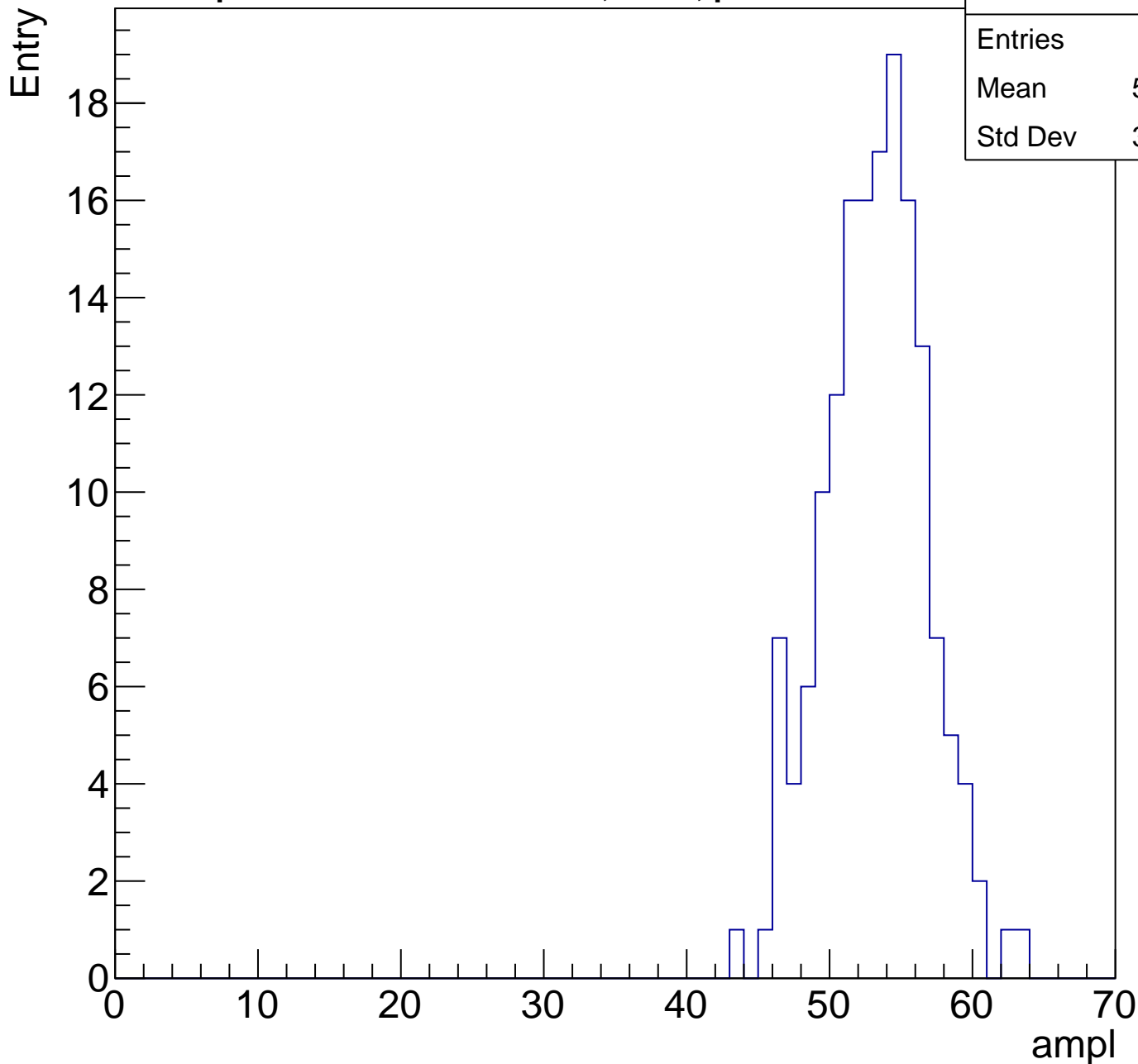
Entries	124
Mean	45.4
Std Dev	3.172



# B0L101S, U23-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	52.72
Std Dev	3.554



# B0L101S, U23-ch21, adc4

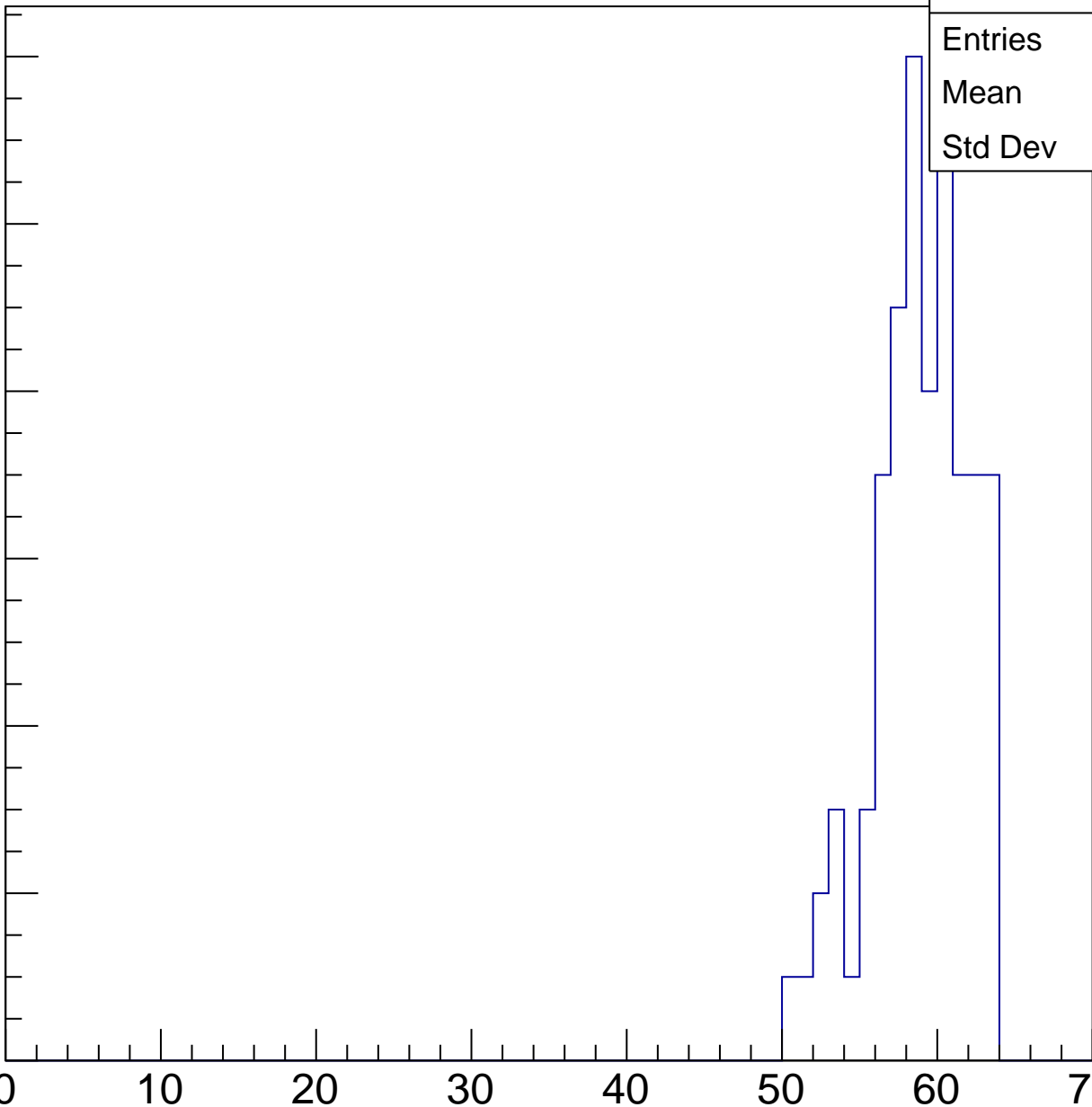
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	80
Mean	58.48
Std Dev	3.017

ampl



# B0L101S, U23-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

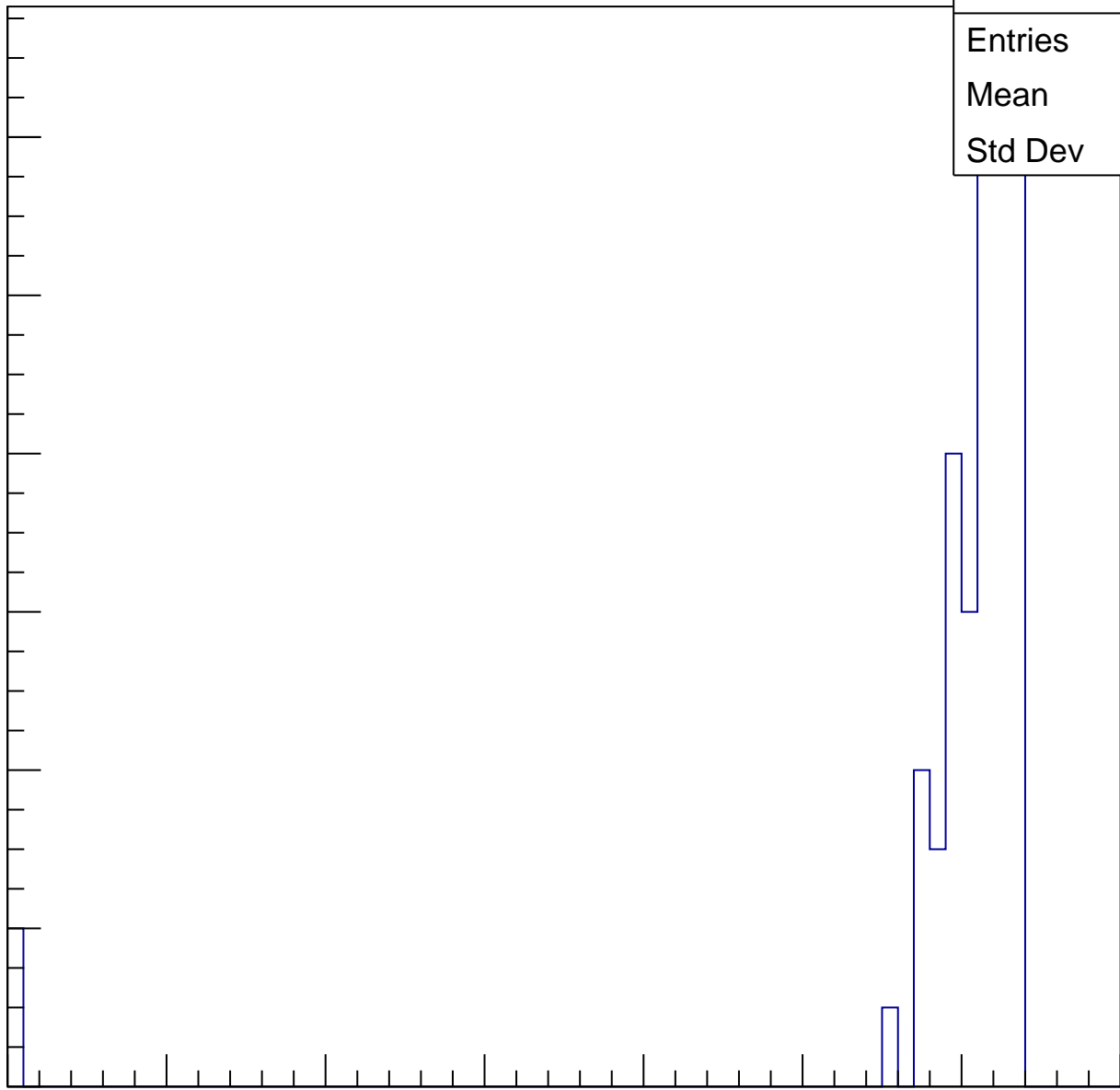
2

0

Entries	61
Mean	58.72
Std Dev	10.98

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch22, adc0

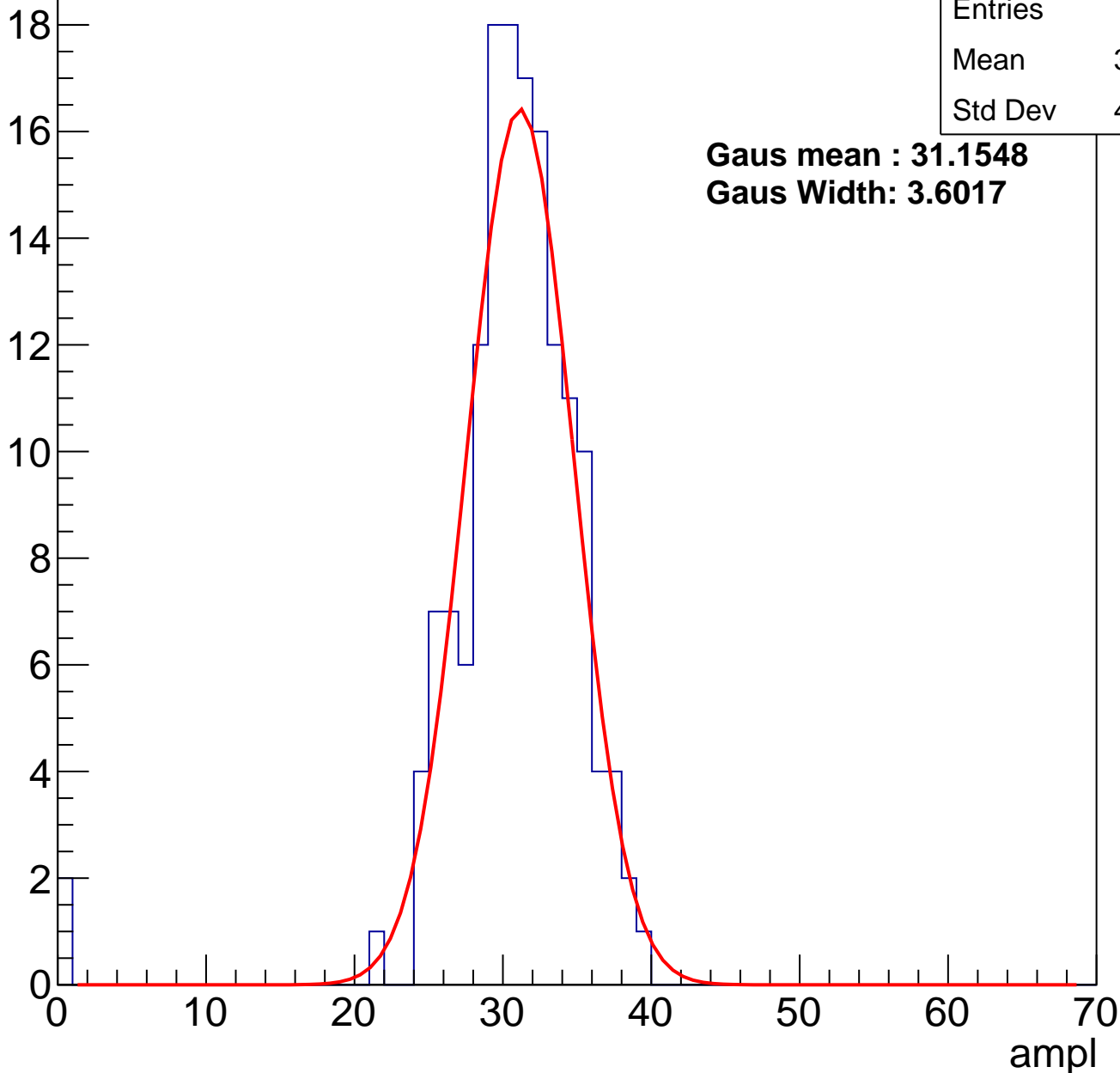
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	30.26
Std Dev	4.857

**Gaus mean : 31.1548**

**Gaus Width: 3.6017**

Entry



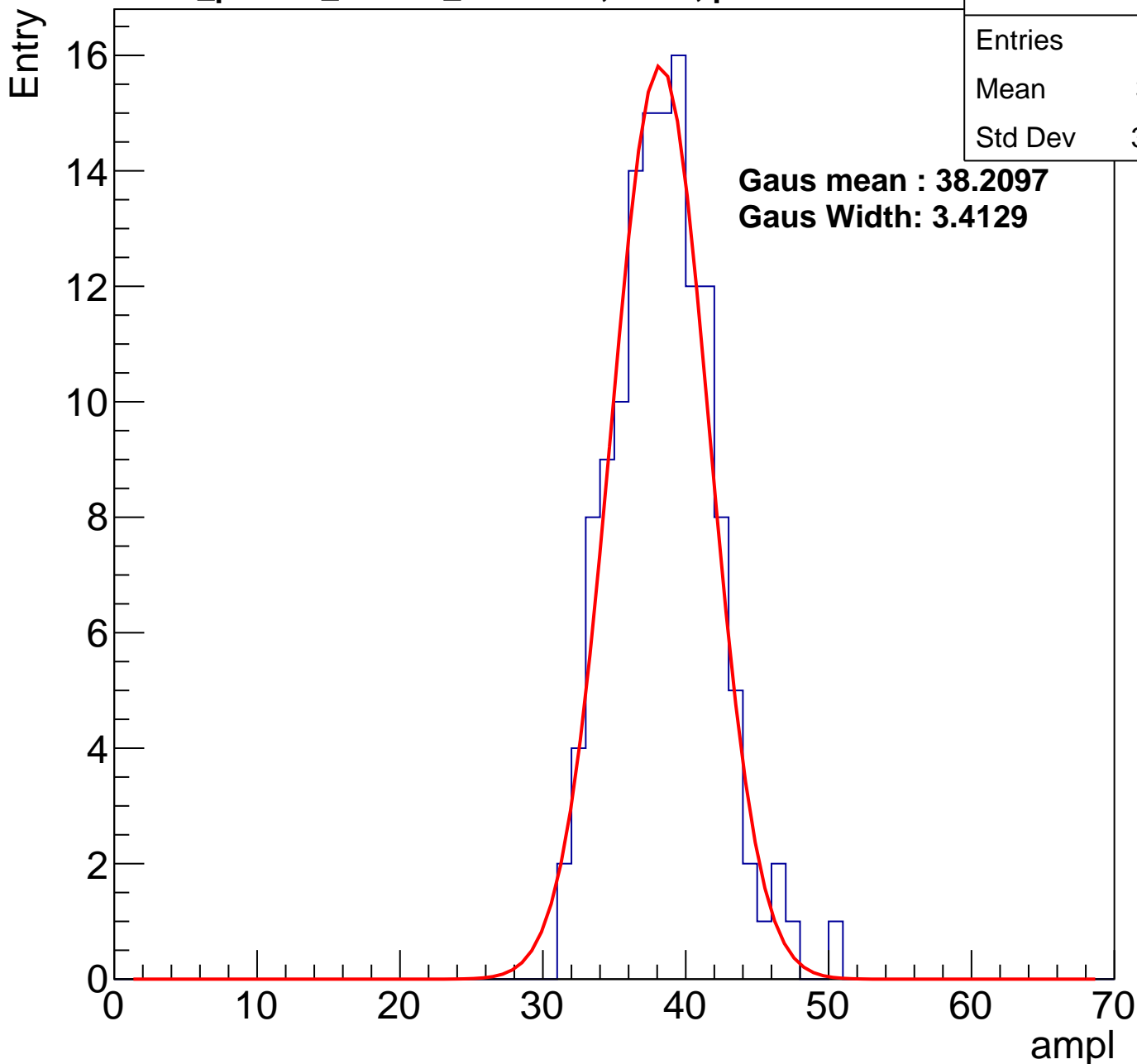
# B0L101S, U23-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	38.01
Std Dev	3.466

**Gaus mean : 38.2097**

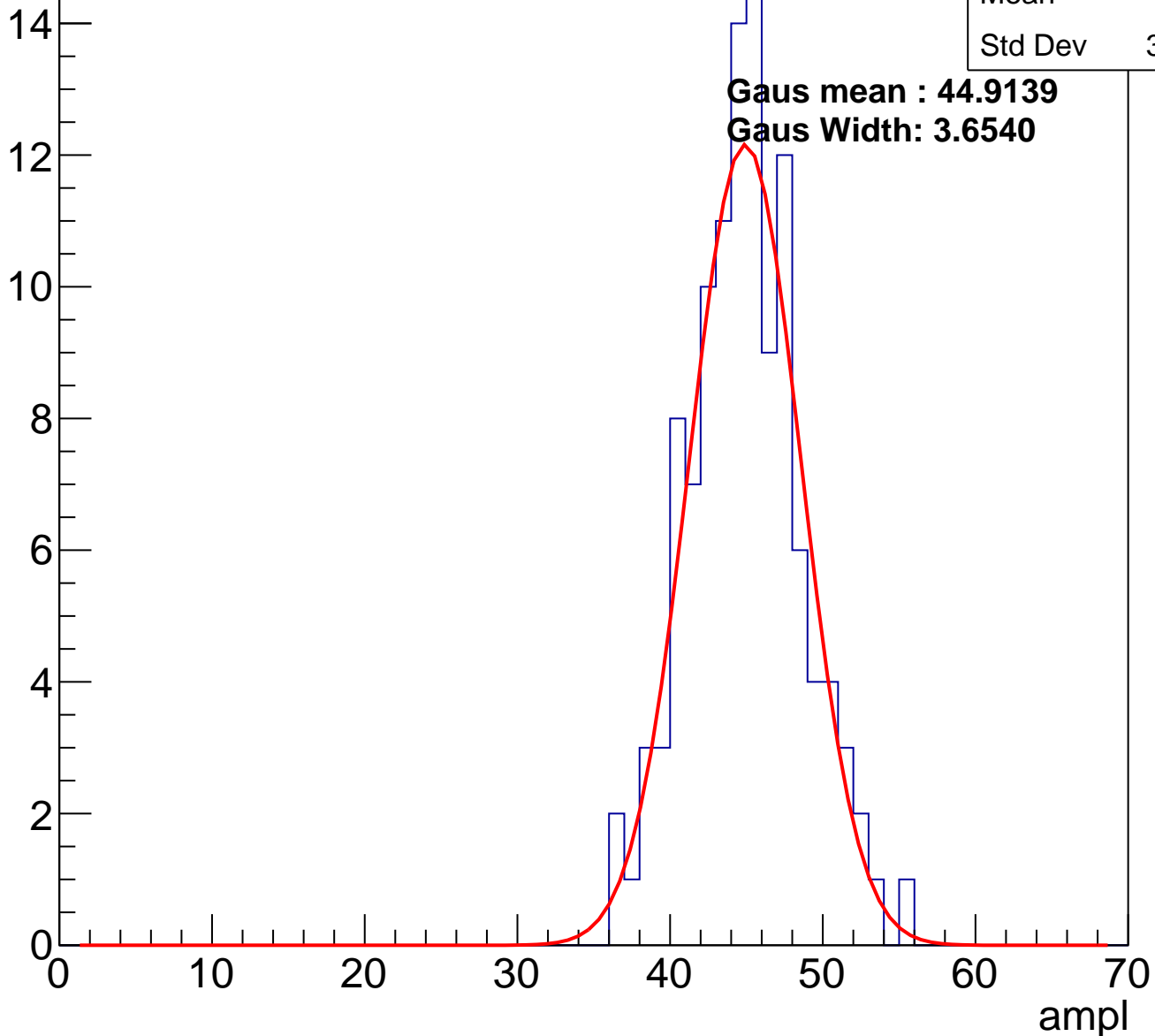
**Gaus Width: 3.4129**



# B0L101S, U23-ch22, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

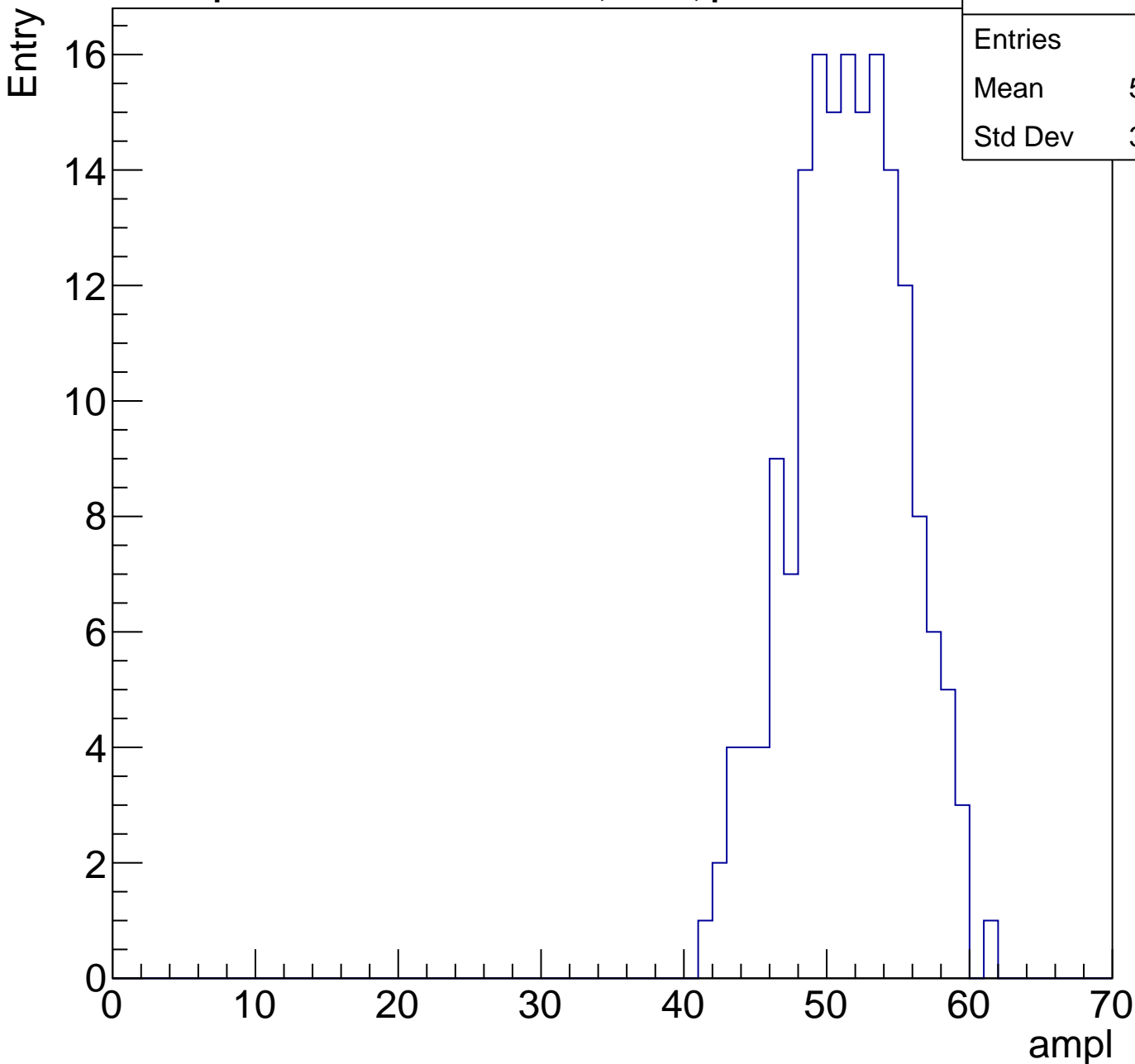
Entry



# B0L101S, U23-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	172
Mean	51.05
Std Dev	3.998



# B0L101S, U23-ch22, adc4

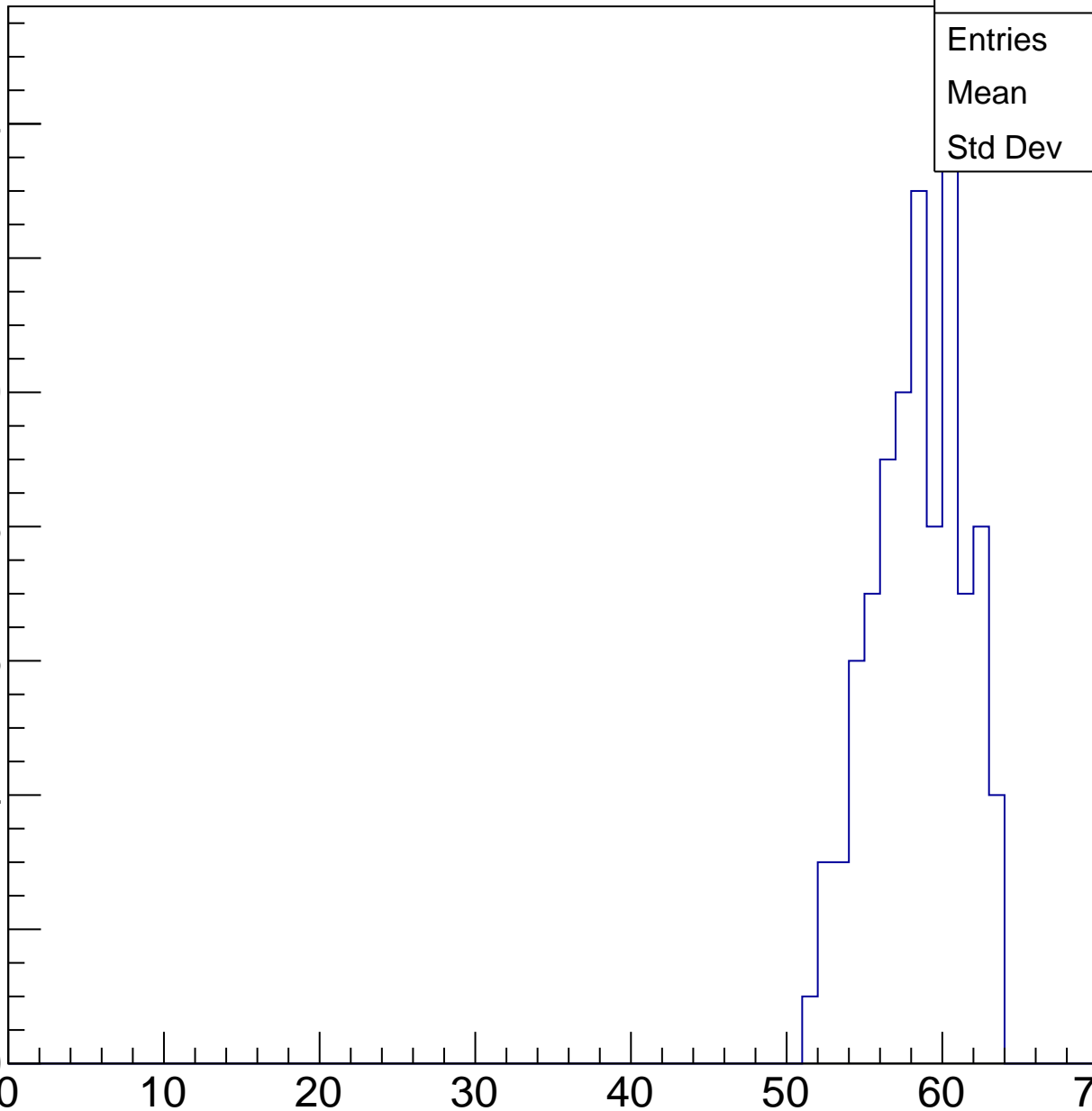
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	94
Mean	57.98
Std Dev	2.91

ampl



# B0L101S, U23-ch22, adc5

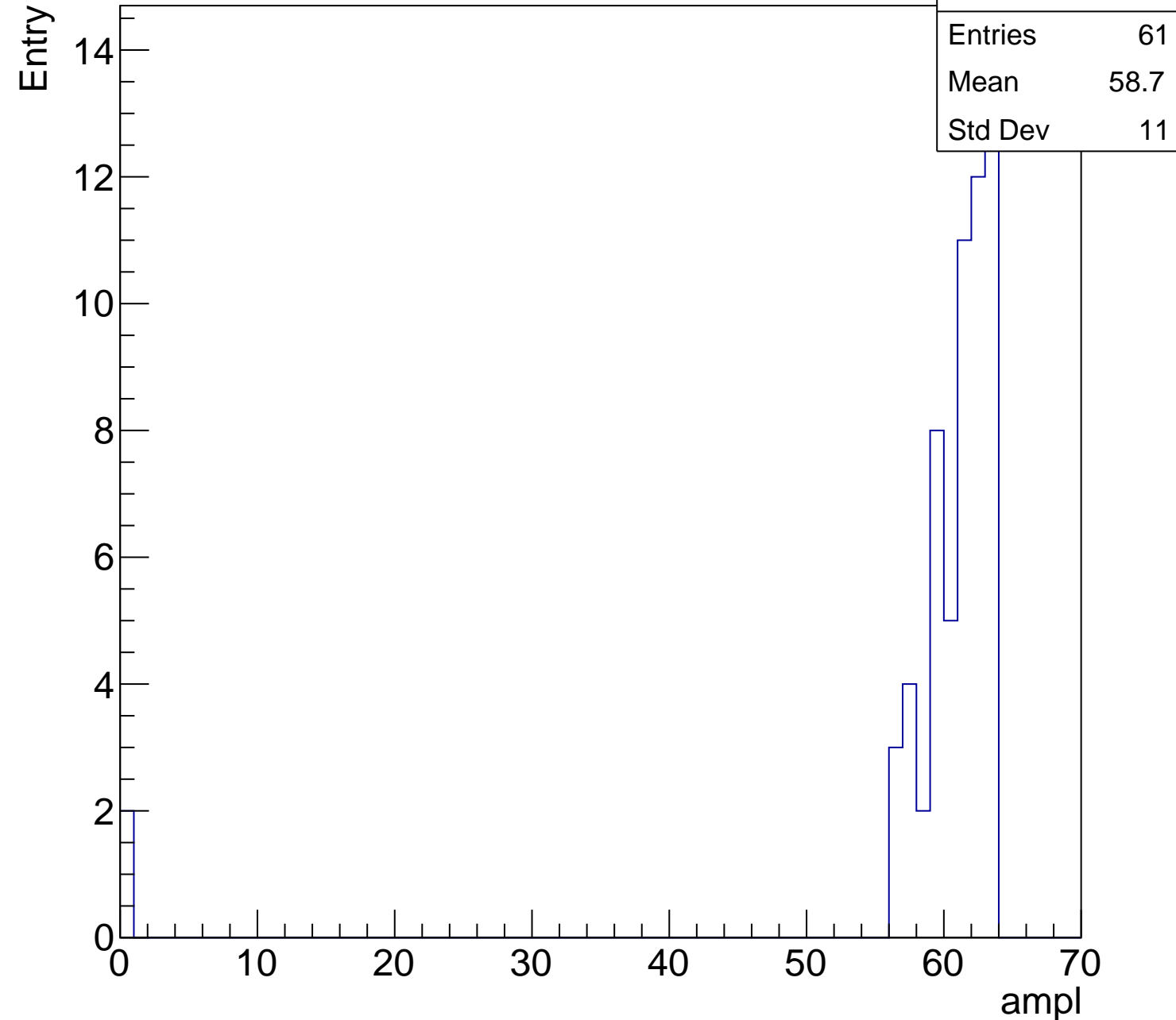
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	61
Mean	58.7
Std Dev	11

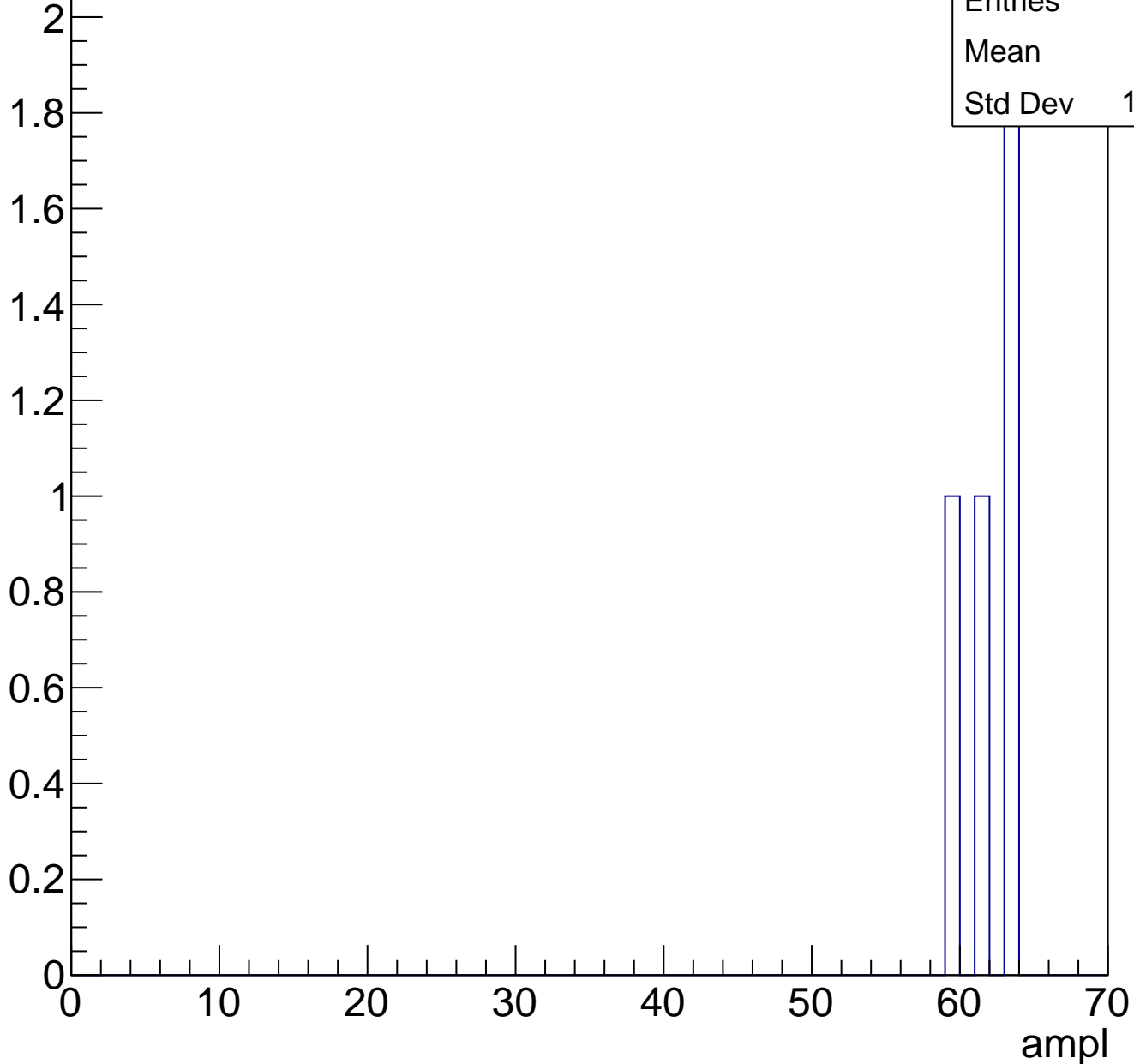
ampl



# B0L101S, U23-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch23, adc0

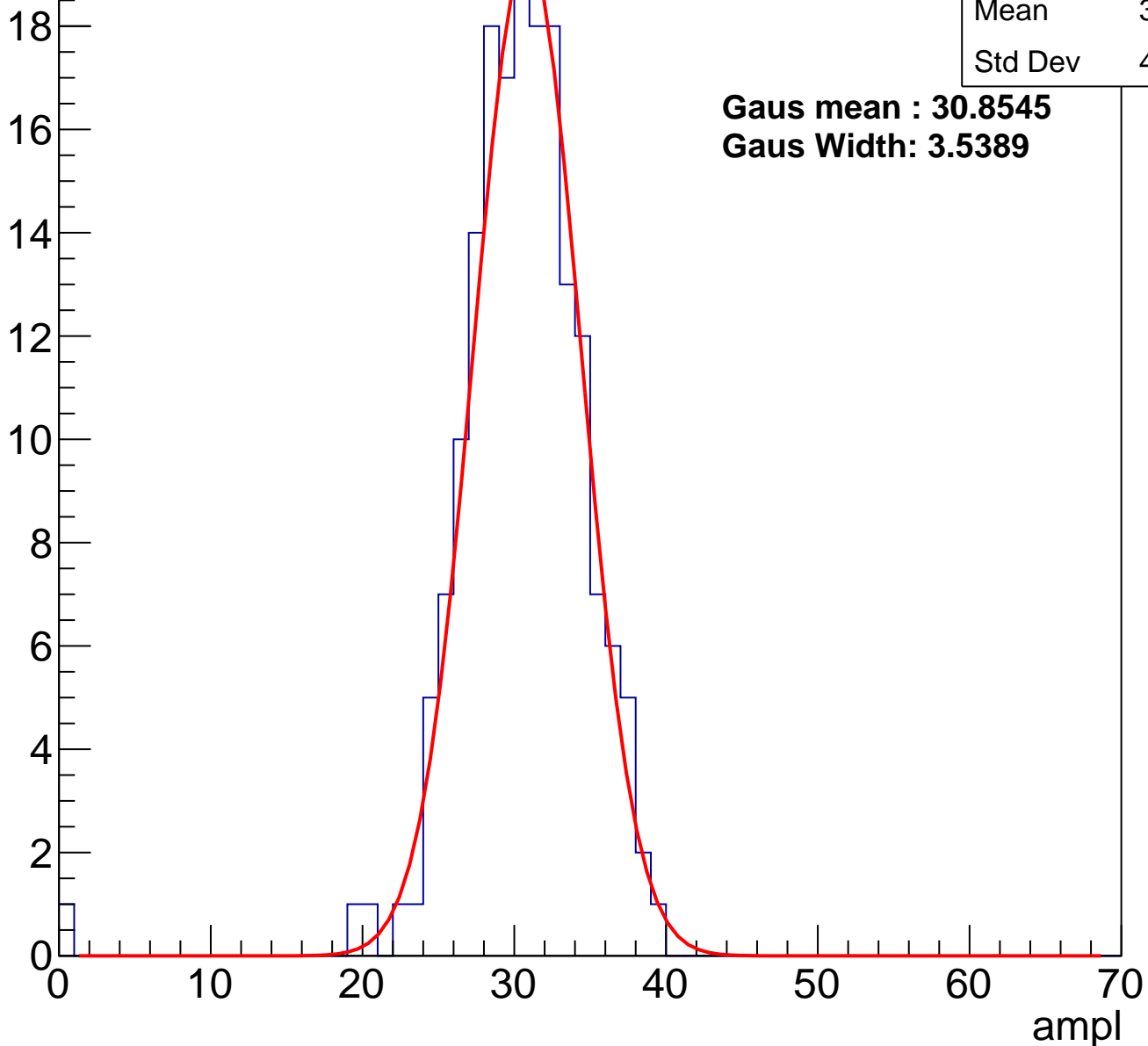
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	177
Mean	30.03
Std Dev	4.257

**Gaus mean : 30.8545**

**Gaus Width: 3.5389**

Entry

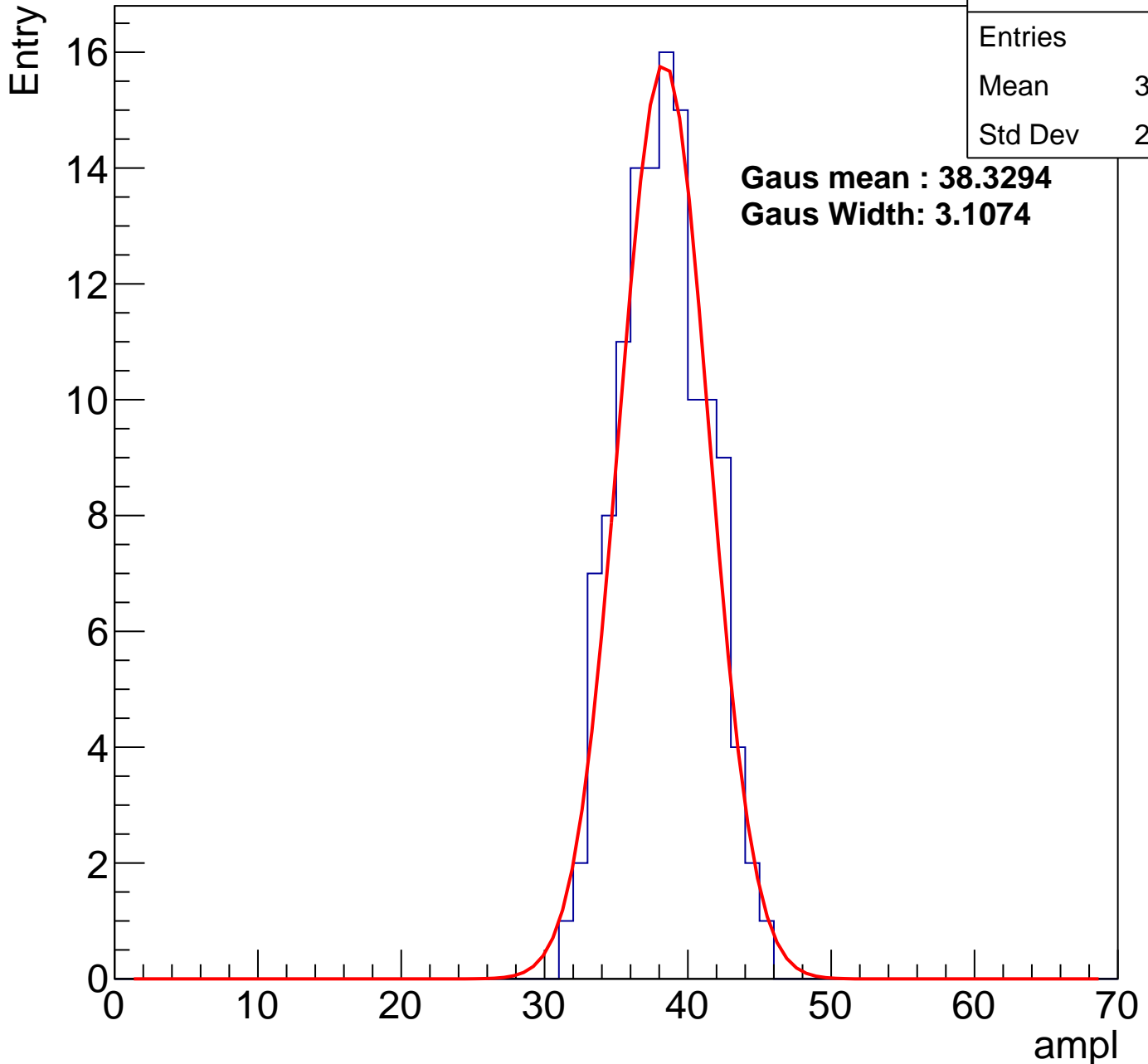


# B0L101S, U23-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	37.83
Std Dev	2.978

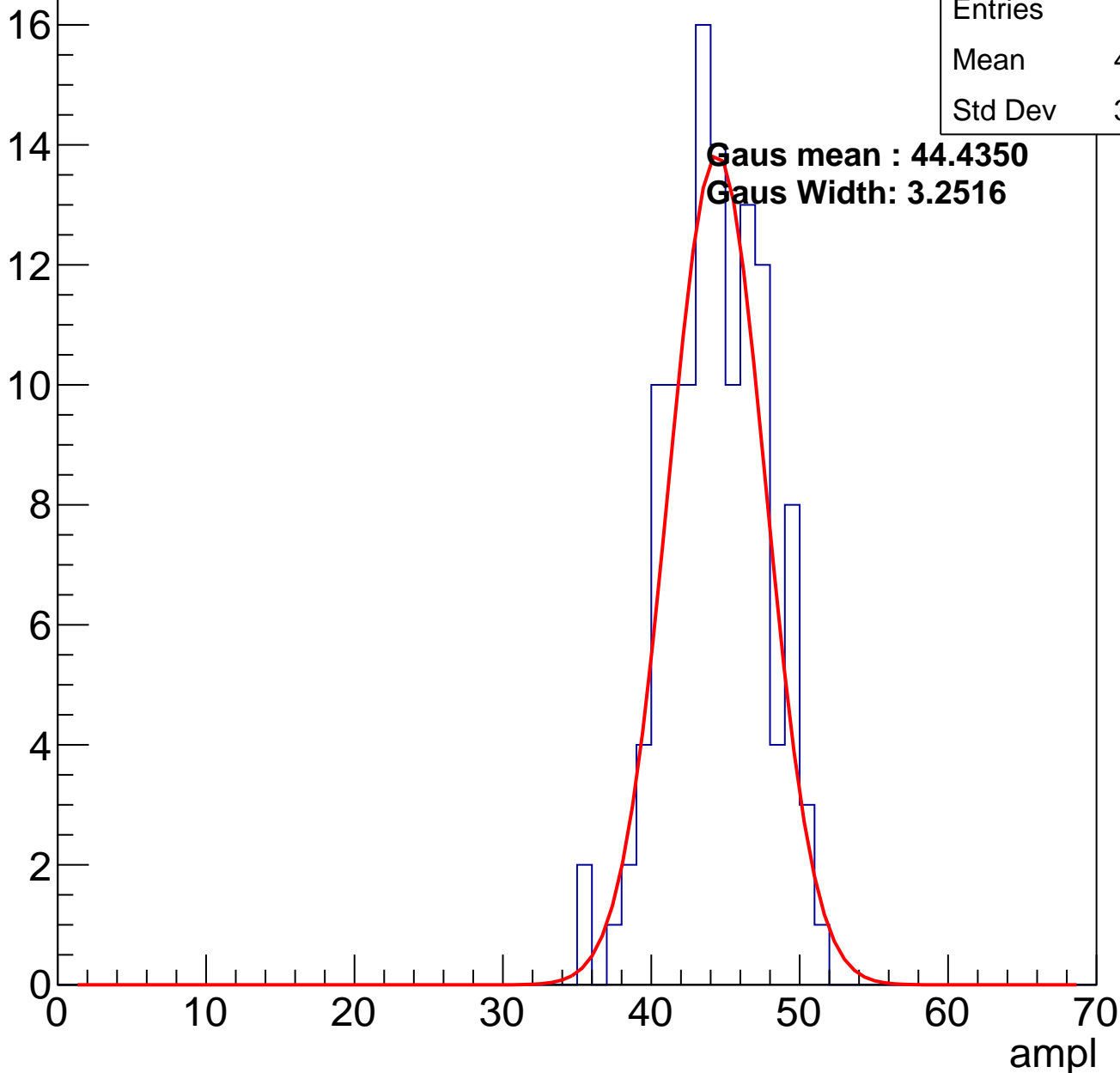
**Gaus mean : 38.3294**  
**Gaus Width: 3.1074**



# B0L101S, U23-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

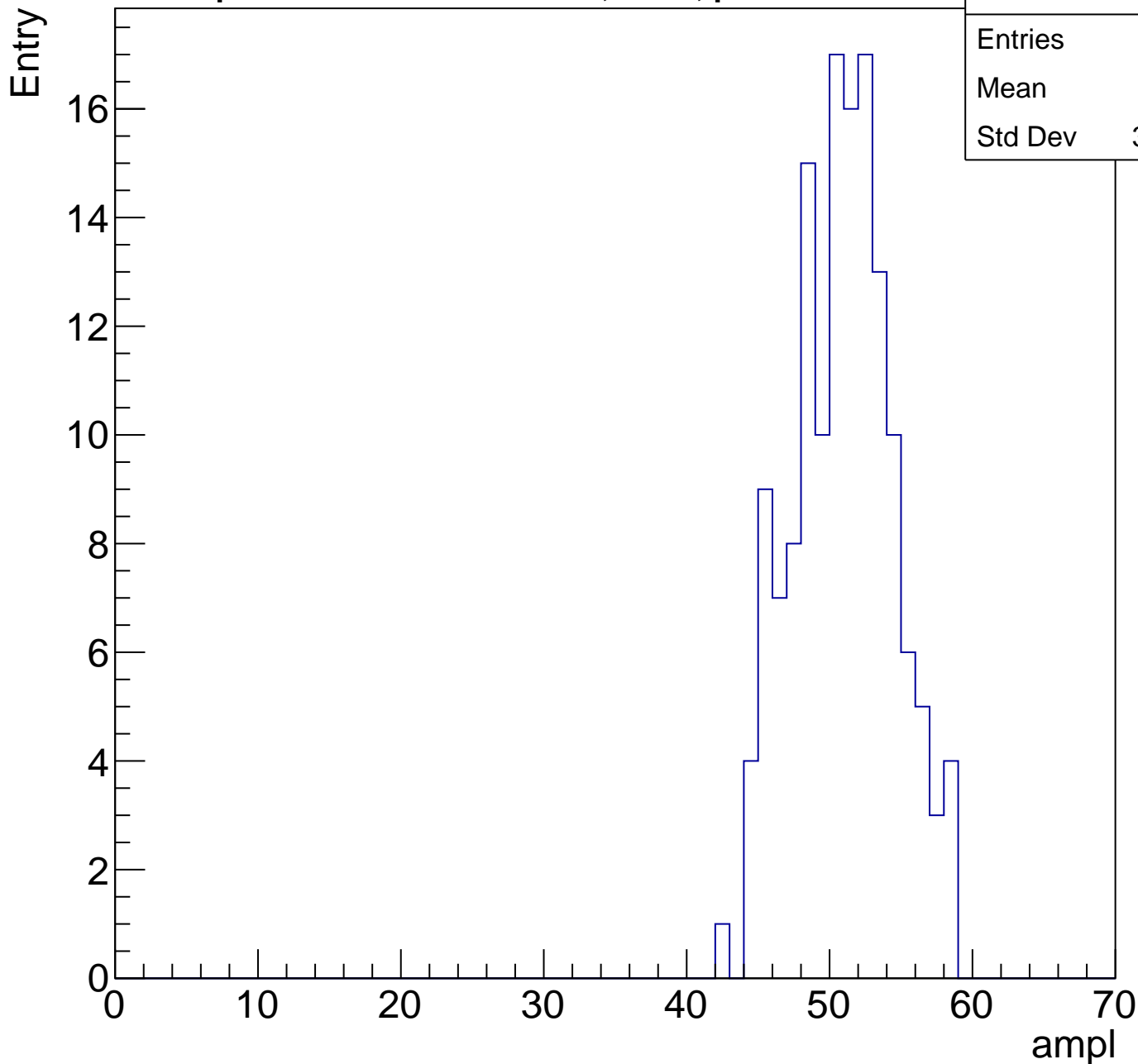
Entry



# B0L101S, U23-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	50.5
Std Dev	3.468



# B0L101S, U23-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

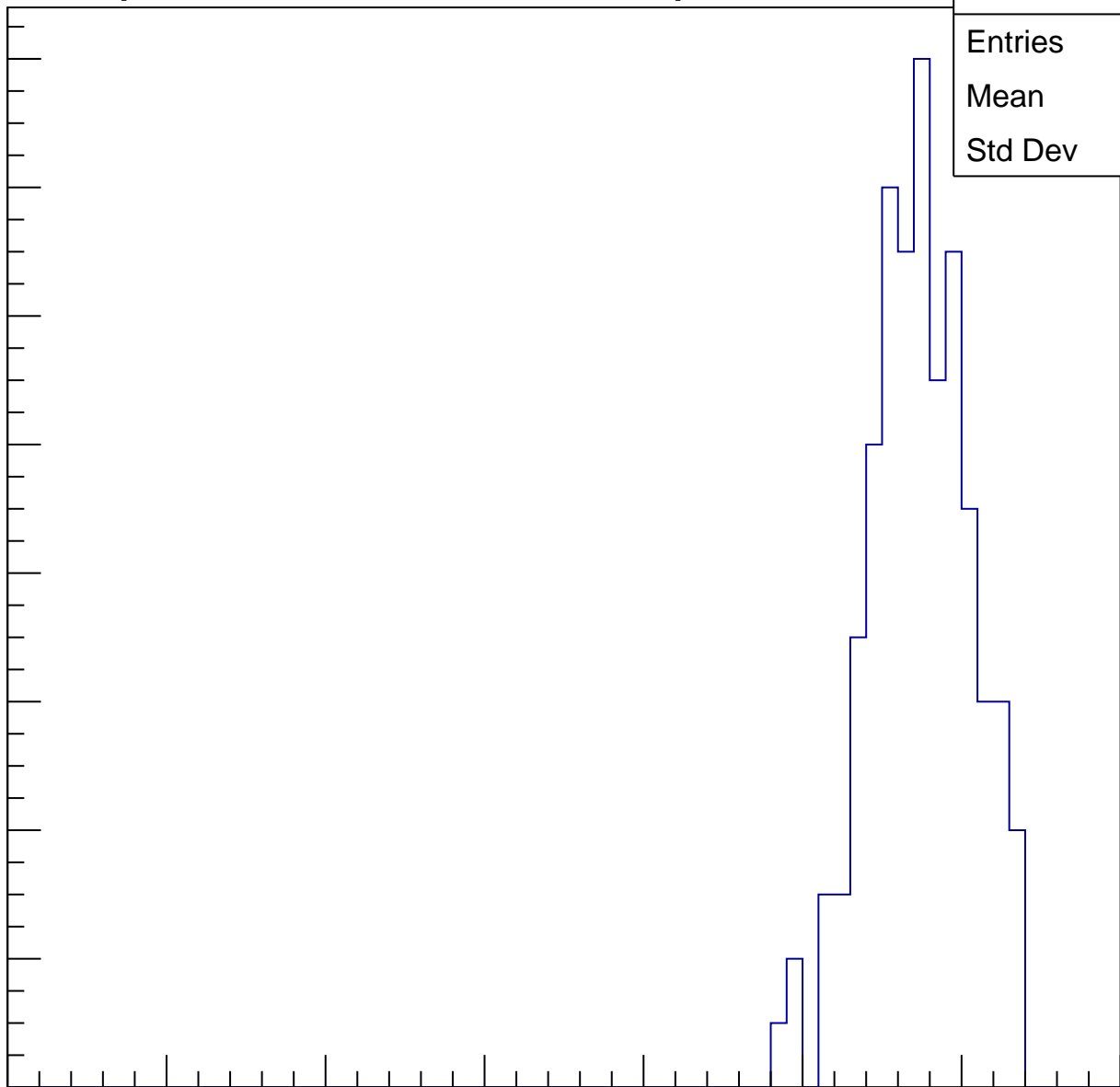
Entries	118
Mean	56.87
Std Dev	3.182

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch23, adc5

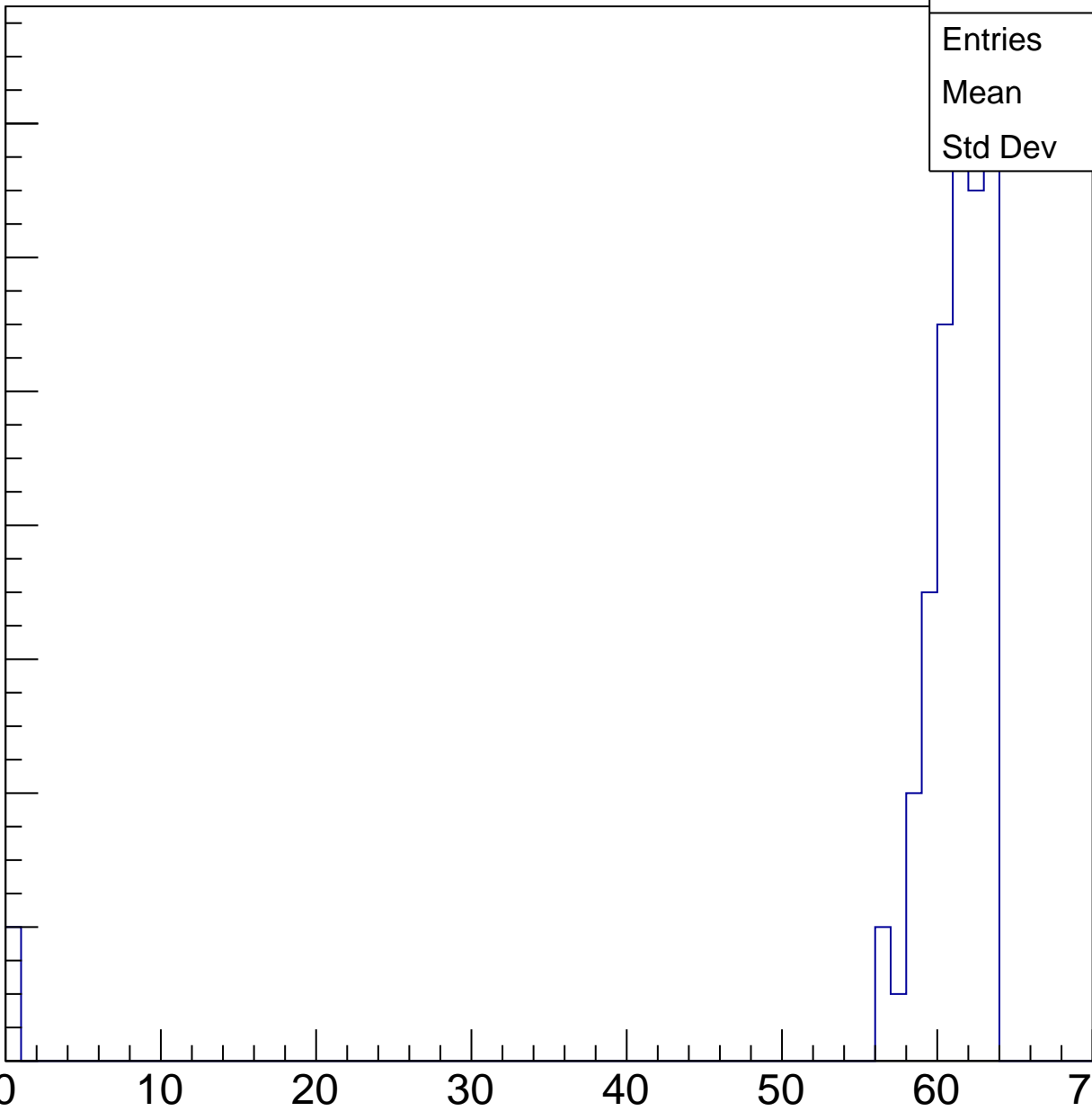
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	59.12
Std Dev	10.36

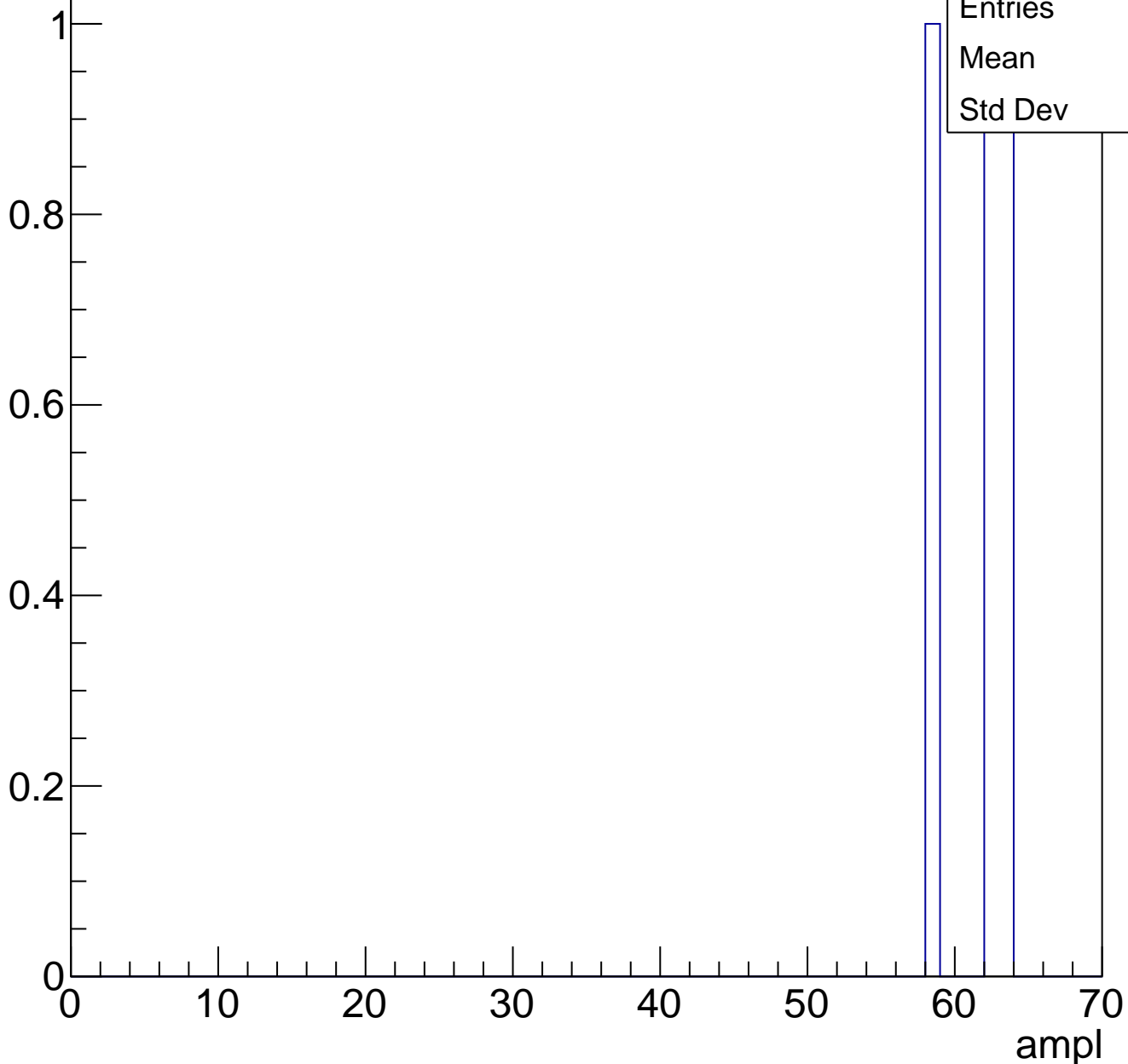
ampl



# B0L101S, U23-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch24, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	28.69
Std Dev	3.575

**Gaus mean : 29.0704**

**Gaus Width: 3.8650**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

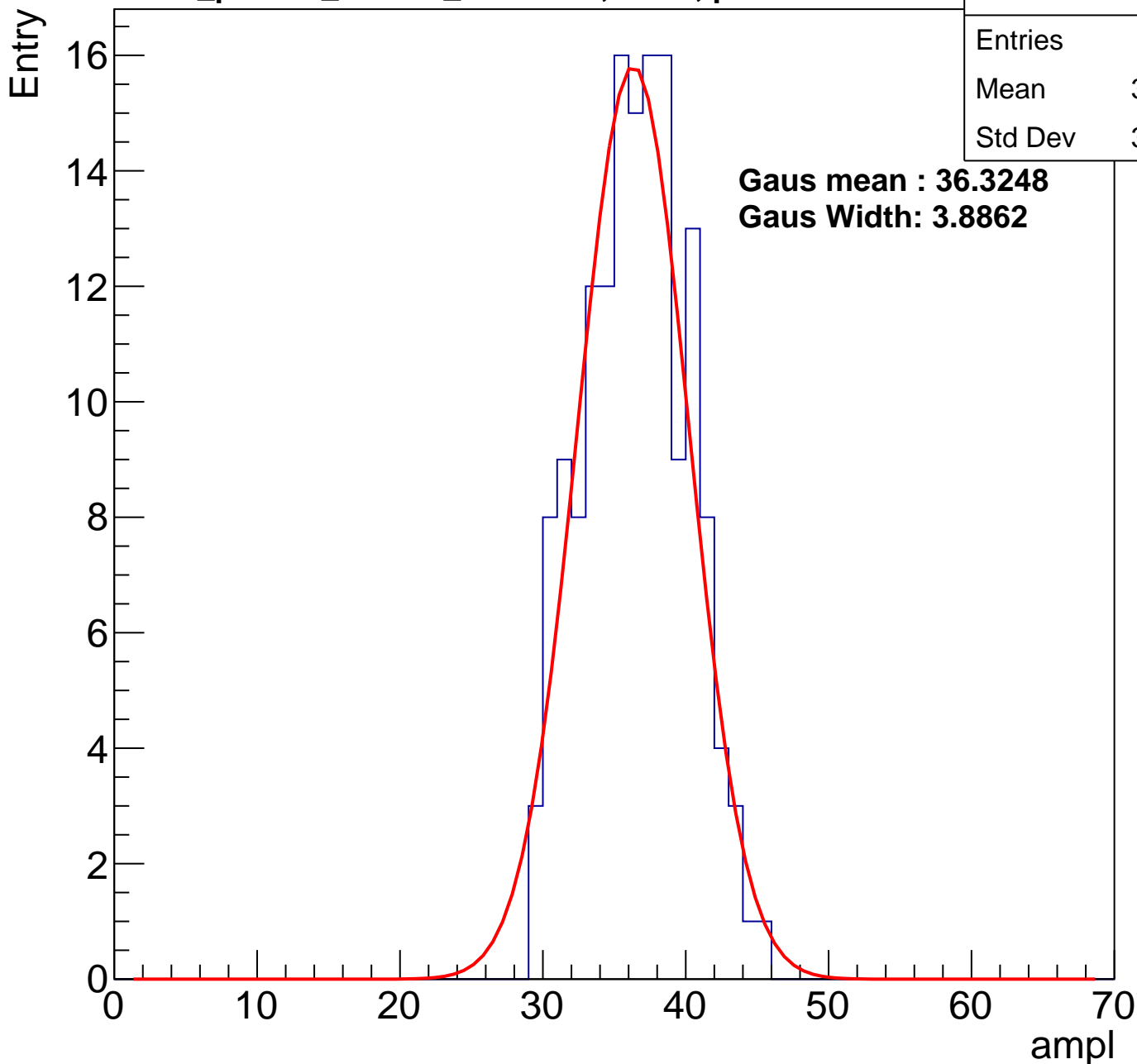
# B0L101S, U23-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	36.05
Std Dev	3.572

**Gaus mean : 36.3248**

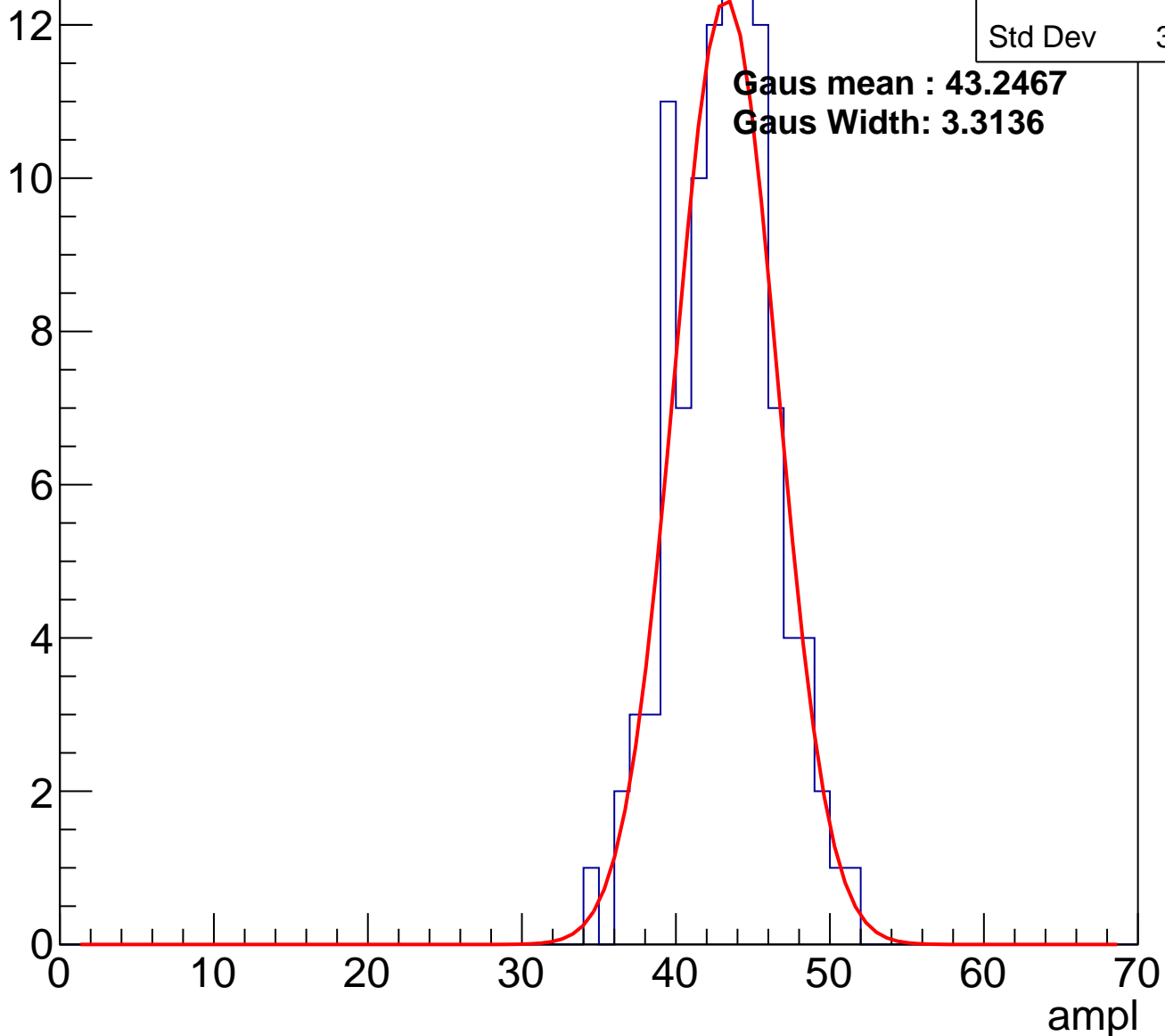
**Gaus Width: 3.8862**



# B0L101S, U23-ch24, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

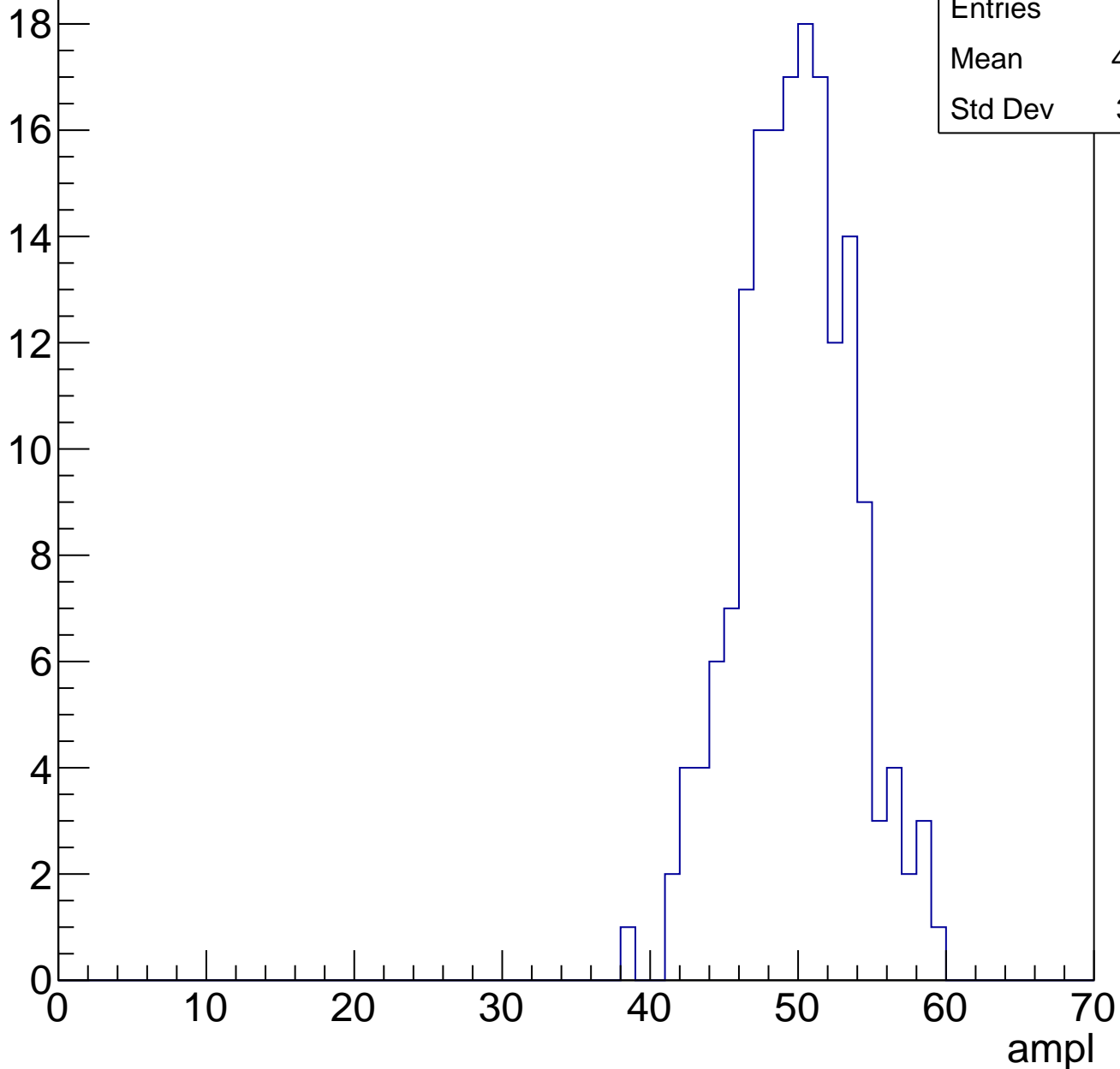


# B0L101S, U23-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	49.38
Std Dev	3.801

Entry



# B0L101S, U23-ch24, adc4

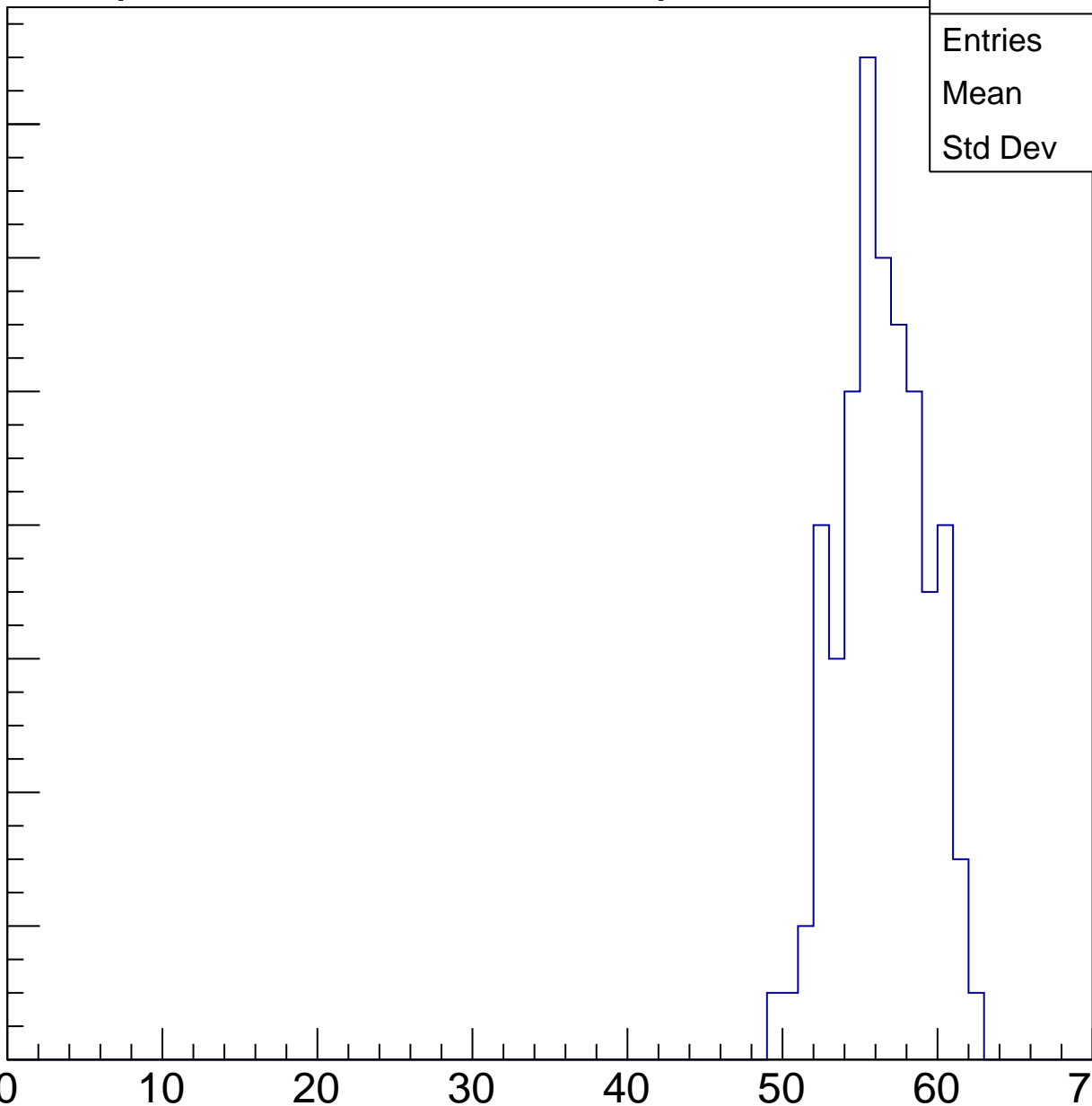
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	95
Mean	55.97
Std Dev	2.762

ampl



# B0L101S, U23-ch24, adc5

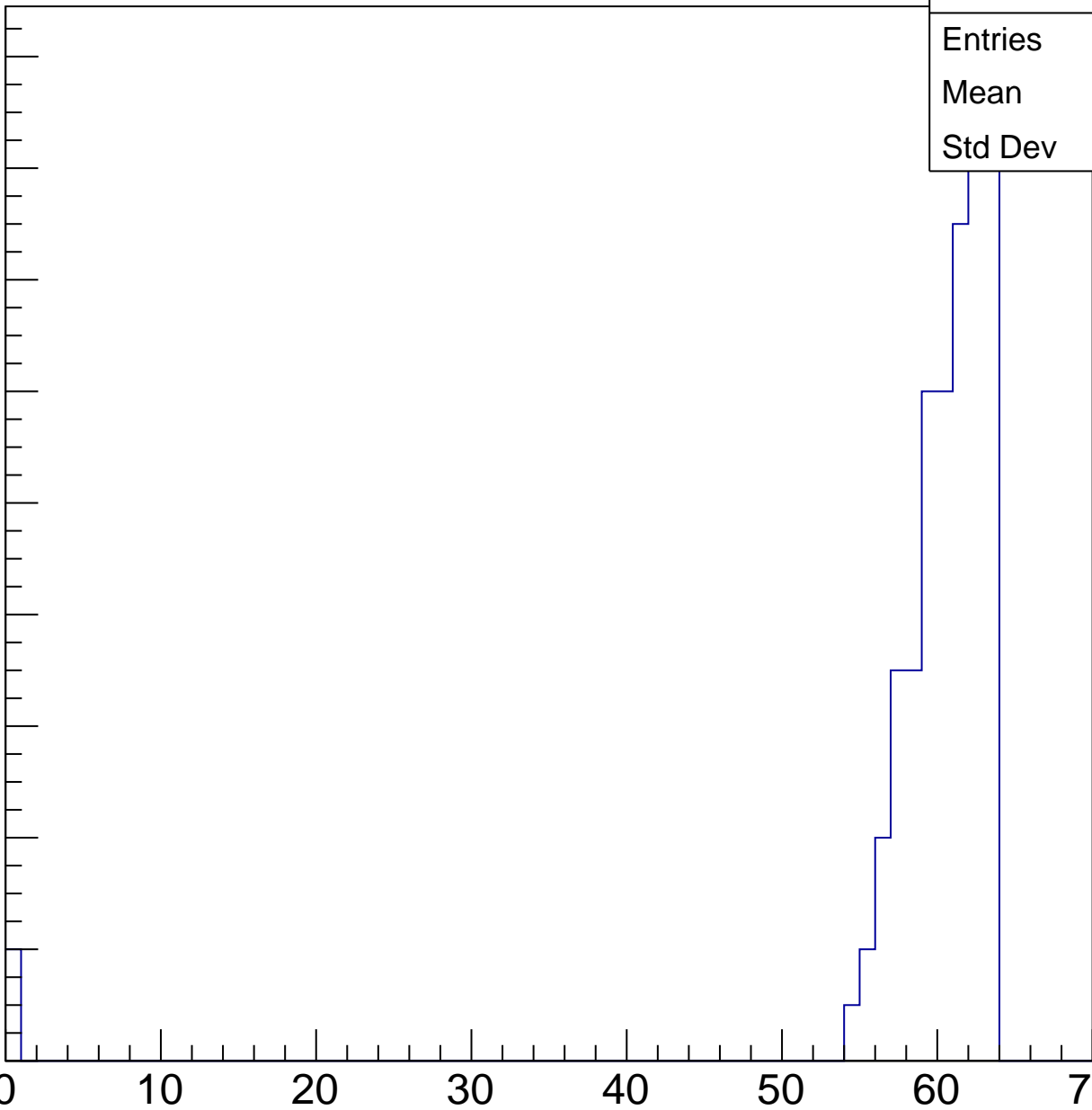
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	96
Mean	58.96
Std Dev	8.886

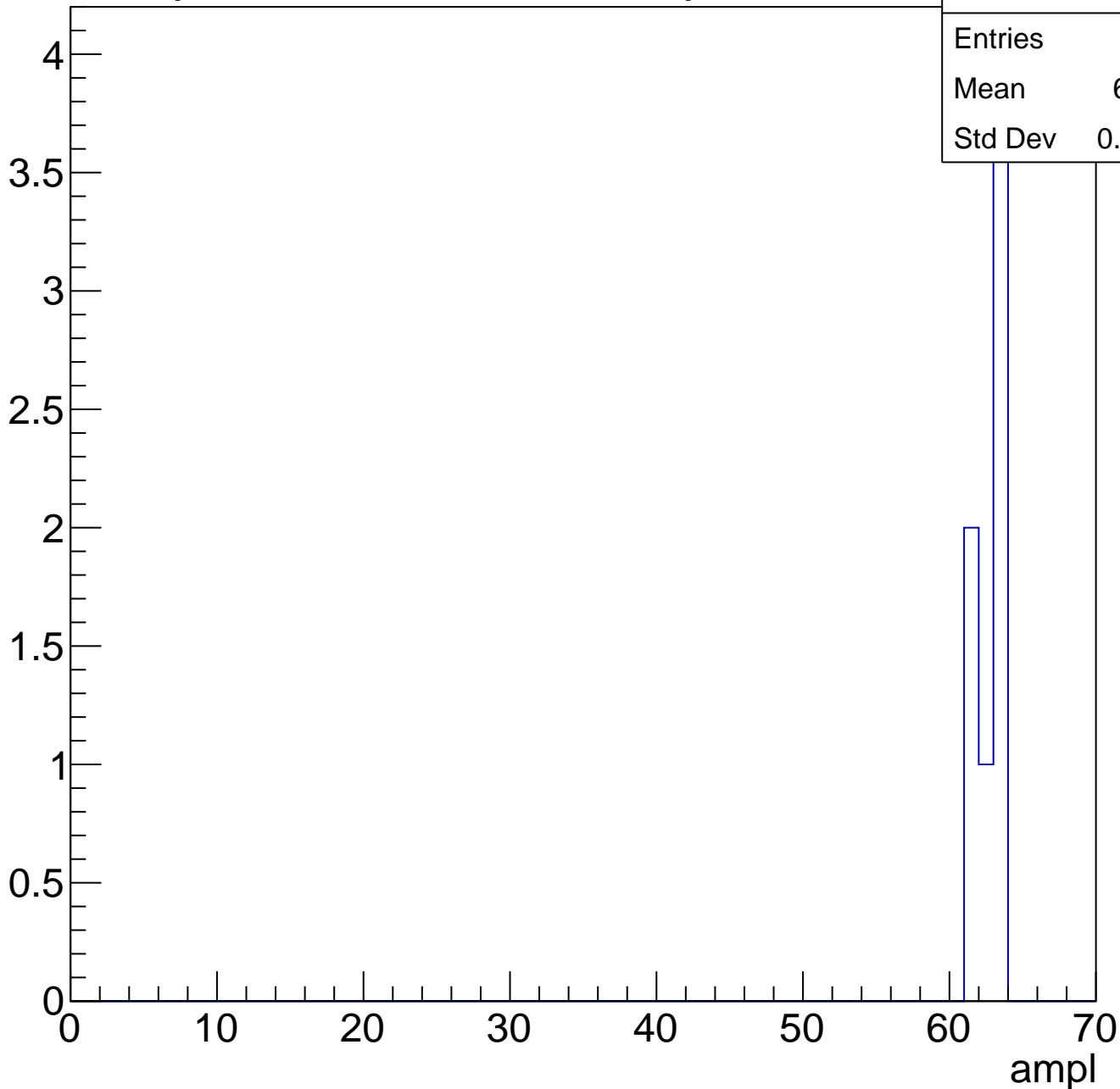
ampl



# B0L101S, U23-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

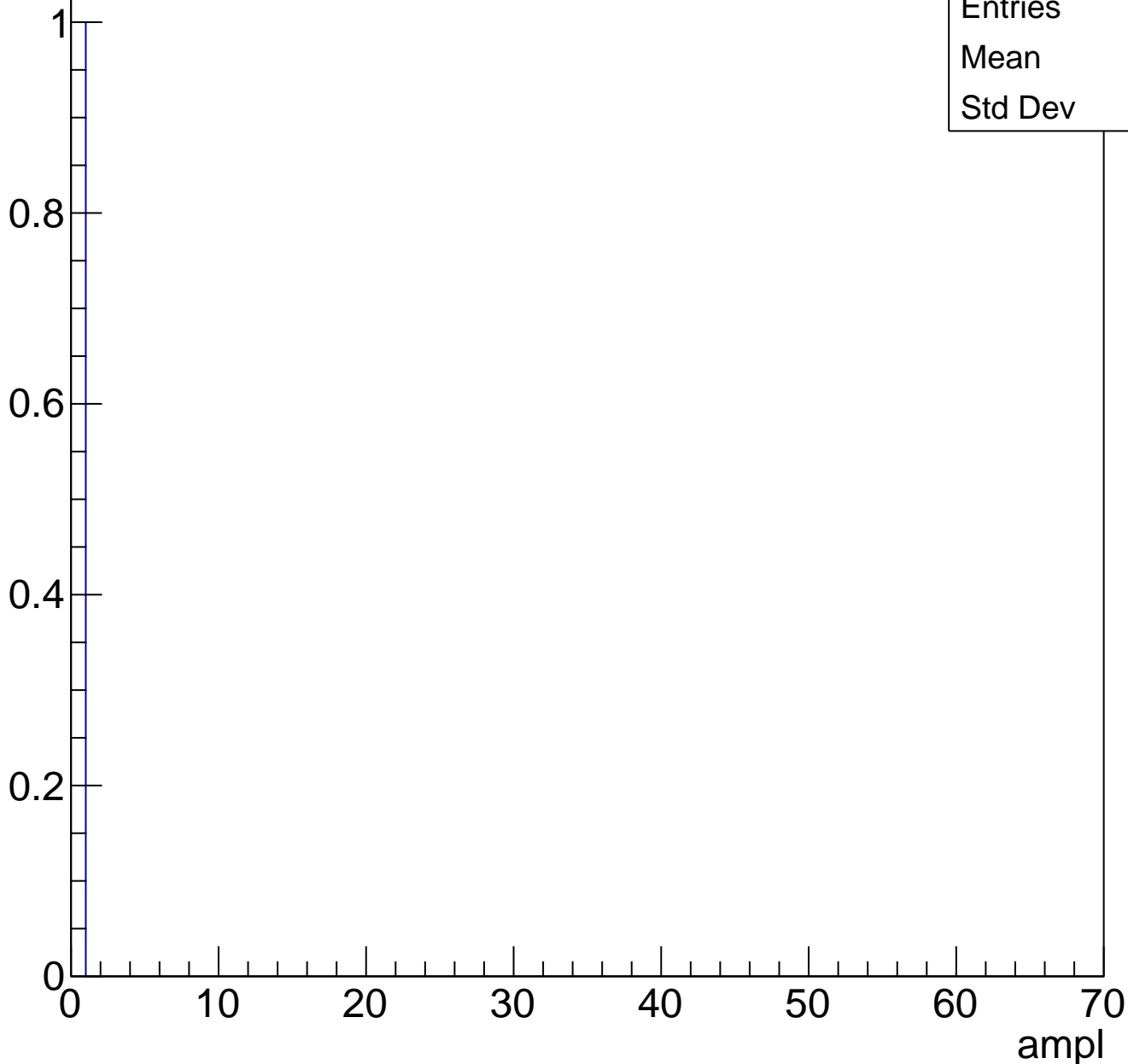




# B0L101S, U23-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch25, adc0

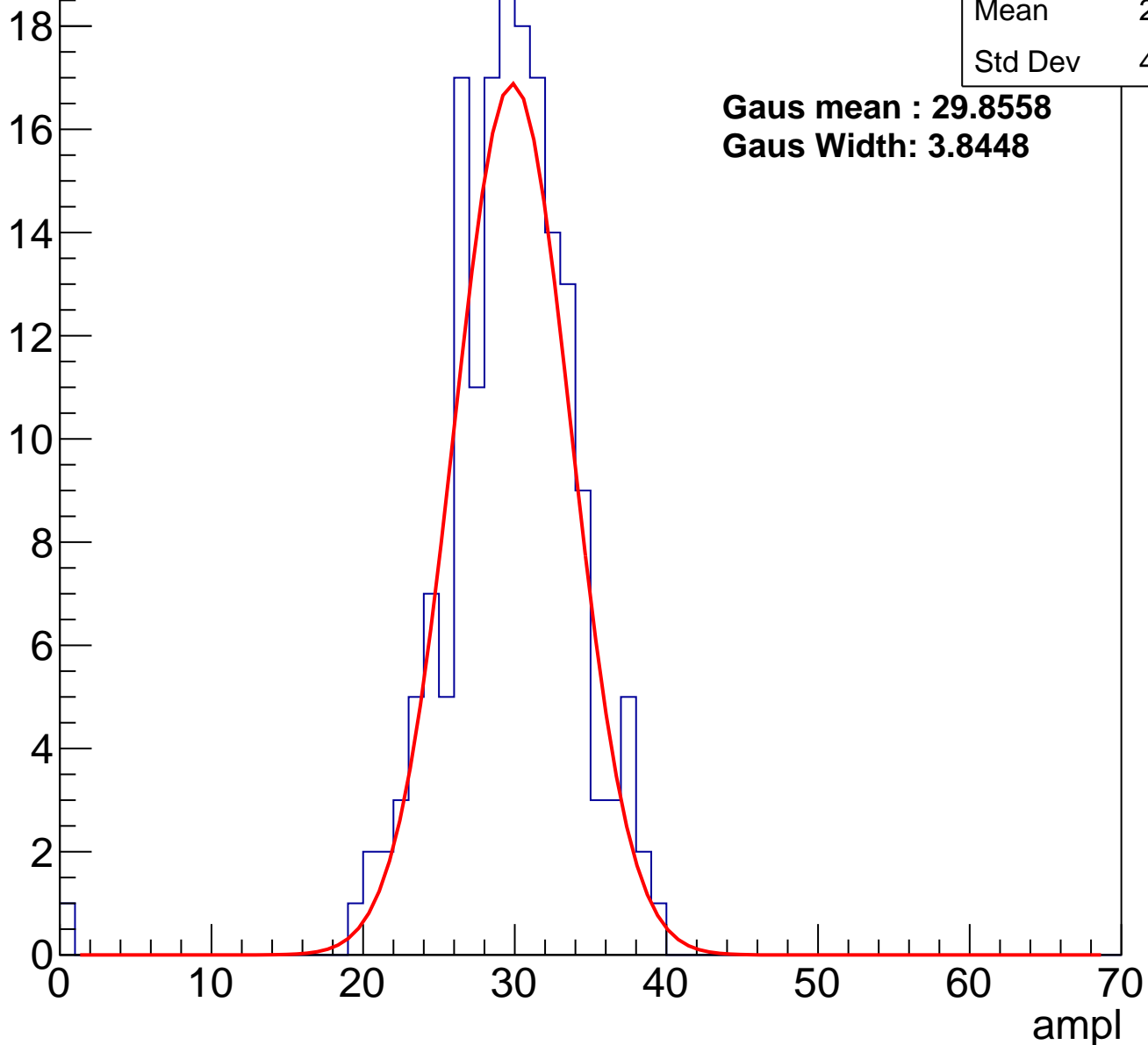
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	29.17
Std Dev	4.475

**Gaus mean : 29.8558**

**Gaus Width: 3.8448**

Entry



# B0L101S, U23-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	37.01
Std Dev	3.339

**Gaus mean : 37.2787**

**Gaus Width: 3.2317**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

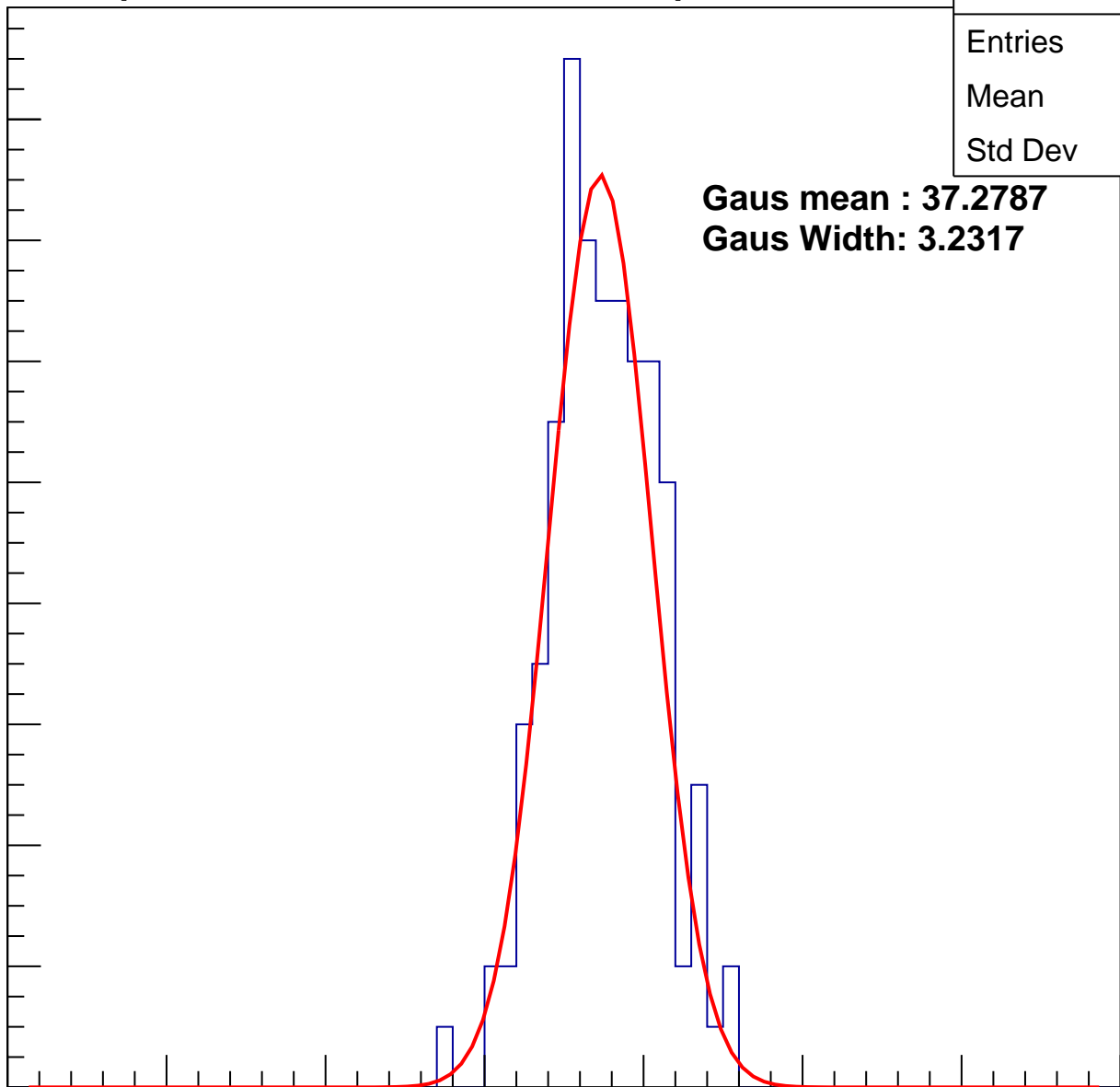
40

50

60

70

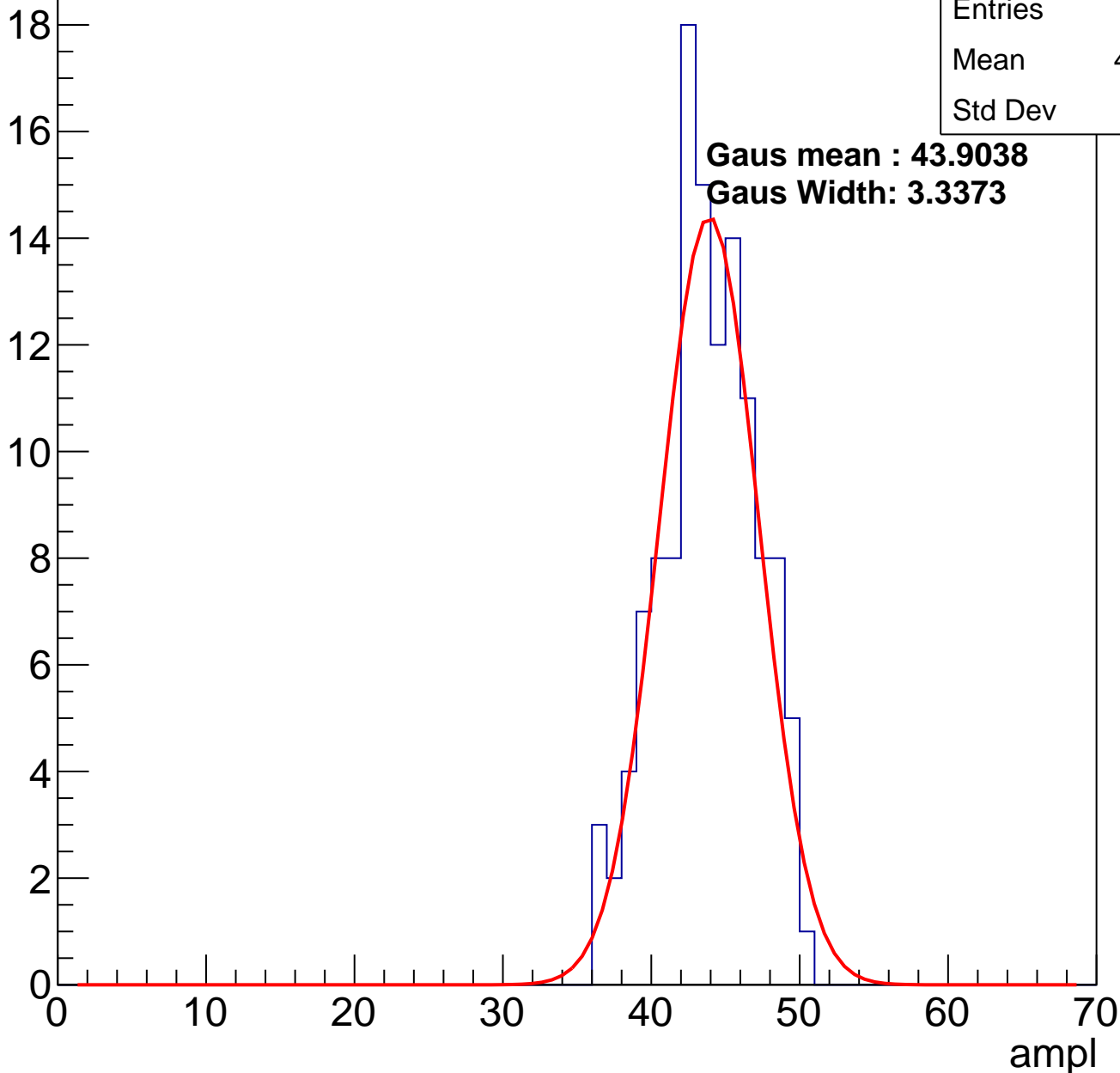
ampl



# B0L101S, U23-ch25, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

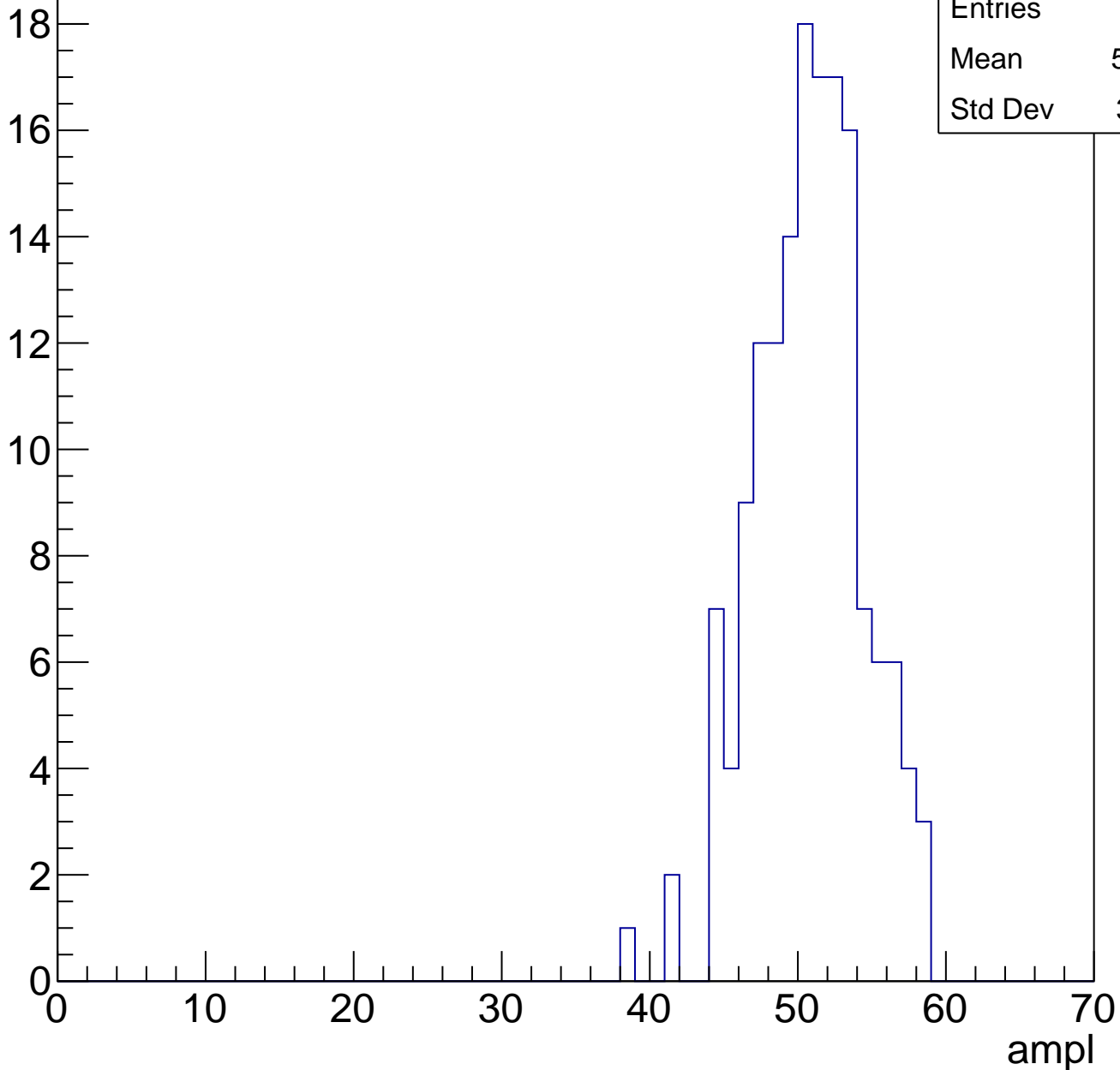


# B0L101S, U23-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	50.28
Std Dev	3.641

Entry



# B0L101S, U23-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	111
Mean	56.87
Std Dev	2.932

Entry

14

12

10

8

6

4

2

0

0

10

20

30

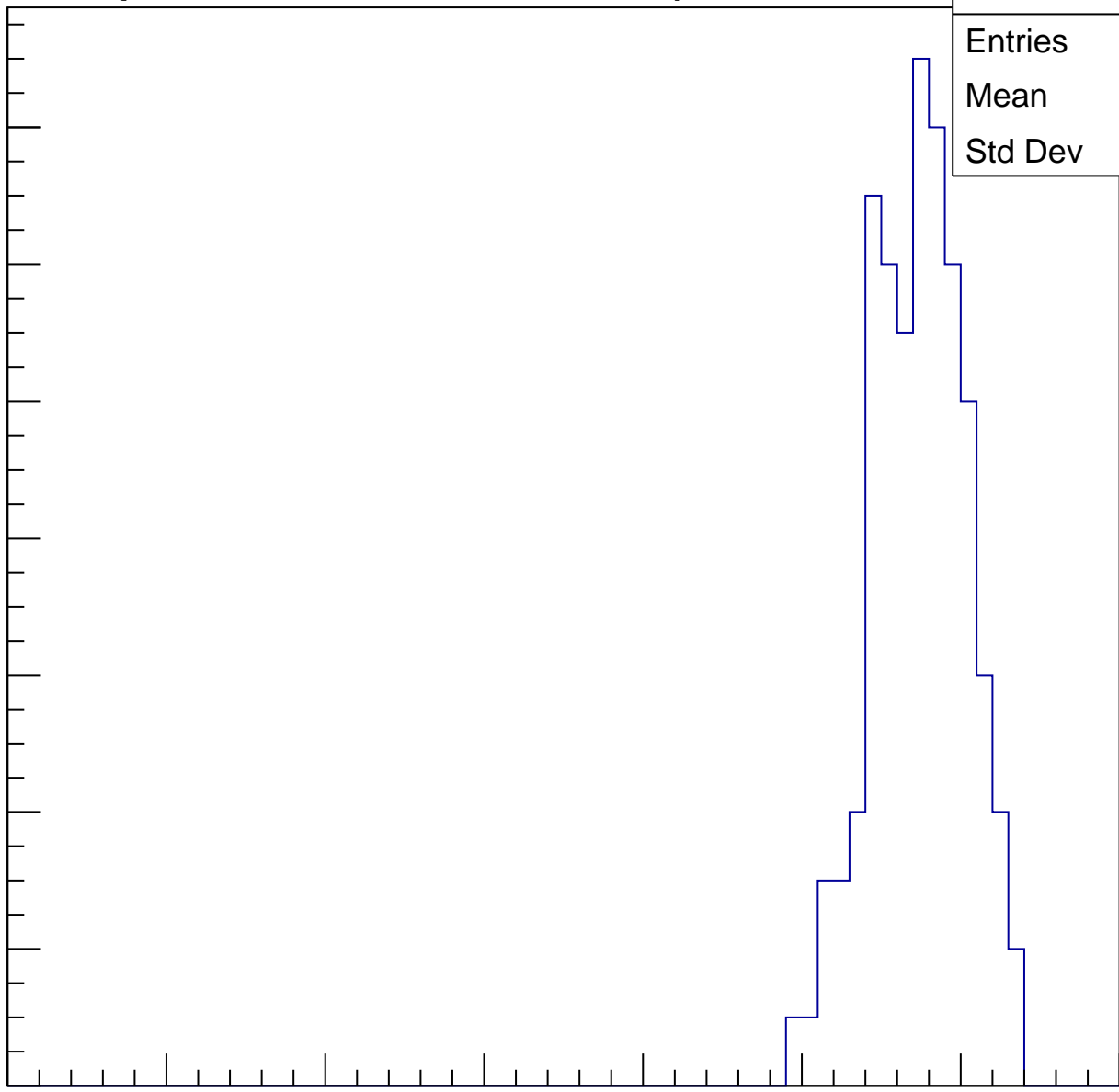
40

50

60

70

ampl



# B0L101S, U23-ch25, adc5

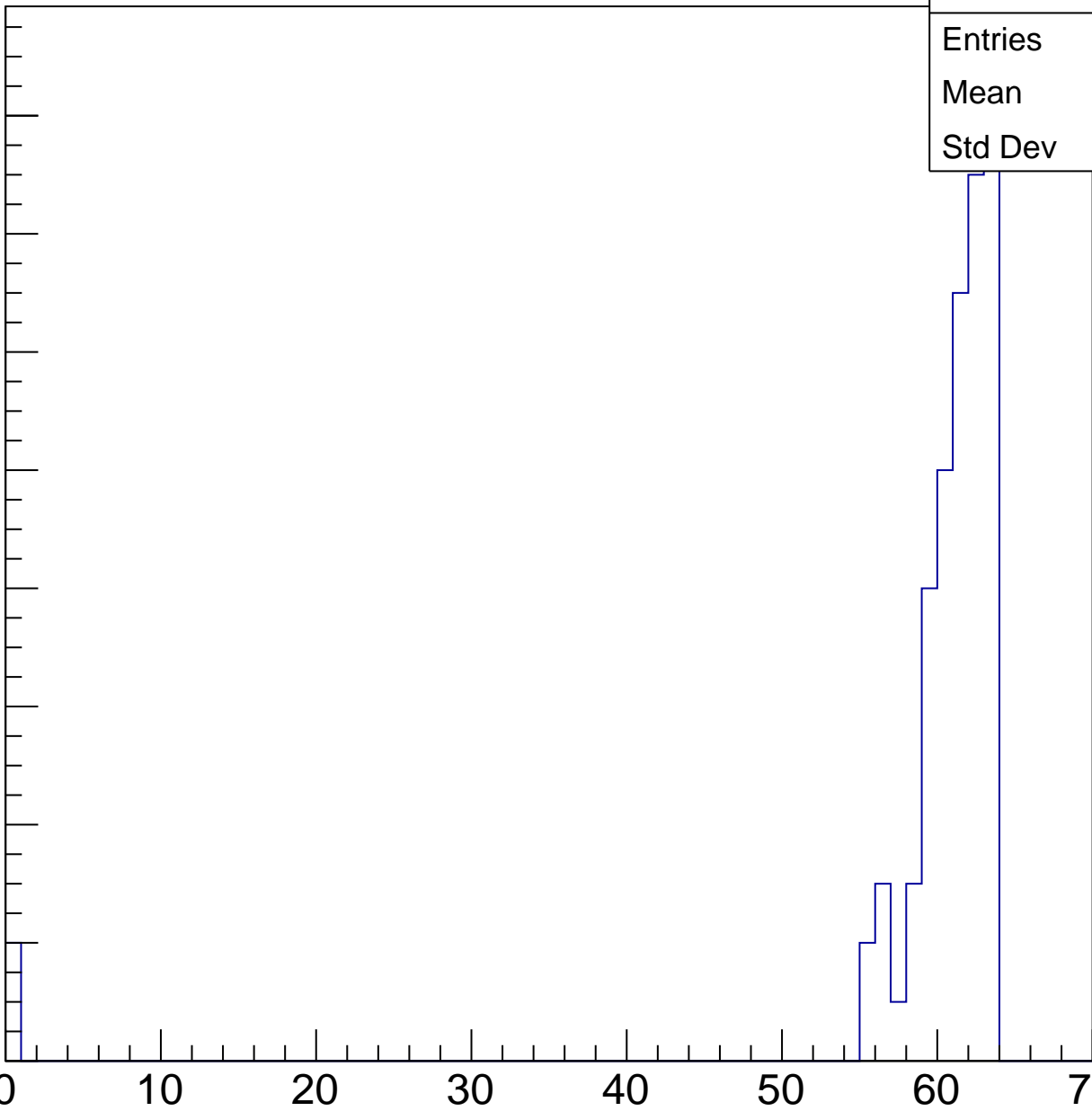
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	74
Mean	59.12
Std Dev	10.06

ampl



# B0L101S, U23-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U23-ch26, adc0

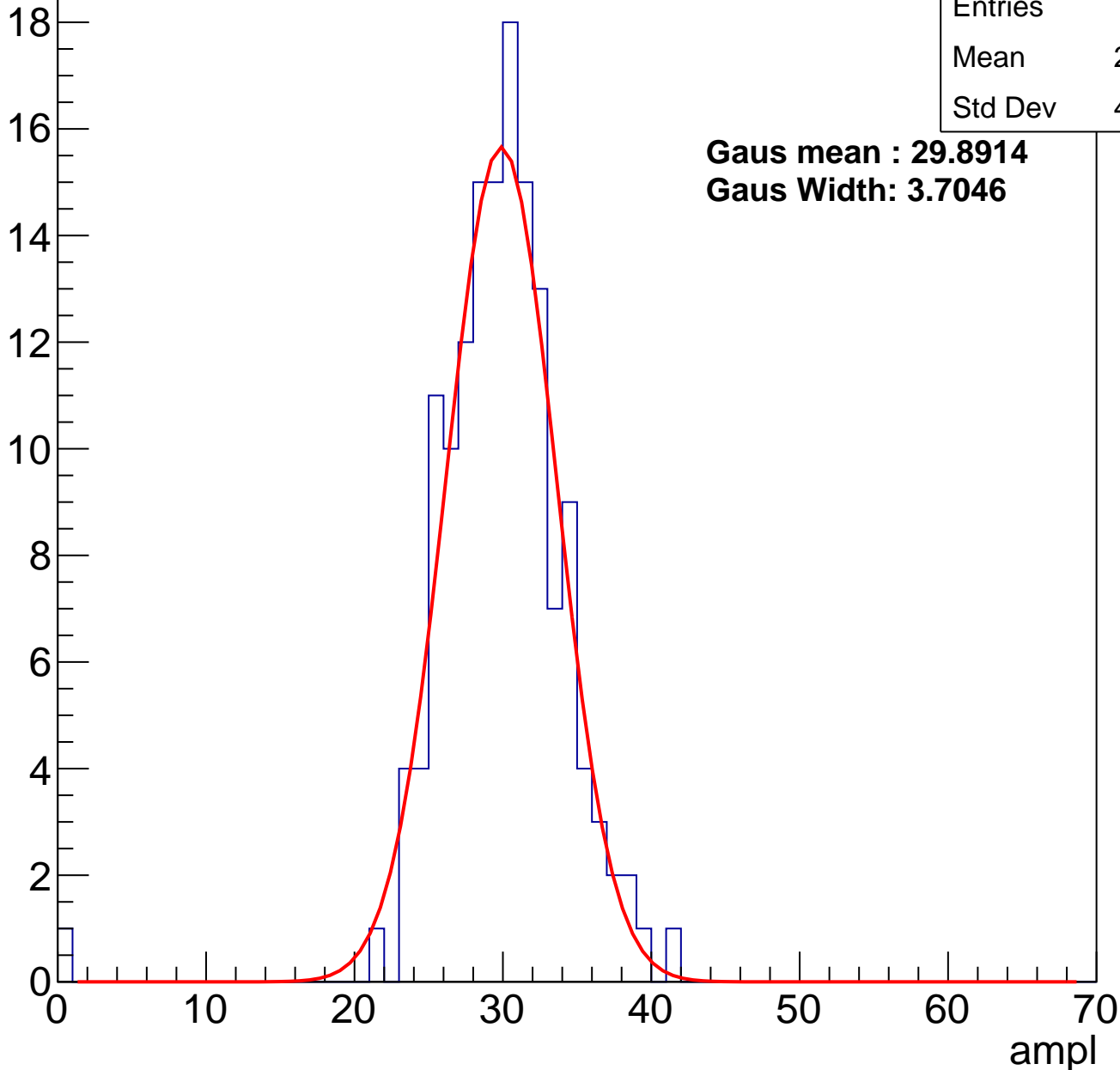
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	29.45
Std Dev	4.353

**Gaus mean : 29.8914**

**Gaus Width: 3.7046**

Entry



# B0L101S, U23-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

36.39

Std Dev

3.421

**Gaus mean : 36.8618**

**Gaus Width: 3.3351**

0

10

20

30

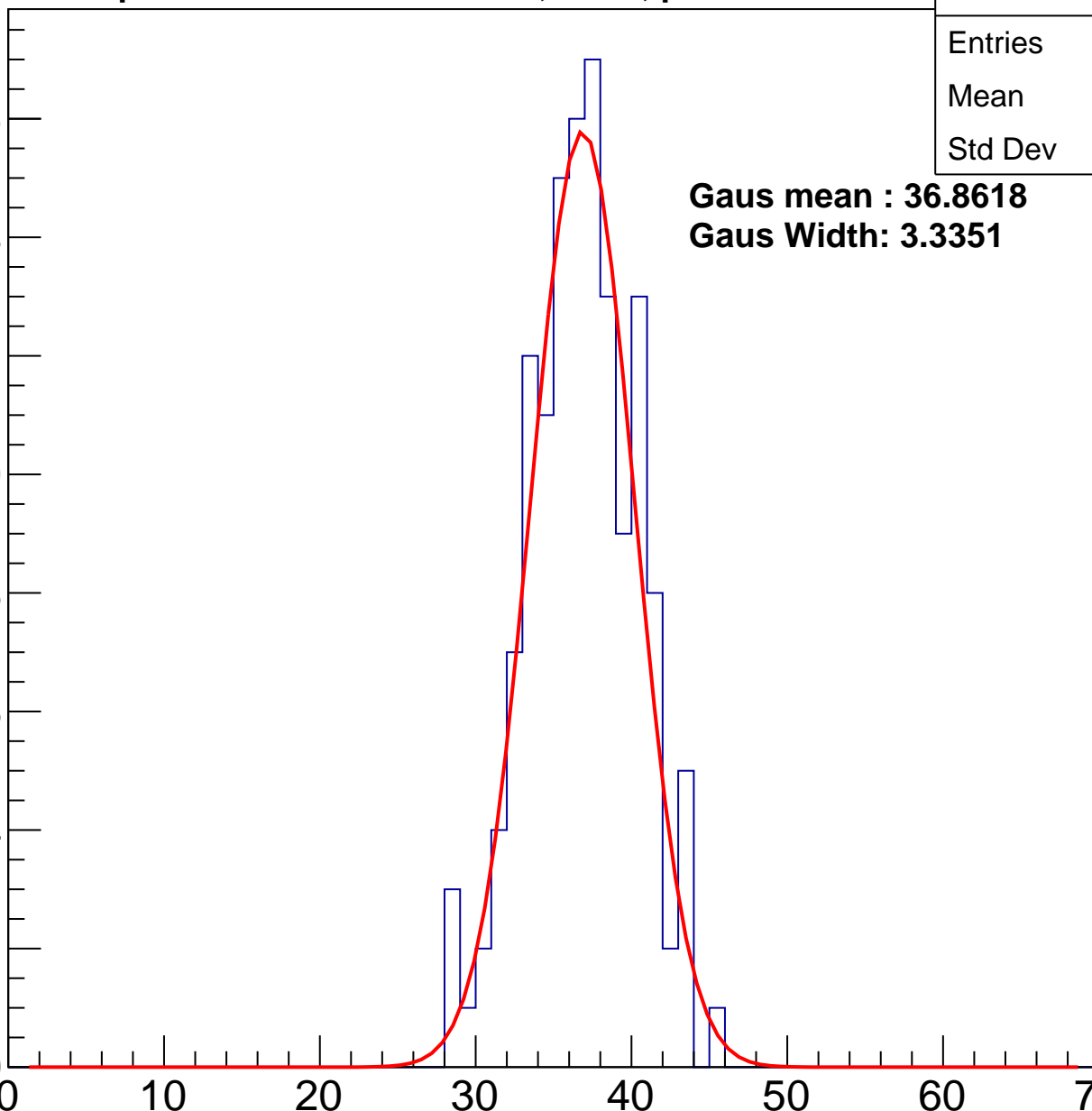
40

50

60

70

ampl



# B0L101S, U23-ch26, adc2

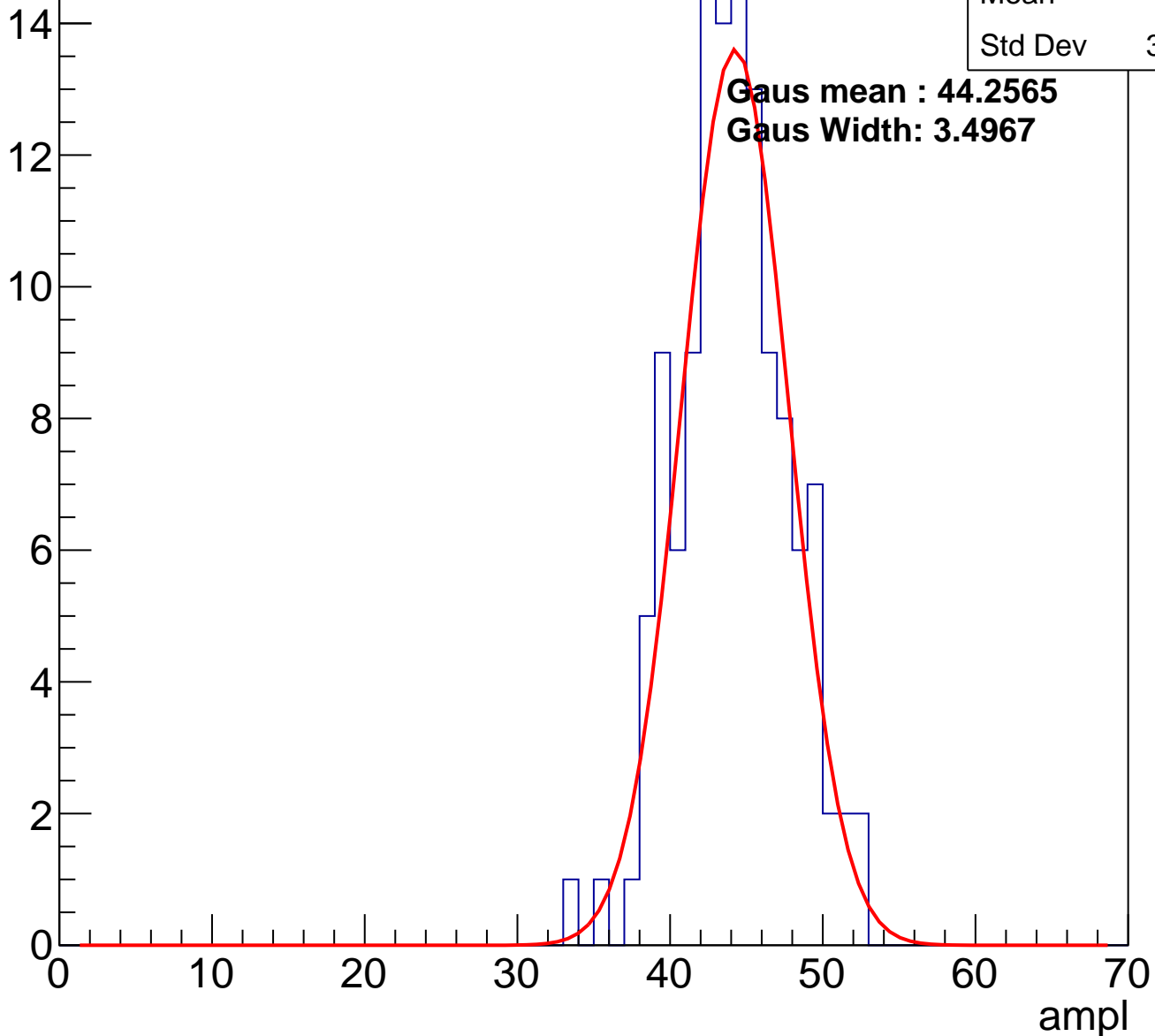
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	43.67
Std Dev	3.559

**Gaus mean : 44.2565**

**Gaus Width: 3.4967**

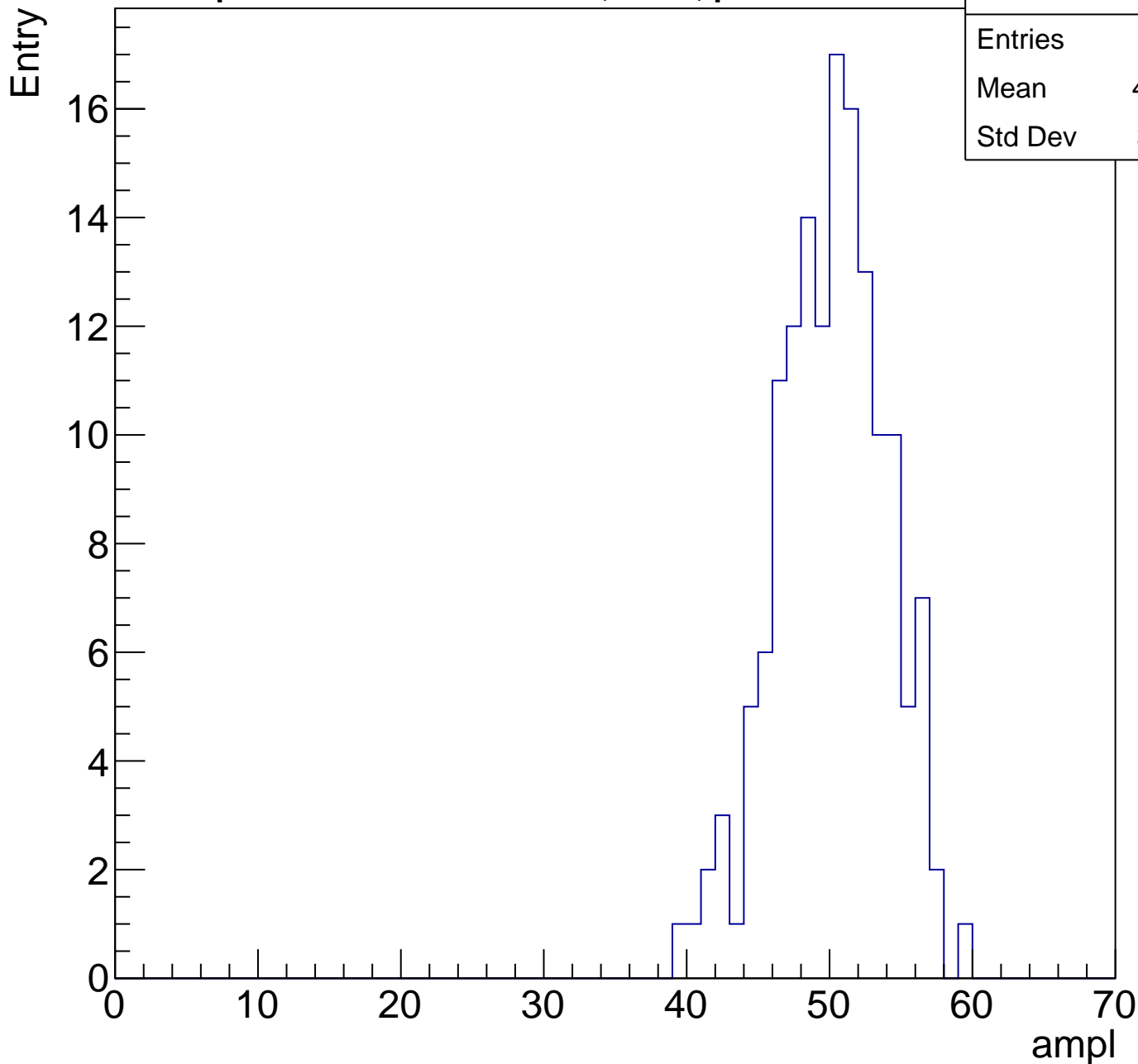
Entry



# B0L101S, U23-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

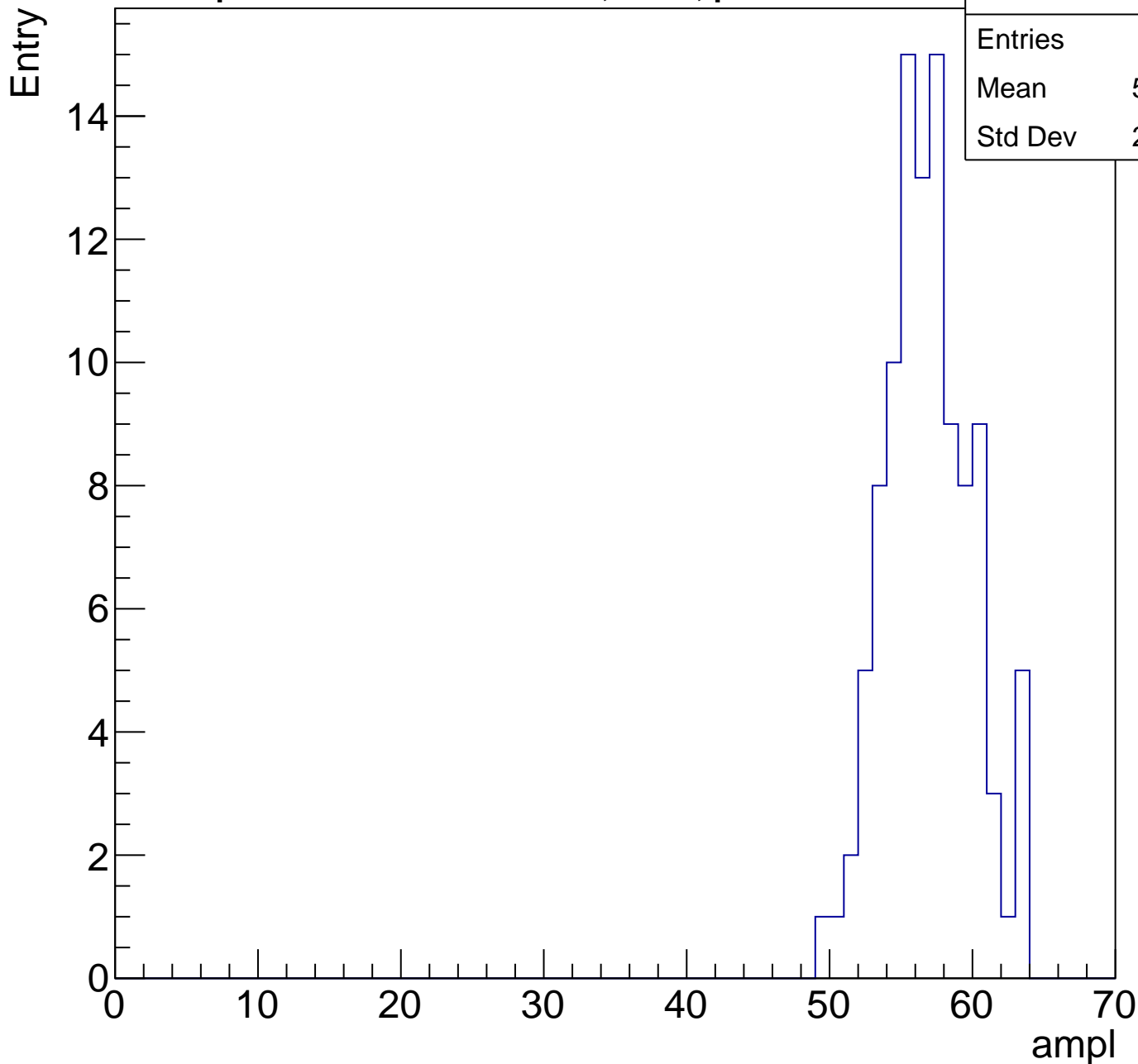
Entries	149
Mean	49.68
Std Dev	3.811



# B0L101S, U23-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	56.45
Std Dev	2.998



# B0L101S, U23-ch26, adc5

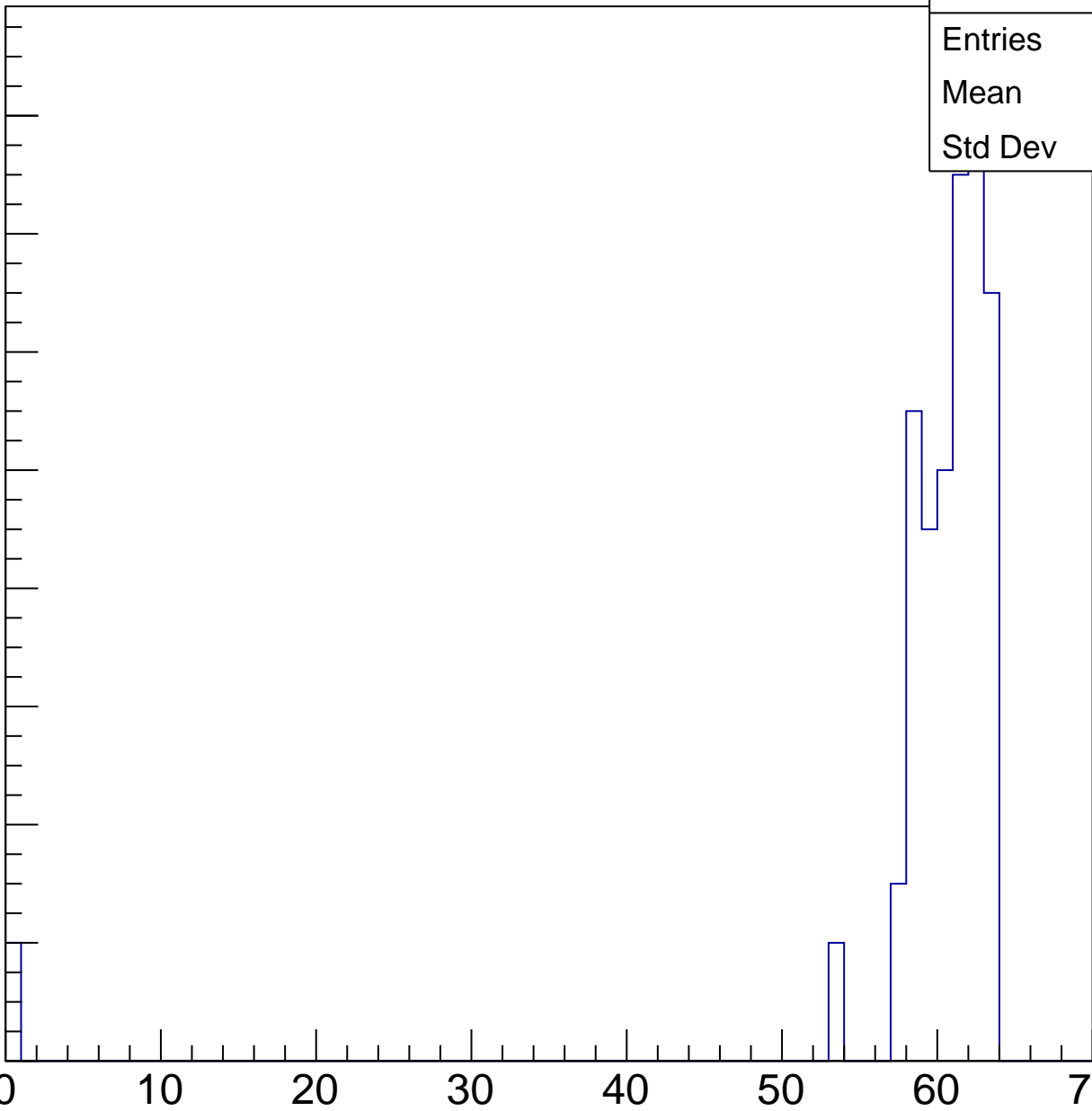
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	82
Mean	58.95
Std Dev	9.556

ampl



# B0L101S, U23-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.11
Std Dev	1.449



# B0L101S, U23-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	29.92
Std Dev	3.708

**Gaus mean : 30.4464**

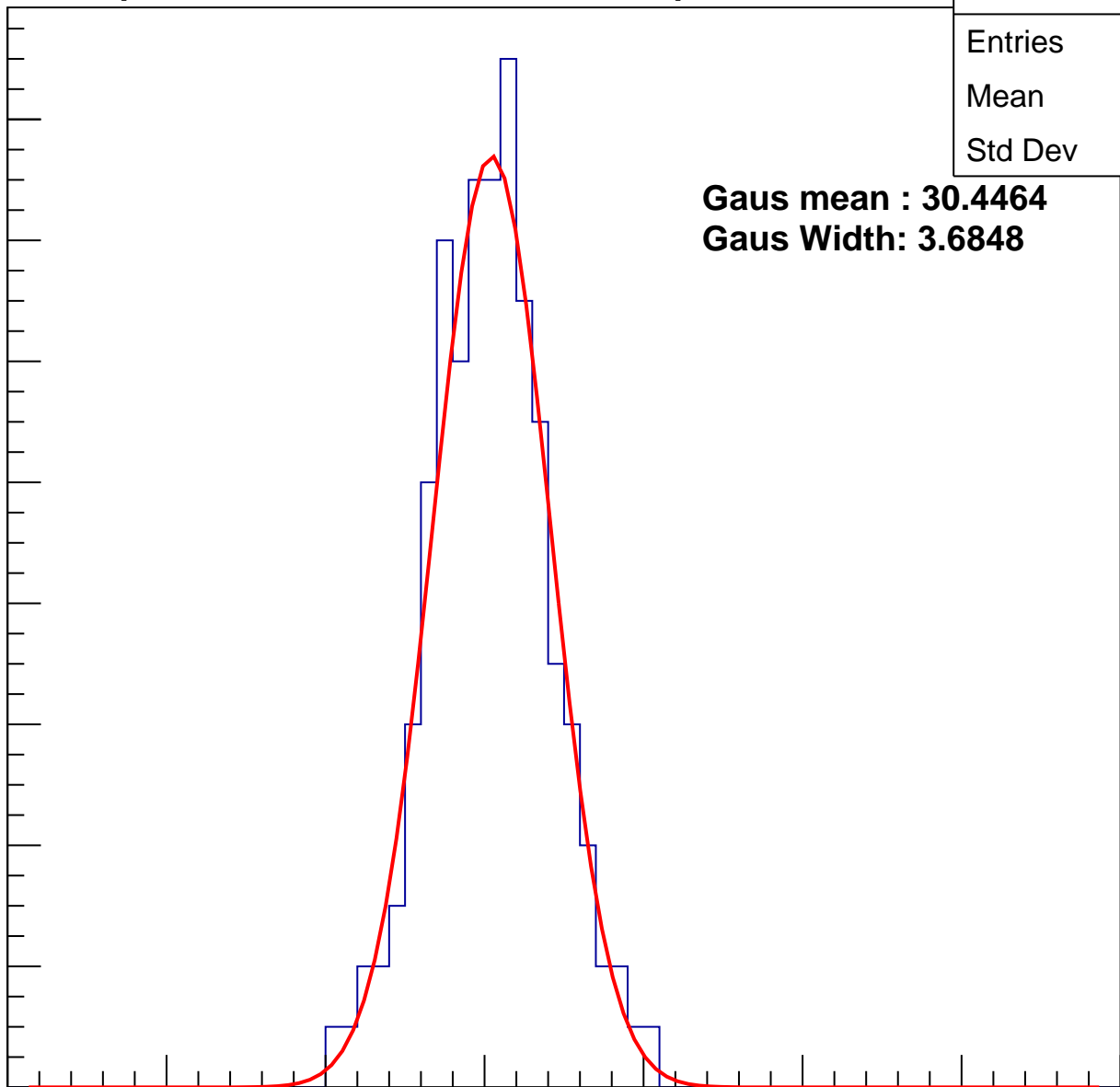
**Gaus Width: 3.6848**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

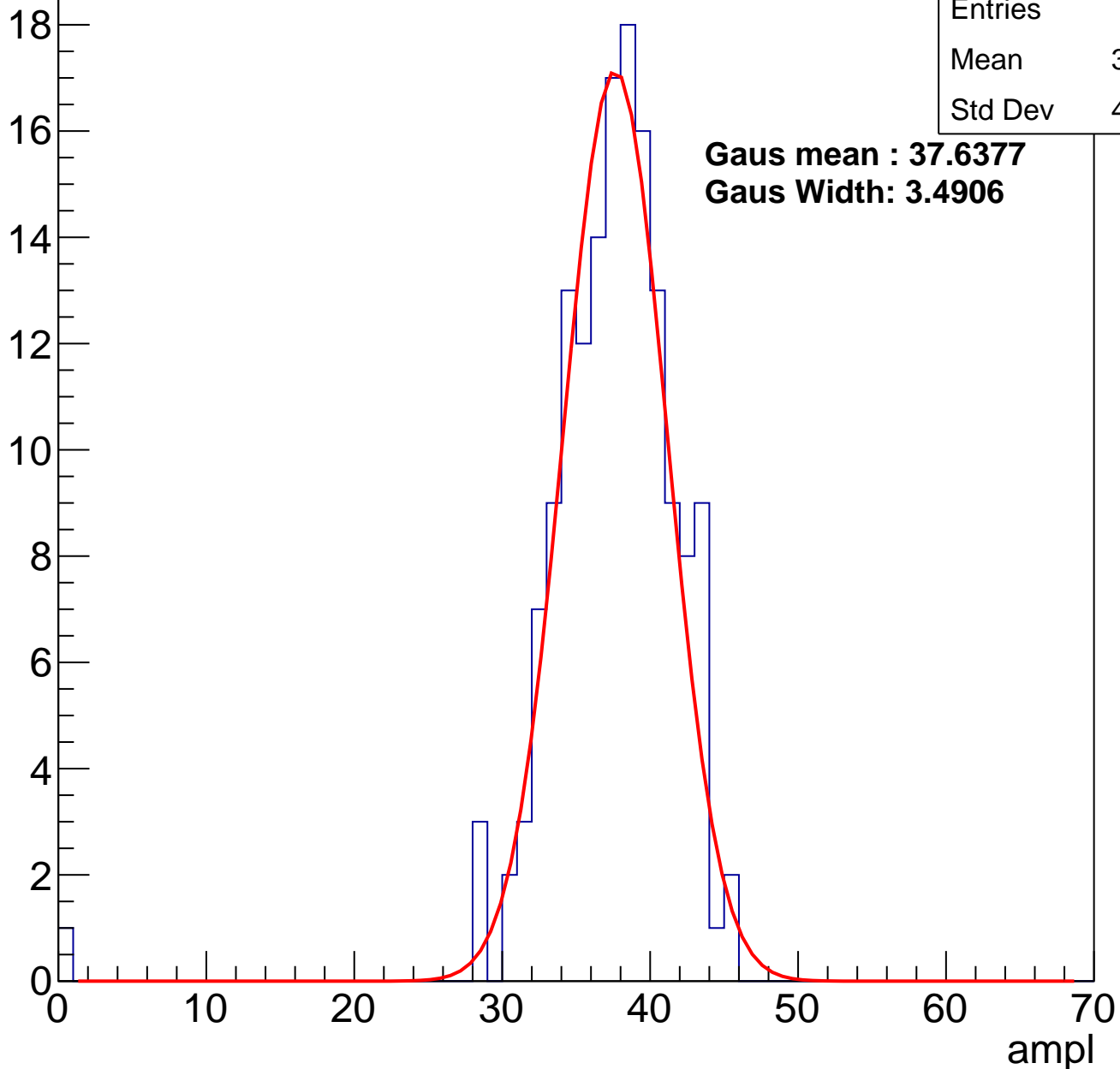
0 10 20 30 40 50 60 70



# B0L101S, U23-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

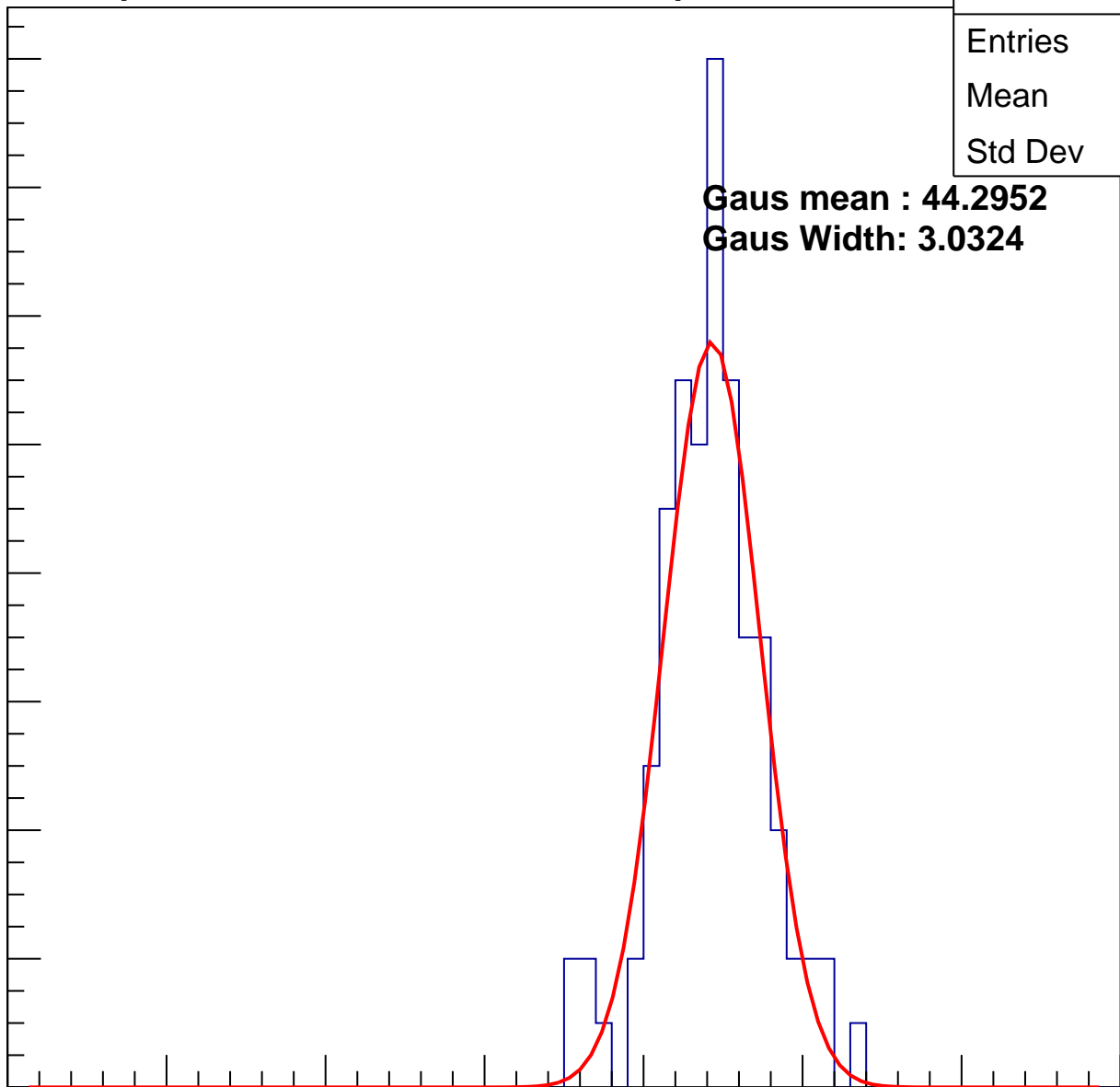
Entries	94
Mean	43.76
Std Dev	3.367

**Gaus mean : 44.2952**

**Gaus Width: 3.0324**

0 10 20 30 40 50 60 70

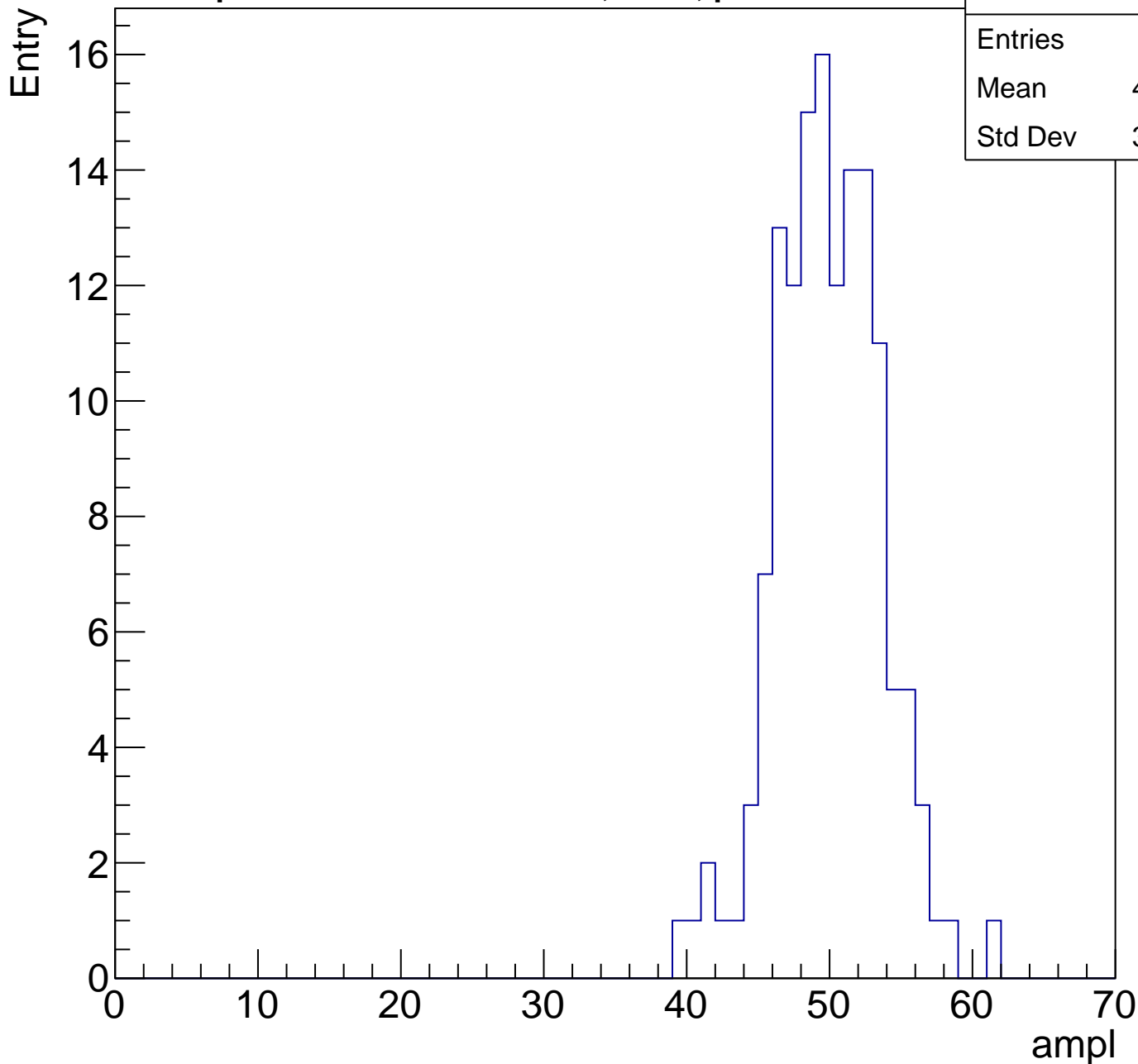
ampl



# B0L101S, U23-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	49.45
Std Dev	3.643



# B0L101S, U23-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

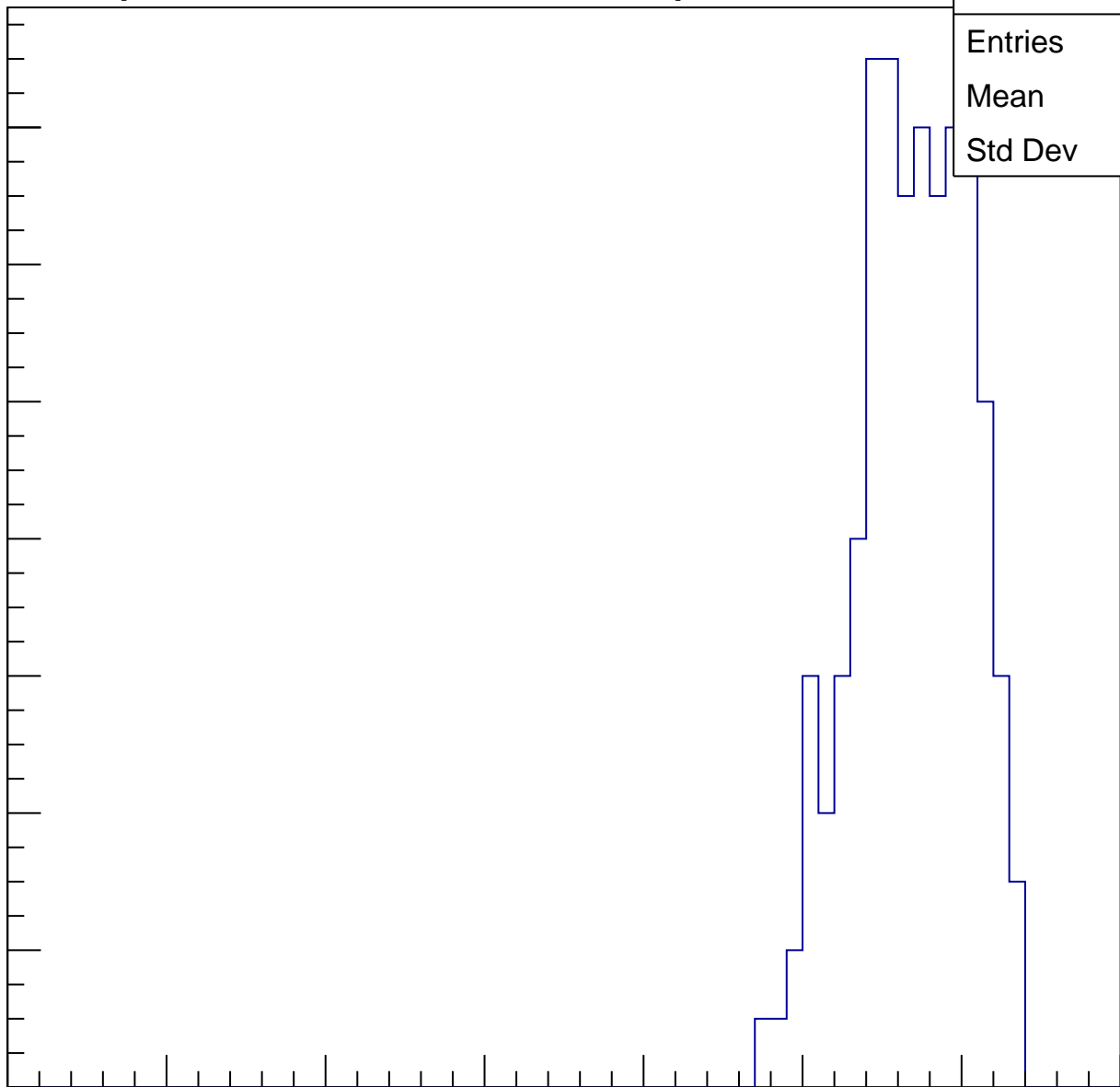
50

60

70

ampl

Entries	145
Mean	56.45
Std Dev	3.525



# B0L101S, U23-ch27, adc5

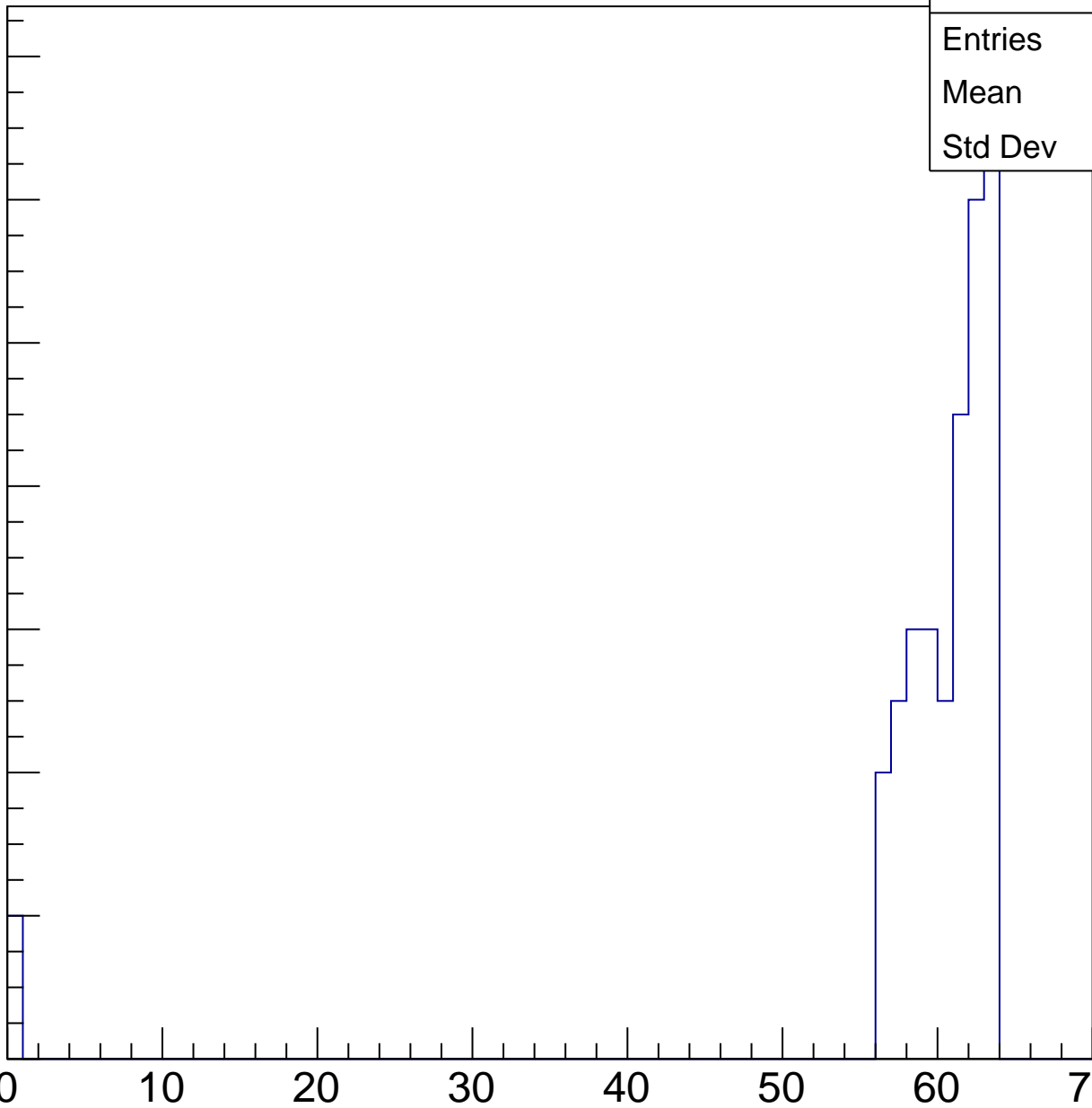
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	63
Mean	58.51
Std Dev	10.82

ampl



# B0L101S, U23-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L101S, U23-ch28, adc0

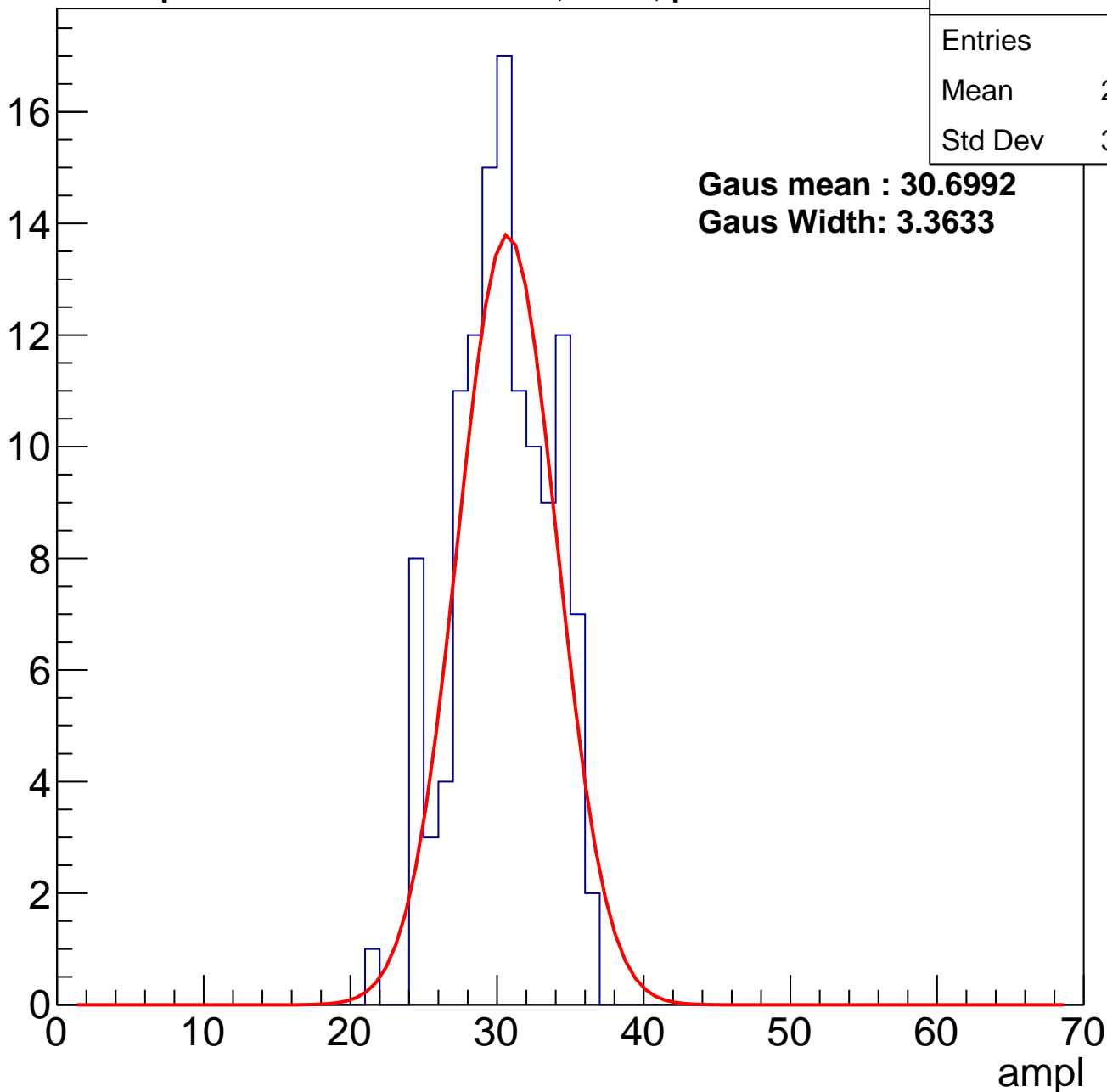
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	29.94
Std Dev	3.189

**Gaus mean : 30.6992**

**Gaus Width: 3.3633**

Entry



# B0L101S, U23-ch28, adc1

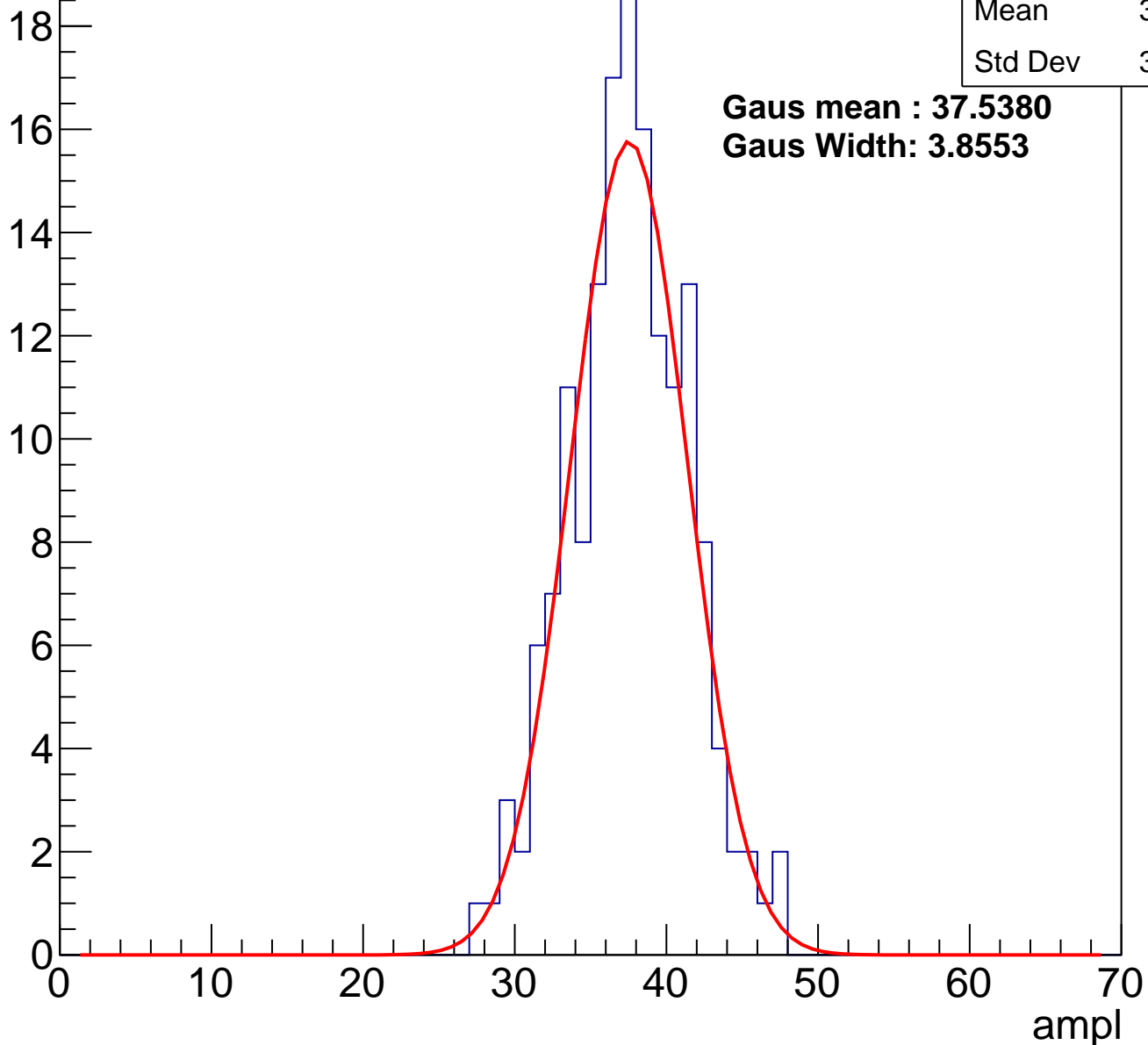
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	37.06
Std Dev	3.886

**Gaus mean : 37.5380**

**Gaus Width: 3.8553**

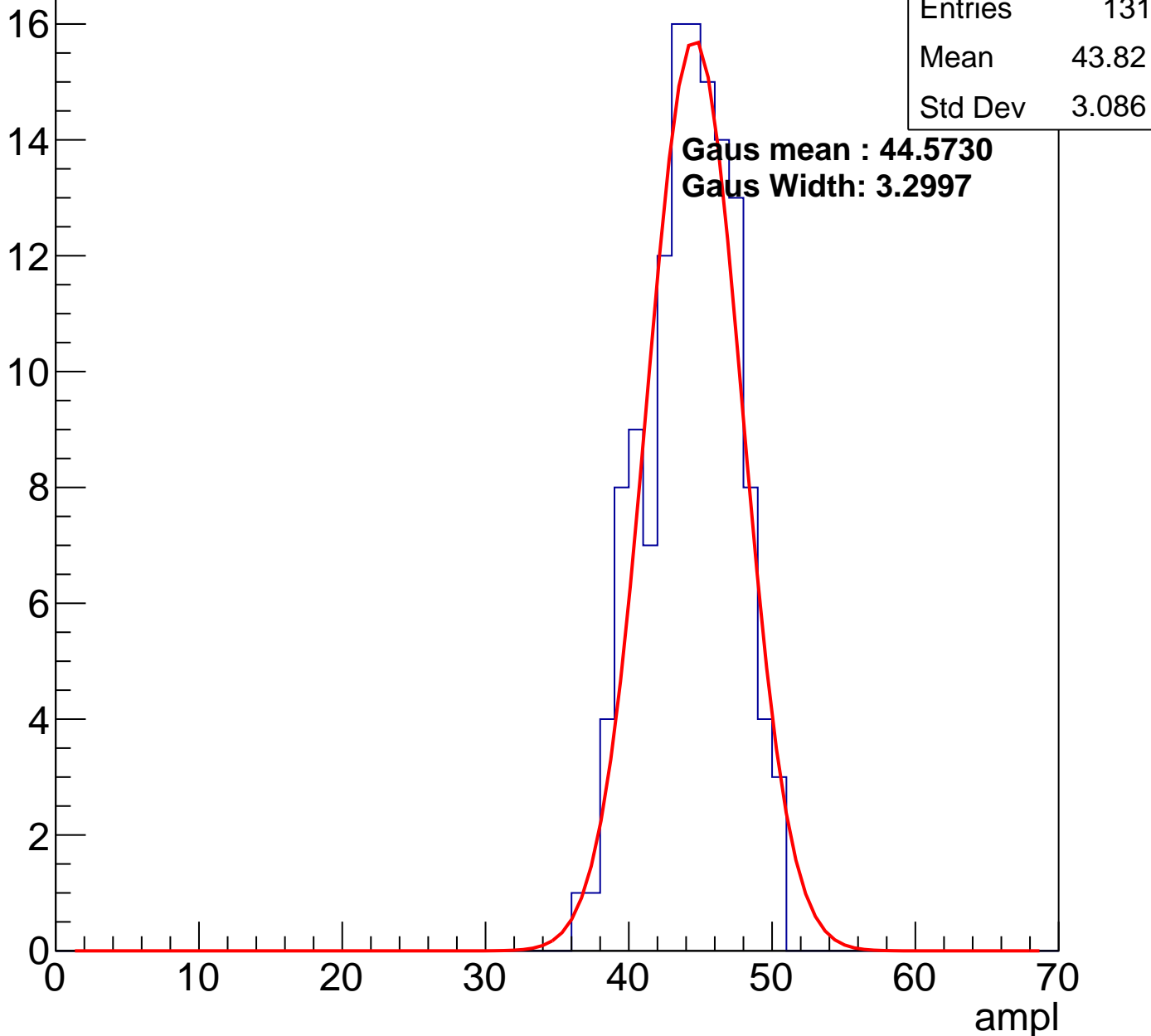
Entry



# B0L101S, U23-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

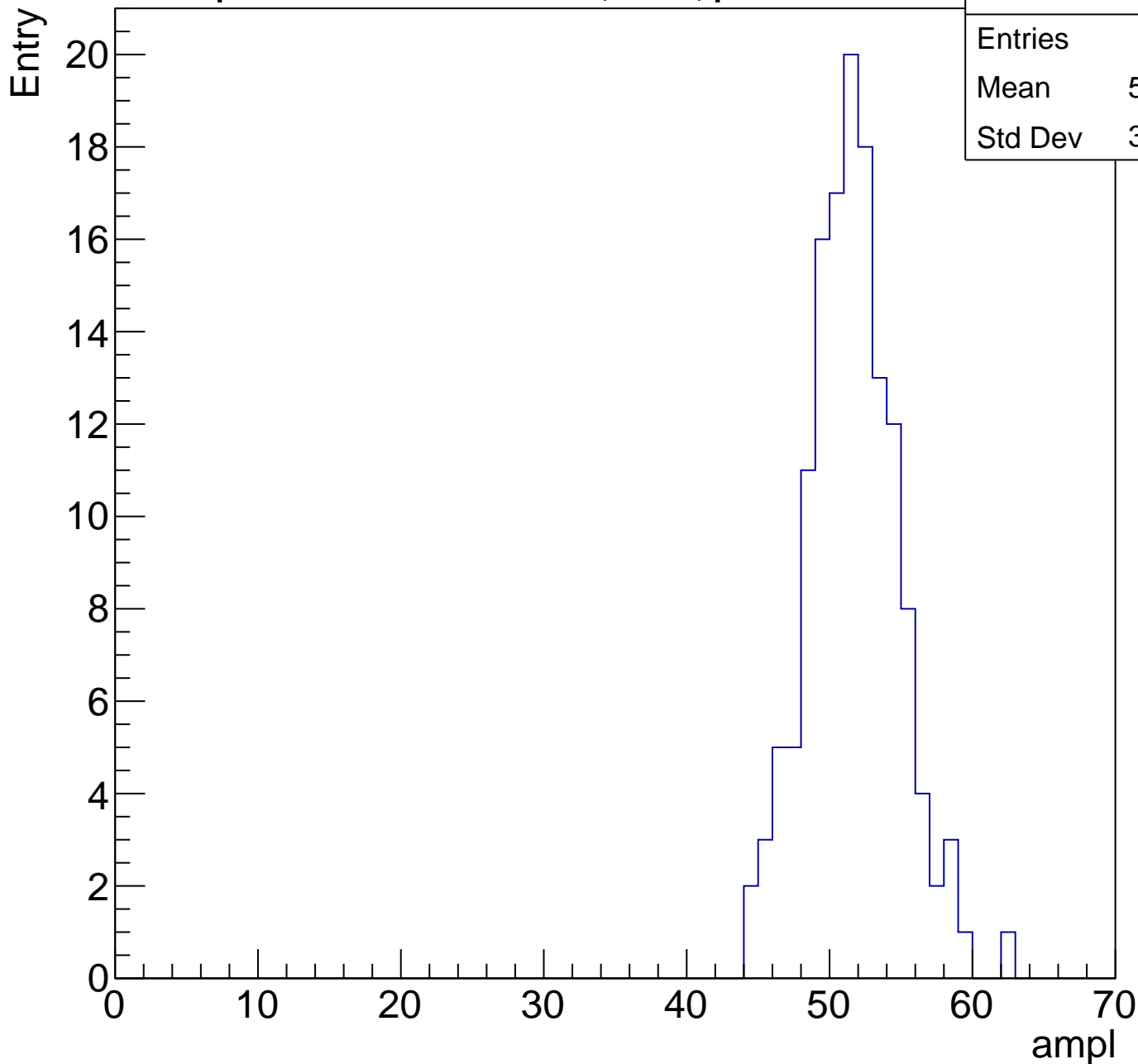
Entry



# B0L101S, U23-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	51.18
Std Dev	3.147



# B0L101S, U23-ch28, adc4

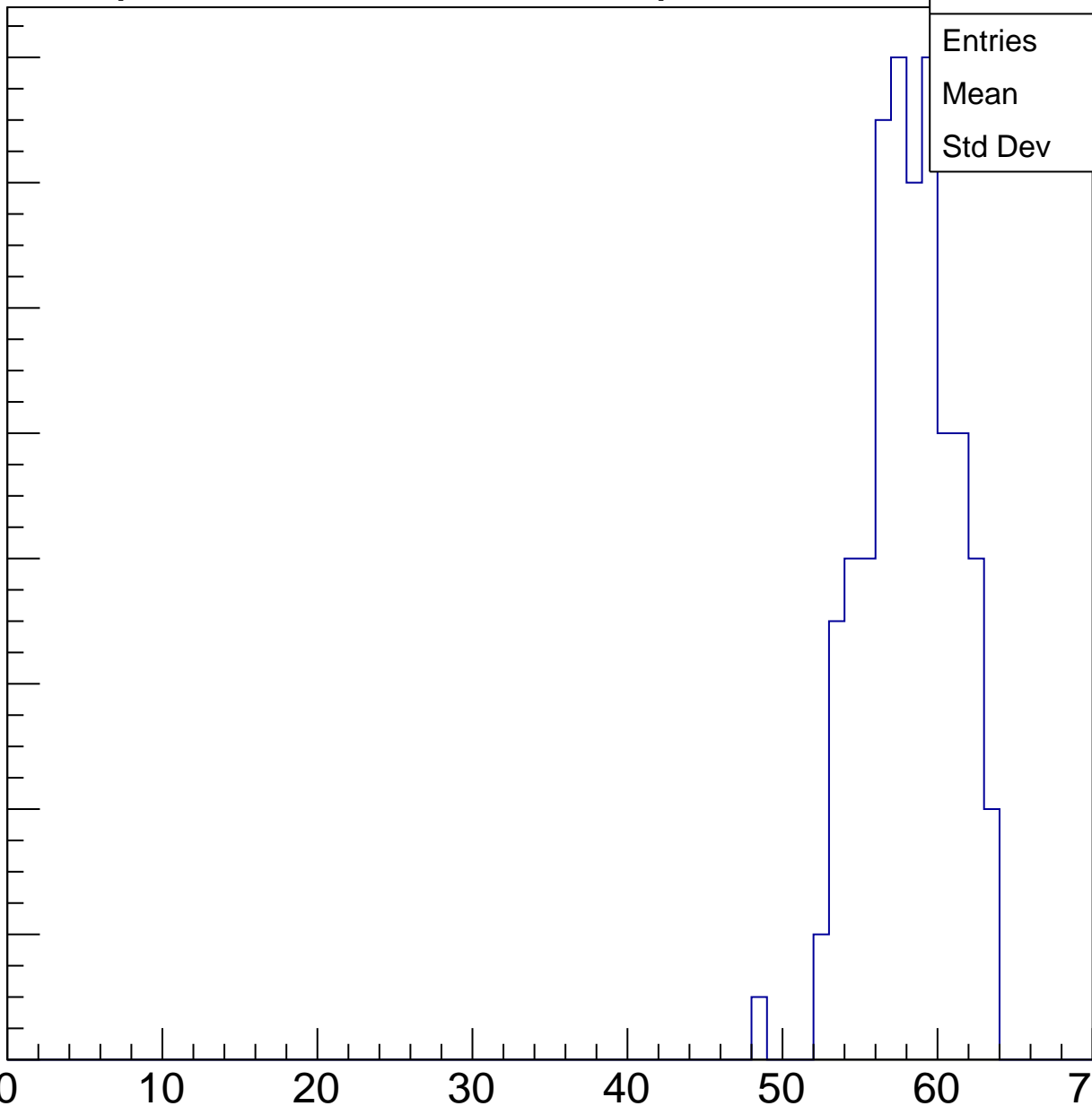
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	57.66
Std Dev	2.874

ampl



# B0L101S, U23-ch28, adc5

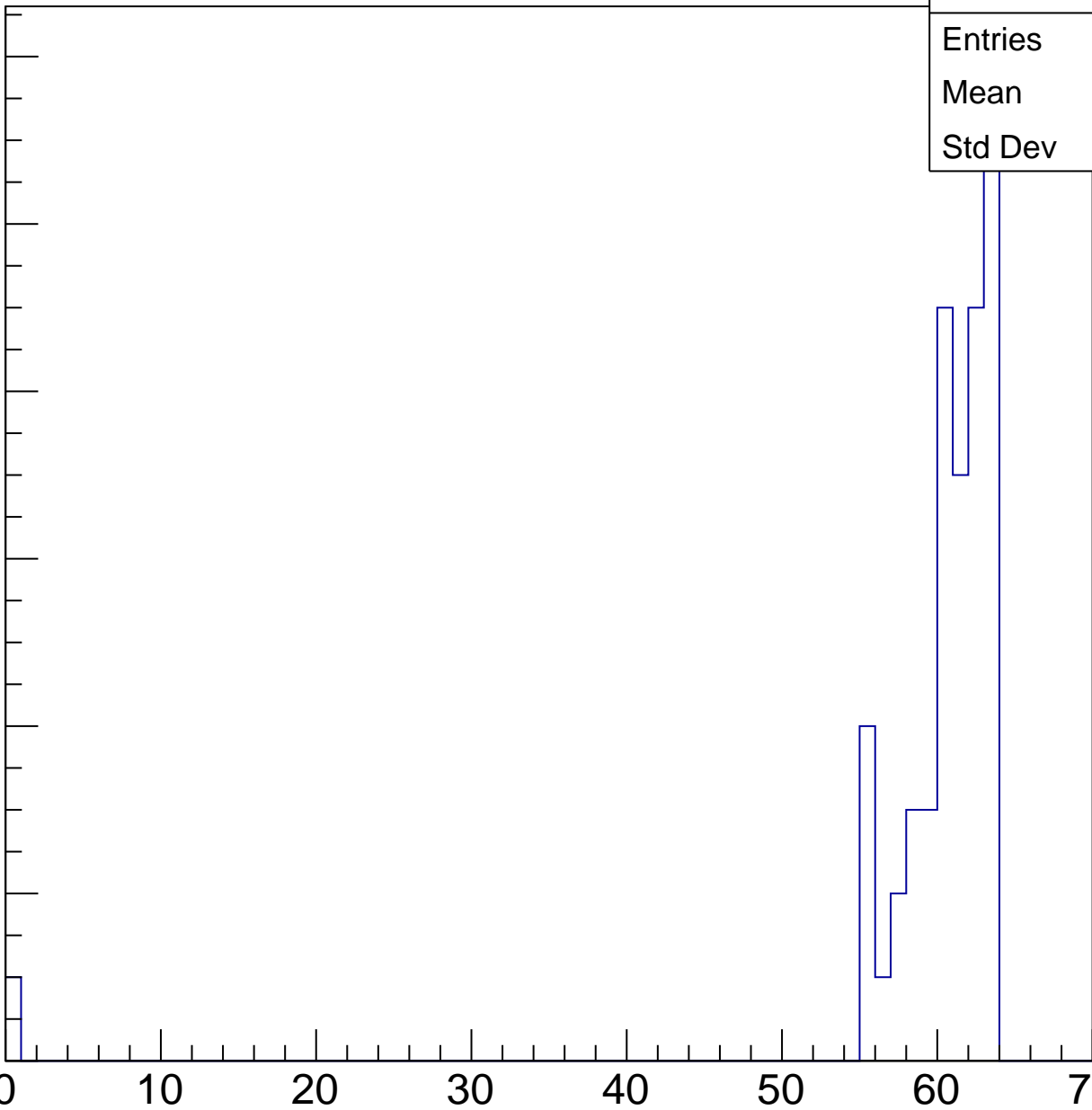
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	51
Mean	59.25
Std Dev	8.713

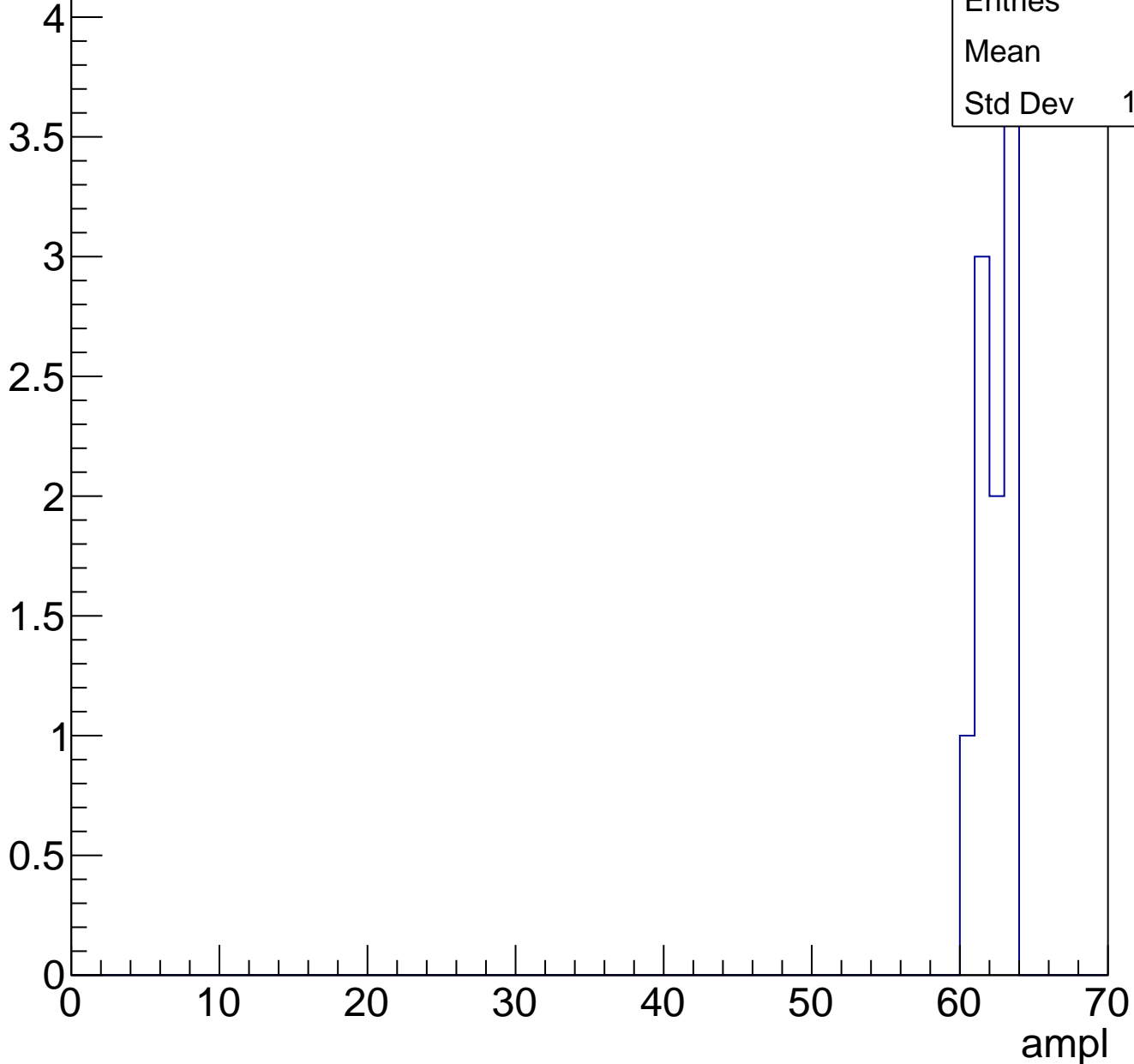
ampl



# B0L101S, U23-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L101S, U23-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	30.35
Std Dev	4.533

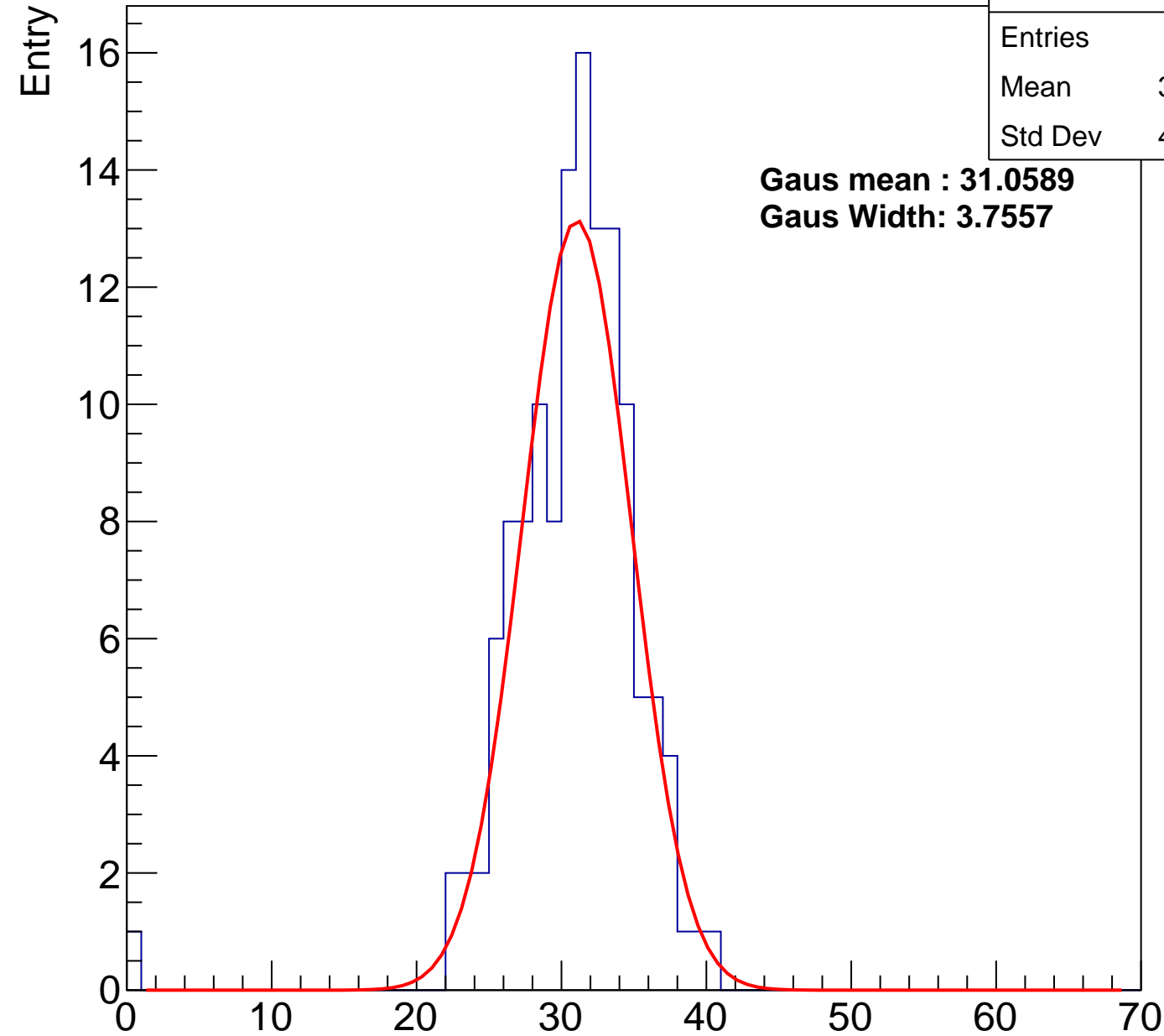
**Gaus mean : 31.0589**

**Gaus Width: 3.7557**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



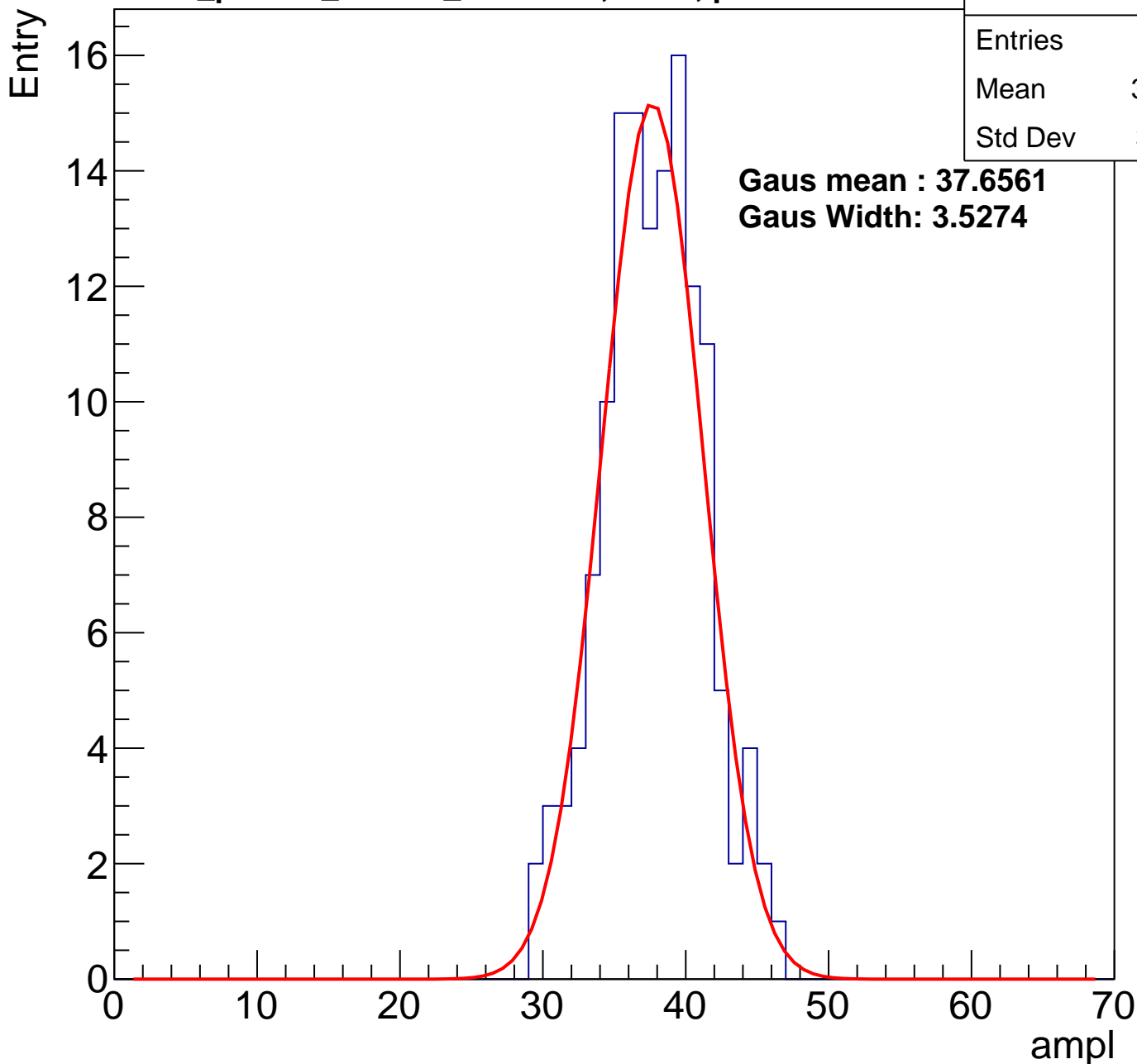
# B0L101S, U23-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	37.27
Std Dev	3.511

**Gaus mean : 37.6561**

**Gaus Width: 3.5274**



# B0L101S, U23-ch29, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	43.75
Std Dev	3.176

**Gaus mean : 44.1955**

**Gaus Width: 3.1562**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

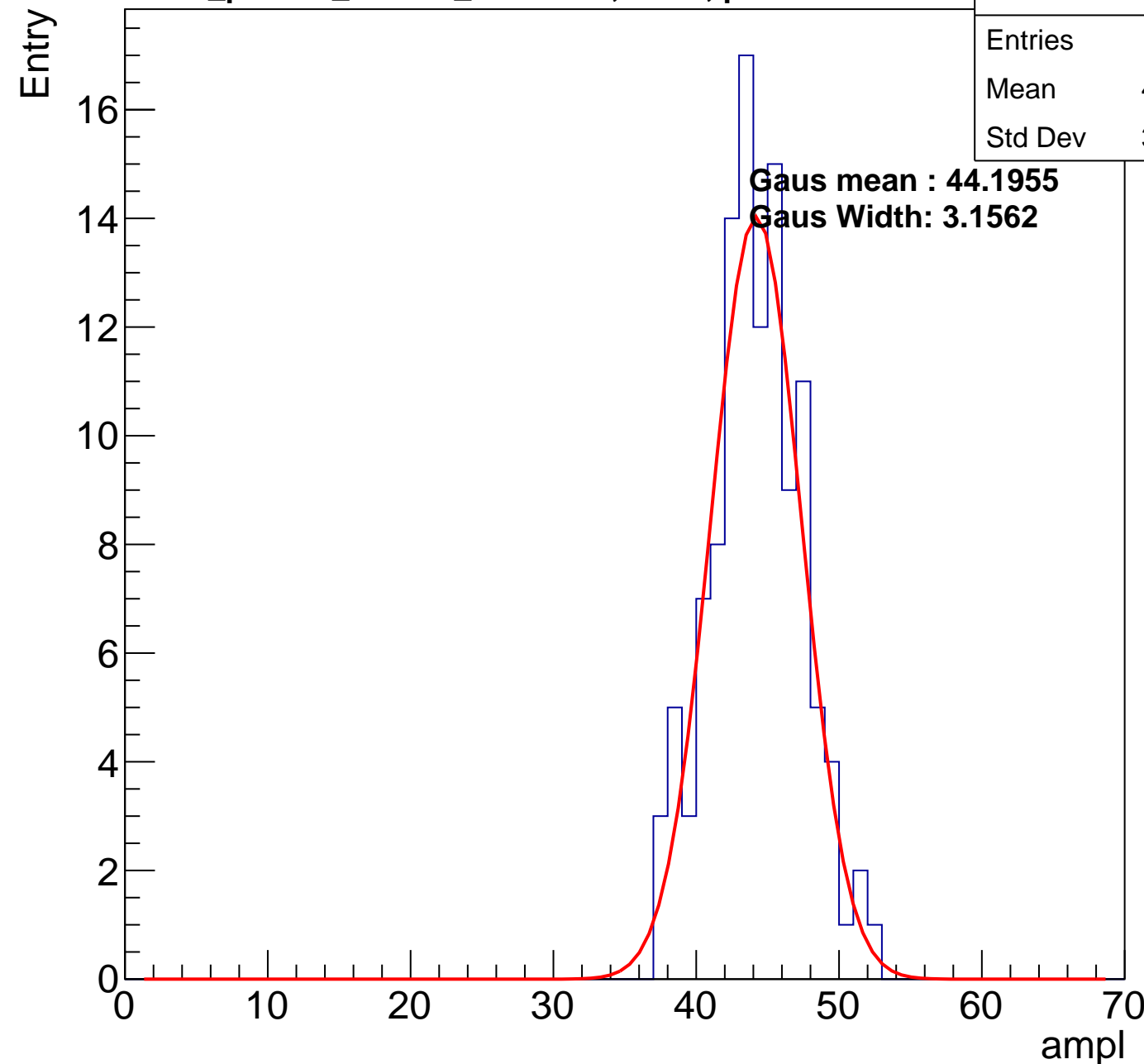
40

50

60

70

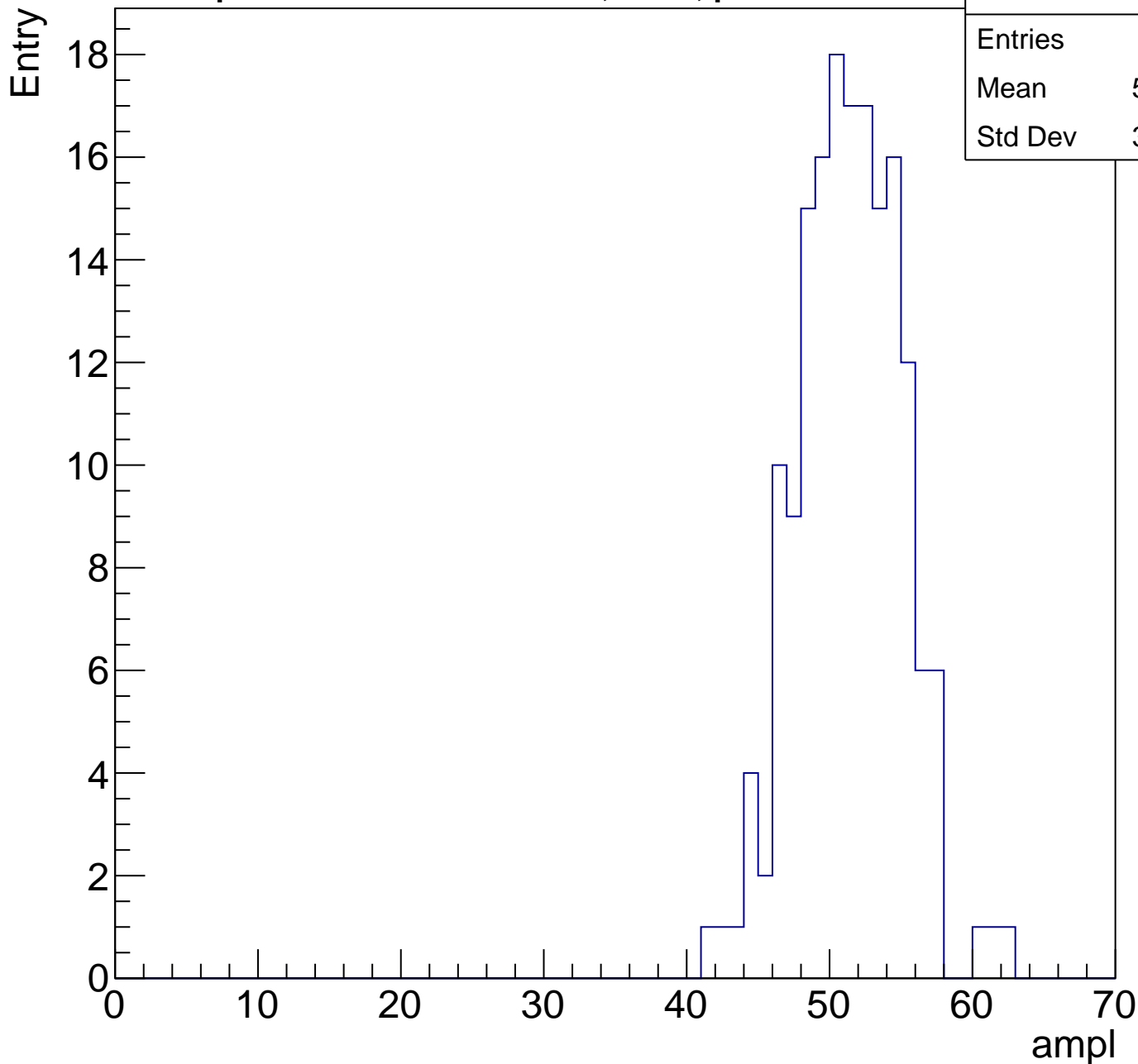
ampl



# B0L101S, U23-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	50.95
Std Dev	3.612



# B0L101S, U23-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	99
Mean	57.7
Std Dev	3.183

Entry

12

10

8

6

4

2

0

0

10

20

30

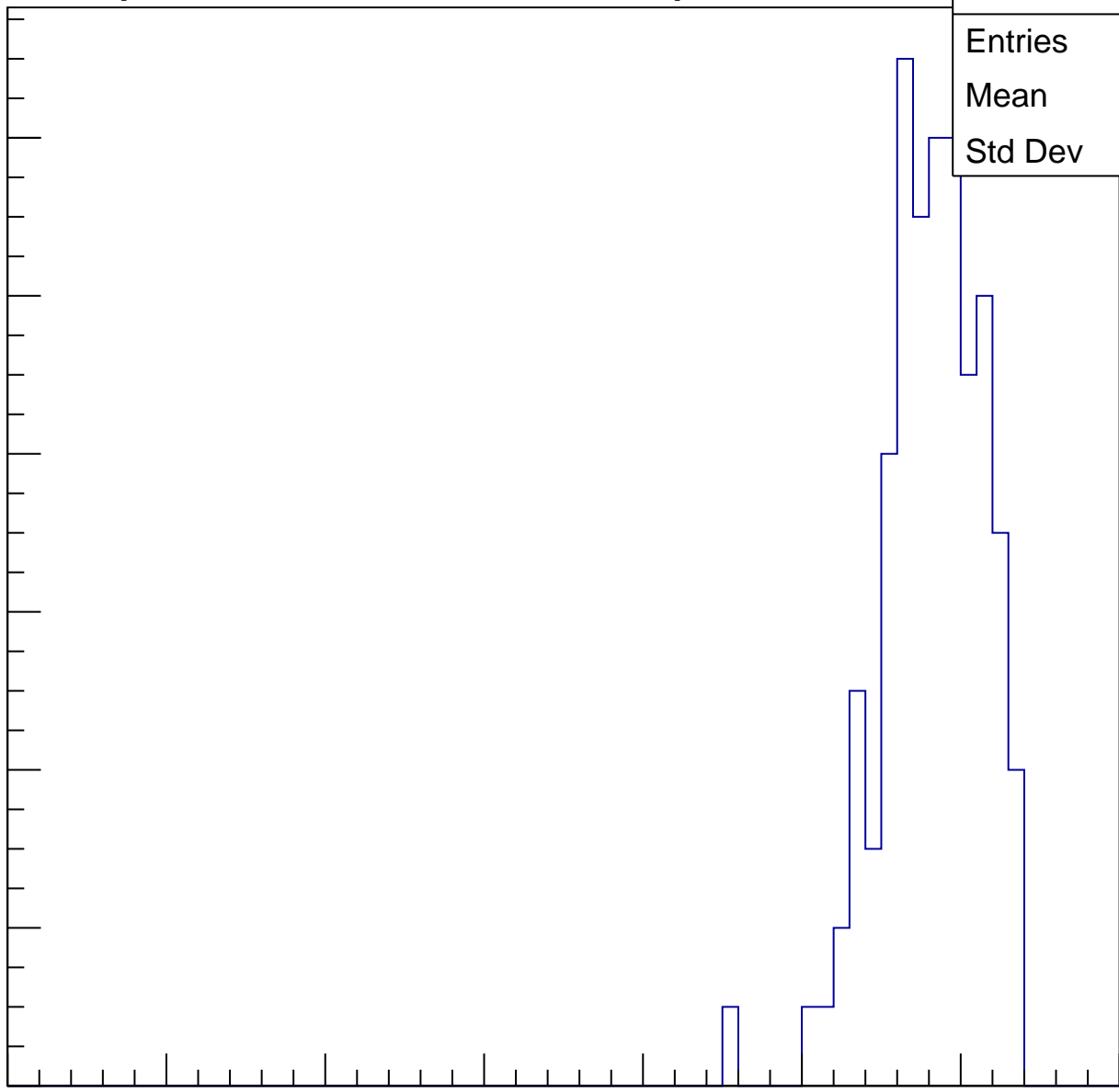
40

50

60

70

ampl



# B0L101S, U23-ch29, adc5

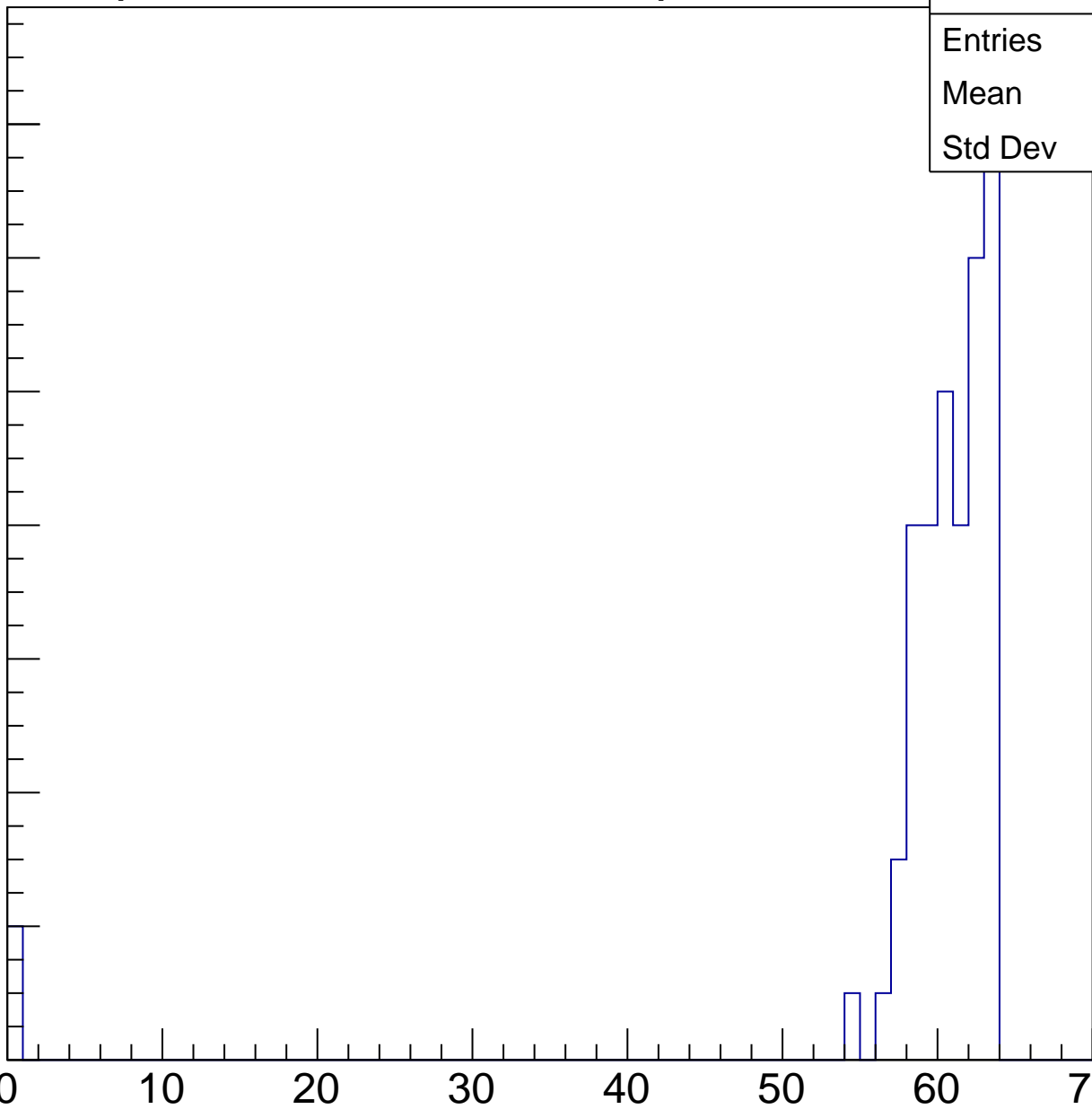
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	68
Mean	58.74
Std Dev	10.43

ampl



# B0L101S, U23-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch30, adc0

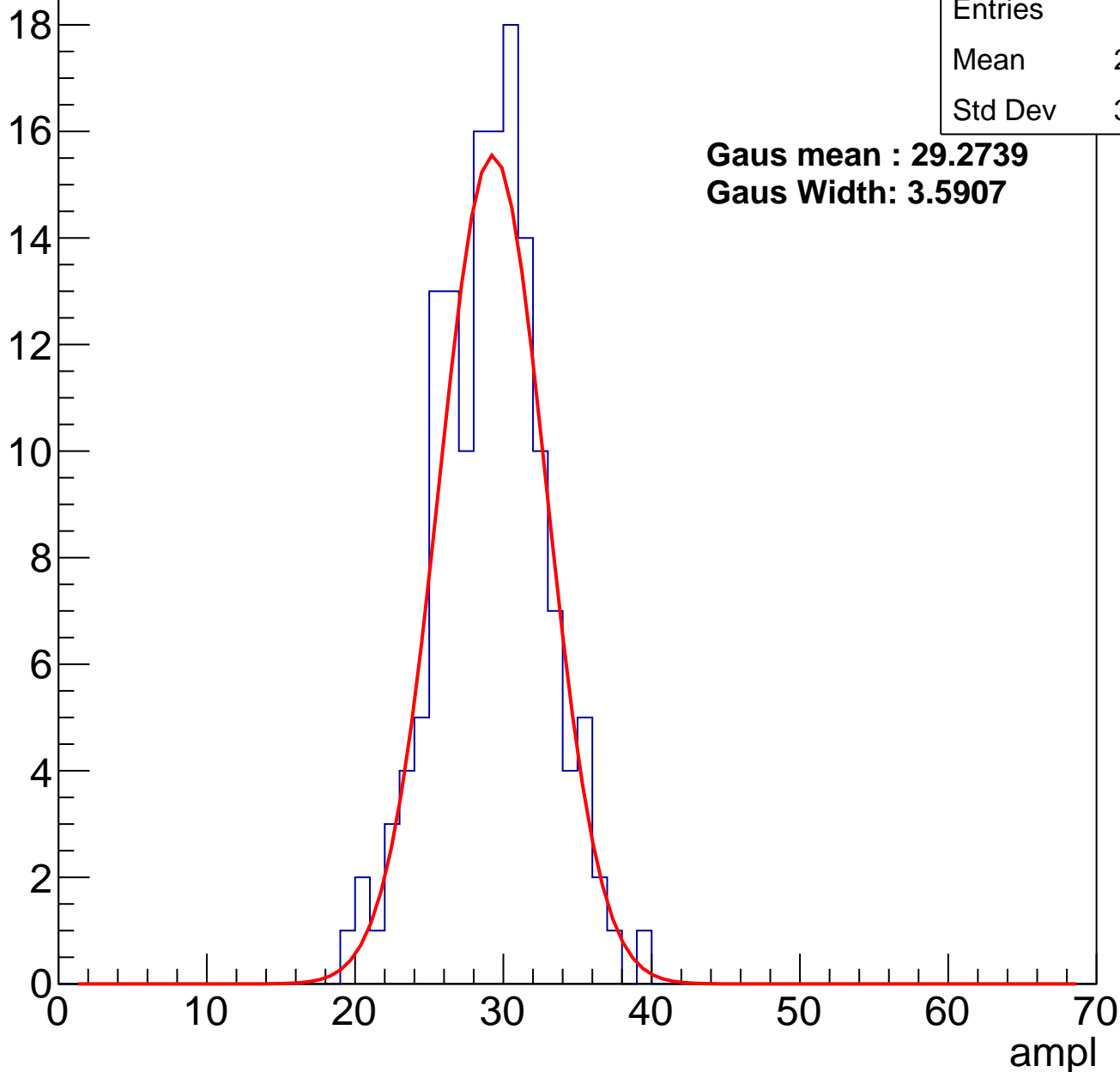
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	28.68
Std Dev	3.632

**Gaus mean : 29.2739**

**Gaus Width: 3.5907**

Entry



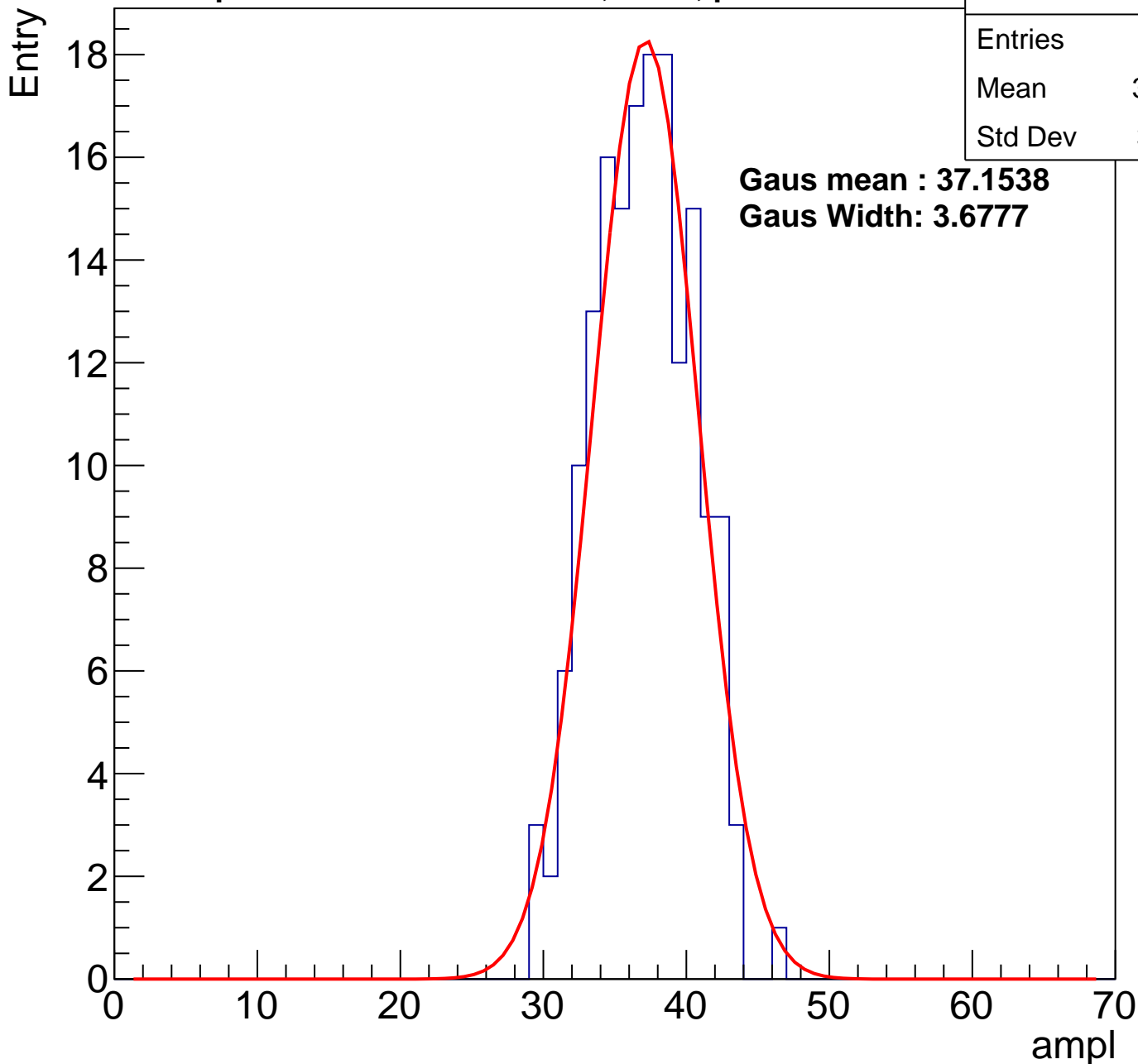
# B0L101S, U23-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	167
Mean	36.54
Std Dev	3.381

**Gaus mean : 37.1538**

**Gaus Width: 3.6777**



# B0L101S, U23-ch30, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

44.25

Std Dev

3.283

**Gaus mean : 44.5918**

**Gaus Width: 3.1946**

0

10

20

30

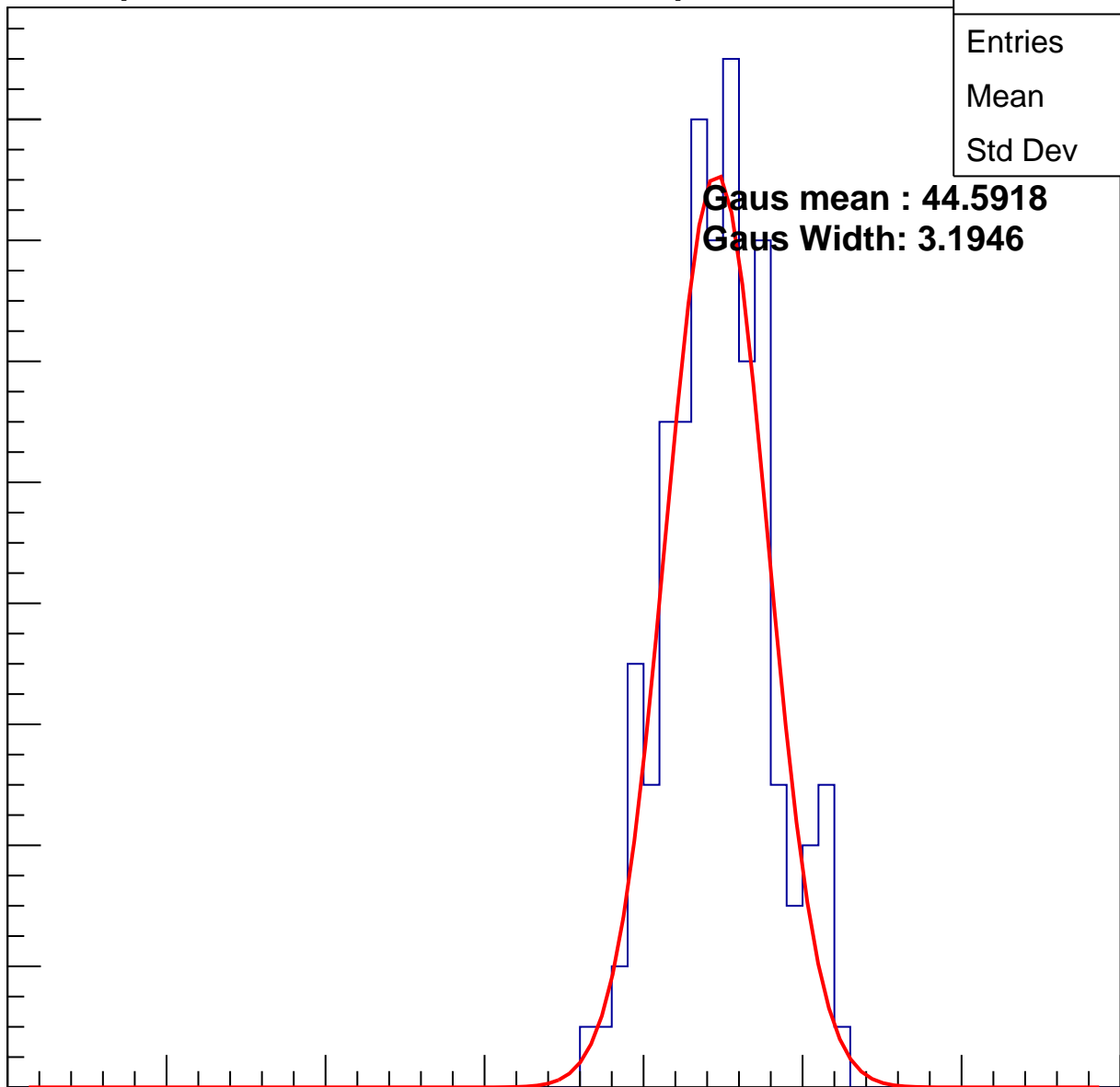
40

50

60

70

ampl



# B0L101S, U23-ch30, adc3

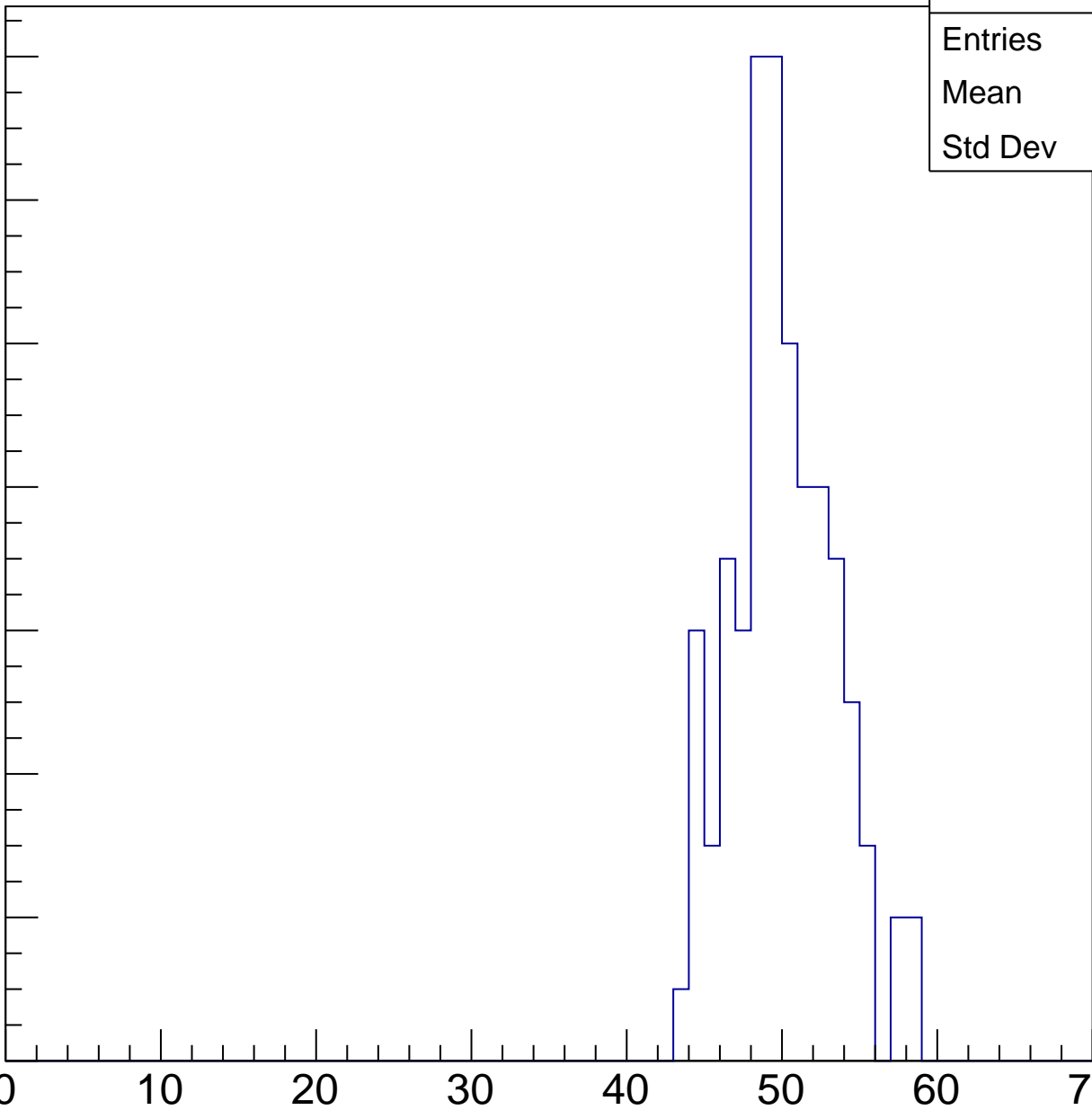
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	96
Mean	49.62
Std Dev	3.302

ampl



# B0L101S, U23-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

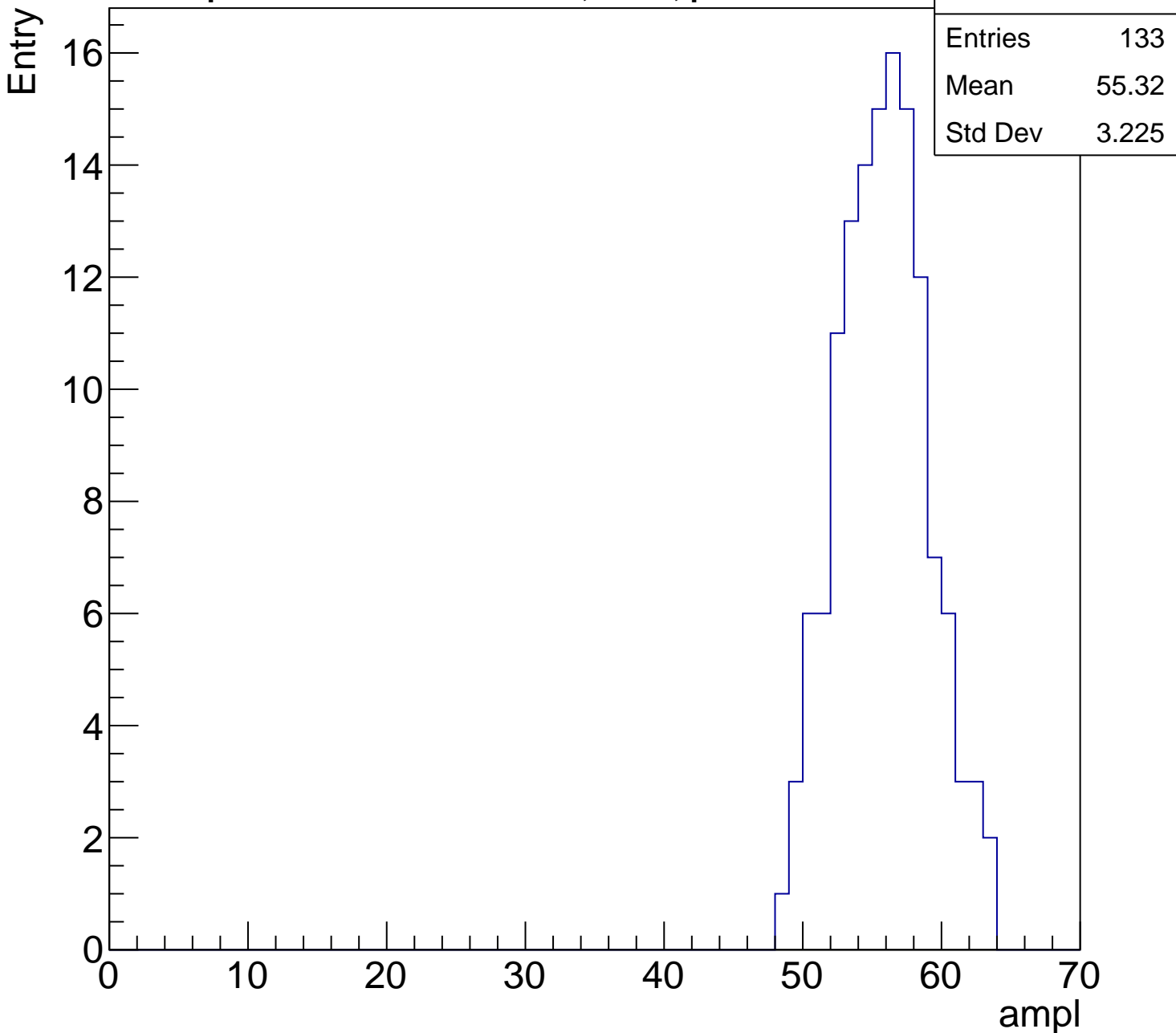
Entries	133
Mean	55.32
Std Dev	3.225

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch30, adc5

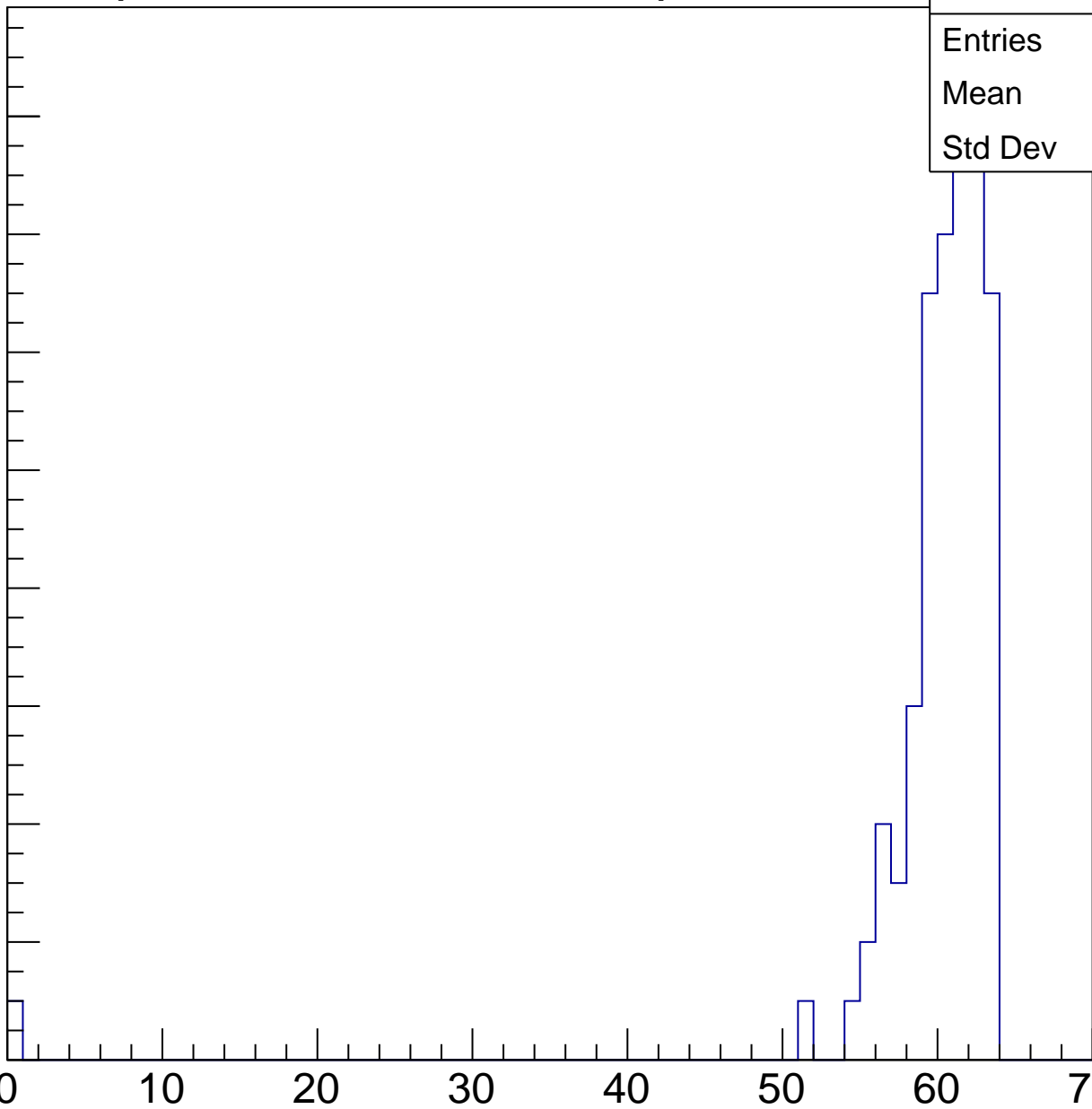
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	91
Mean	59.49
Std Dev	6.69

ampl

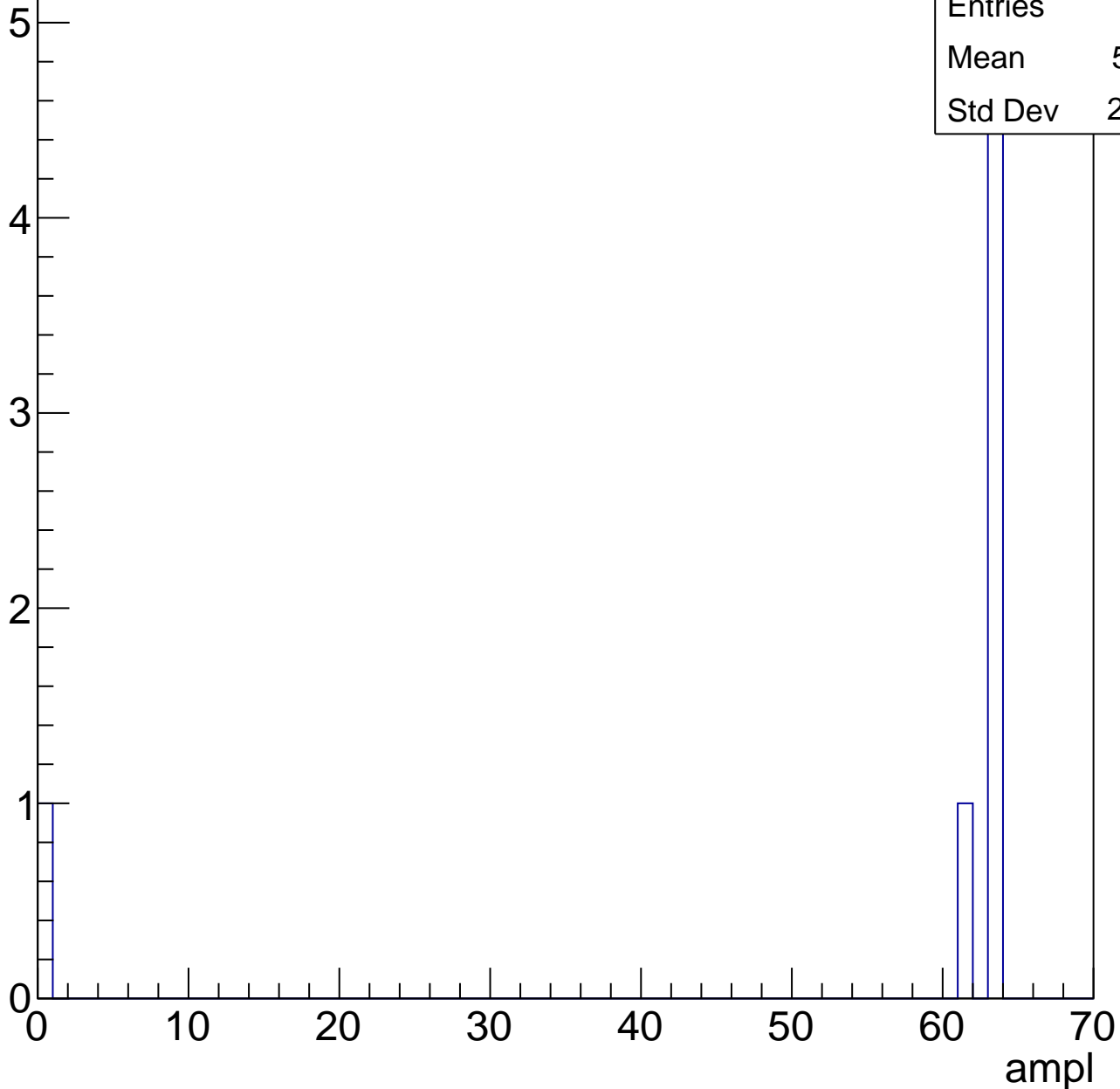


# B0L101S, U23-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	7
Mean	53.71
Std Dev	21.94





# B0L101S, U23-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch31, adc0

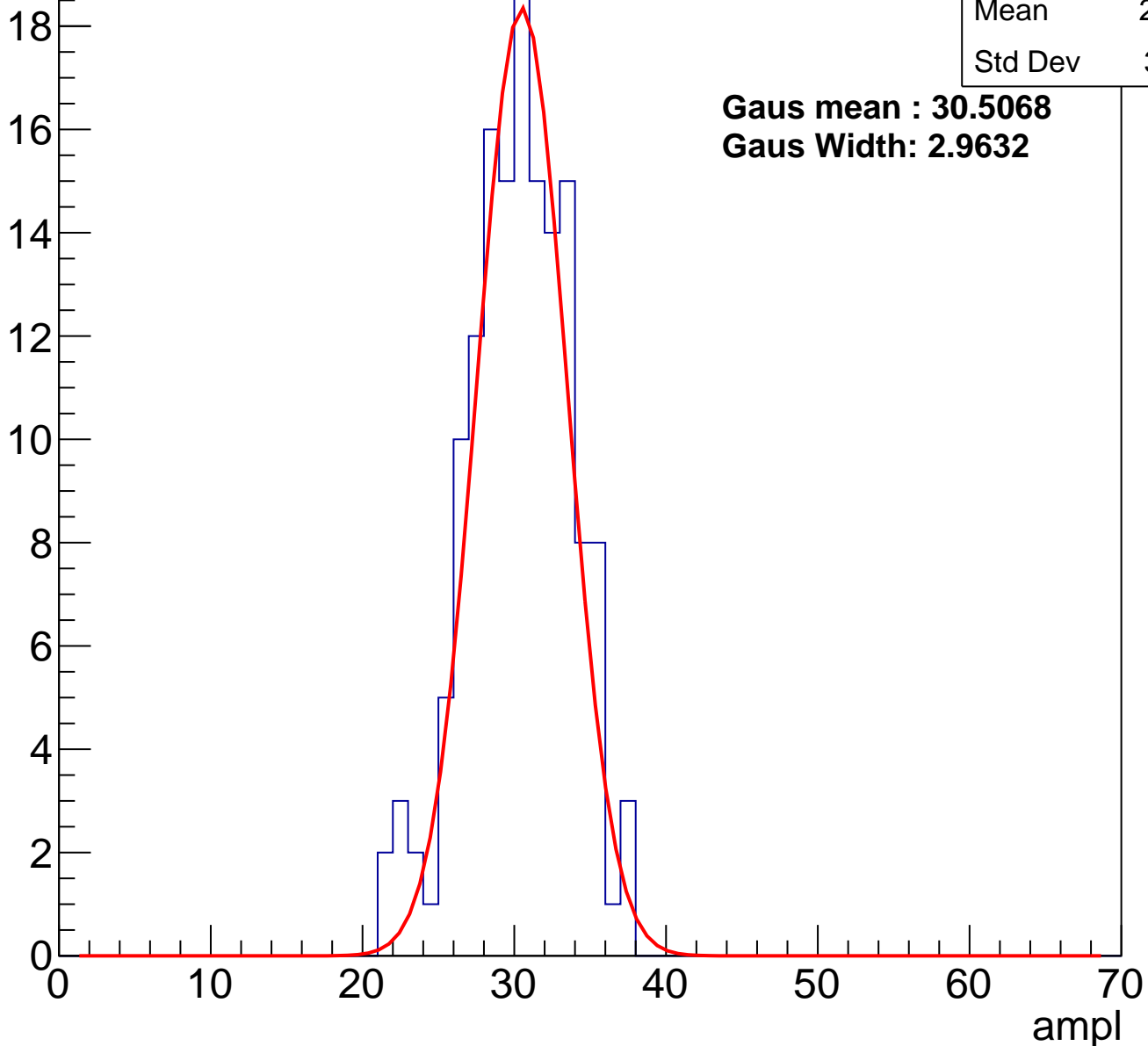
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	29.85
Std Dev	3.351

**Gaus mean : 30.5068**

**Gaus Width: 2.9632**

Entry



# B0L101S, U23-ch31, adc1

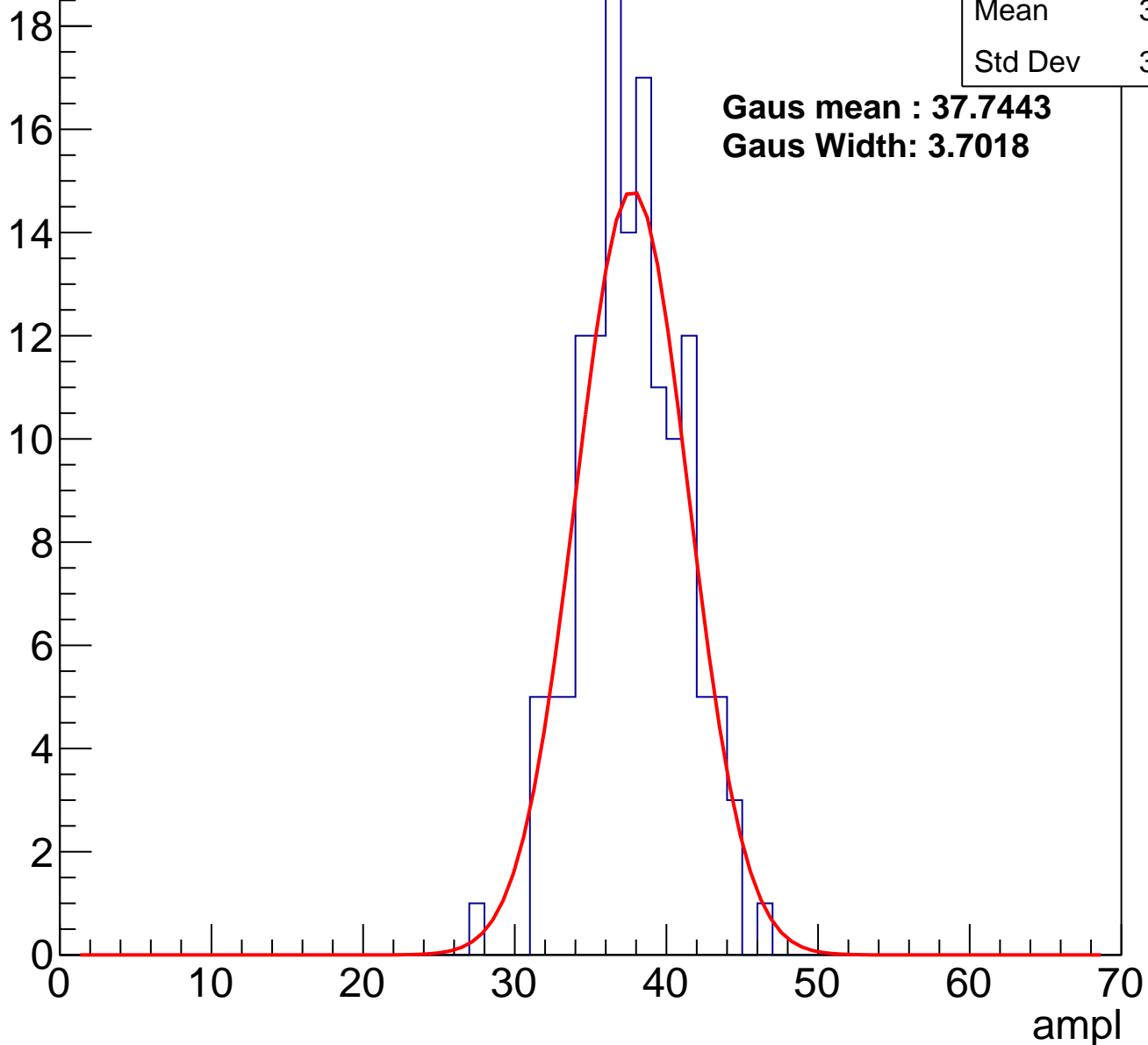
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	37.28
Std Dev	3.337

**Gaus mean : 37.7443**

**Gaus Width: 3.7018**

Entry



# B0L101S, U23-ch31, adc2

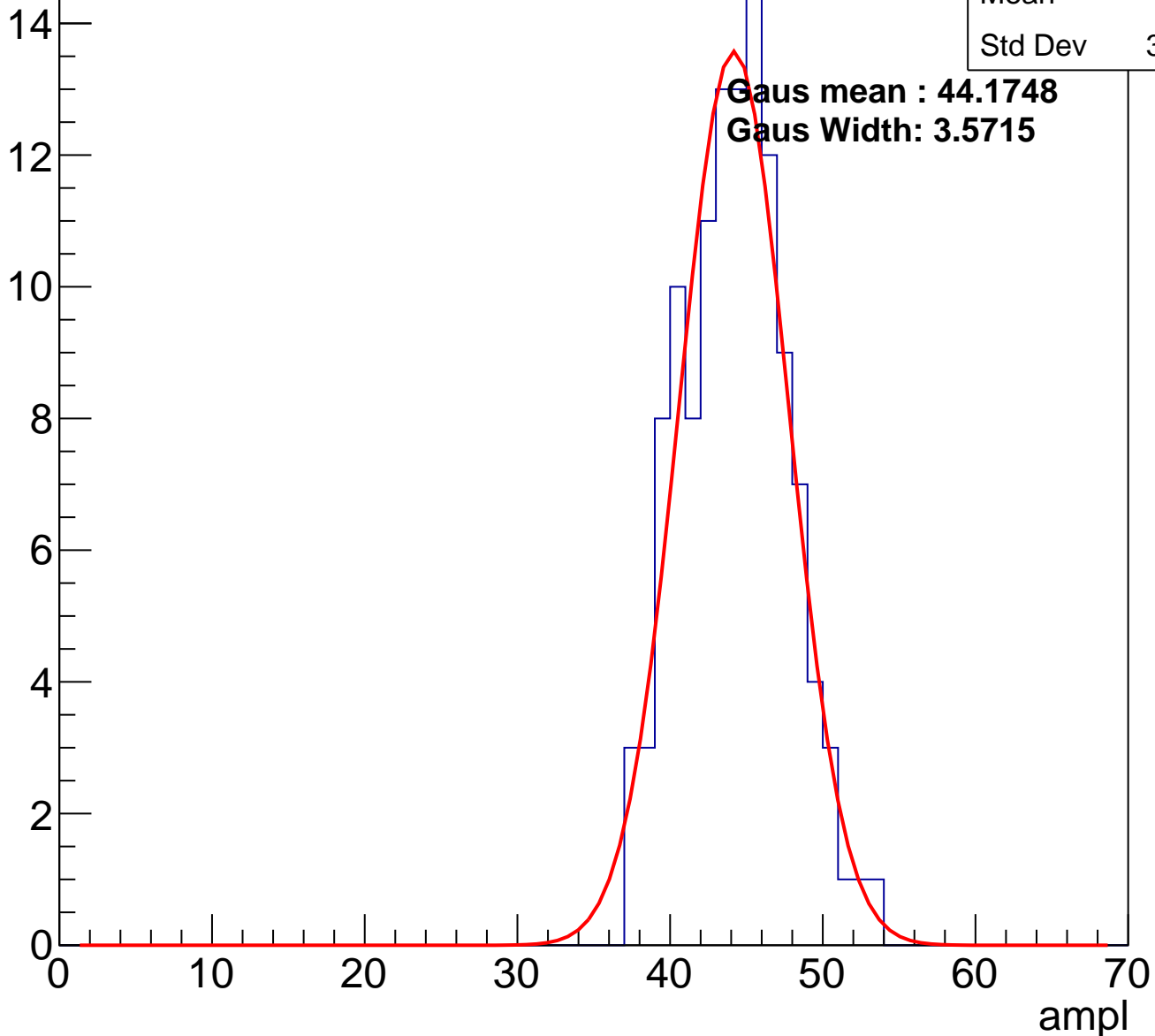
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	43.82
Std Dev	3.385

**Gaus mean : 44.1748**

**Gaus Width: 3.5715**

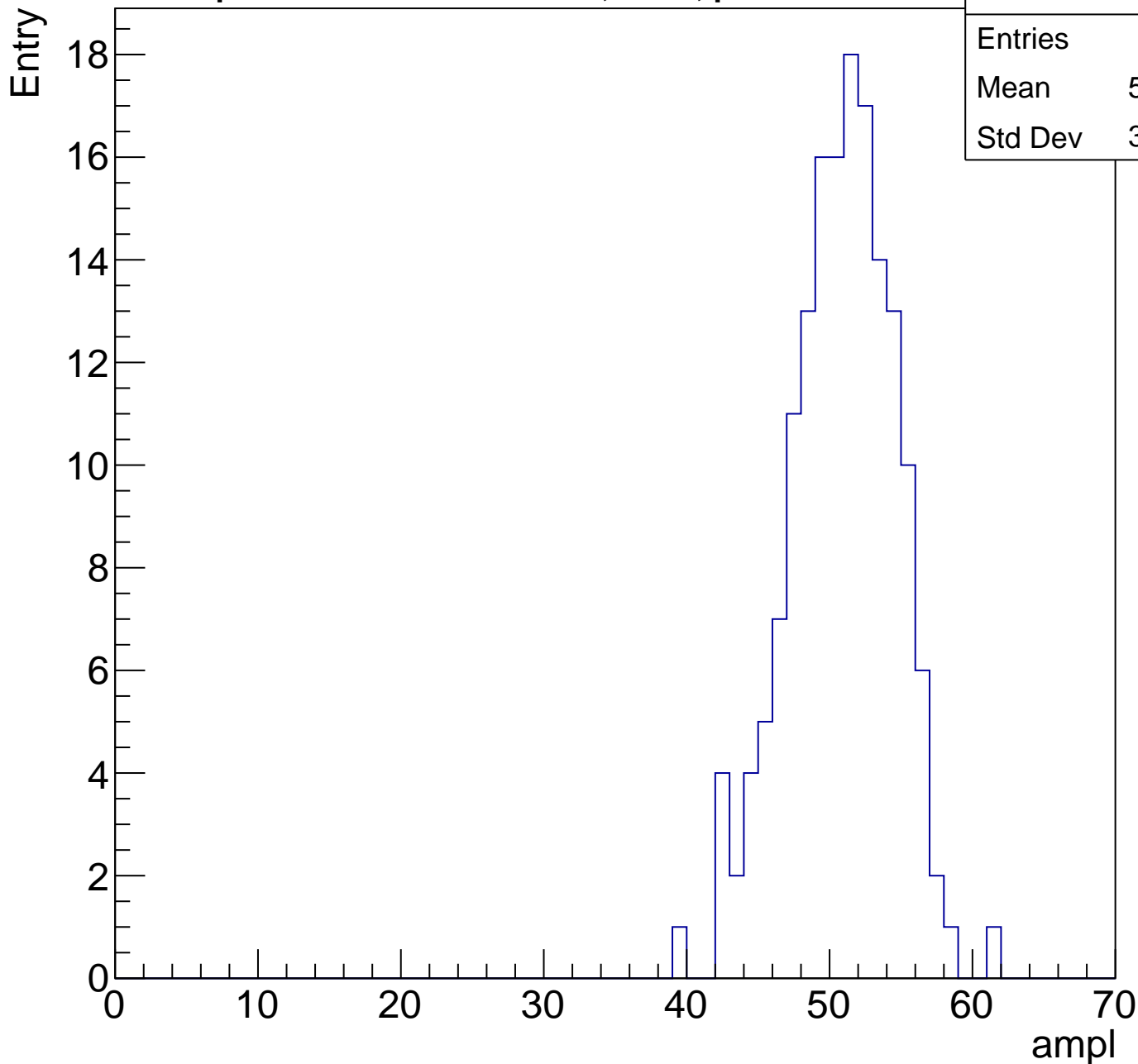
Entry



# B0L101S, U23-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

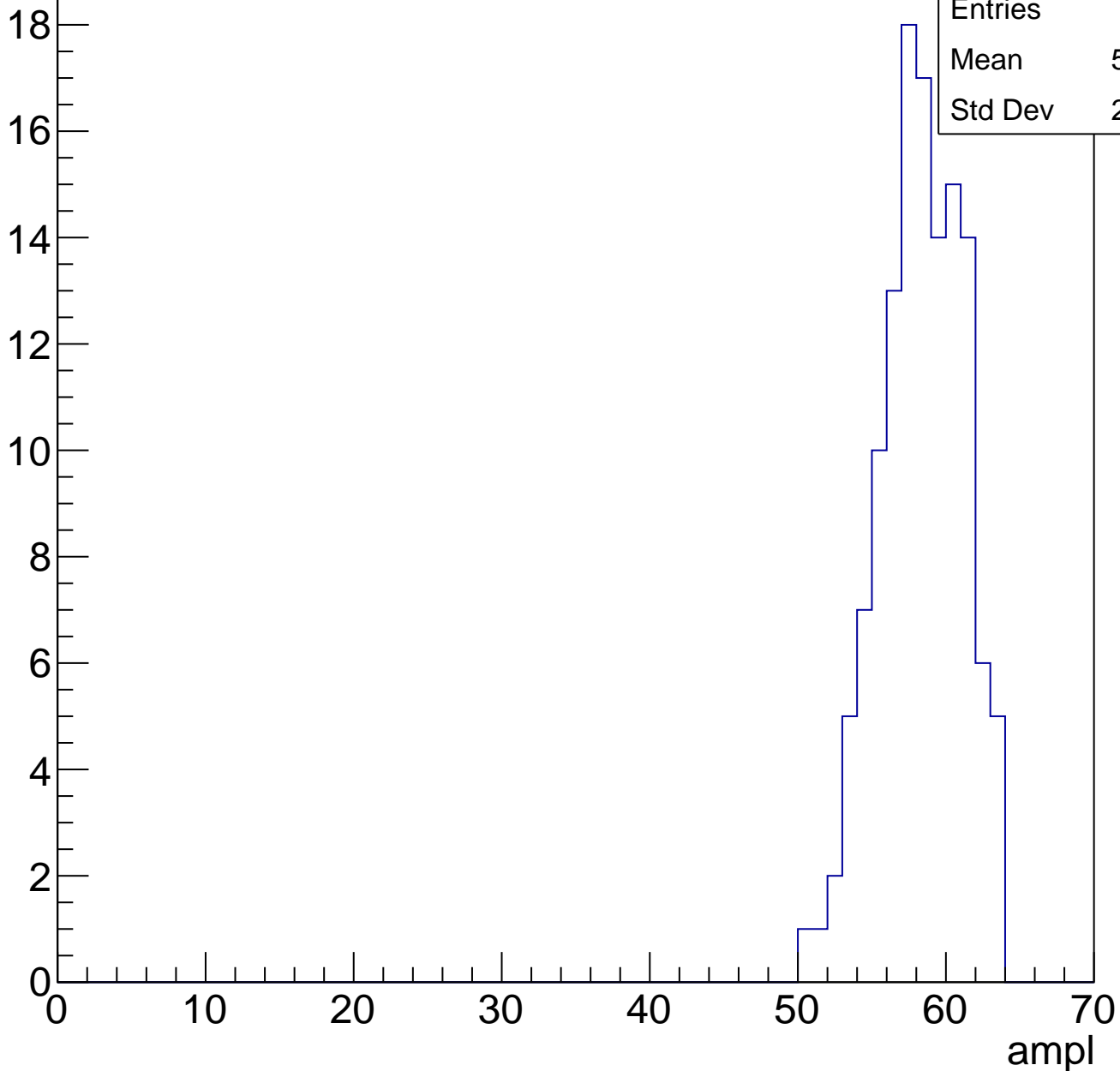
Entries	161
Mean	50.35
Std Dev	3.673



# B0L101S, U23-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

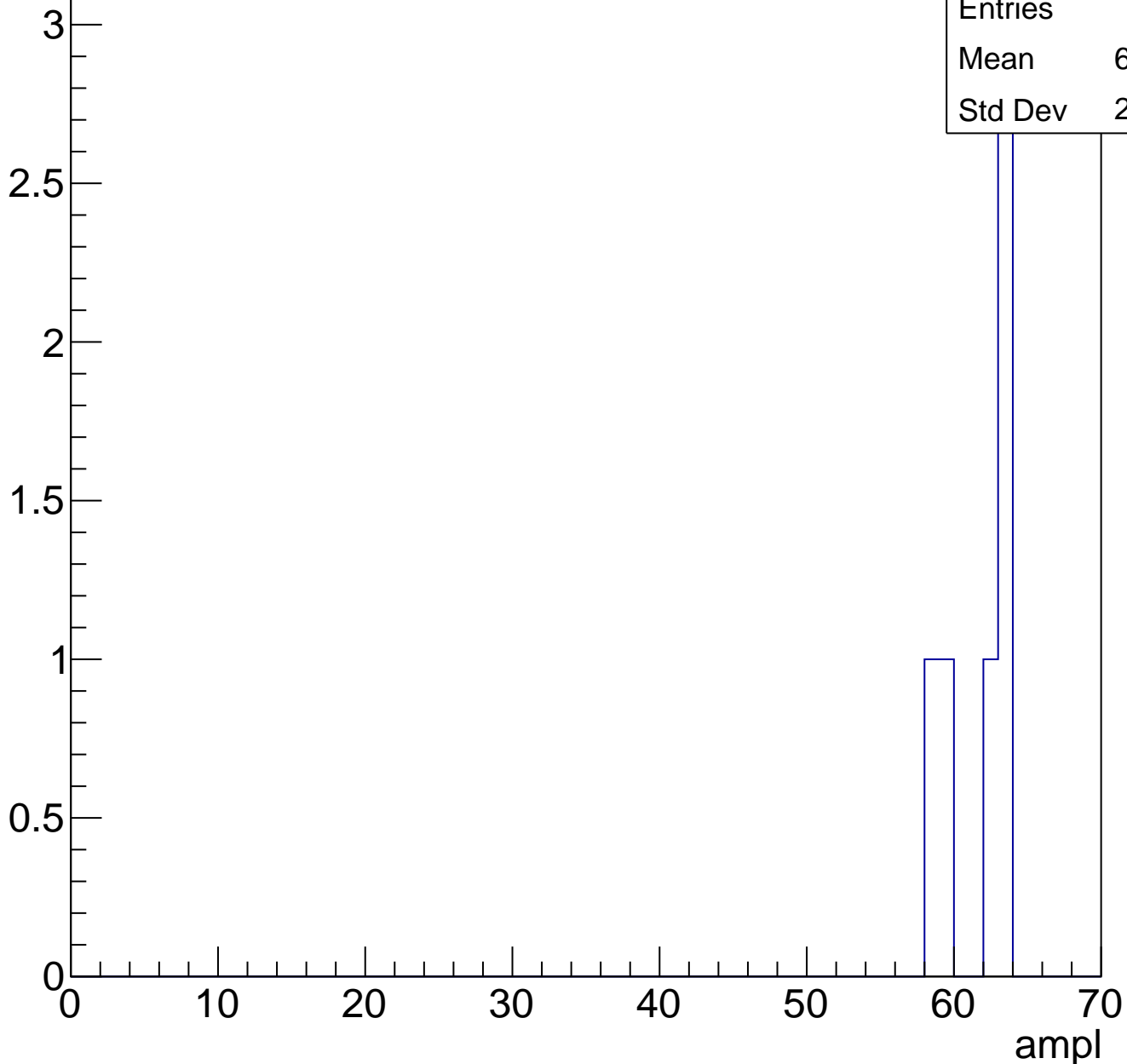
ampl

Entries	44
Mean	58.55
Std Dev	12.88

# B0L101S, U23-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

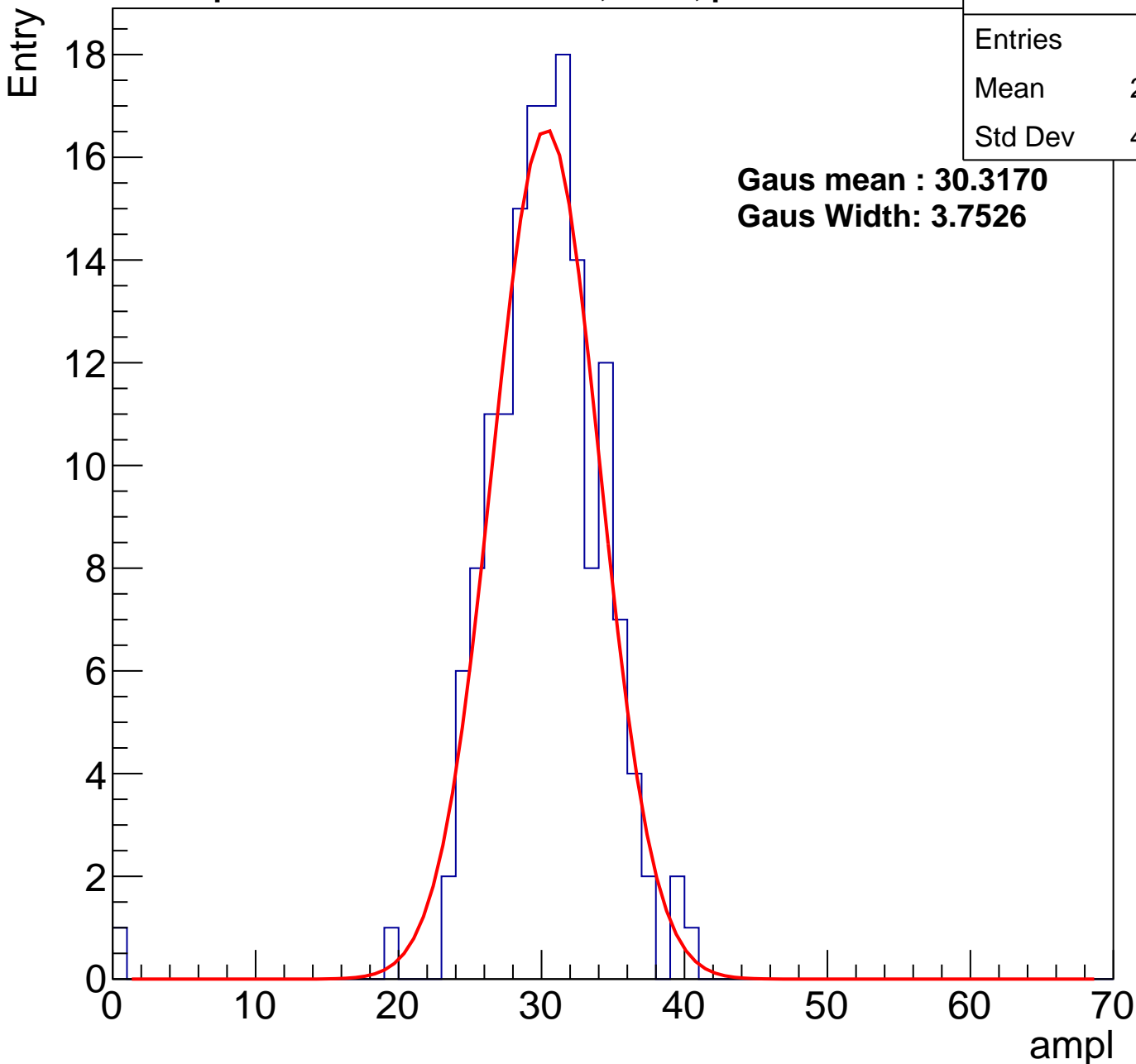
# B0L101S, U23-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	29.77
Std Dev	4.295

**Gaus mean : 30.3170**

**Gaus Width: 3.7526**



# B0L101S, U23-ch32, adc1

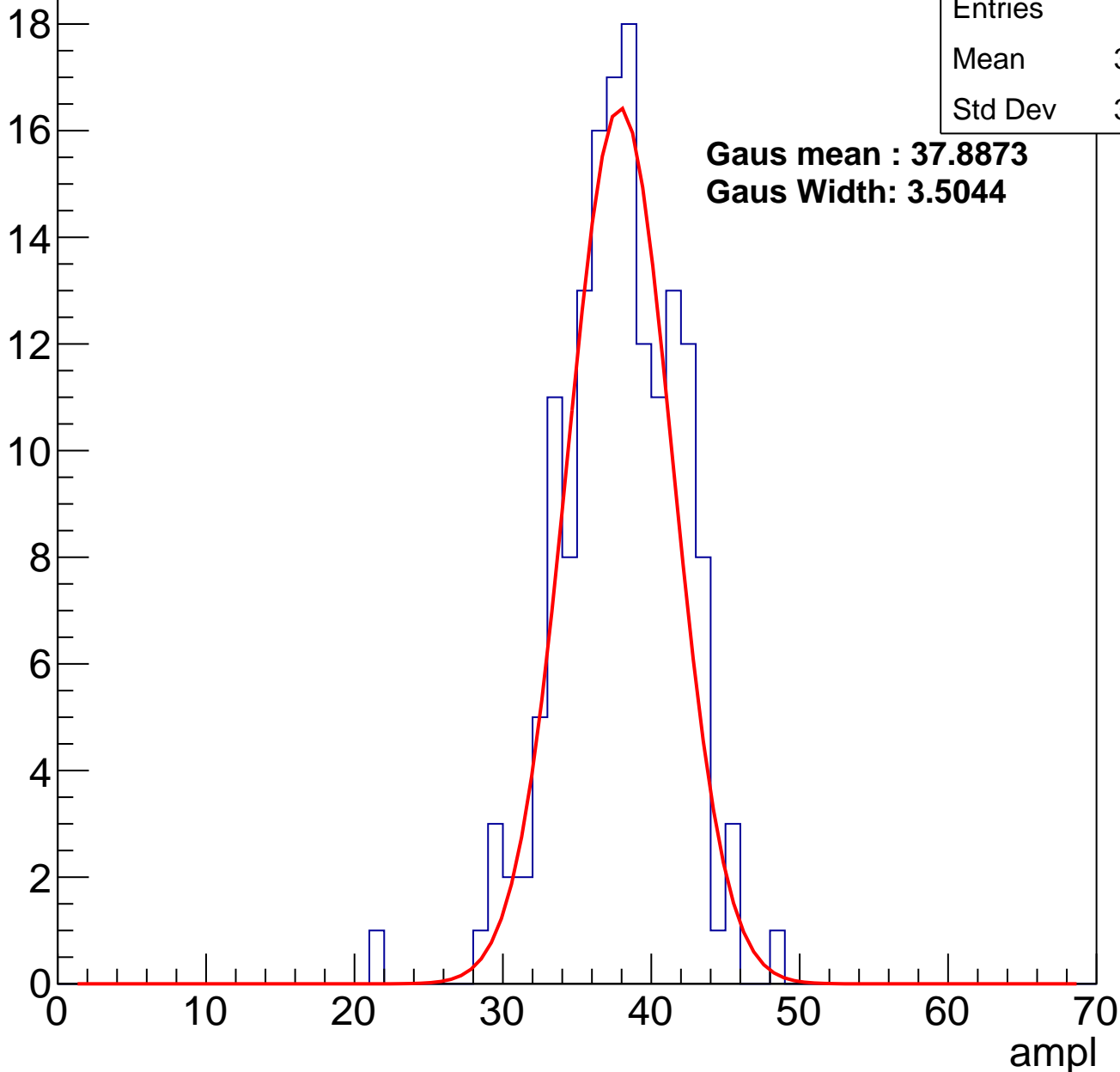
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	37.42
Std Dev	3.925

**Gaus mean : 37.8873**

**Gaus Width: 3.5044**

Entry



# B0L101S, U23-ch32, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	94
Mean	43.93
Std Dev	3.05

**Gaus mean : 44.5749**

**Gaus Width: 3.1635**

0

10

20

30

40

50

60

ampl

# B0L101S, U23-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

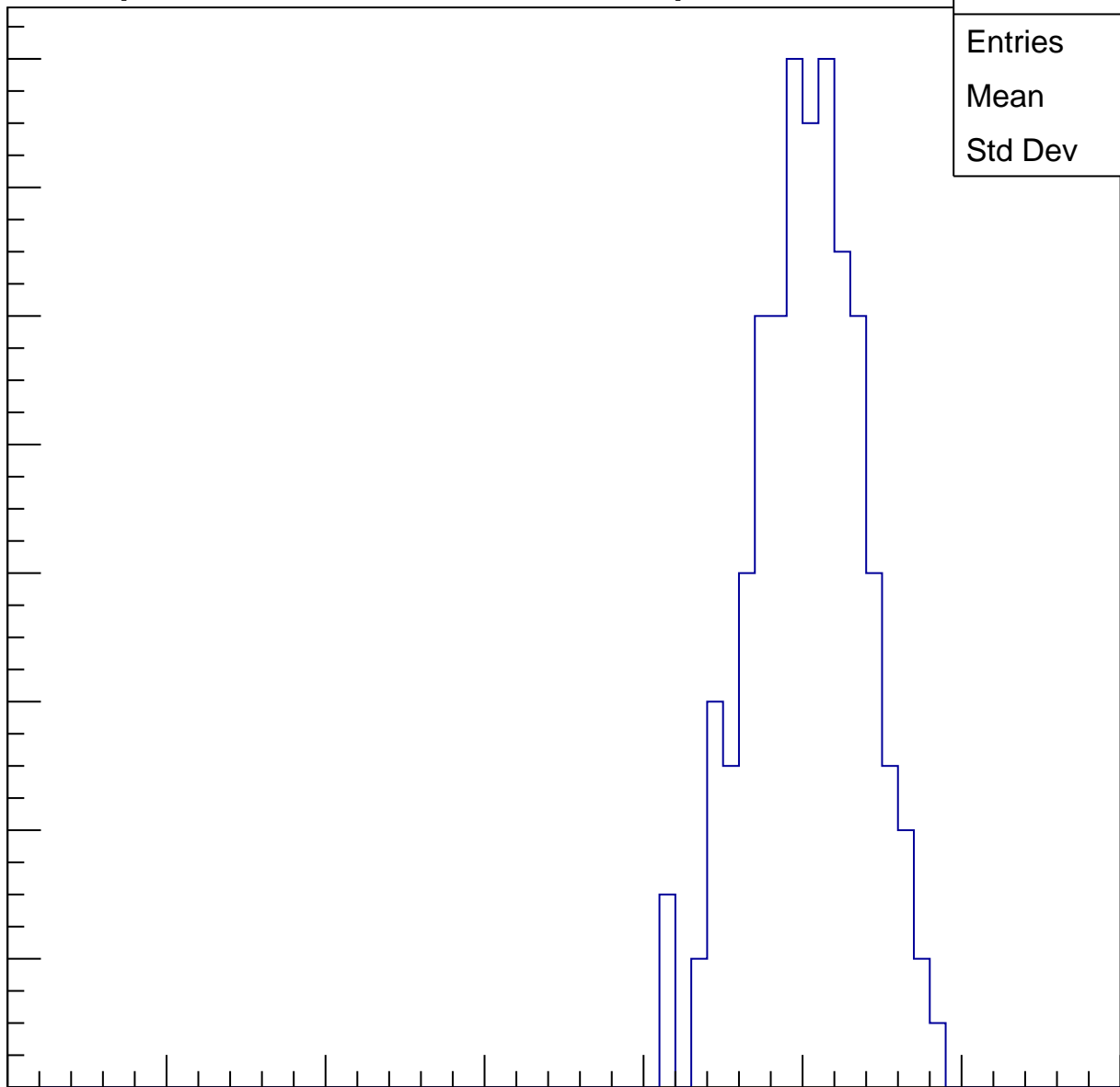
Entries	140
Mean	49.79
Std Dev	3.496

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	122
Mean	56.17
Std Dev	3.432

# B0L101S, U23-ch32, adc5

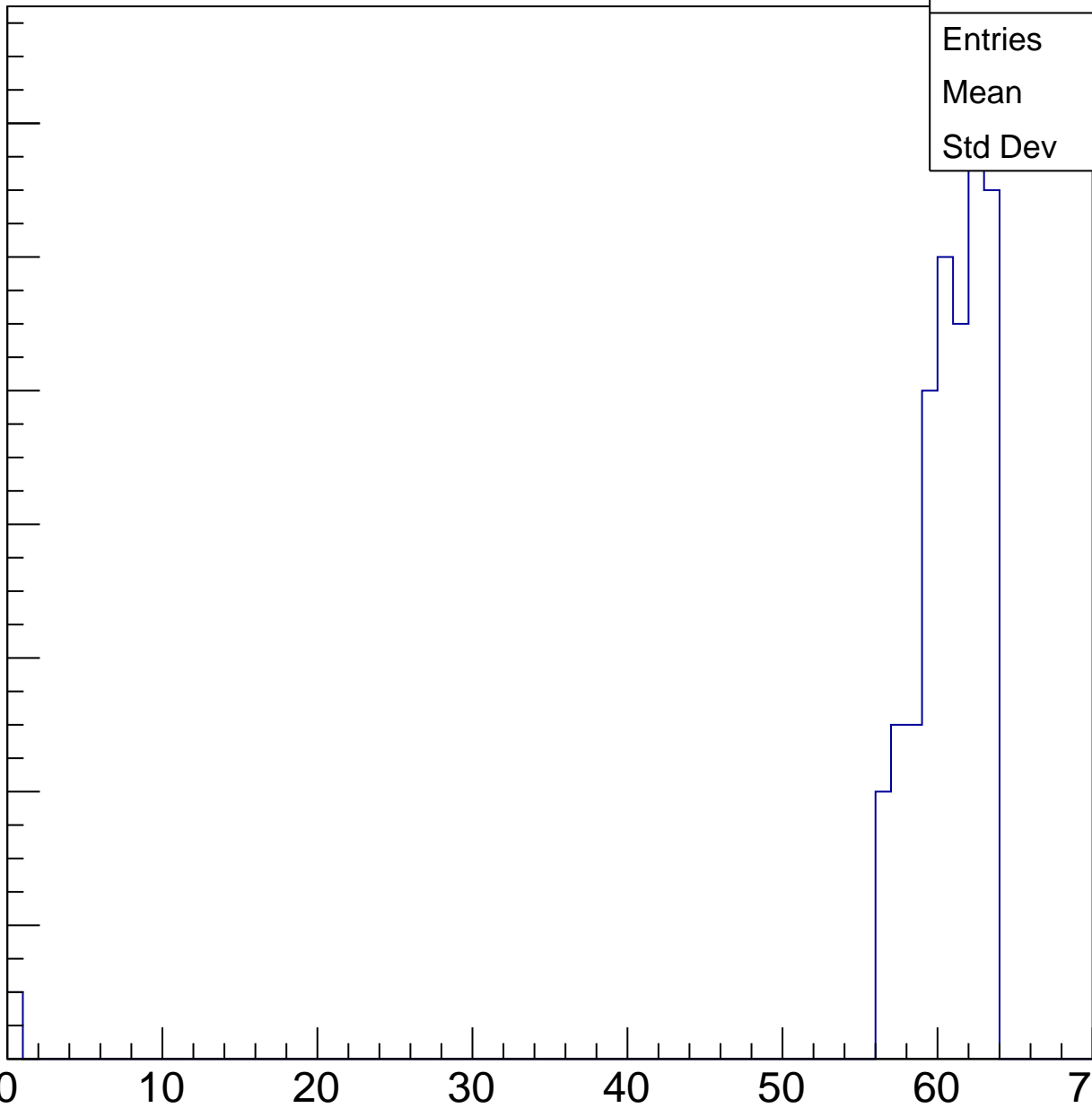
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	76
Mean	59.59
Std Dev	7.177

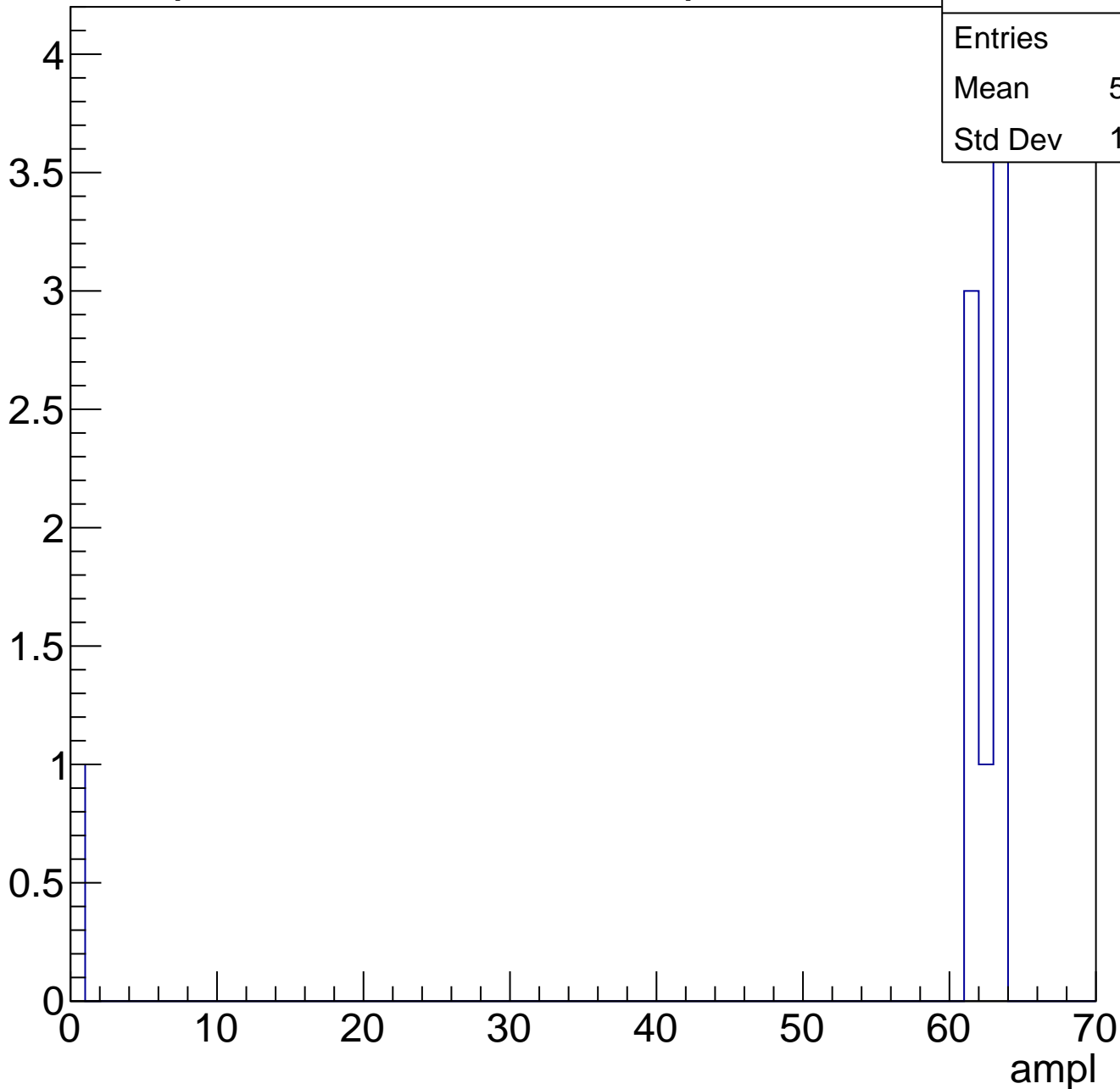
ampl



# B0L101S, U23-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

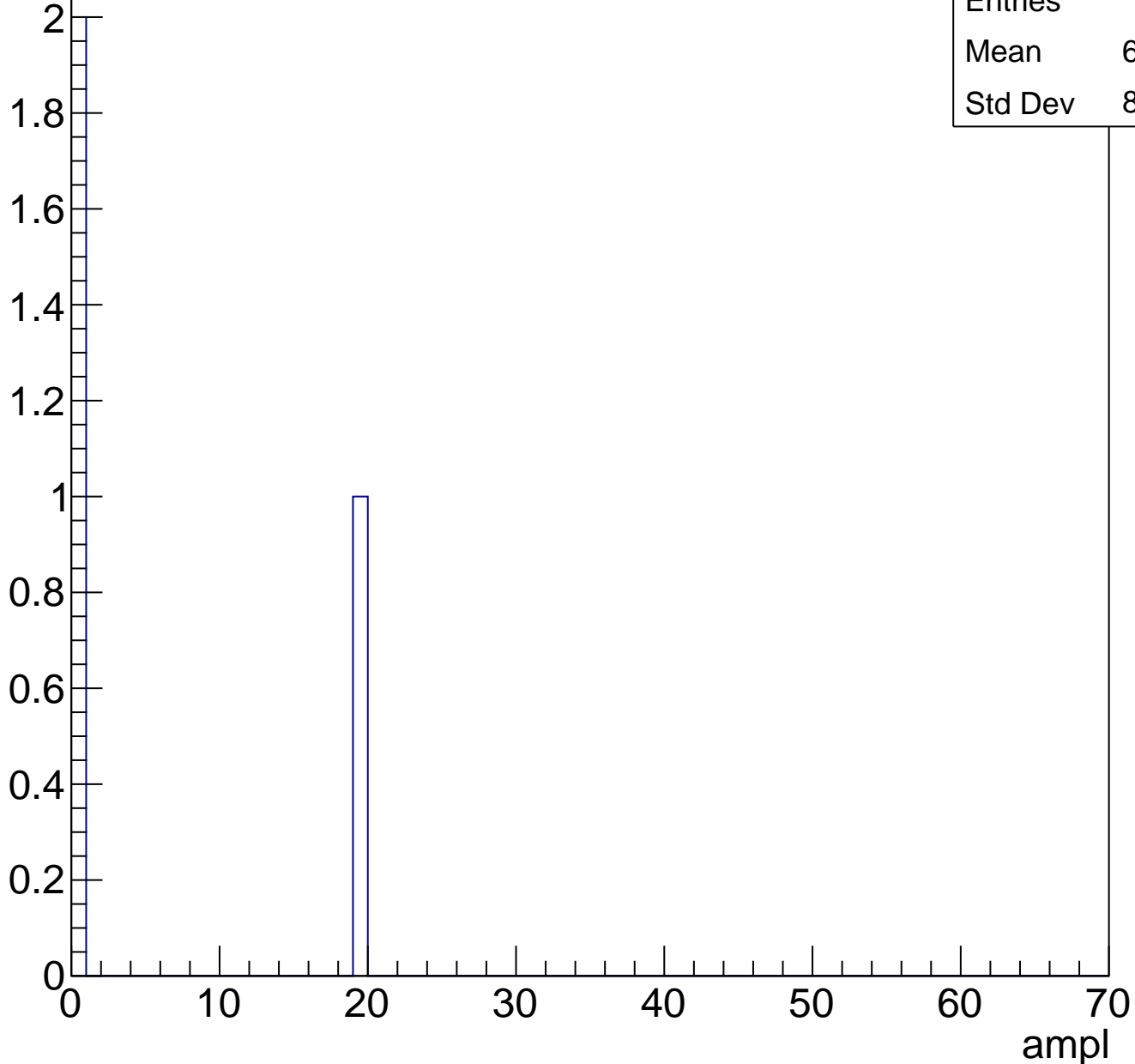




# B0L101S, U23-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

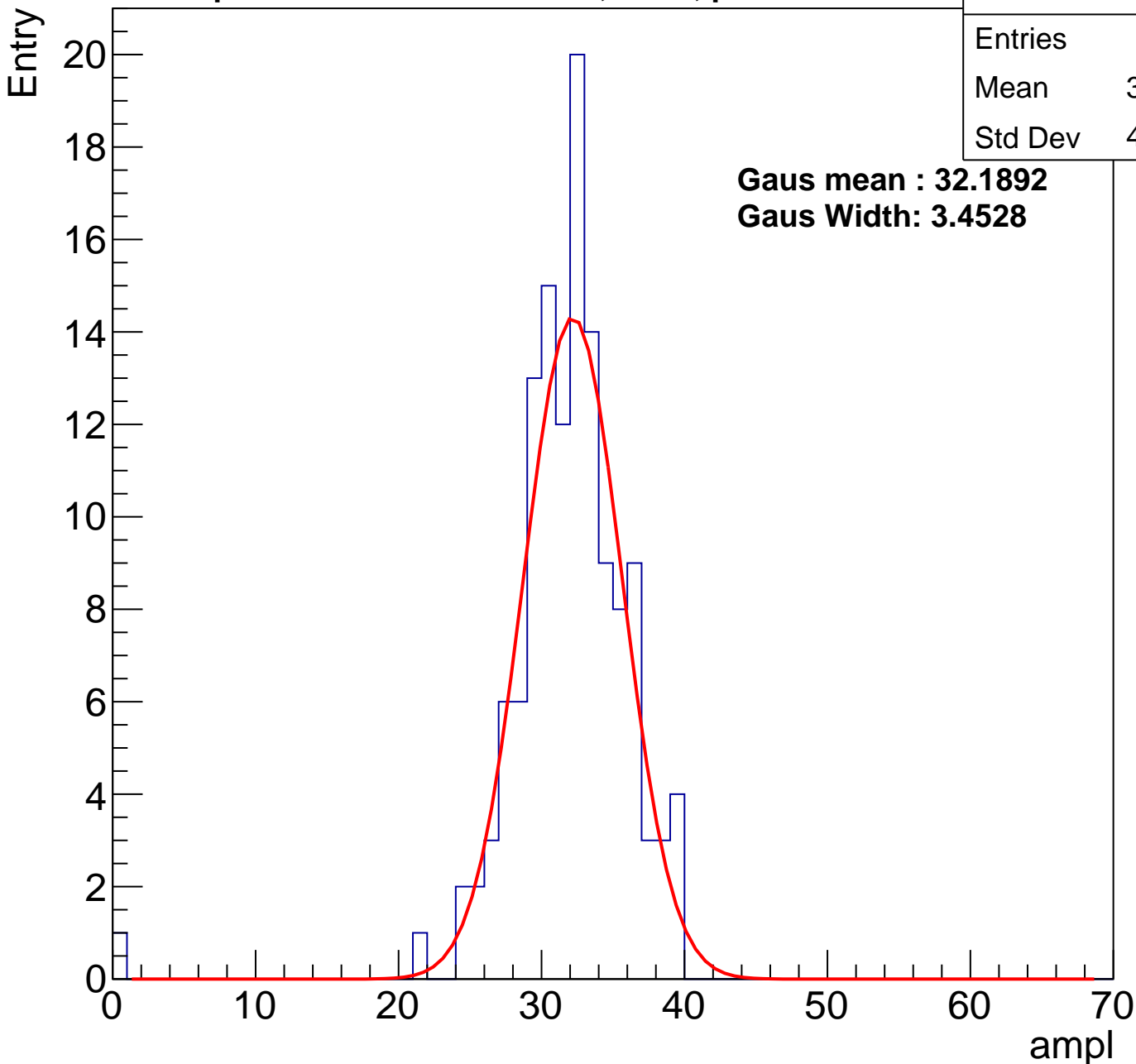
# B0L101S, U23-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	31.44
Std Dev	4.384

**Gaus mean : 32.1892**

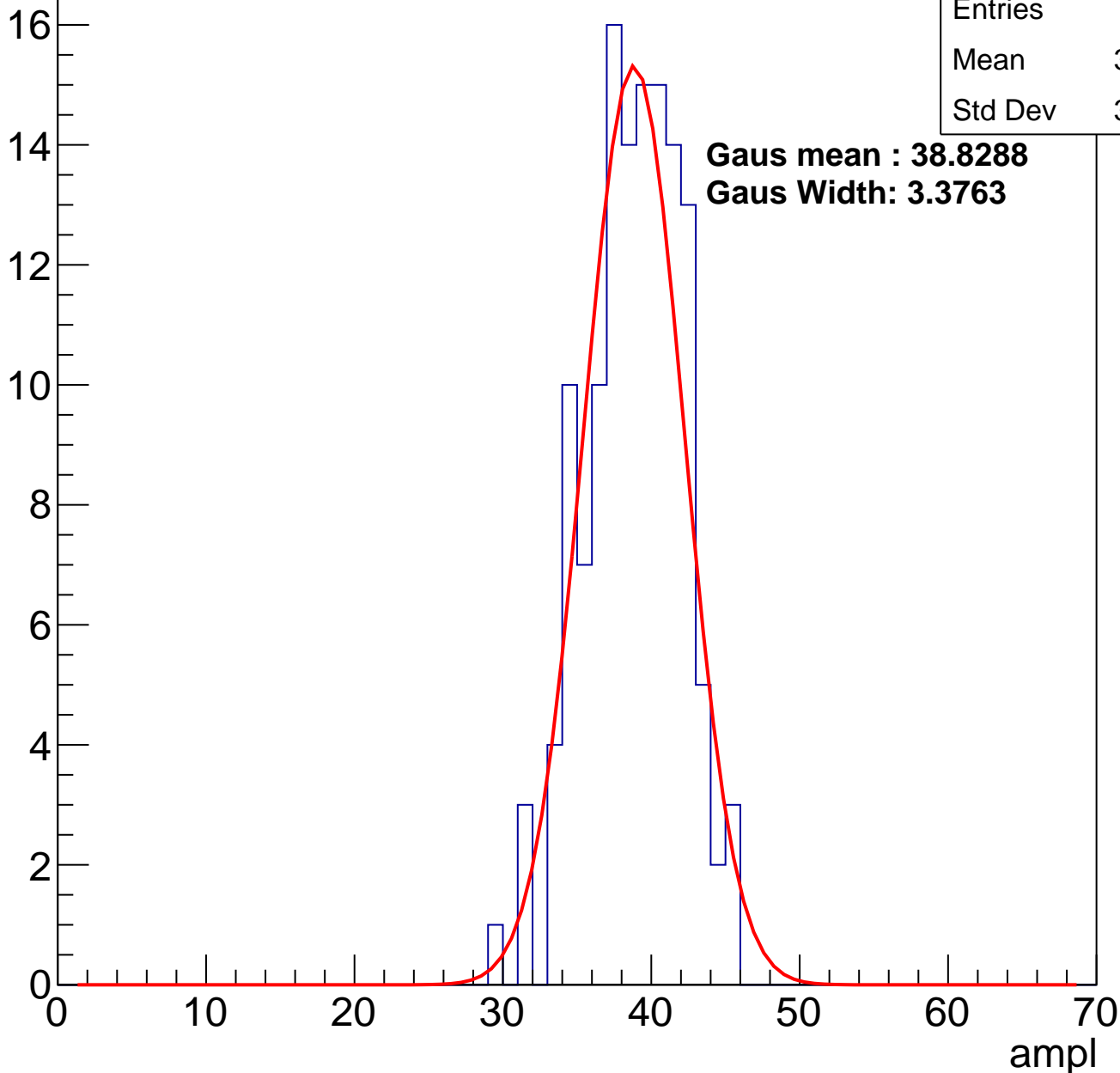
**Gaus Width: 3.4528**



# B0L101S, U23-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

138

Mean

45.11

Std Dev

3.524

**Gaus mean : 45.8320**

**Gaus Width: 3.0372**

0

10

20

30

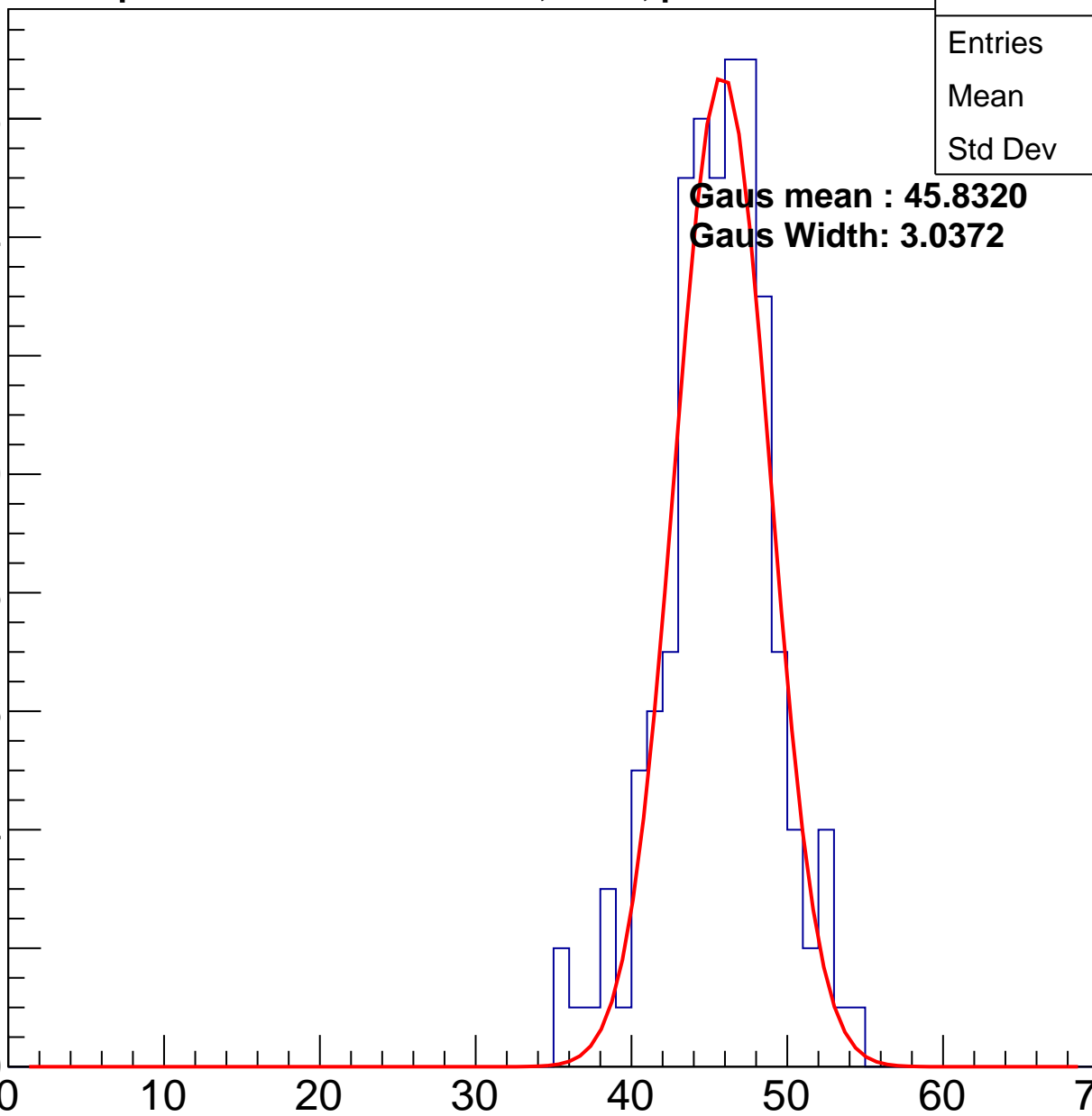
40

50

60

70

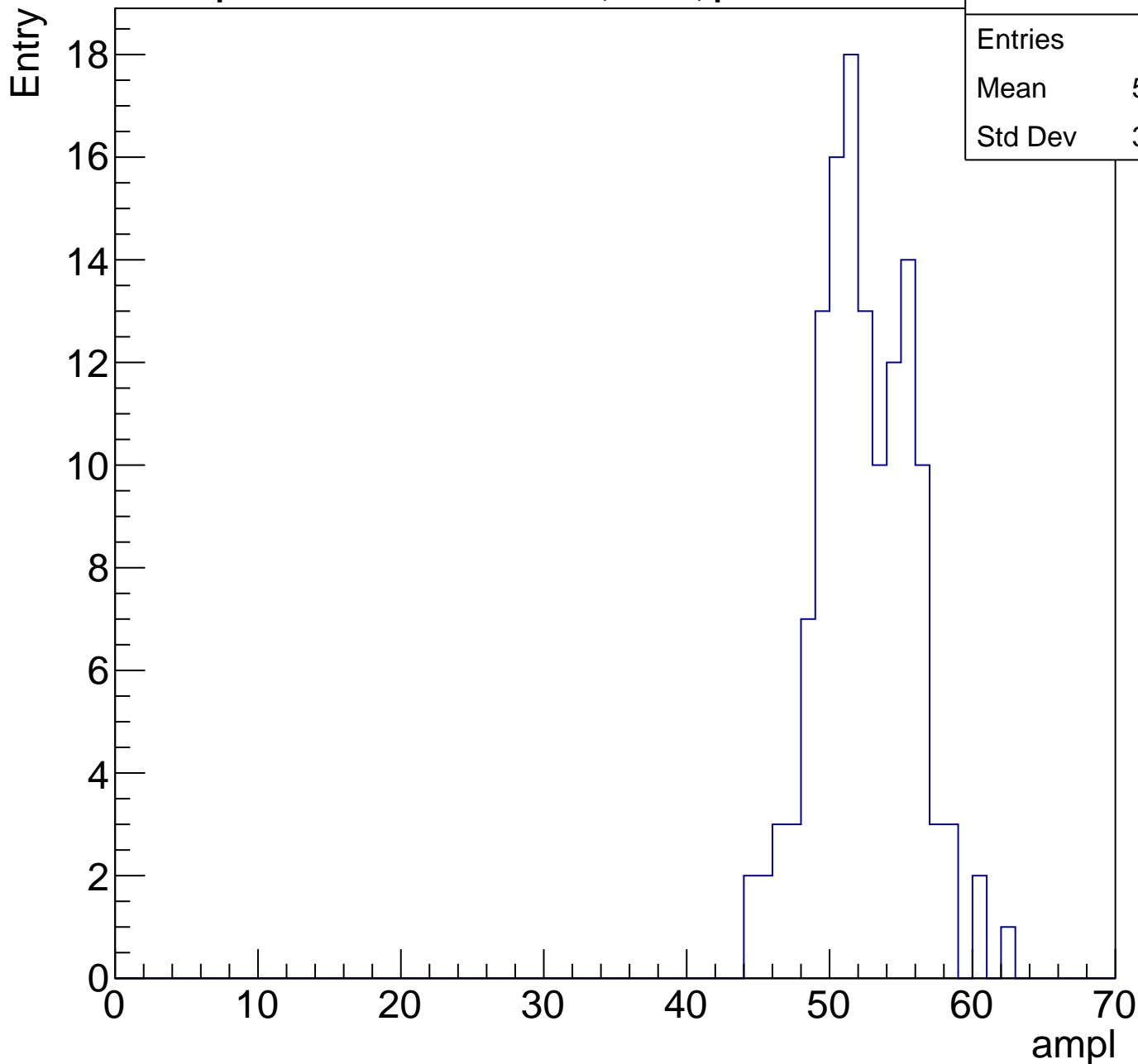
ampl



# B0L101S, U23-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	51.96
Std Dev	3.343



# B0L101S, U23-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

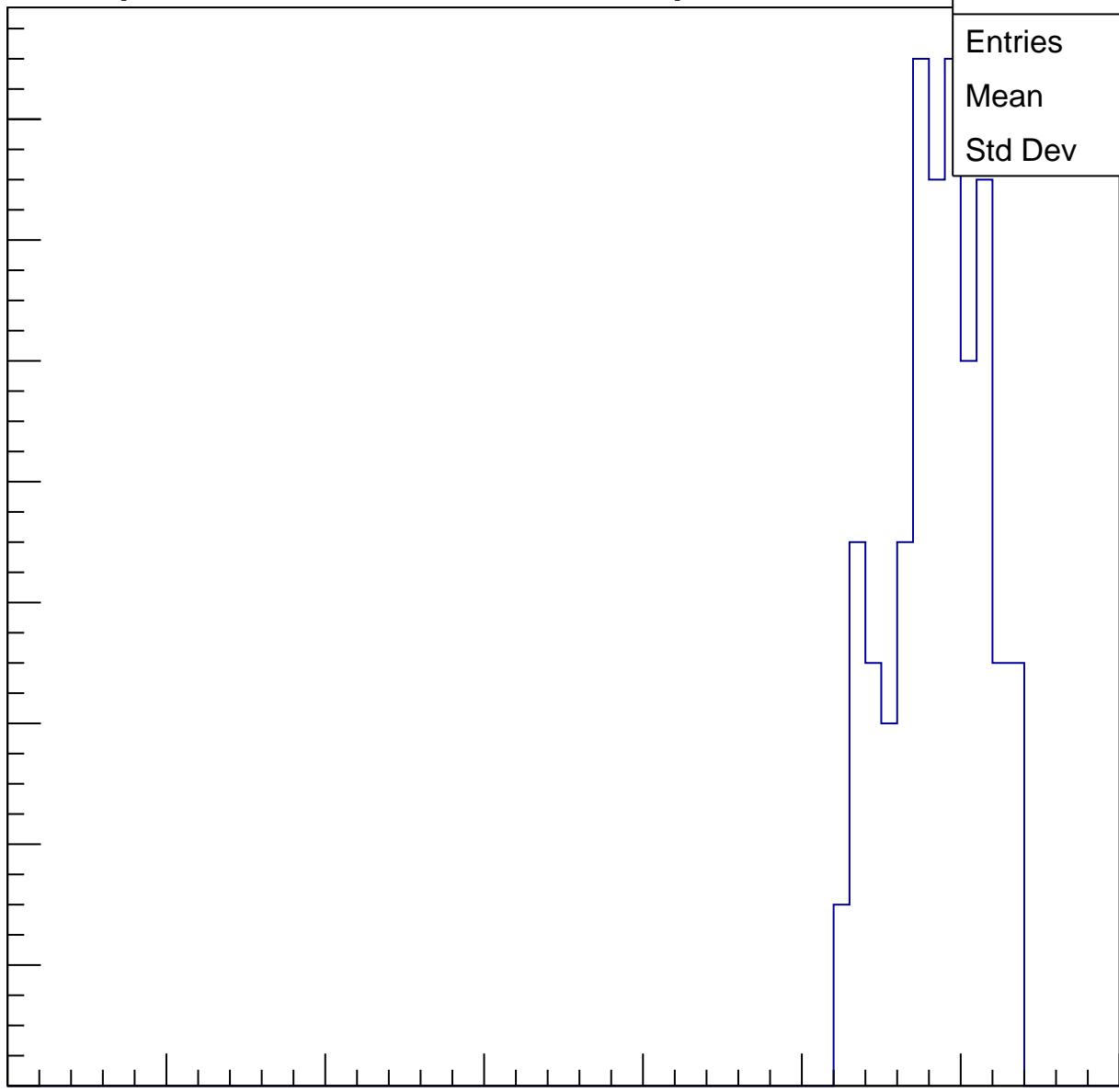
50

60

70

ampl

Entries	124
Mean	58.04
Std Dev	2.894



# B0L101S, U23-ch33, adc5

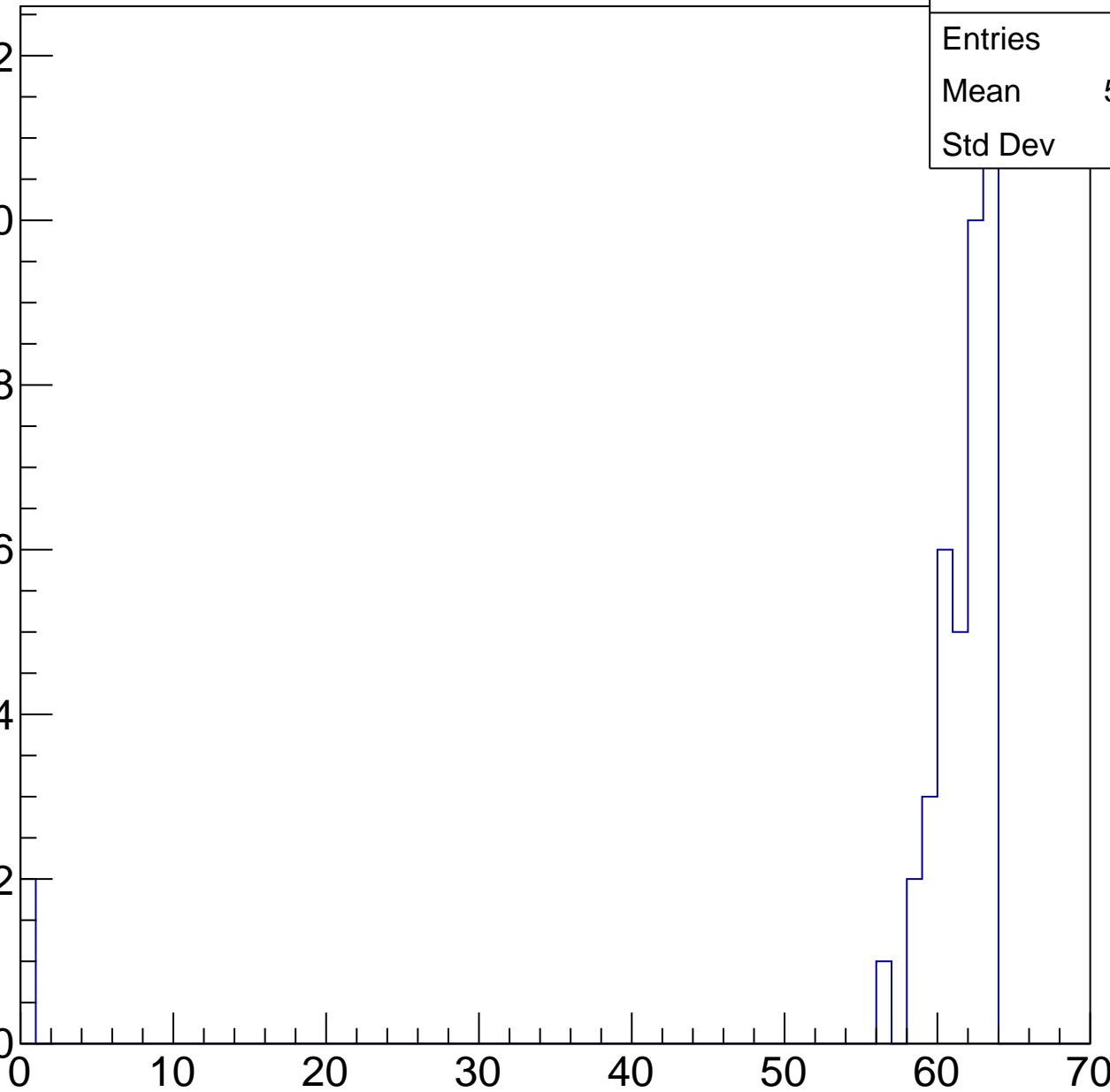
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	41
Mean	58.29
Std Dev	13.31

ampl



# B0L101S, U23-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl





# B0L101S, U23-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

10

20

30

40

50

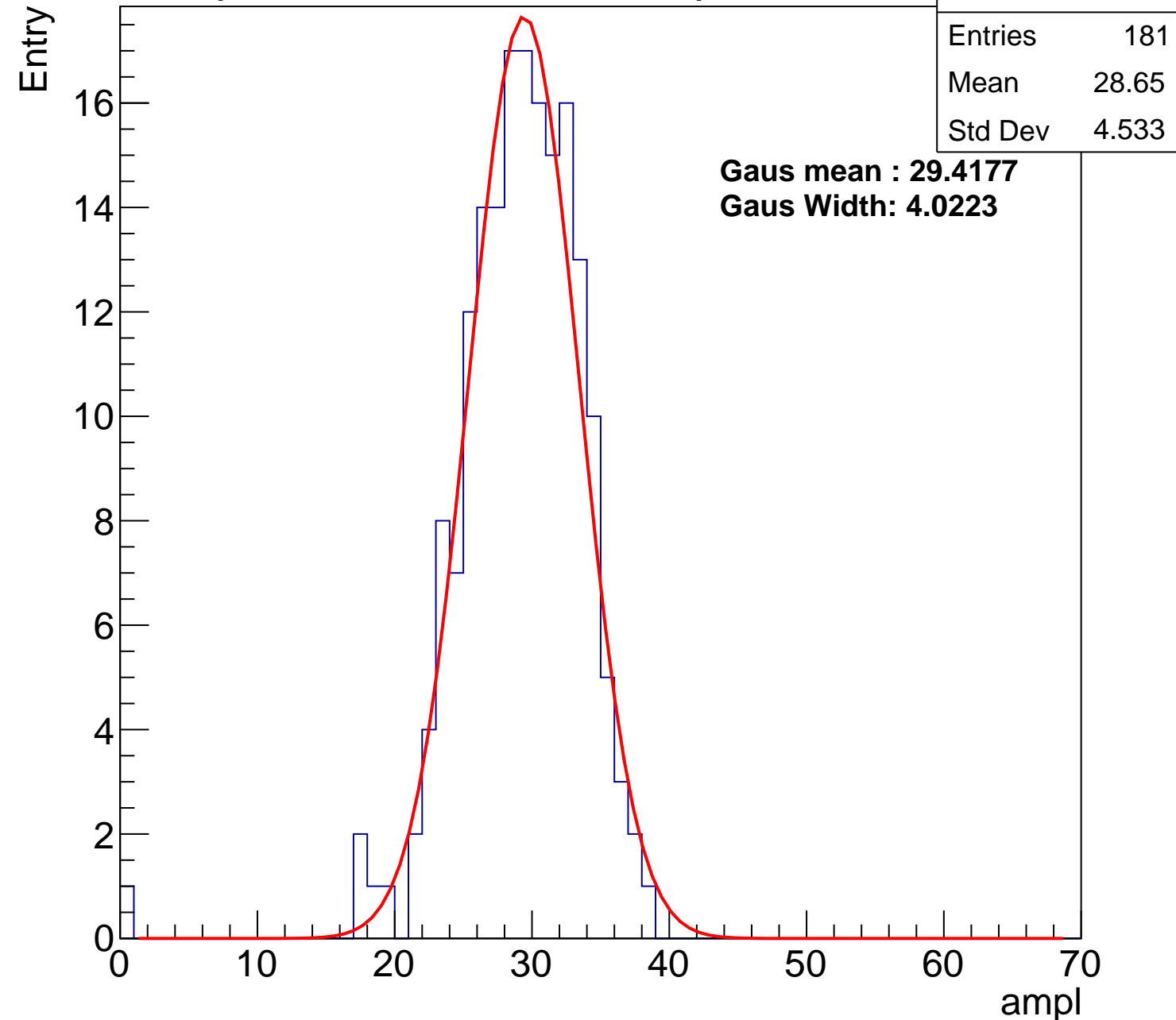
60

ampl

Entries	181
Mean	28.65
Std Dev	4.533

**Gaus mean : 29.4177**

**Gaus Width: 4.0223**

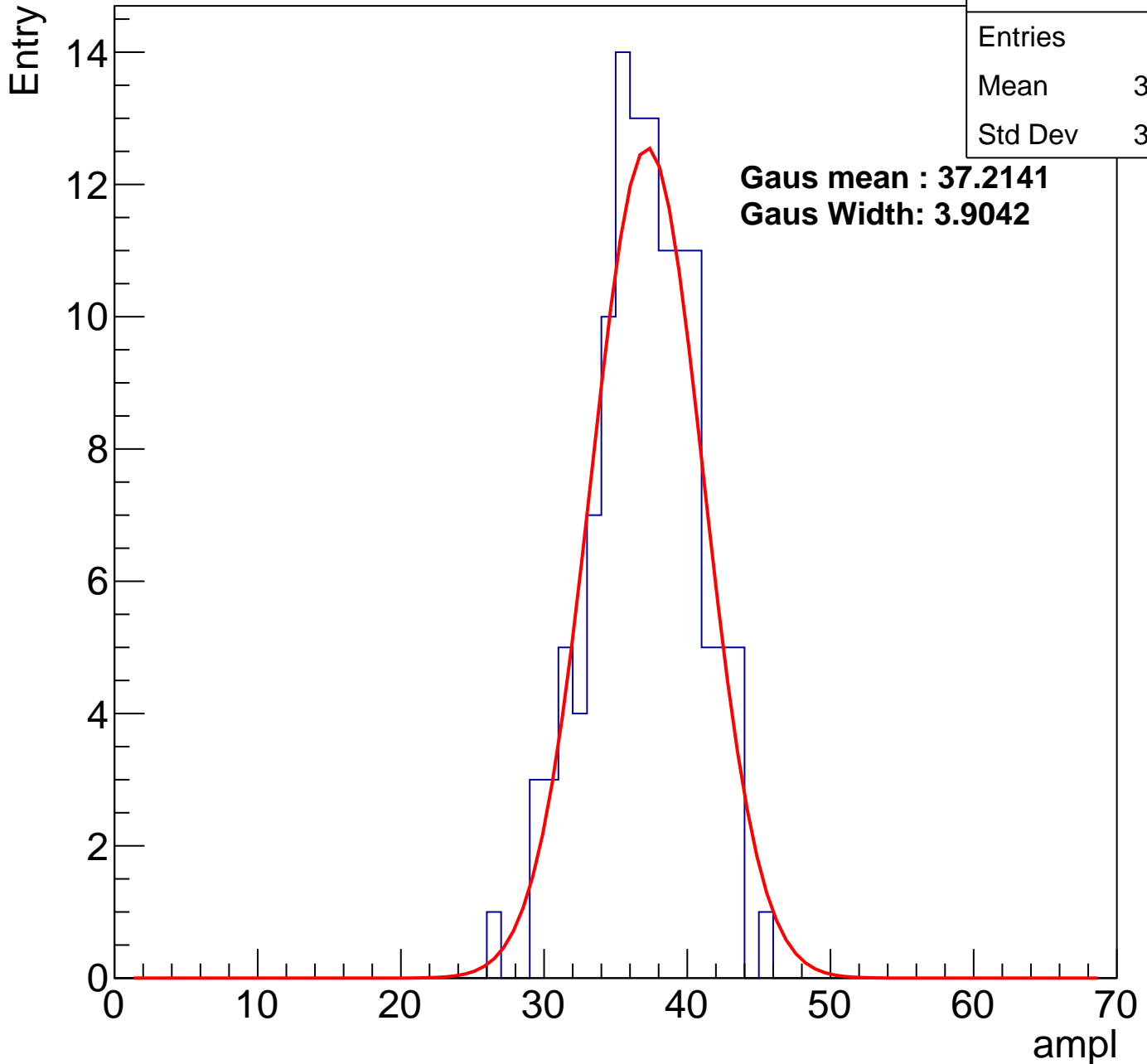


# B0L101S, U23-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	36.54
Std Dev	3.608

**Gaus mean : 37.2141**  
**Gaus Width: 3.9042**



# B0L101S, U23-ch34, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

132

Mean

42.78

Std Dev

3.687

**Gaus mean : 43.0490**

**Gaus Width: 3.6214**

0

10

20

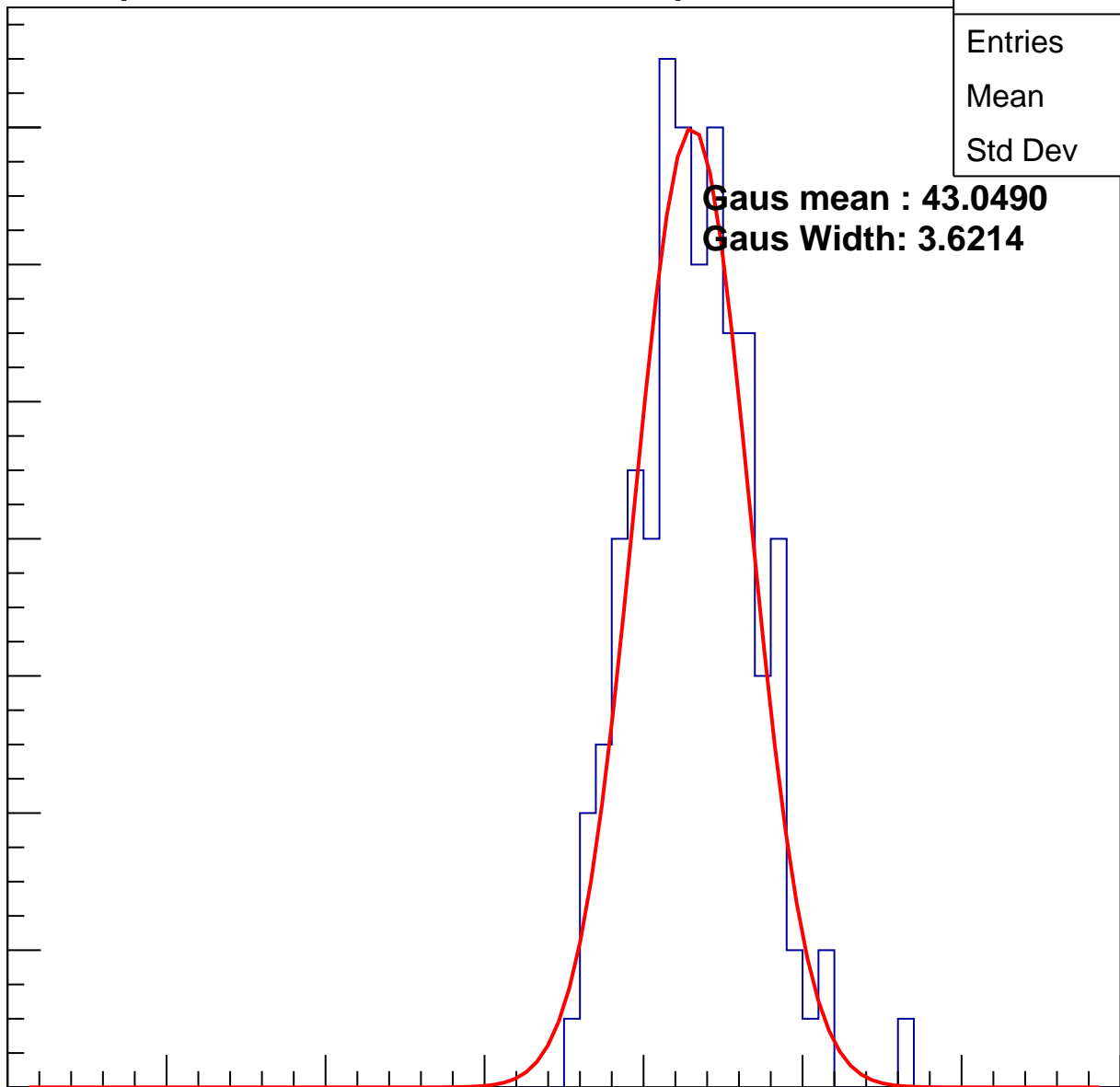
30

40

50

60

ampl



# B0L101S, U23-ch34, adc3

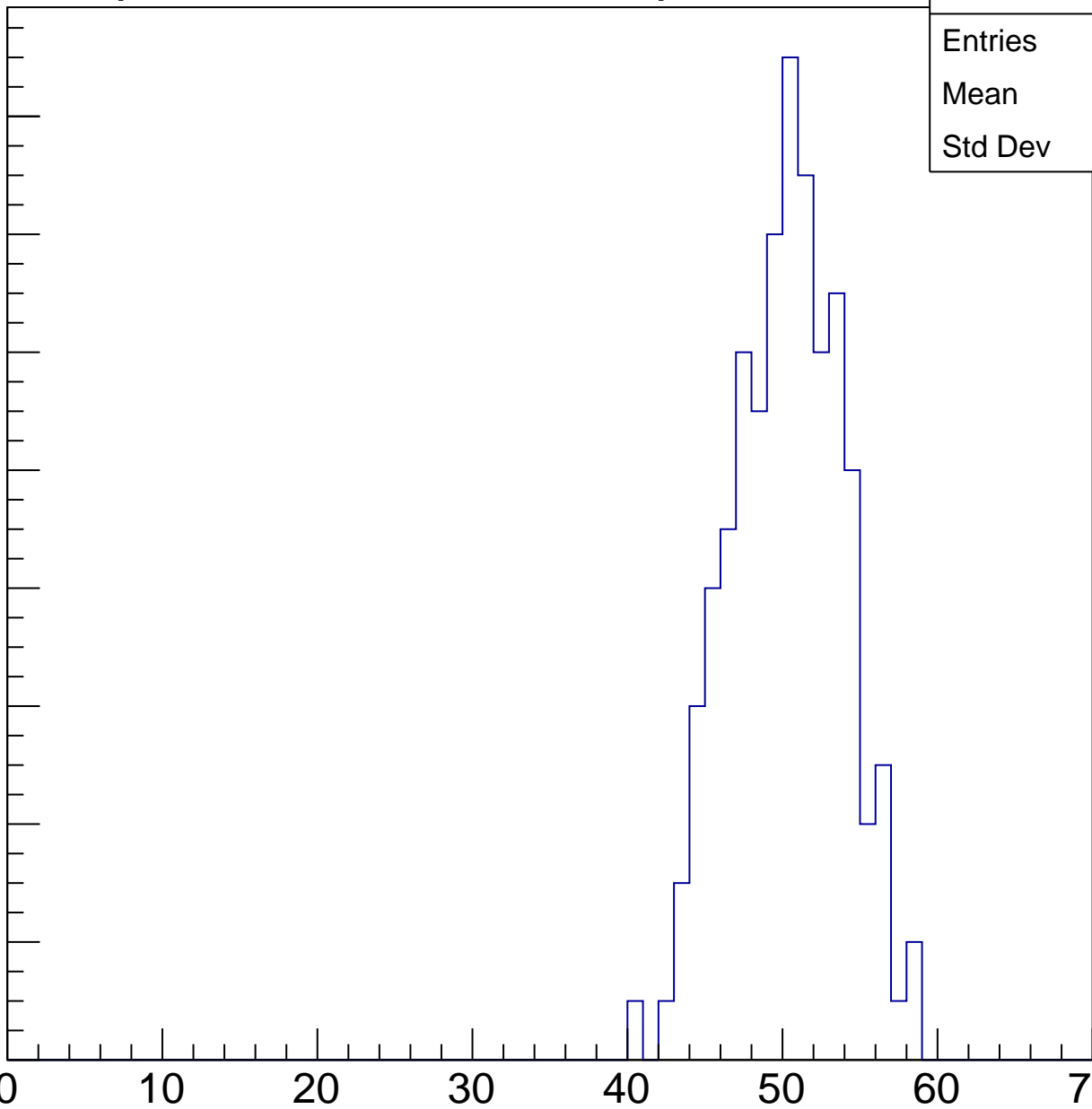
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	144
Mean	49.78
Std Dev	3.562

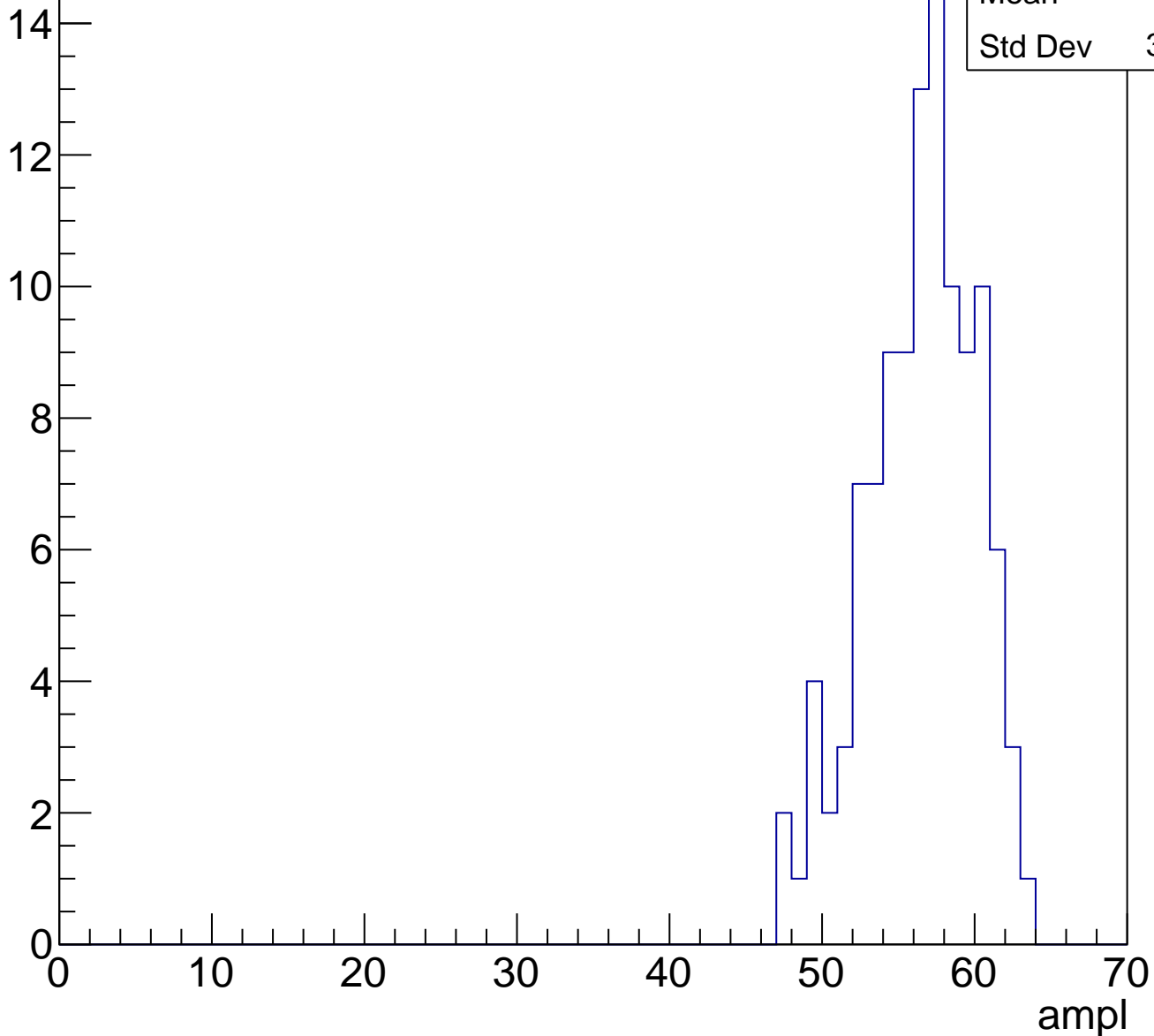
ampl



# B0L101S, U23-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch34, adc5

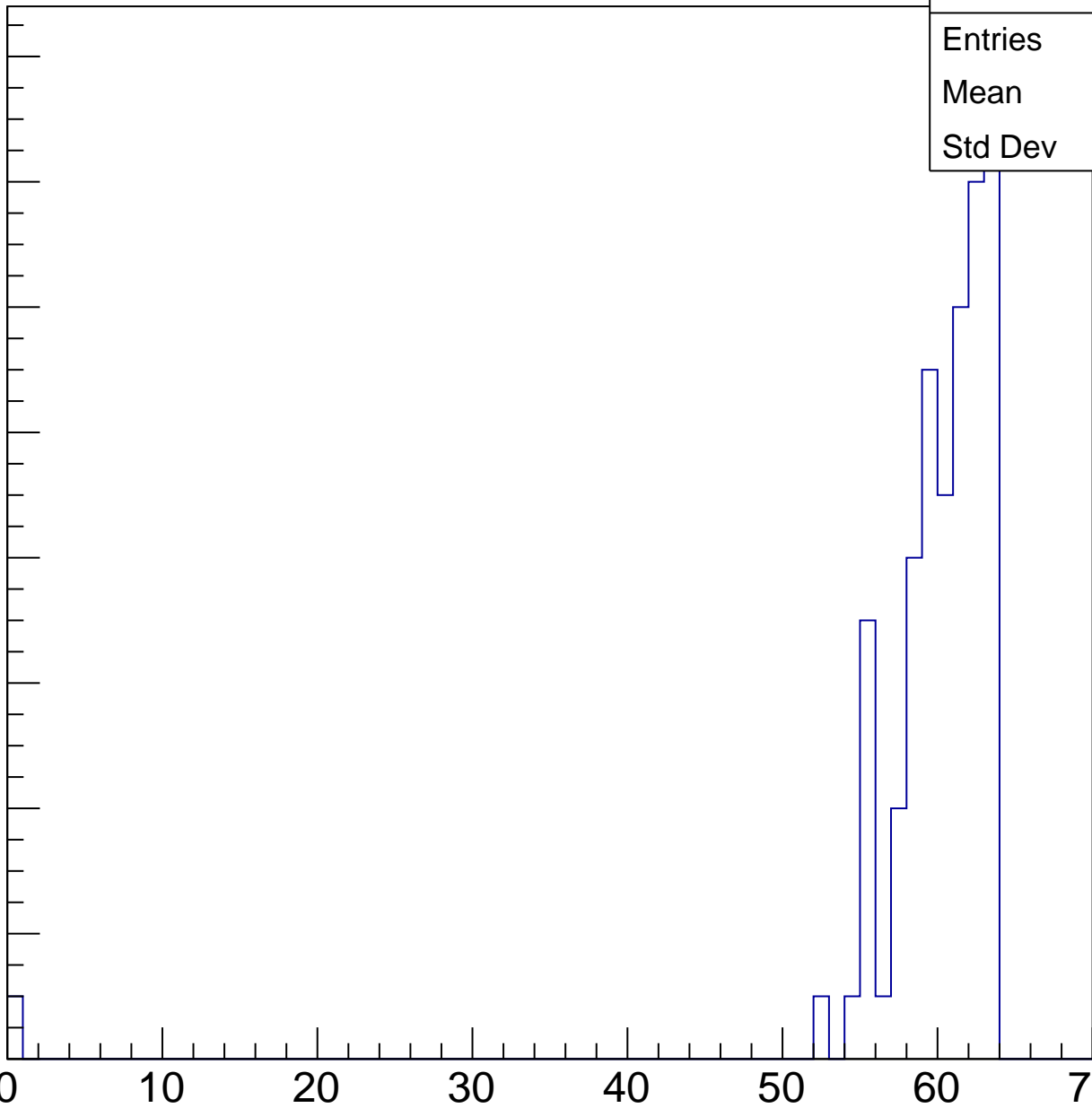
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	59.25
Std Dev	6.974

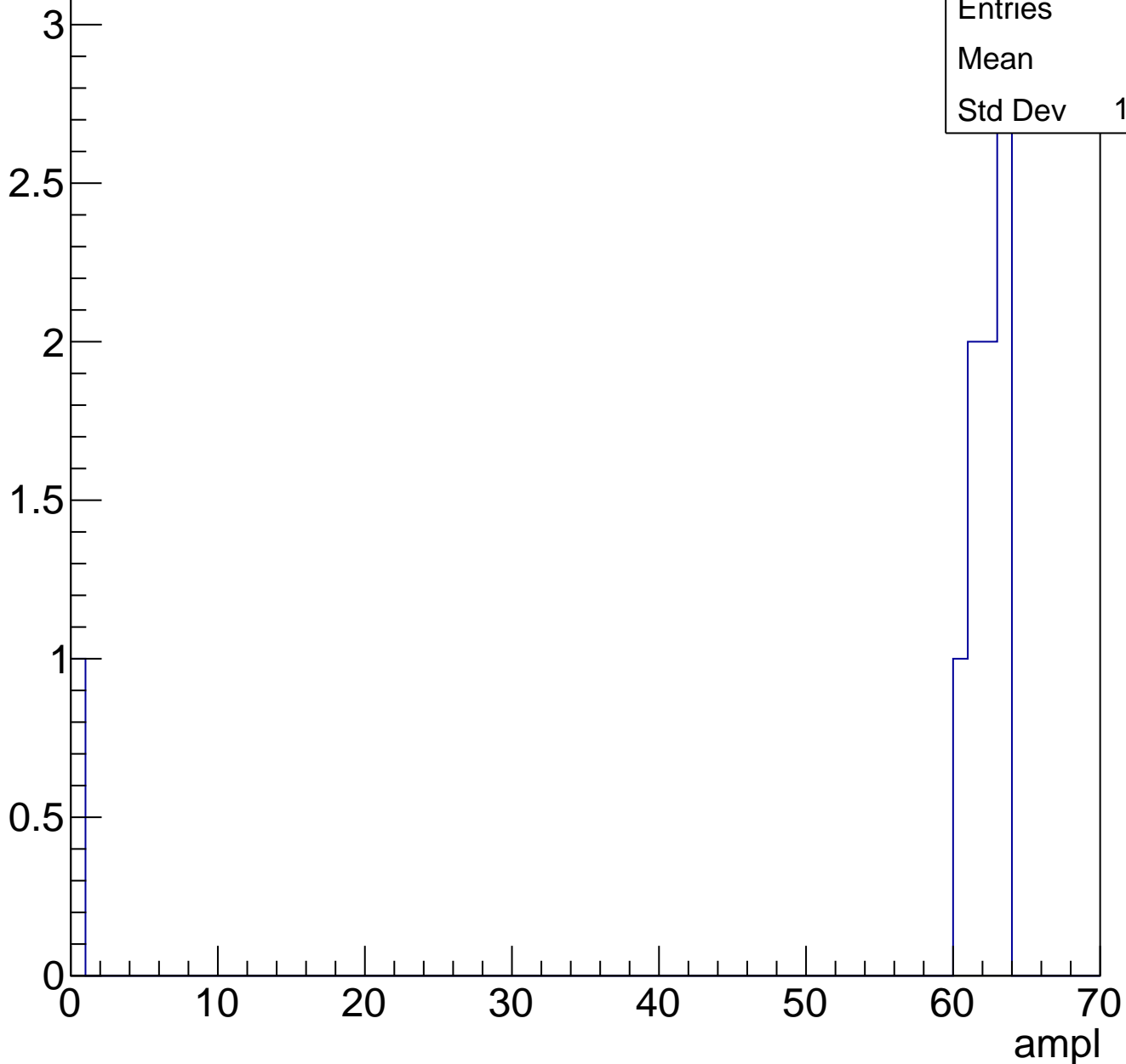
ampl



# B0L101S, U23-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch35, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	29.62
Std Dev	5.111

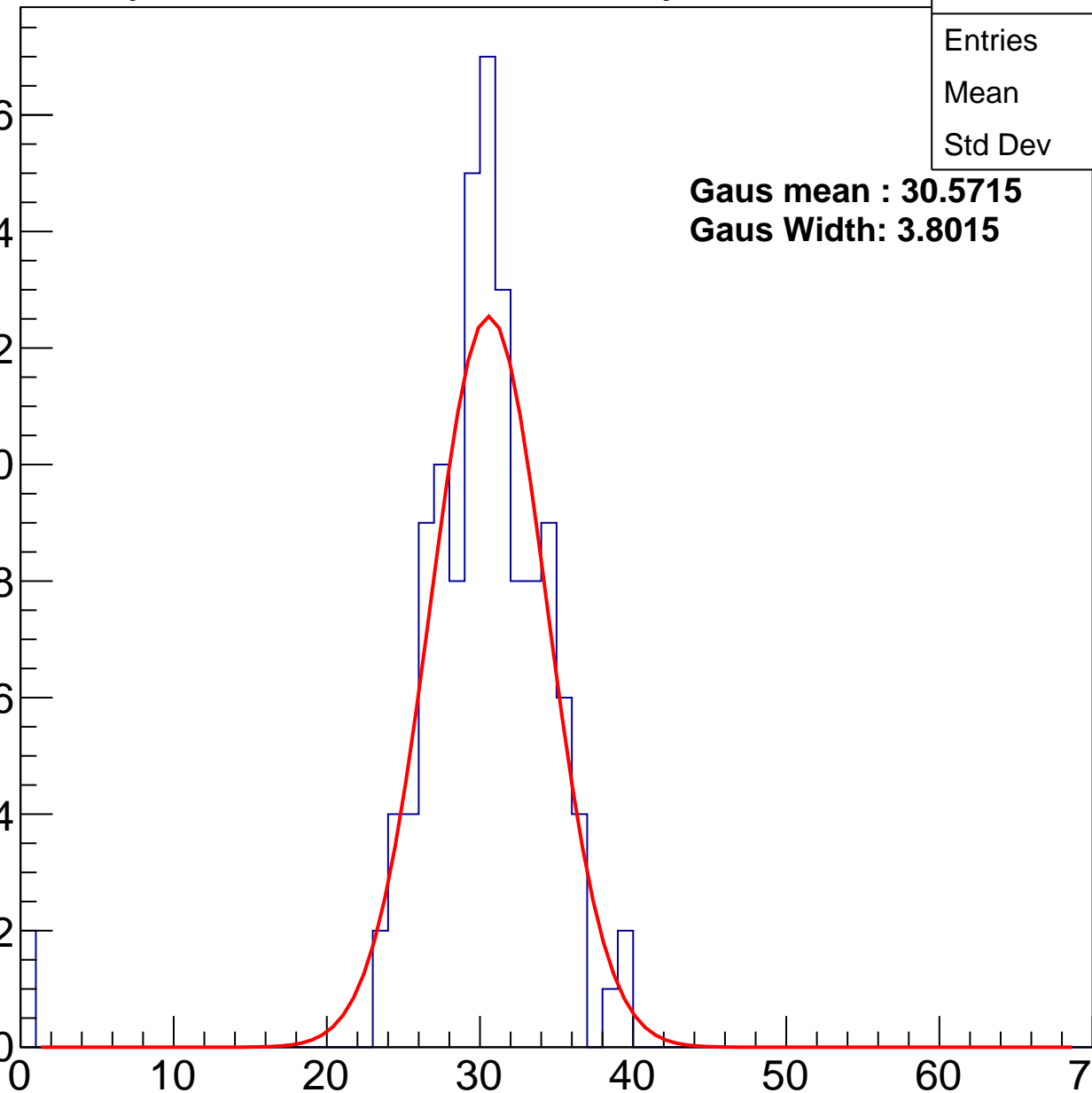
**Gaus mean : 30.5715**

**Gaus Width: 3.8015**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



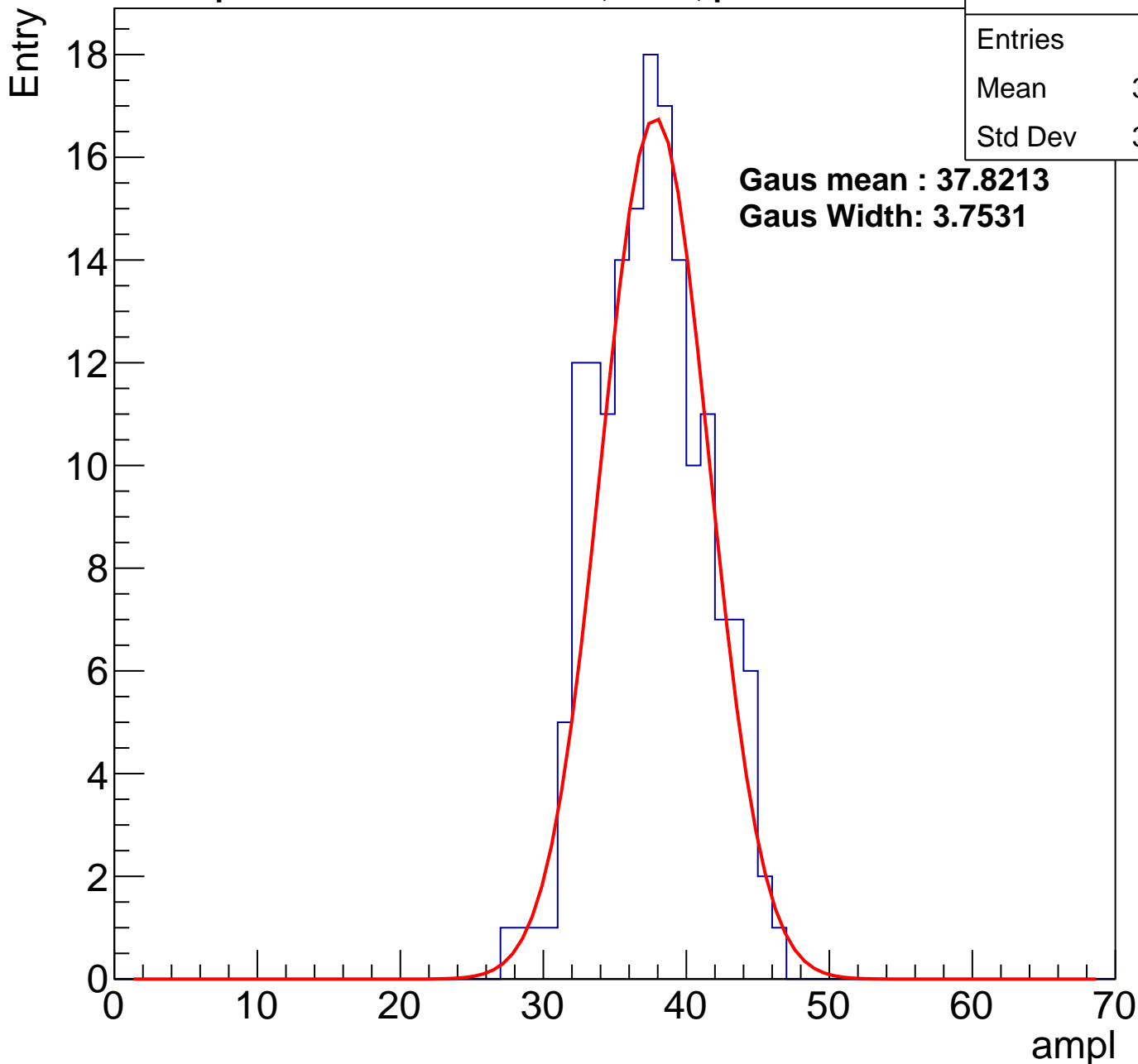
# B0L101S, U23-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	37.09
Std Dev	3.805

**Gaus mean : 37.8213**

**Gaus Width: 3.7531**



# B0L101S, U23-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

44.84

Std Dev

3.614

**Gaus mean : 45.3973**

**Gaus Width: 3.7971**

0

10

20

30

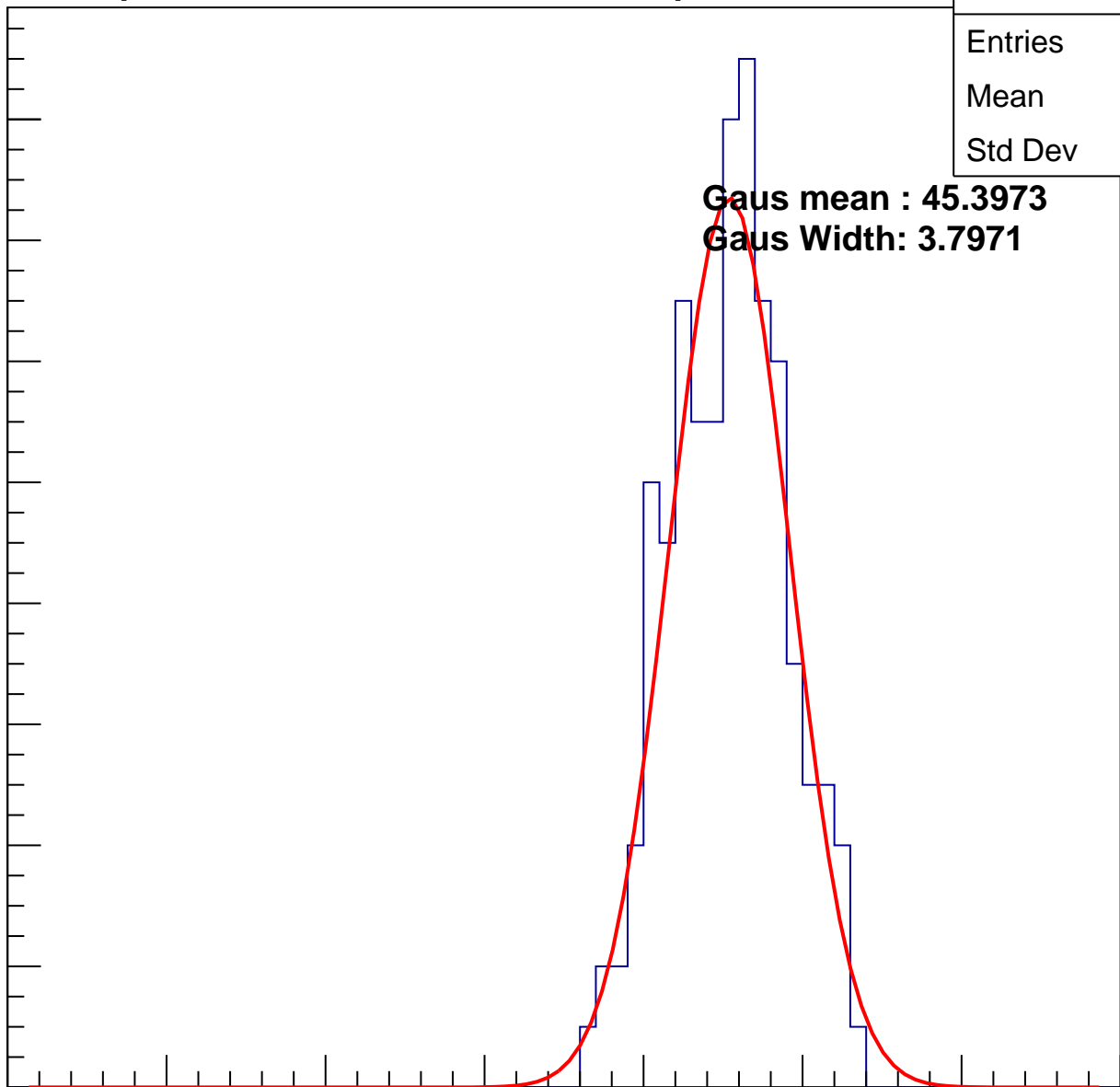
40

50

60

70

ampl

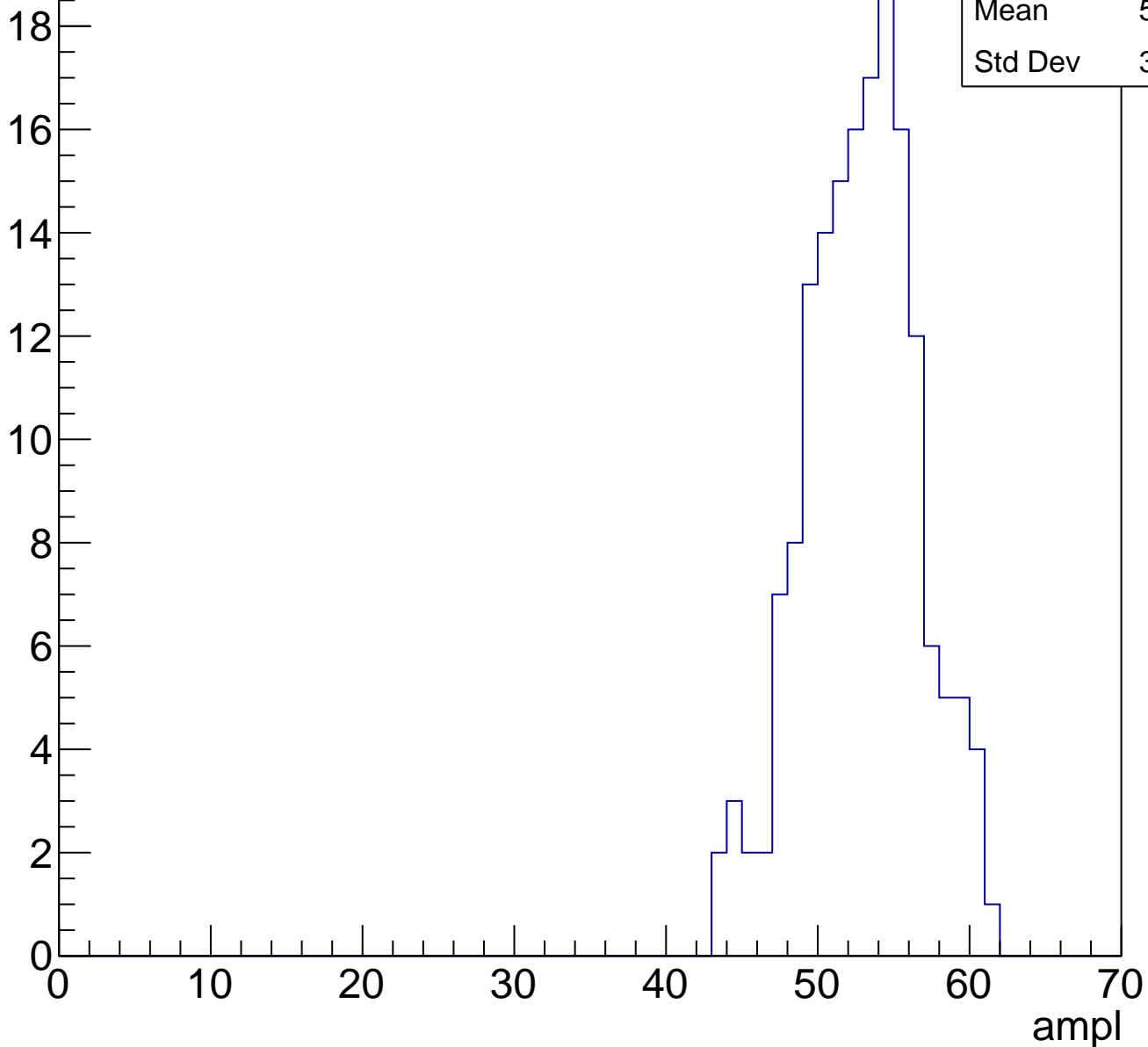


# B0L101S, U23-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	167
Mean	52.42
Std Dev	3.733

Entry



# B0L101S, U23-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

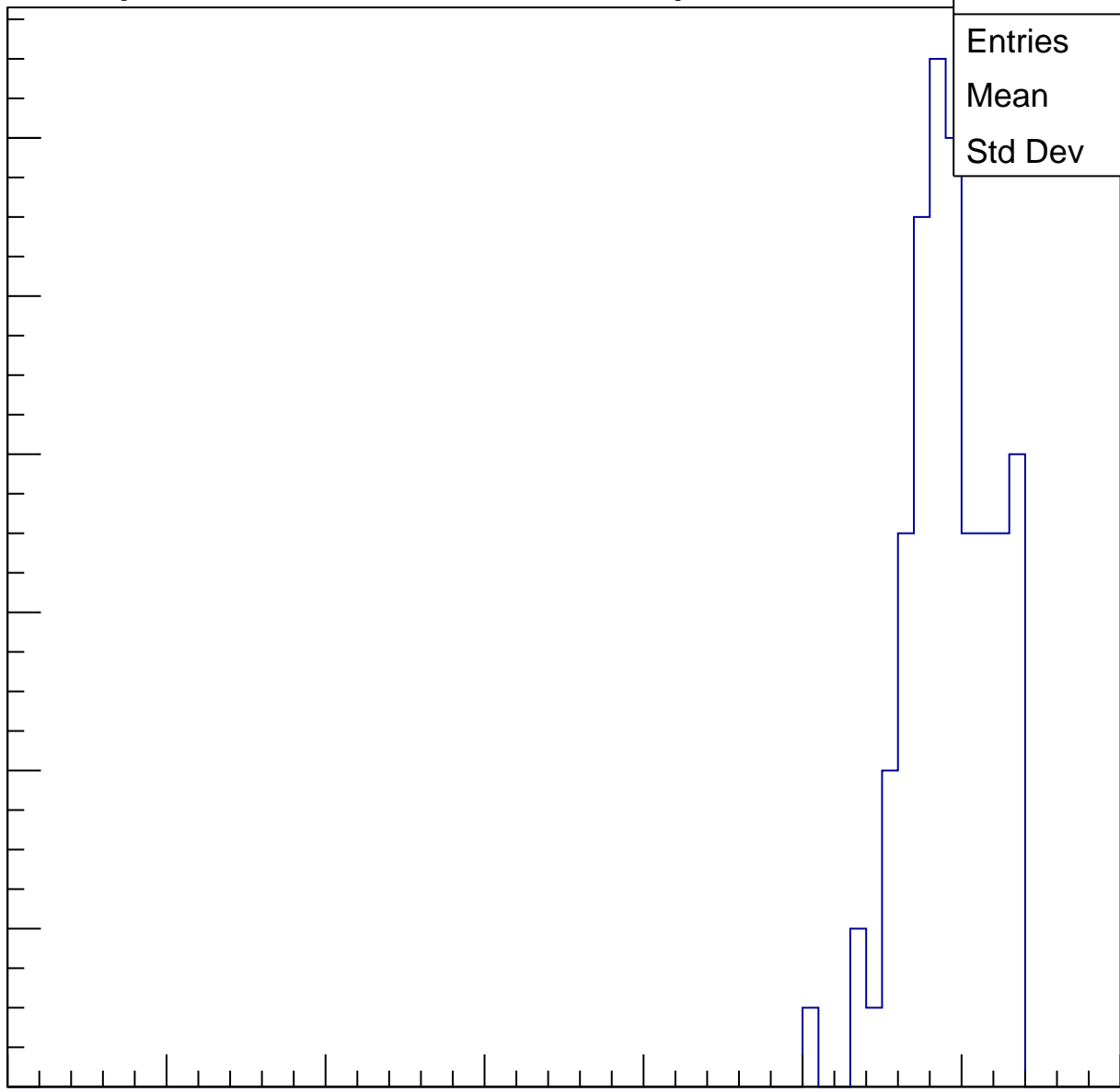
50

60

70

ampl

Entries	80
Mean	58.7
Std Dev	2.694



# B0L101S, U23-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	53
Mean	58.6
Std Dev	11.74

ampl

0 10 20 30 40 50 60 70

# B0L101S, U23-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

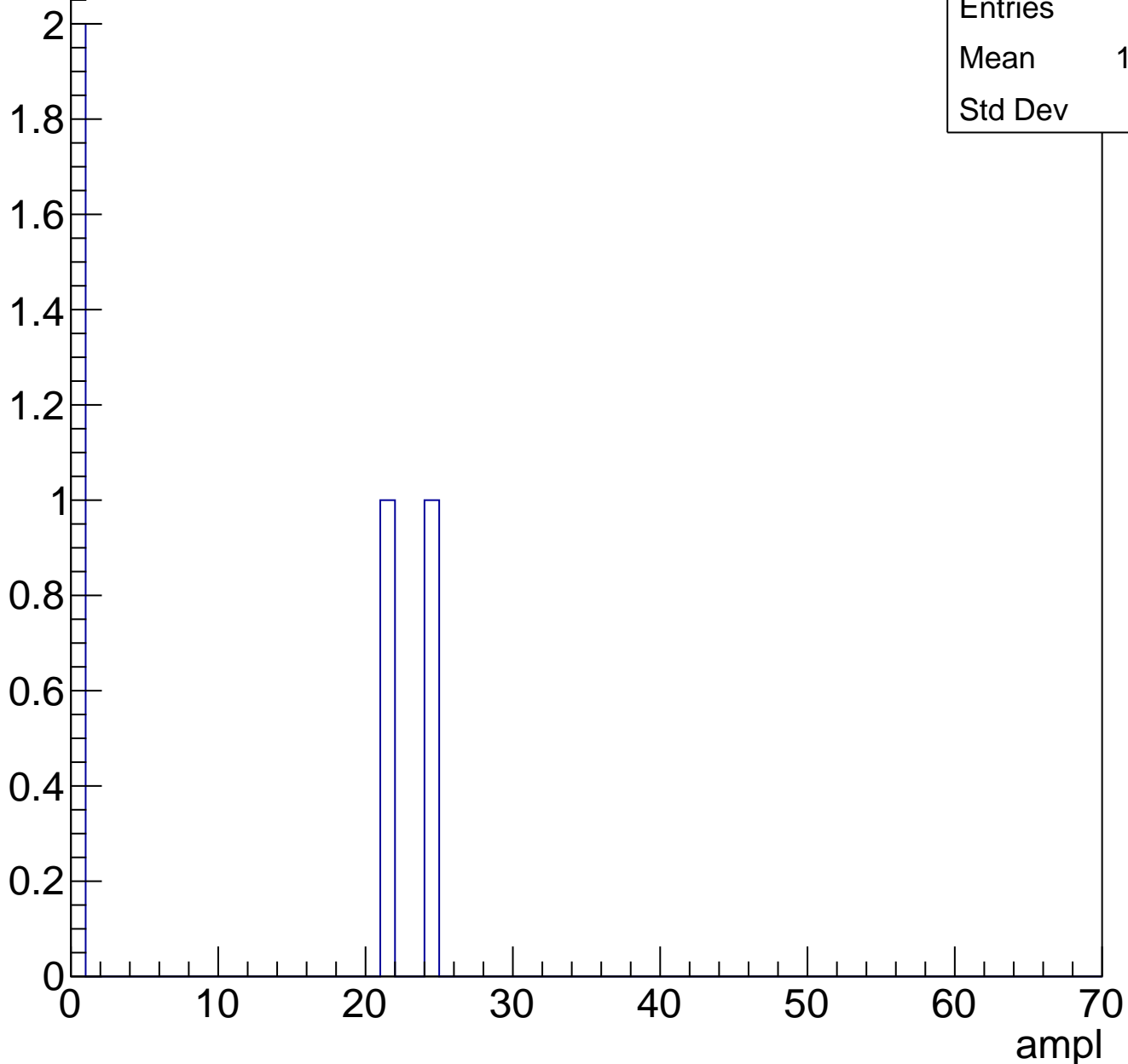




# B0L101S, U23-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	11.25
Std Dev	11.3

# B0L101S, U23-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	30.7
Std Dev	3.802

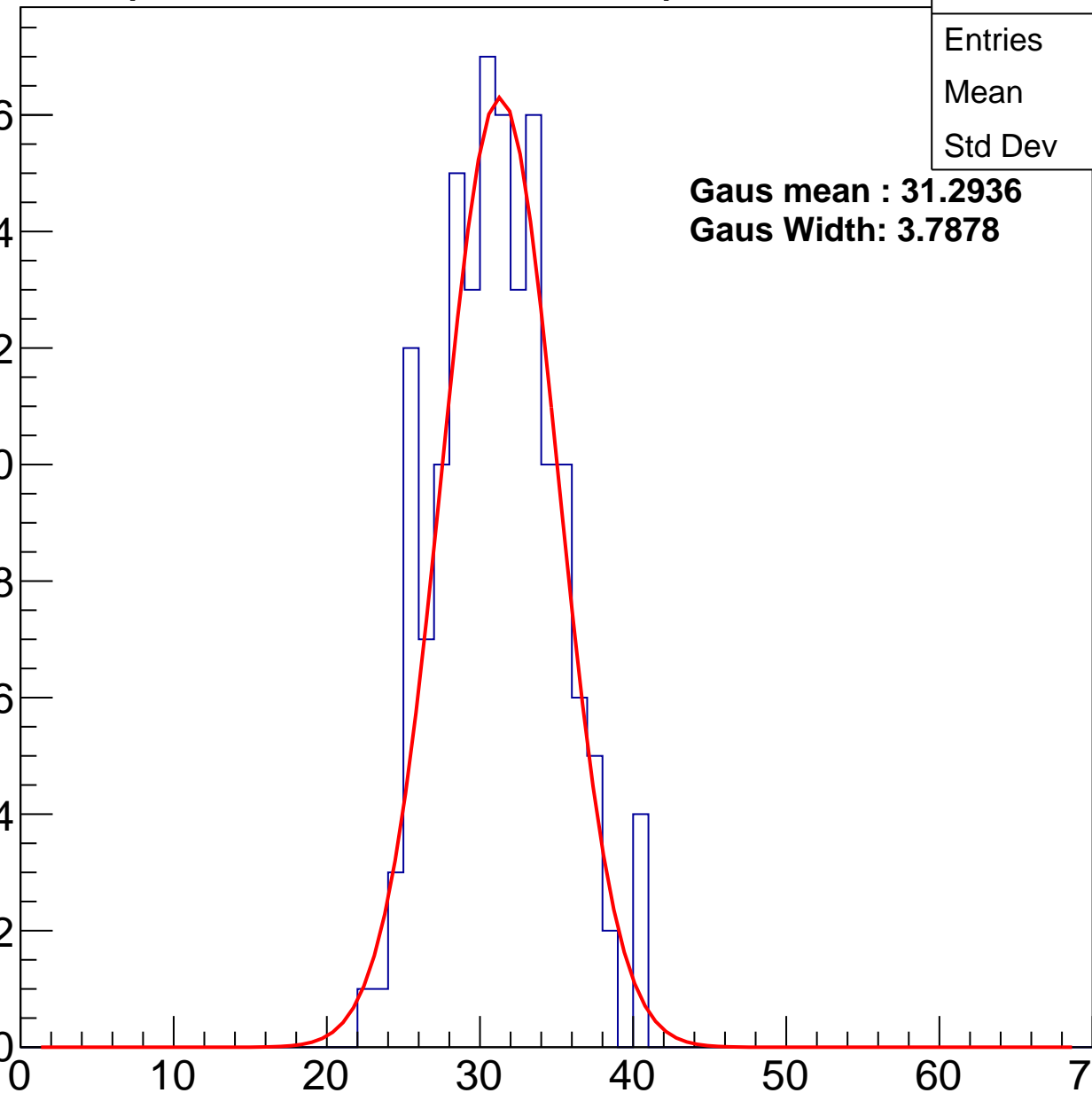
**Gaus mean : 31.2936**

**Gaus Width: 3.7878**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	38.42
Std Dev	3.892

**Gaus mean : 38.7064**

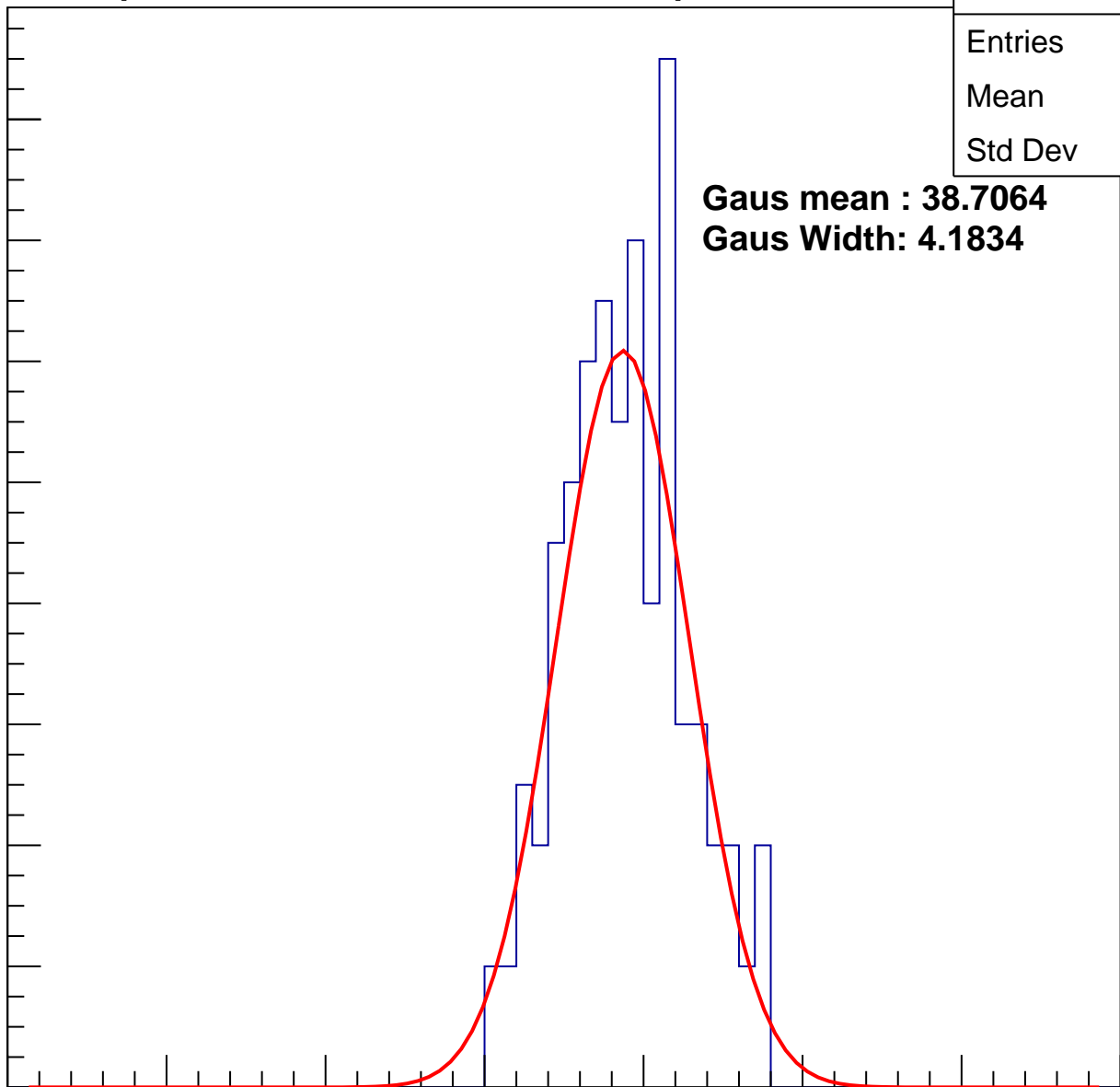
**Gaus Width: 4.1834**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

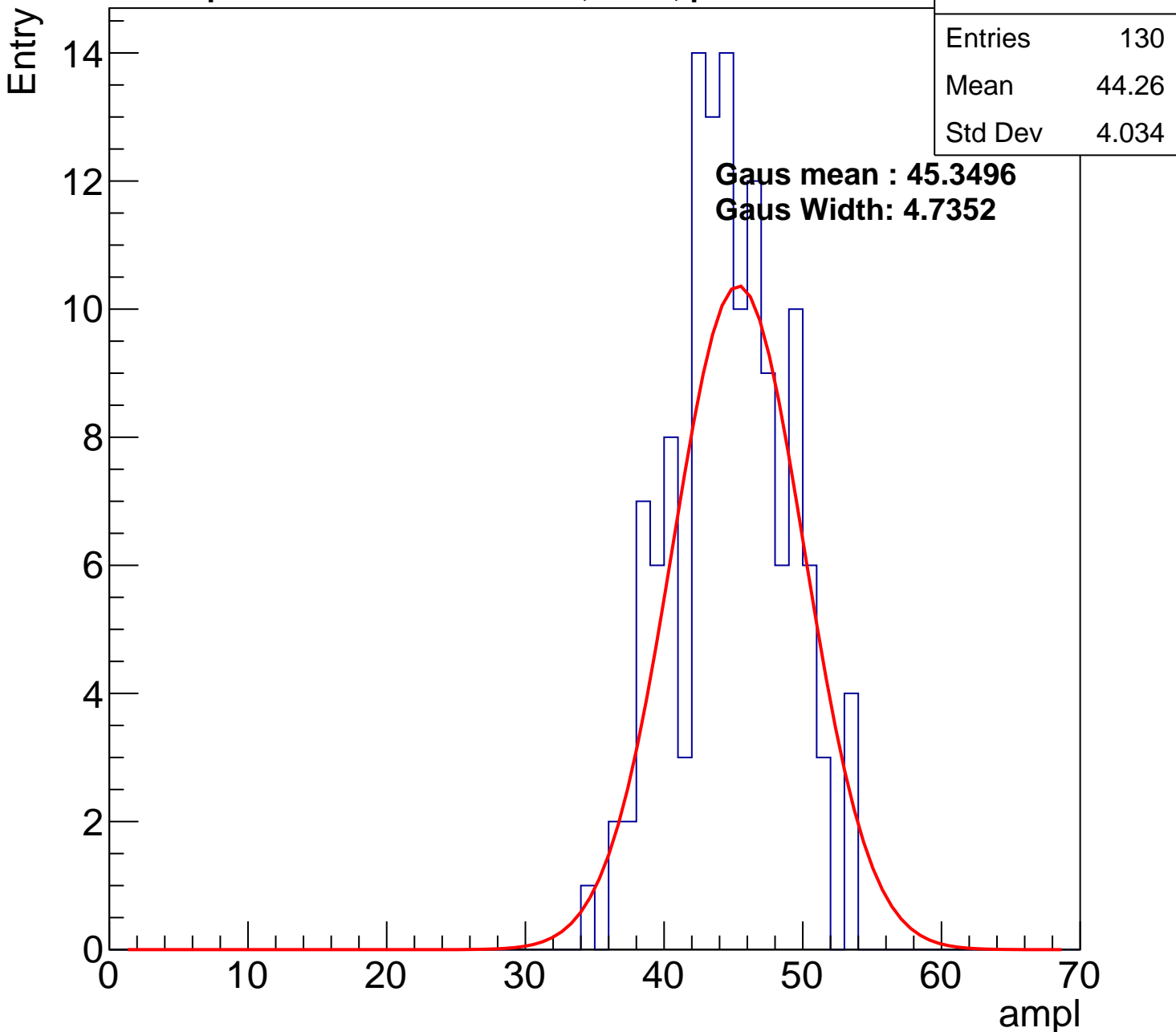
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	130
Mean	44.26
Std Dev	4.034

**Gaus mean : 45.3496**  
**Gaus Width: 4.7352**

0 10 20 30 40 50 60 70  
ampl

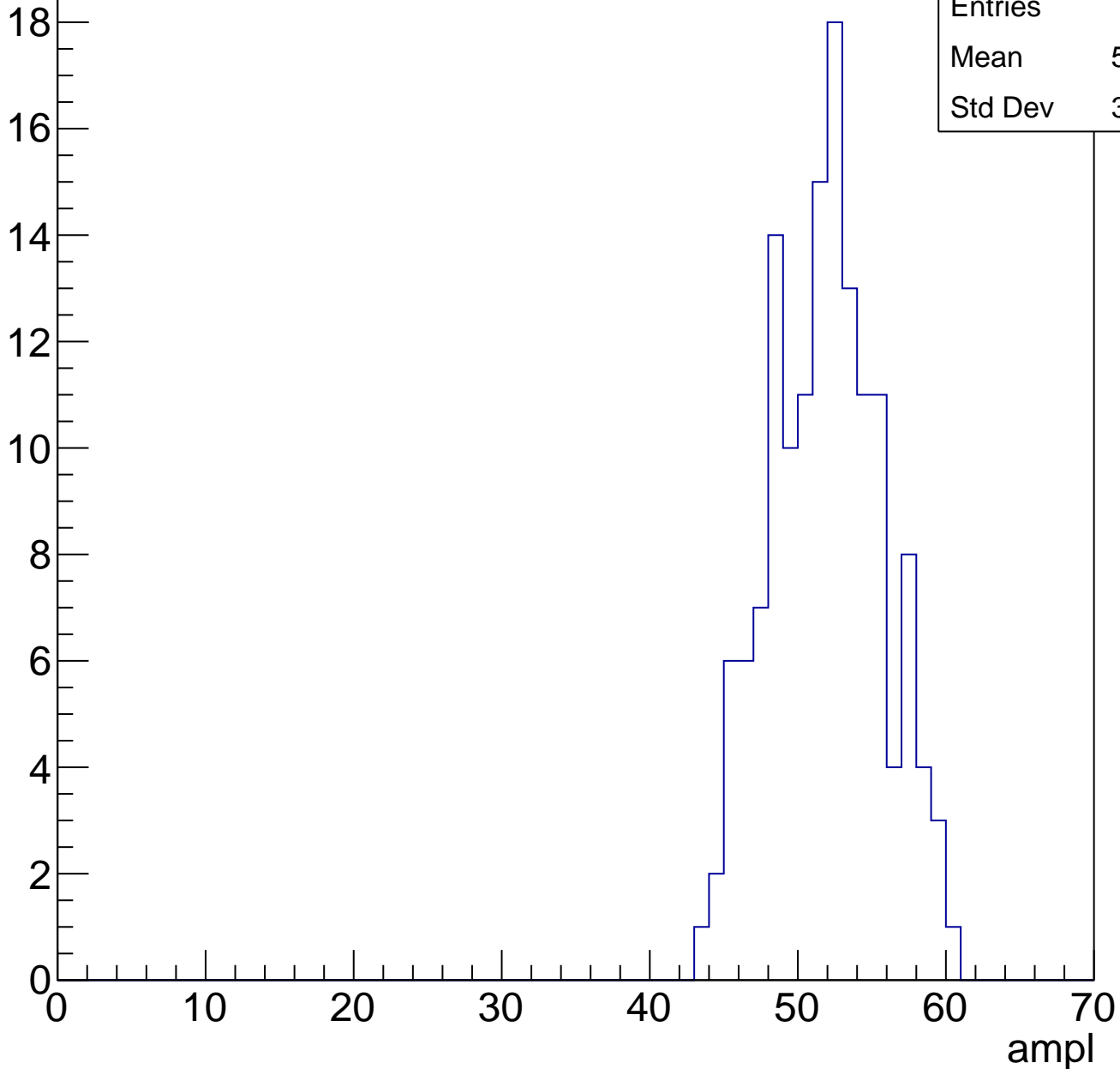


# B0L101S, U23-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	51.42
Std Dev	3.708

Entry



# B0L101S, U23-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

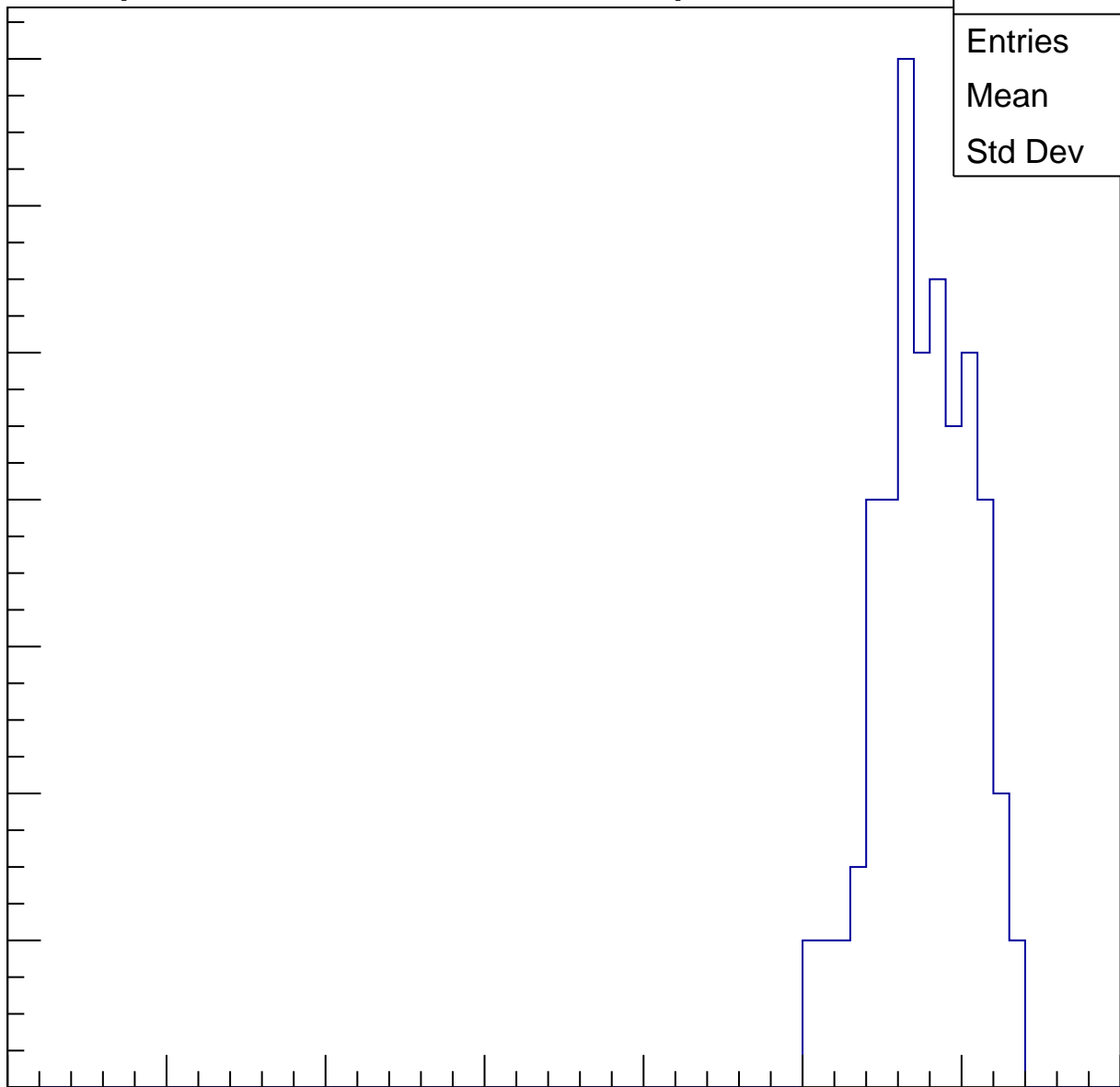
Entries	93
Mean	57.23
Std Dev	2.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch36, adc5

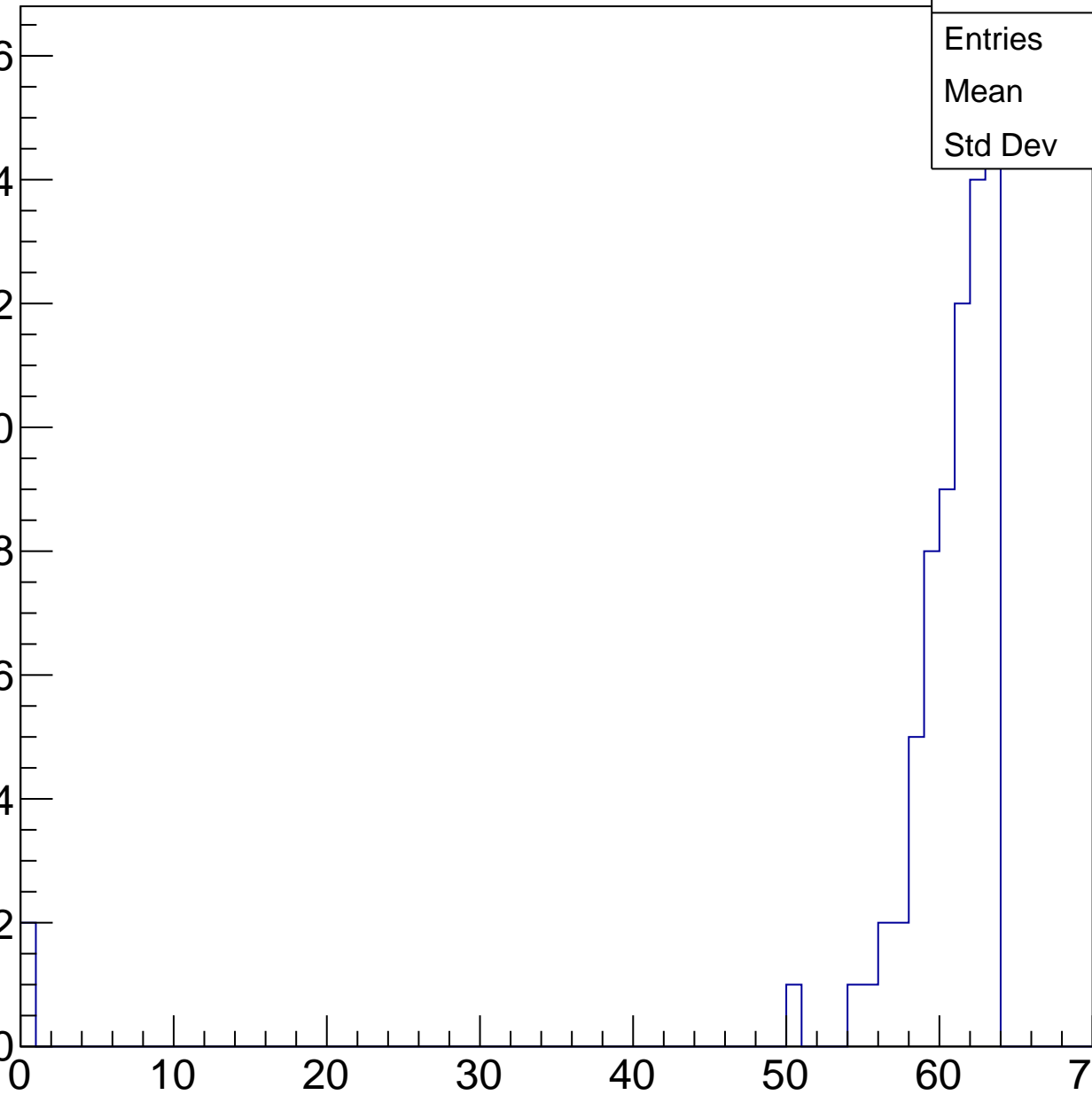
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	58.84
Std Dev	10.17

ampl



# B0L101S, U23-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

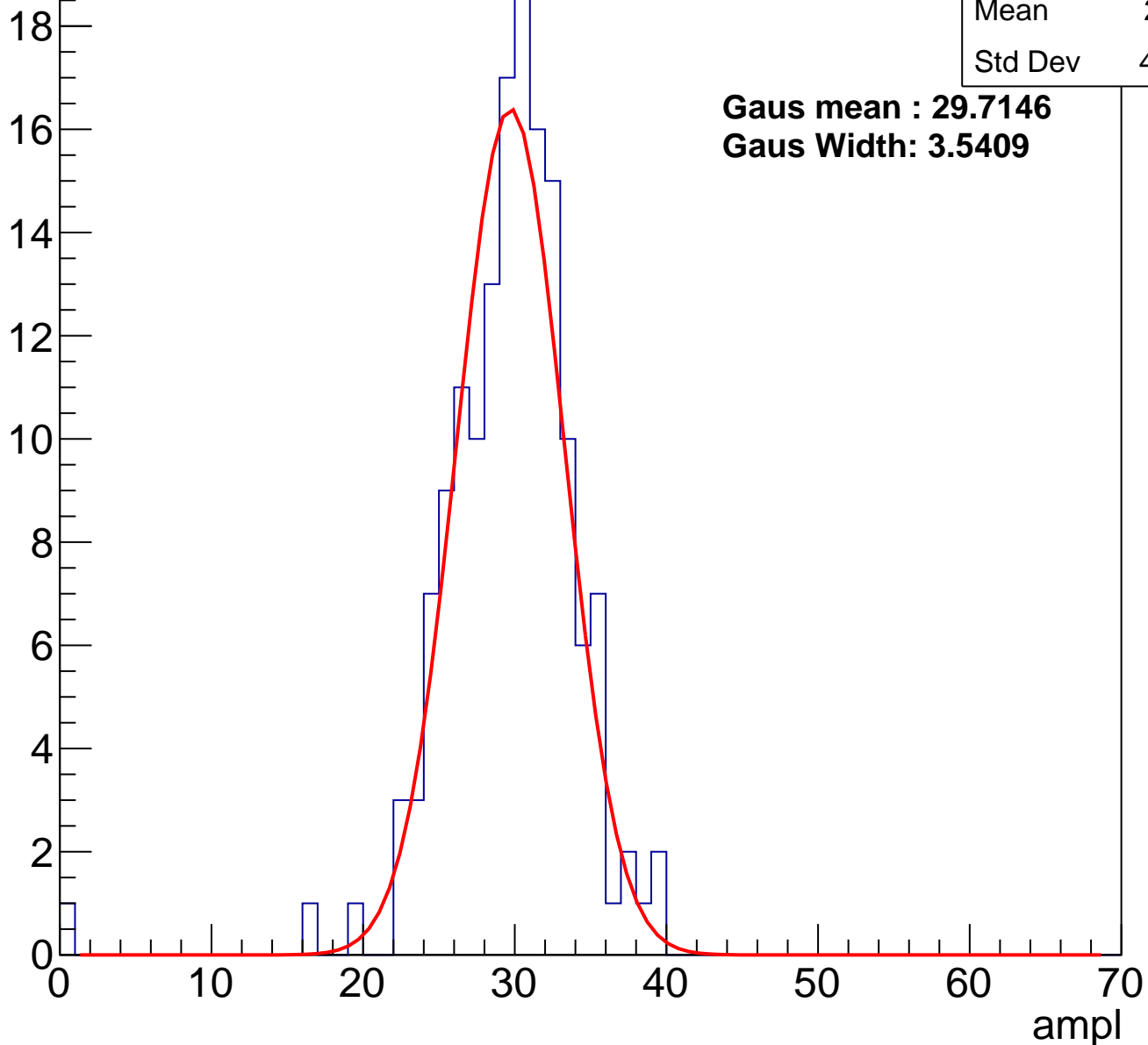


Entries	2
Mean	0
Std Dev	0

# B0L101S, U23-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

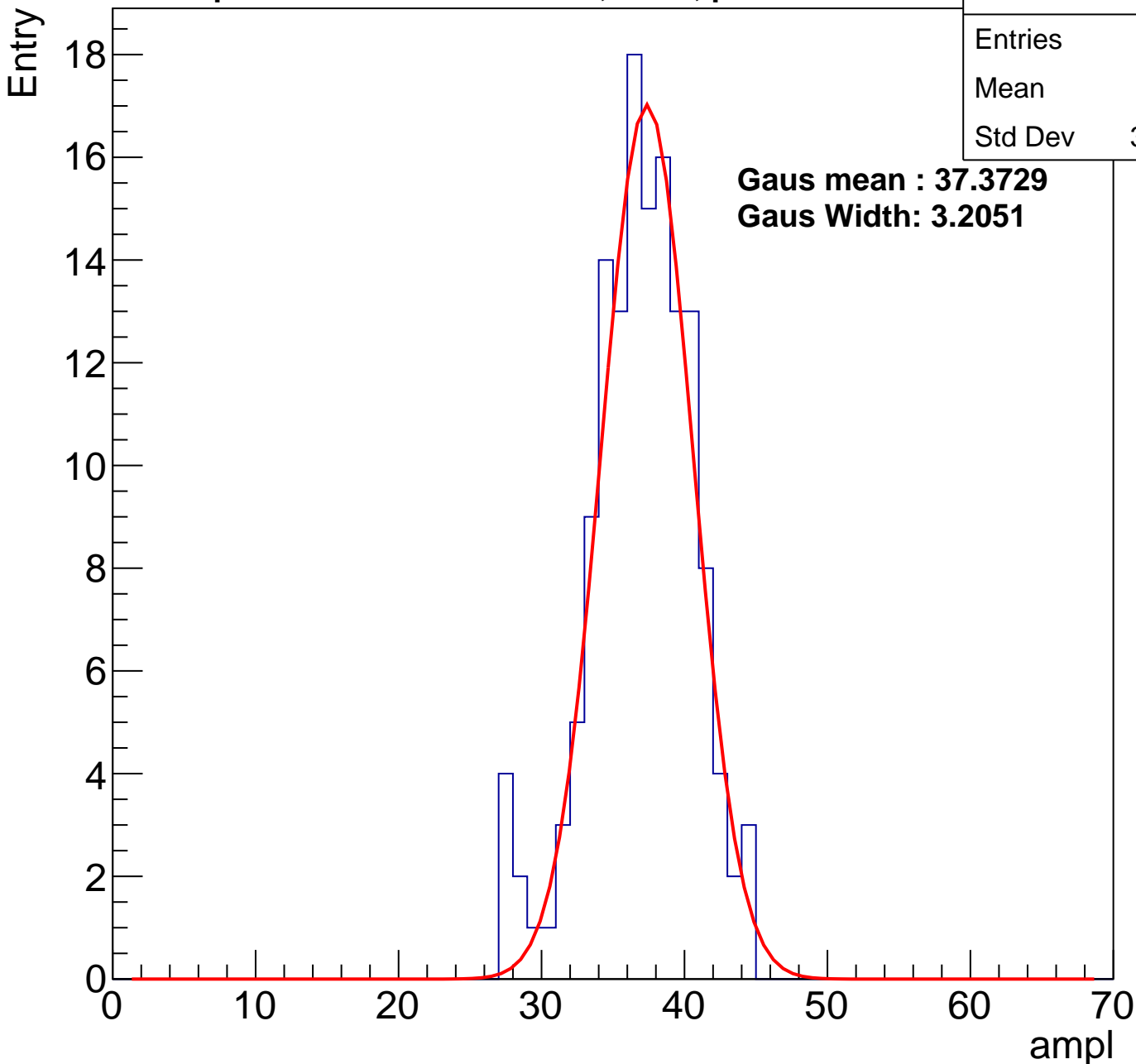


# B0L101S, U23-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	36.5
Std Dev	3.578

**Gaus mean : 37.3729**  
**Gaus Width: 3.2051**



# B0L101S, U23-ch37, adc2

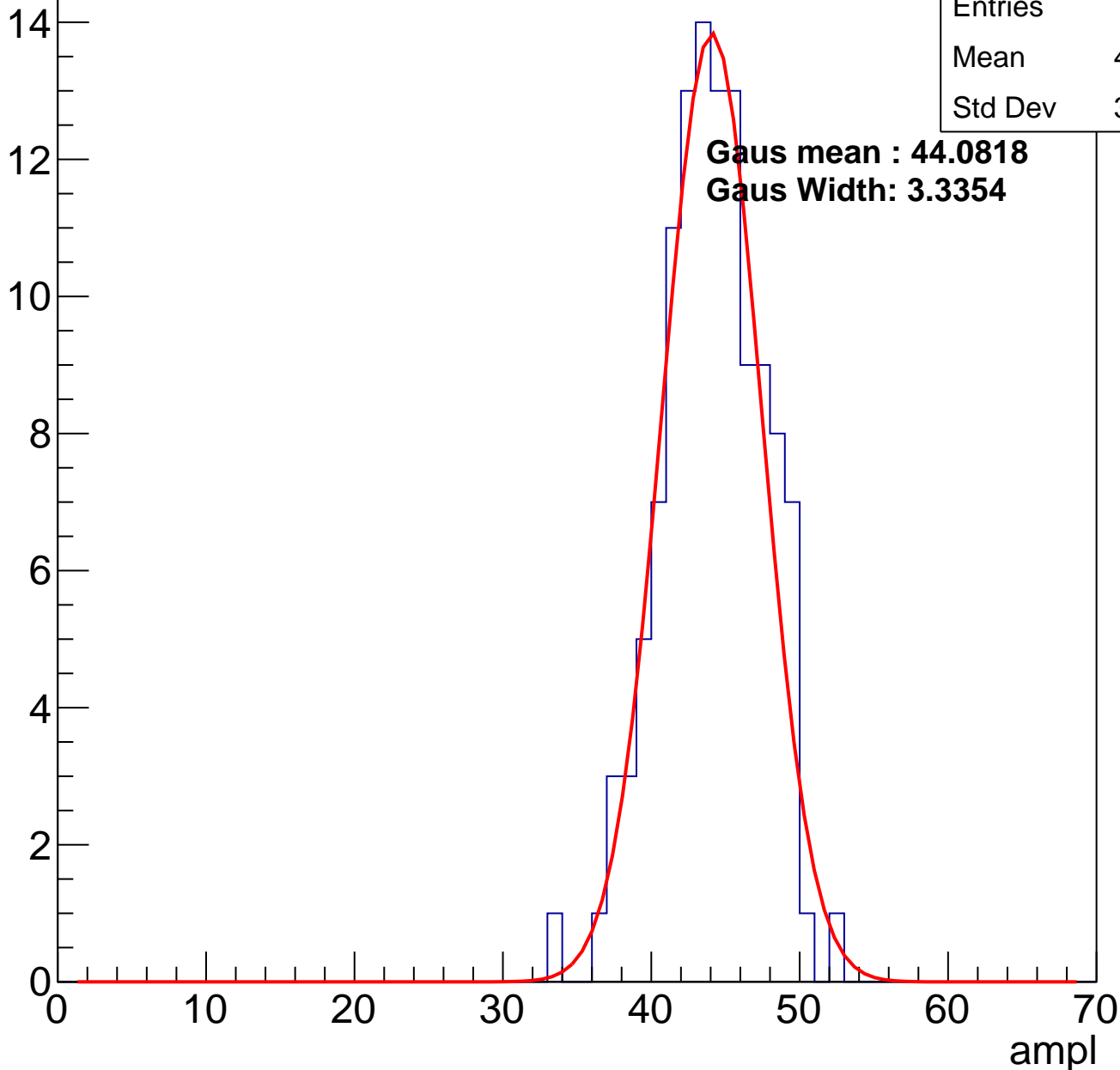
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	119
Mean	43.62
Std Dev	3.378

**Gaus mean : 44.0818**

**Gaus Width: 3.3354**



# B0L101S, U23-ch37, adc3

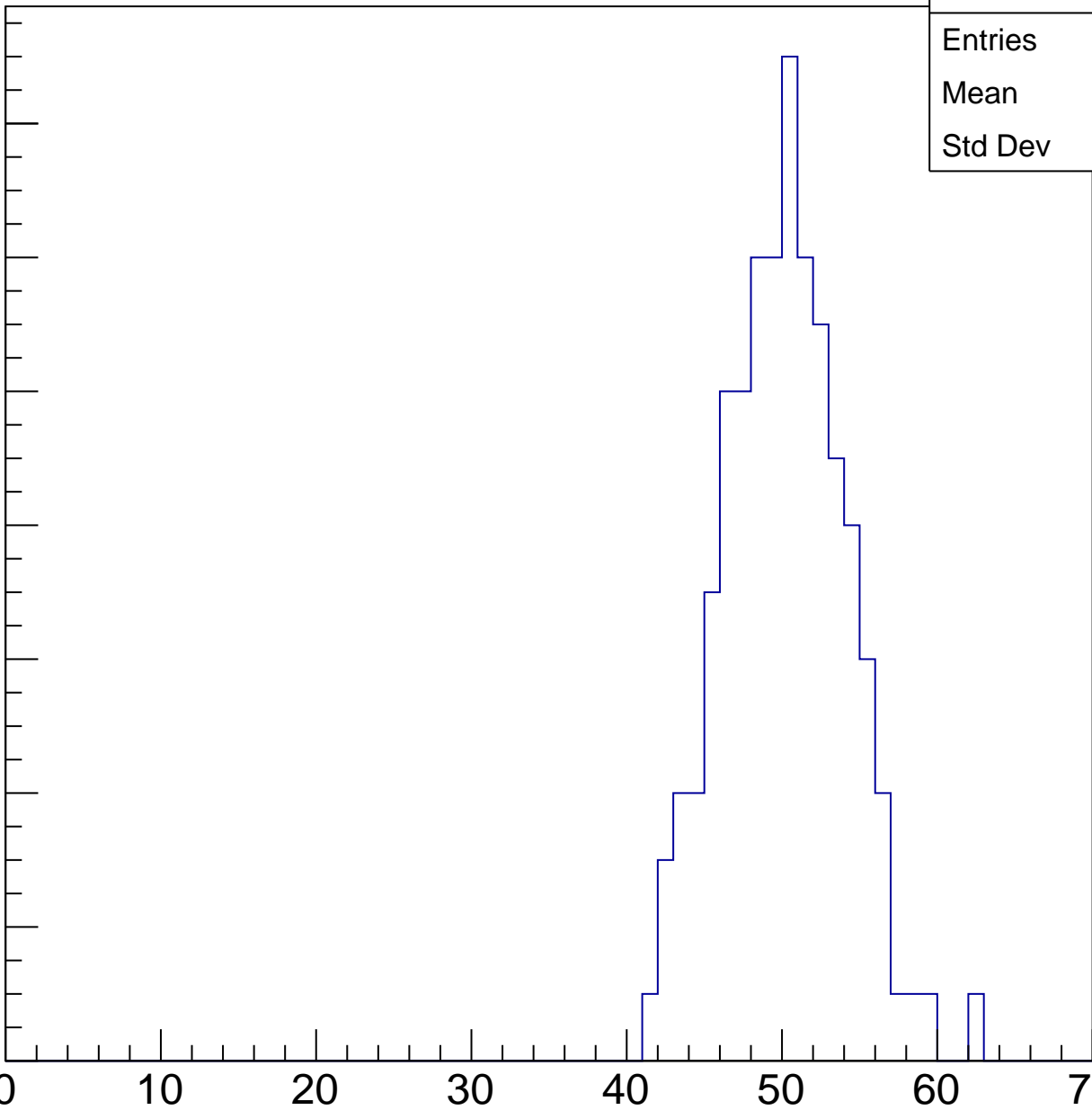
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	49.67
Std Dev	3.874

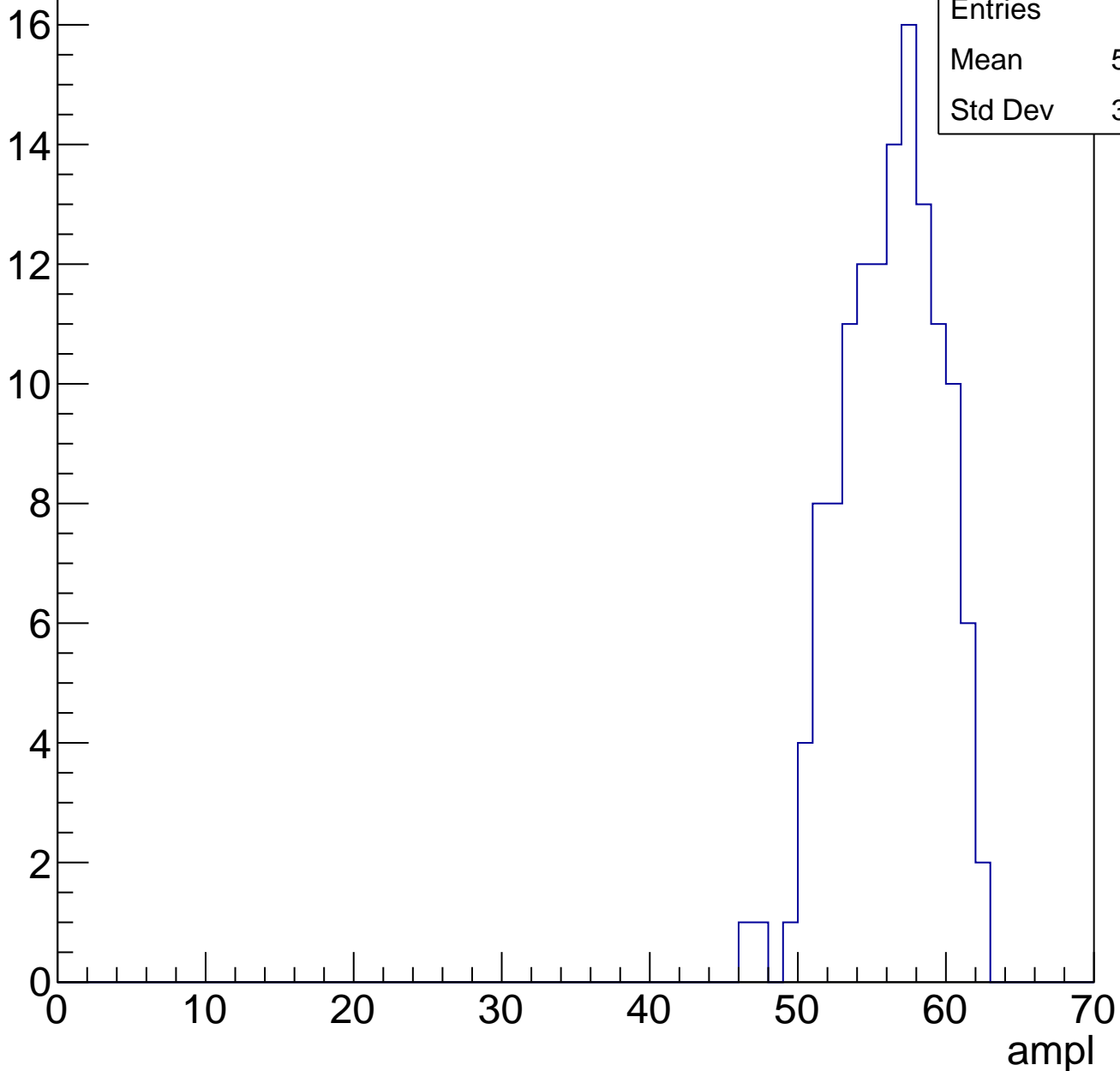
ampl



# B0L101S, U23-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

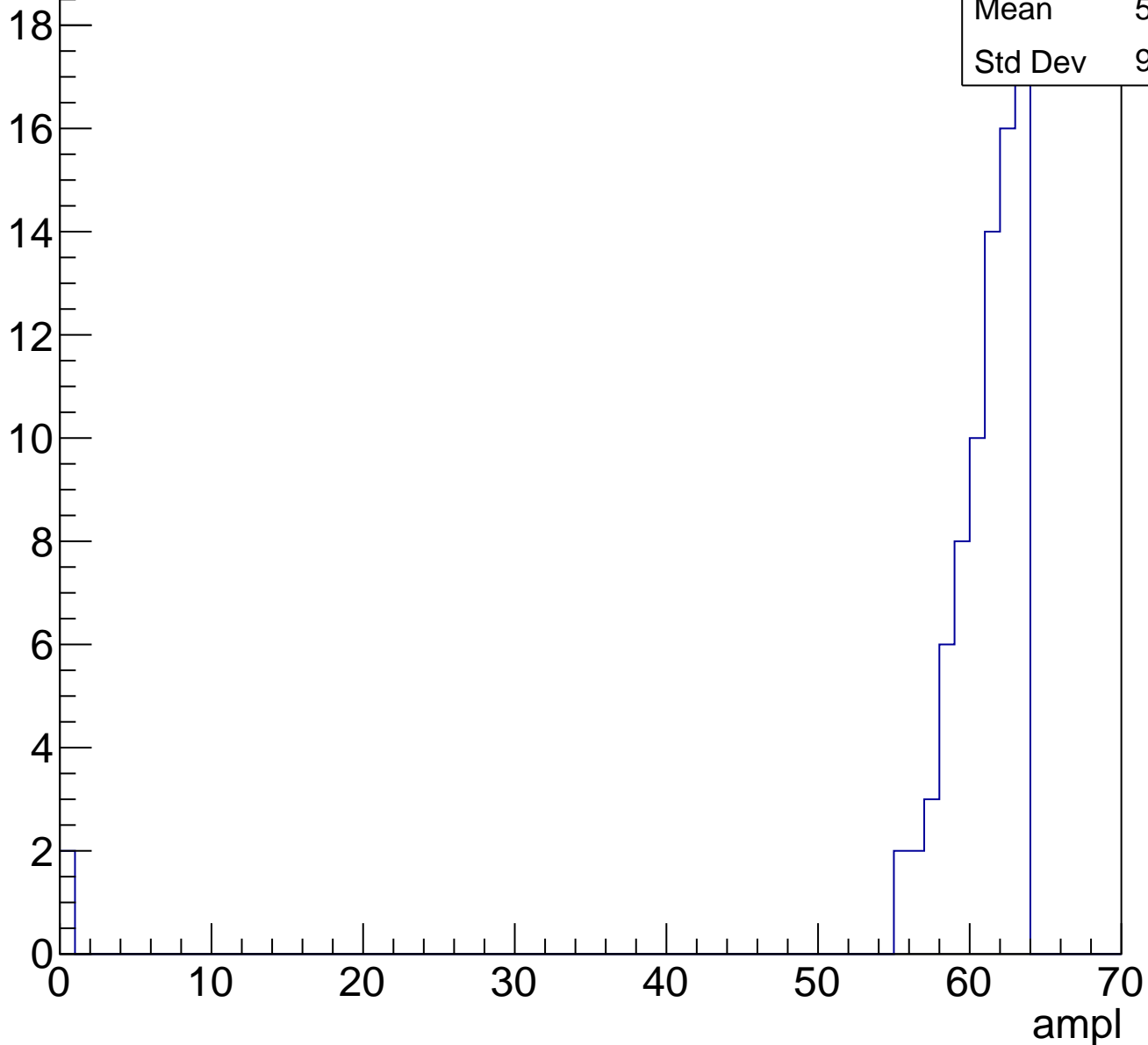
Entry



# B0L101S, U23-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	82
Mean	59.22
Std Dev	9.589

# B0L101S, U23-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch38, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	28.14
Std Dev	4.789

**Gaus mean : 28.7925**

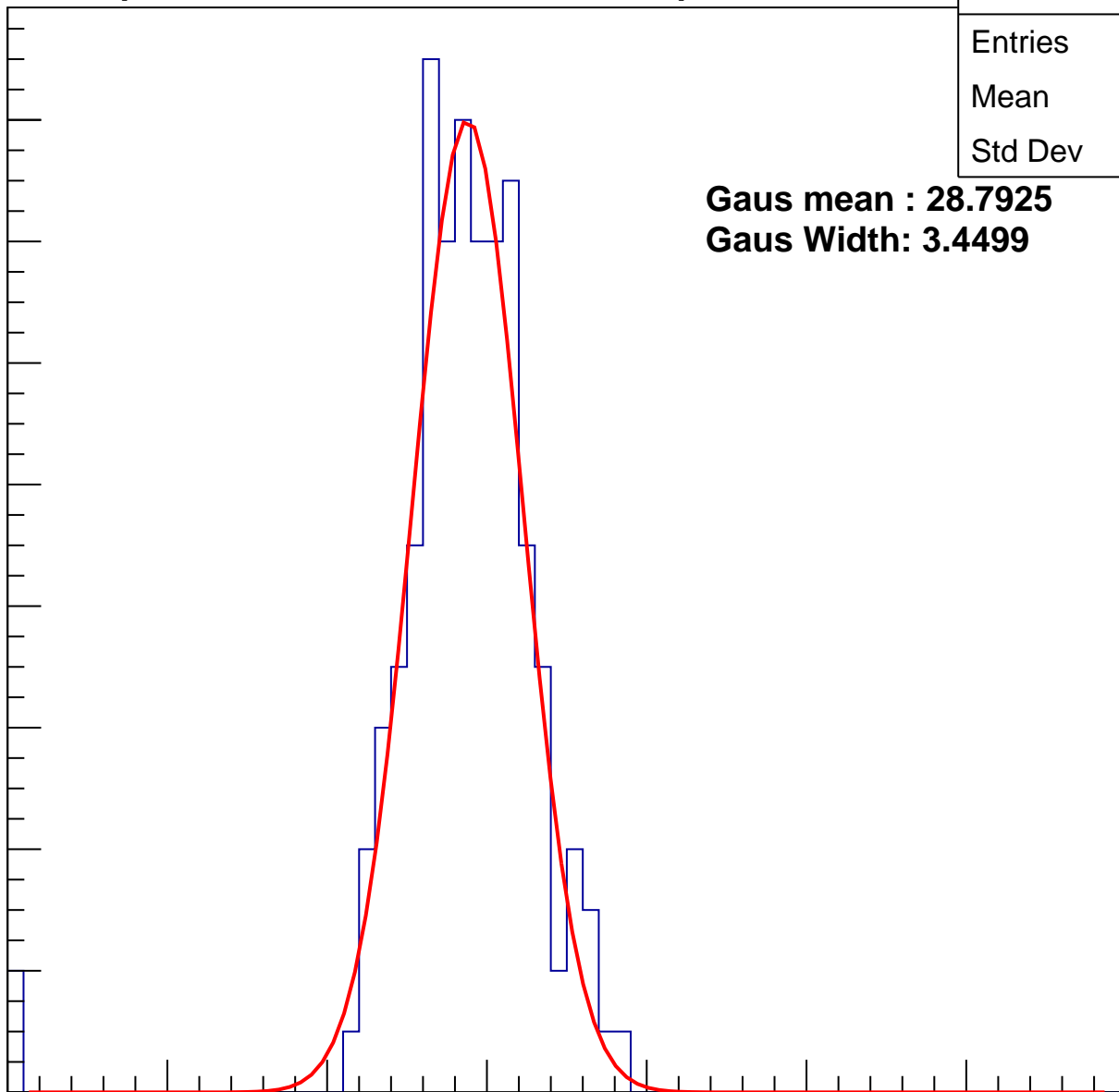
**Gaus Width: 3.4499**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	36.48
Std Dev	3.808

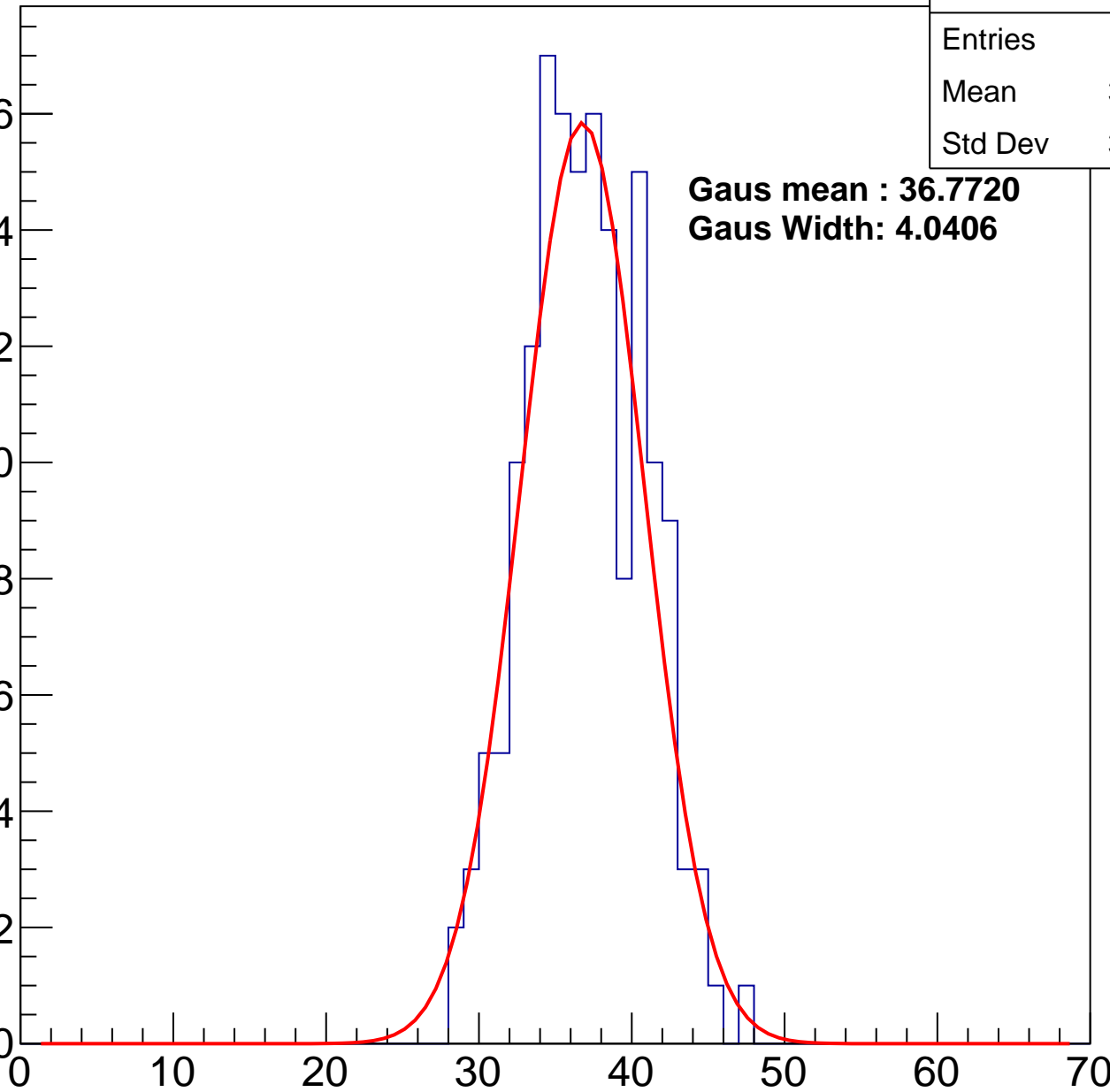
**Gaus mean : 36.7720**

**Gaus Width: 4.0406**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

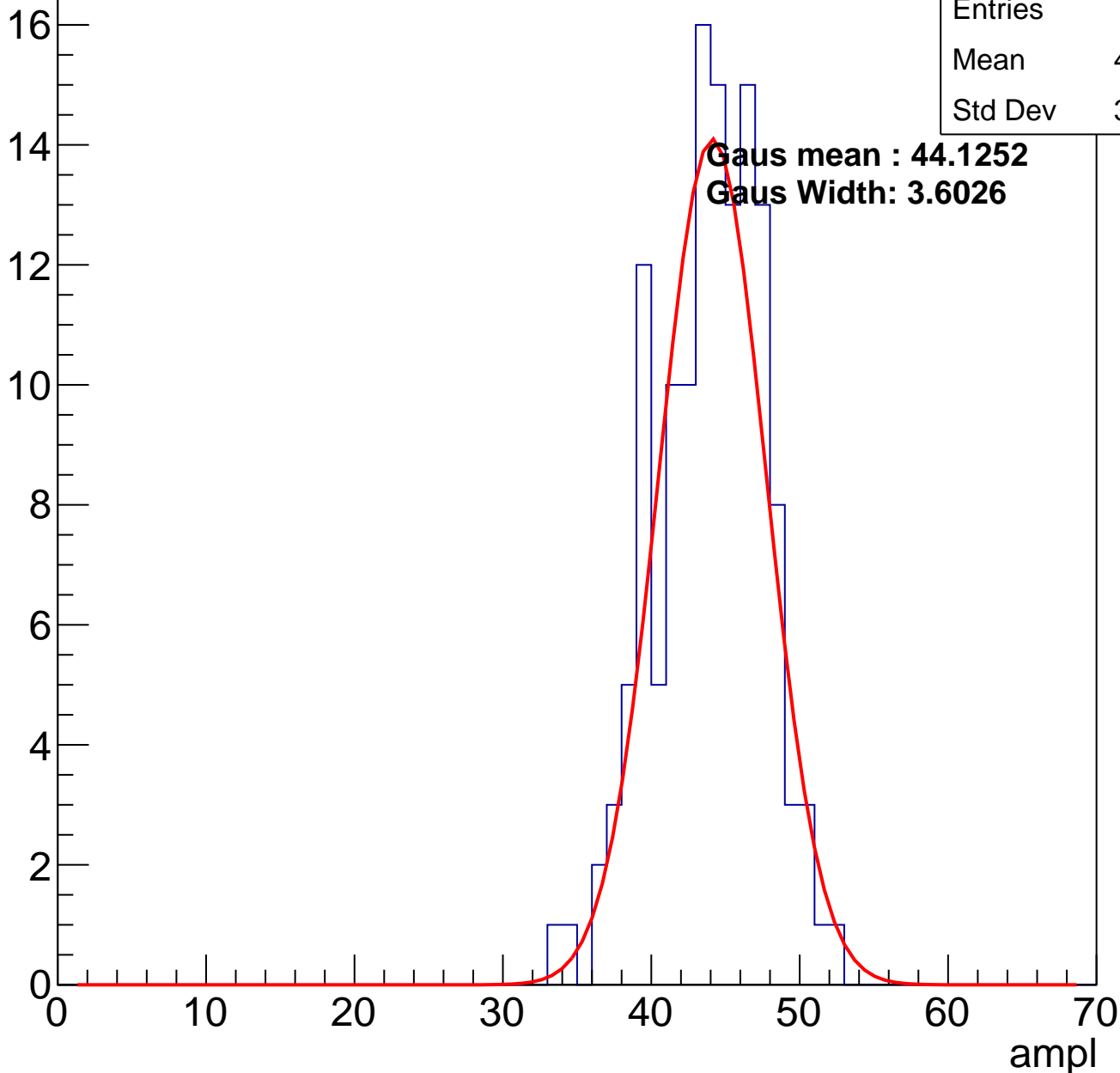
ampl



# B0L101S, U23-ch38, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch38, adc3

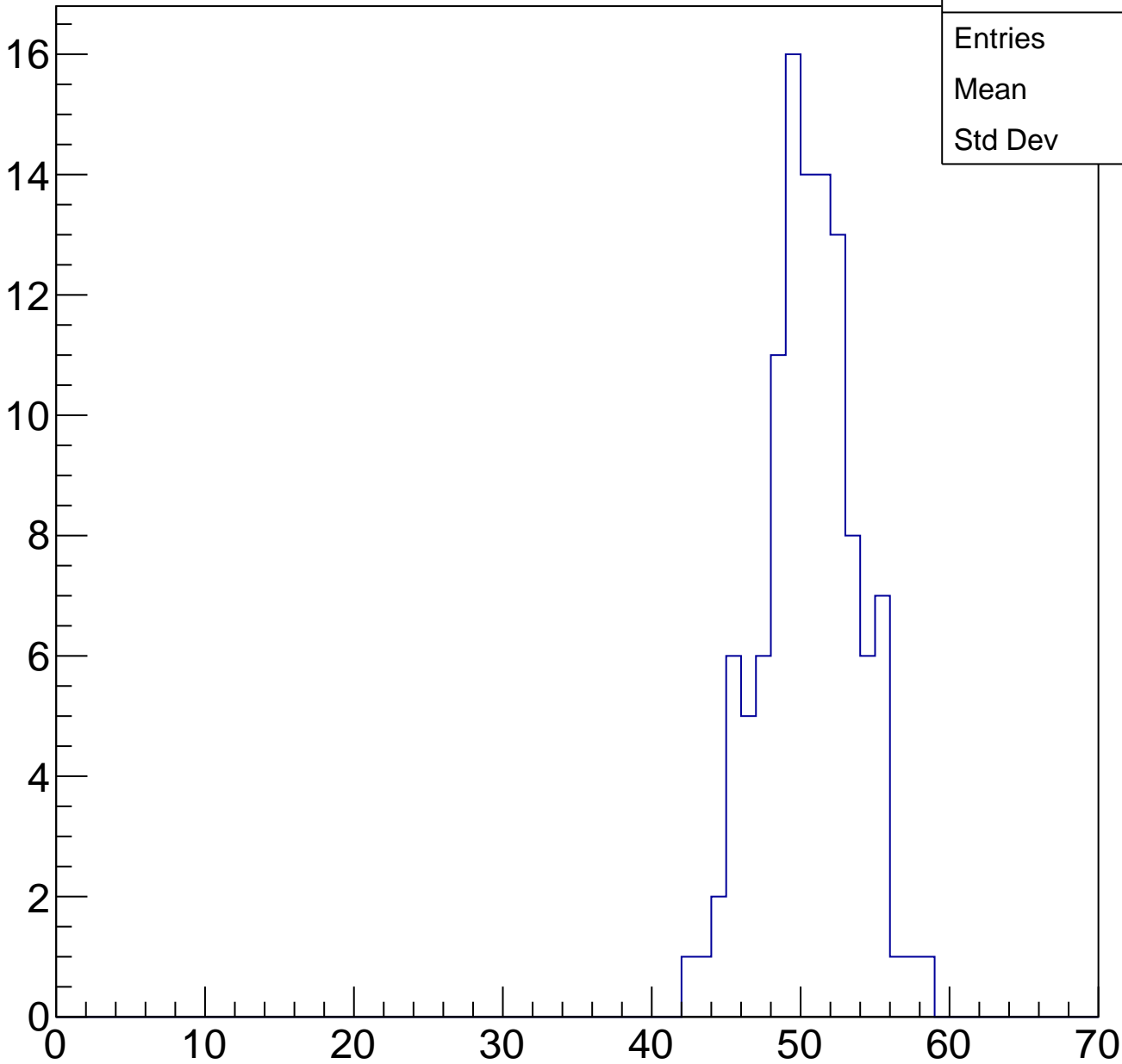
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	50.1
Std Dev	3.1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

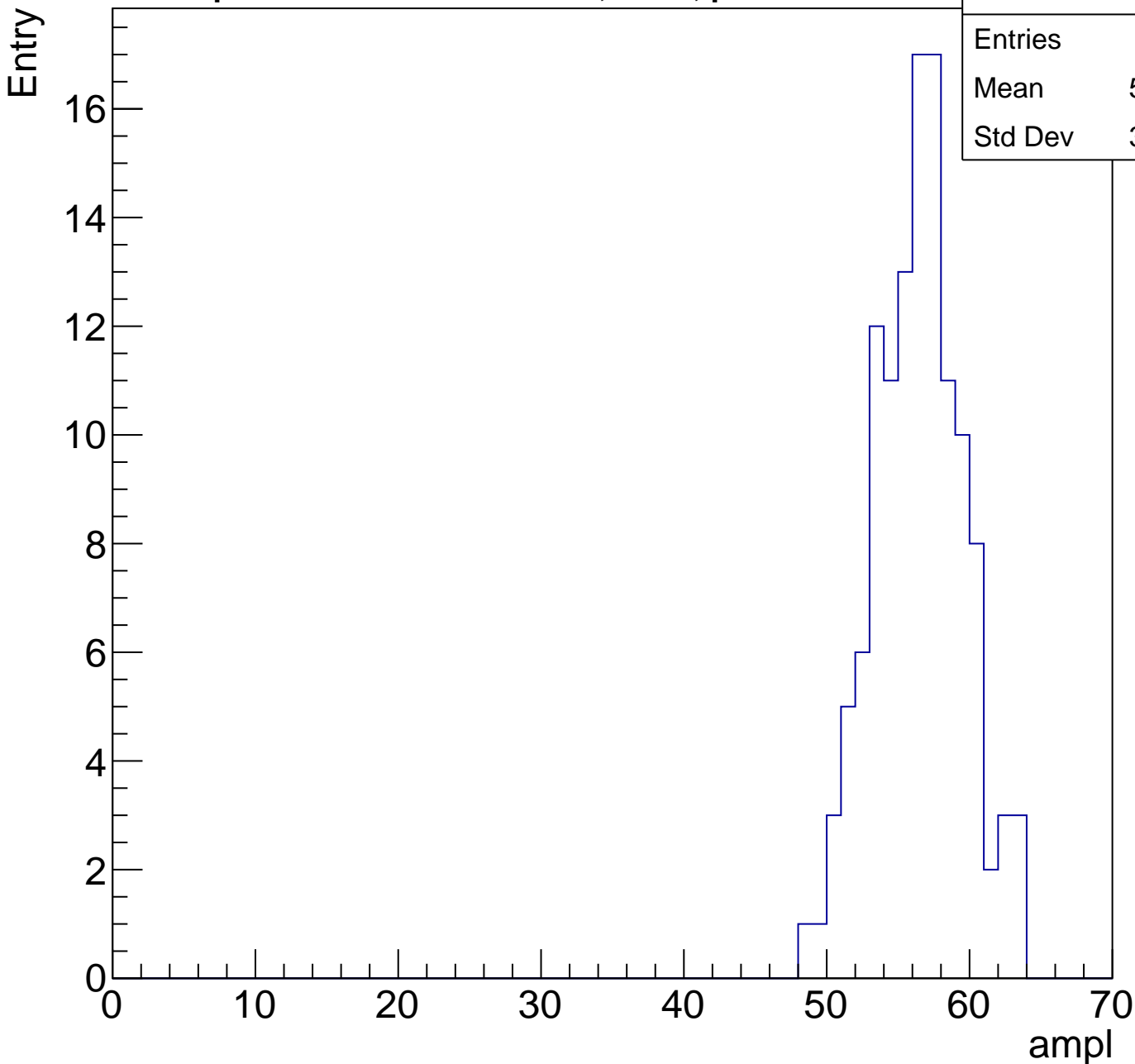
ampl



# B0L101S, U23-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	55.98
Std Dev	3.112



# B0L101S, U23-ch38, adc5

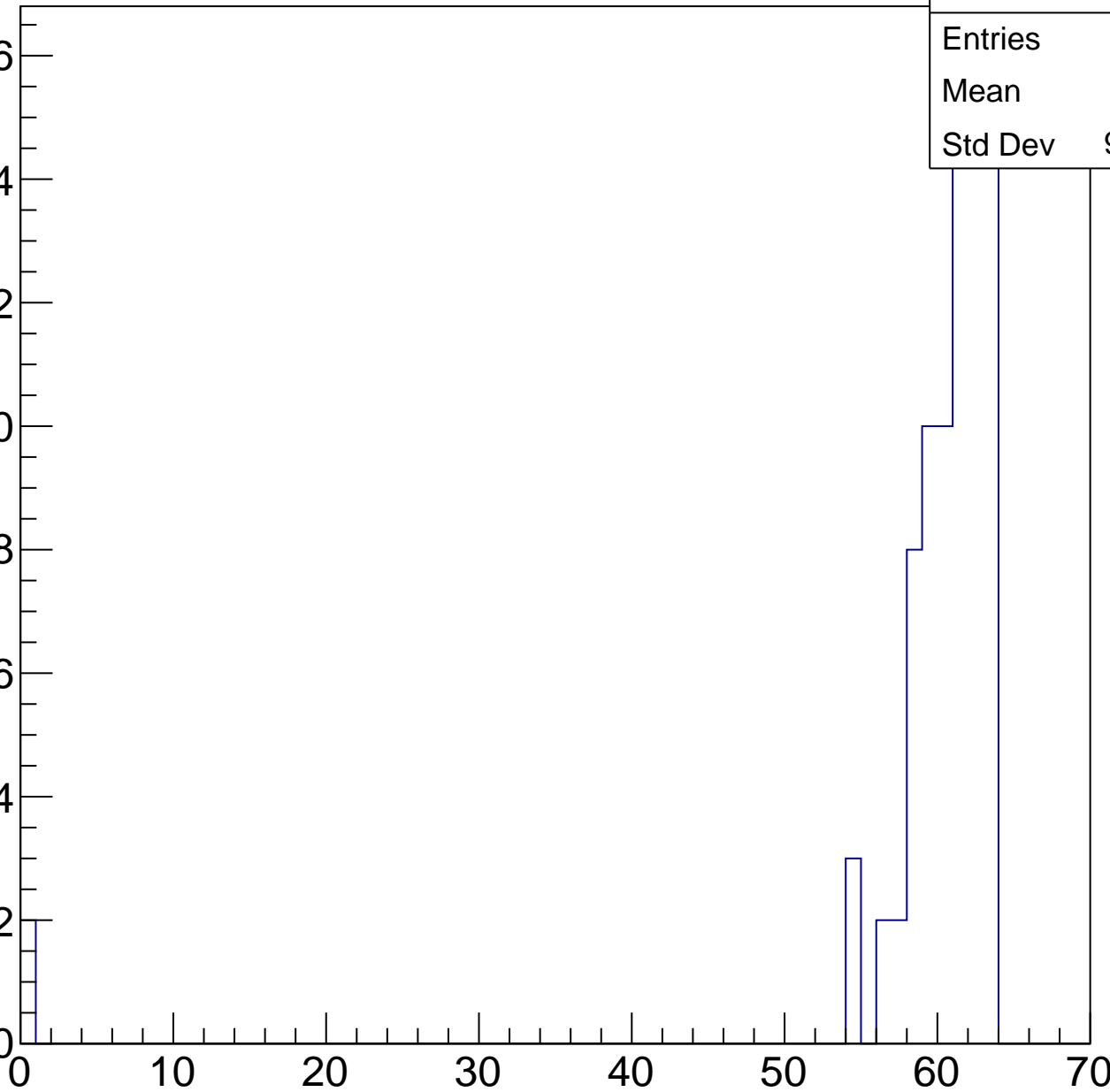
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	84
Mean	59
Std Dev	9.472

ampl



# B0L101S, U23-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

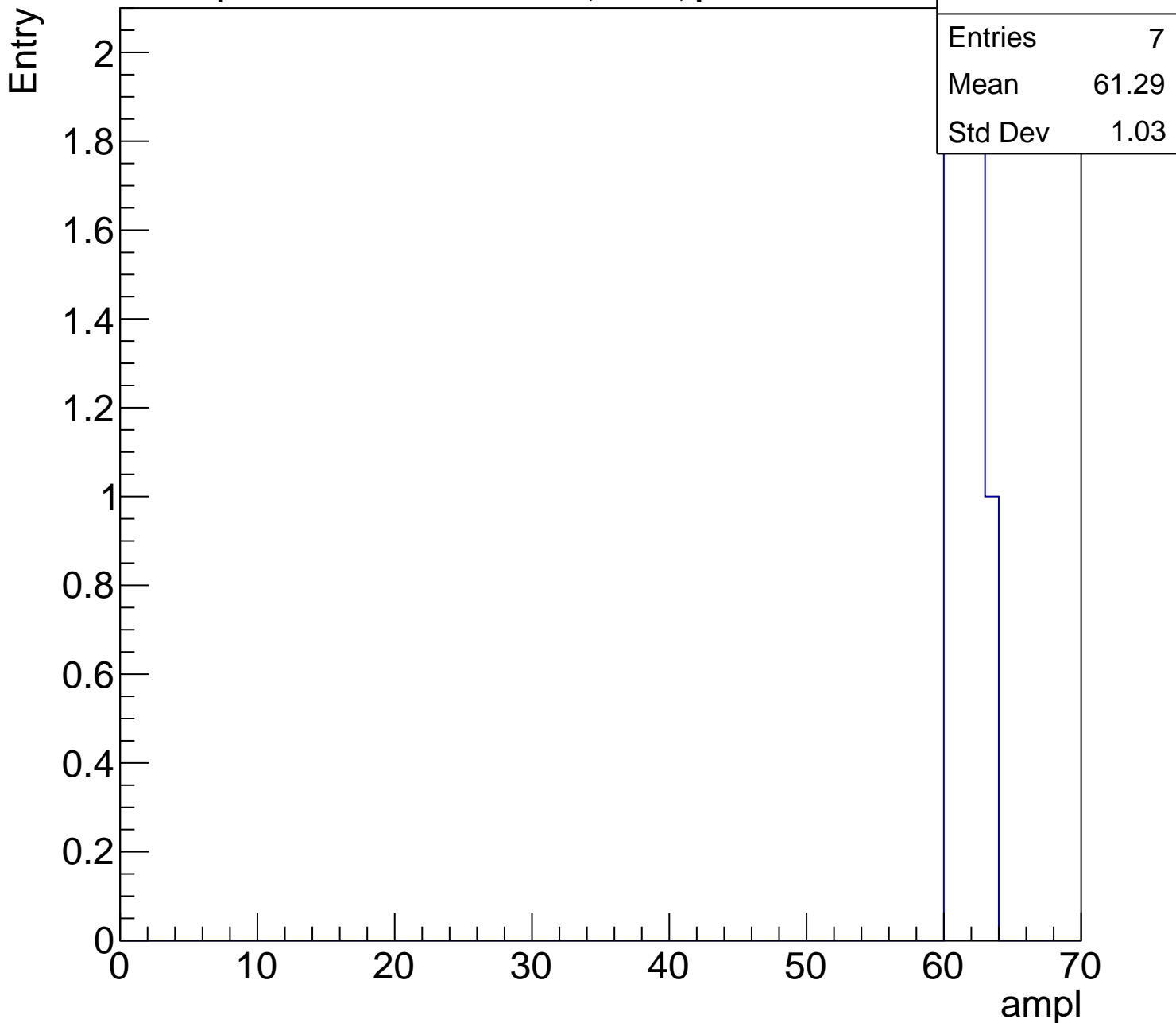
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.29
Std Dev	1.03

0 10 20 30 40 50 60 70

ampl





# B0L101S, U23-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch39, adc0

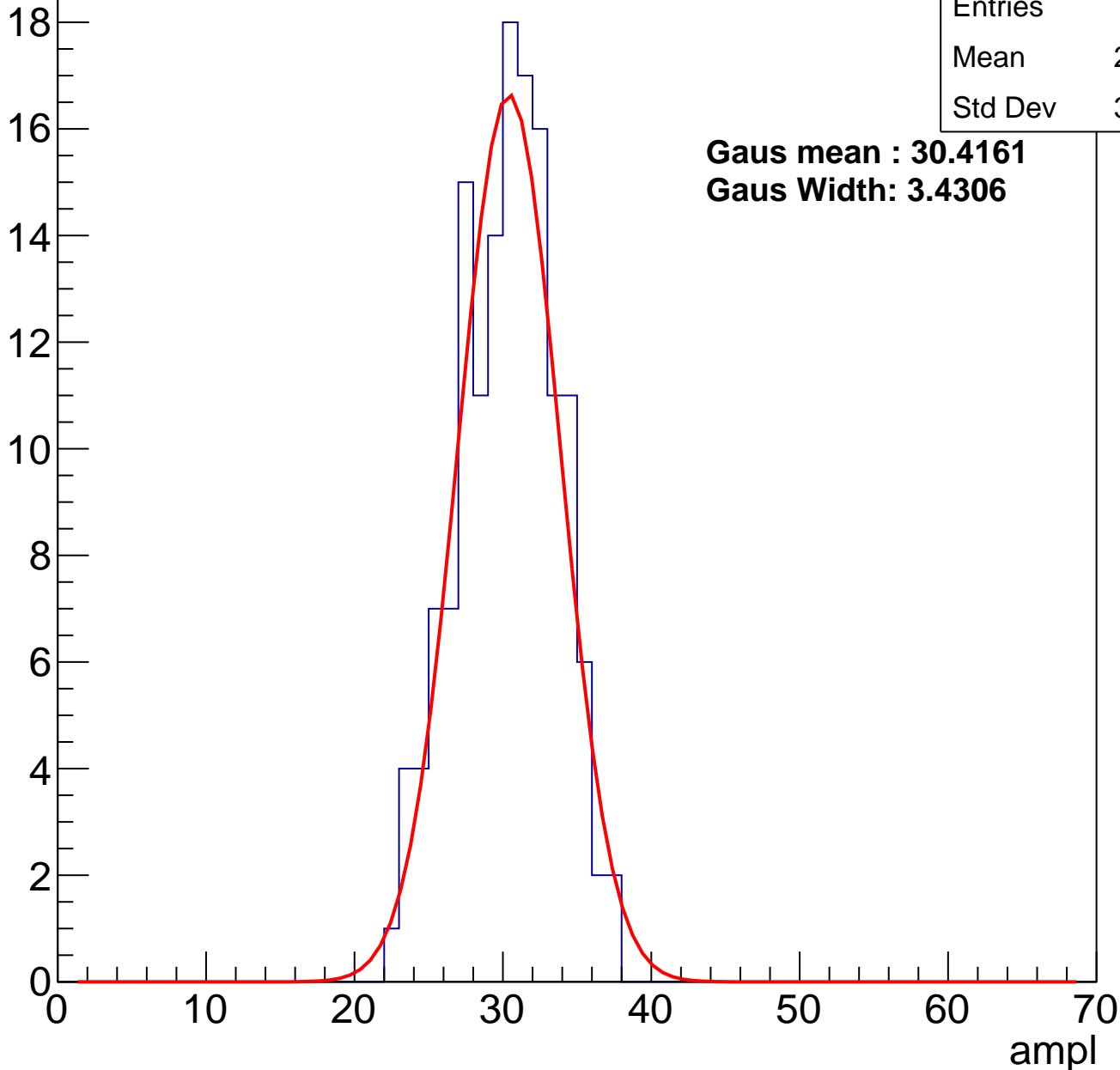
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	29.85
Std Dev	3.265

**Gaus mean : 30.4161**

**Gaus Width: 3.4306**

Entry



# B0L101S, U23-ch39, adc1

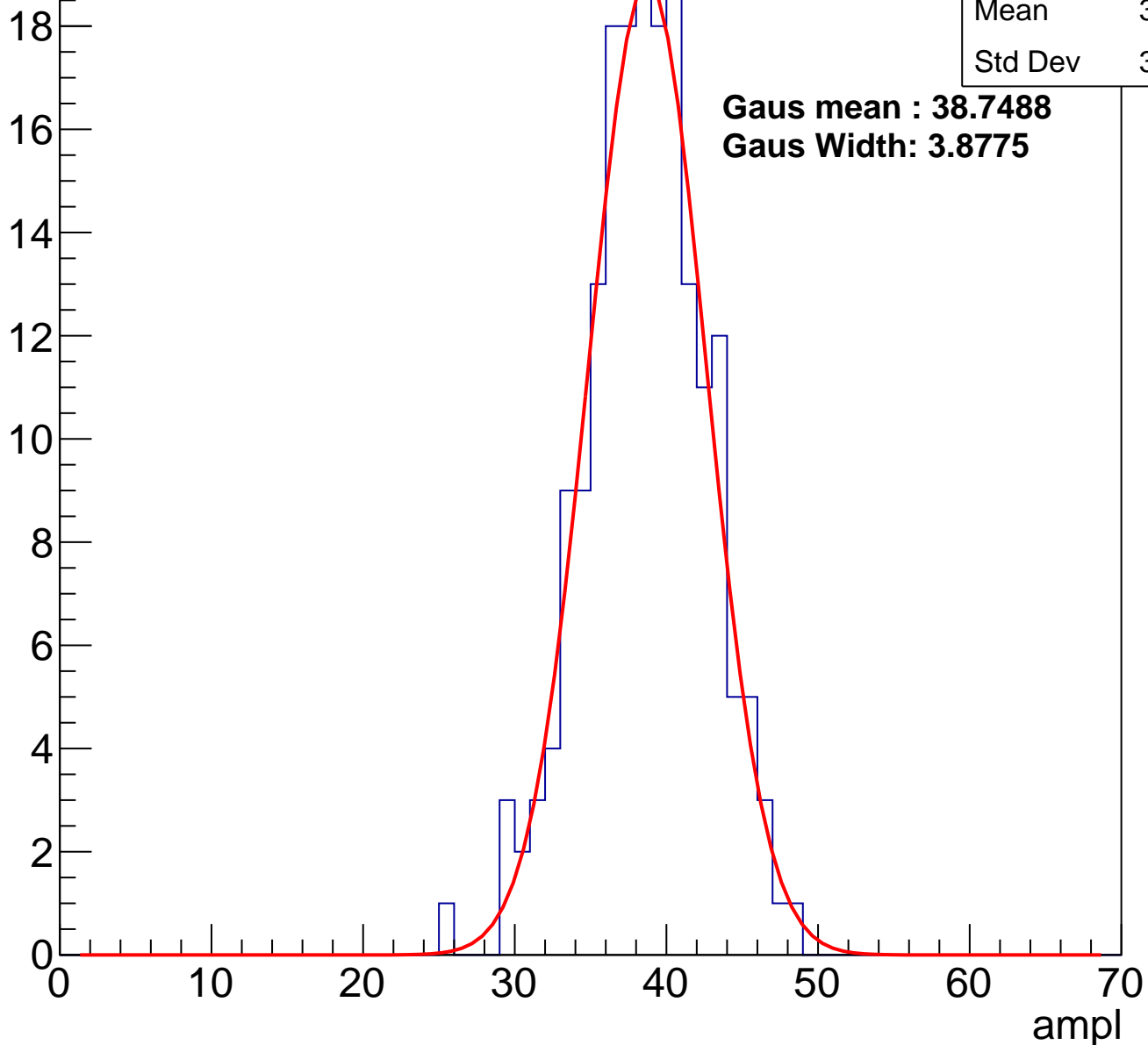
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	187
Mean	38.17
Std Dev	3.929

**Gaus mean : 38.7488**

**Gaus Width: 3.8775**

Entry



# B0L101S, U23-ch39, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

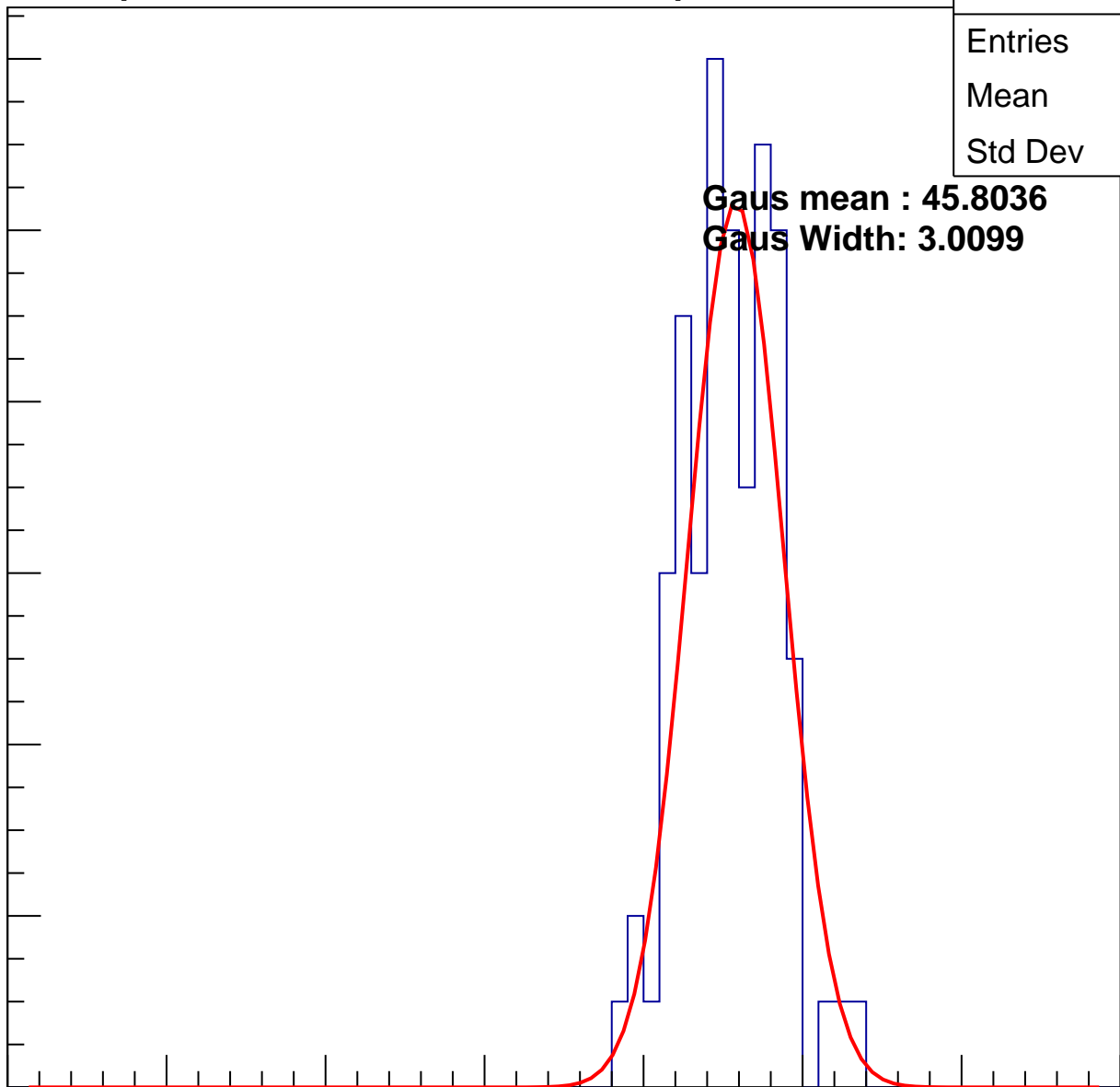
Entries	83
Mean	45.01
Std Dev	2.964

**Gaus mean : 45.8036**

**Gaus Width: 3.0099**

0 10 20 30 40 50 60 70

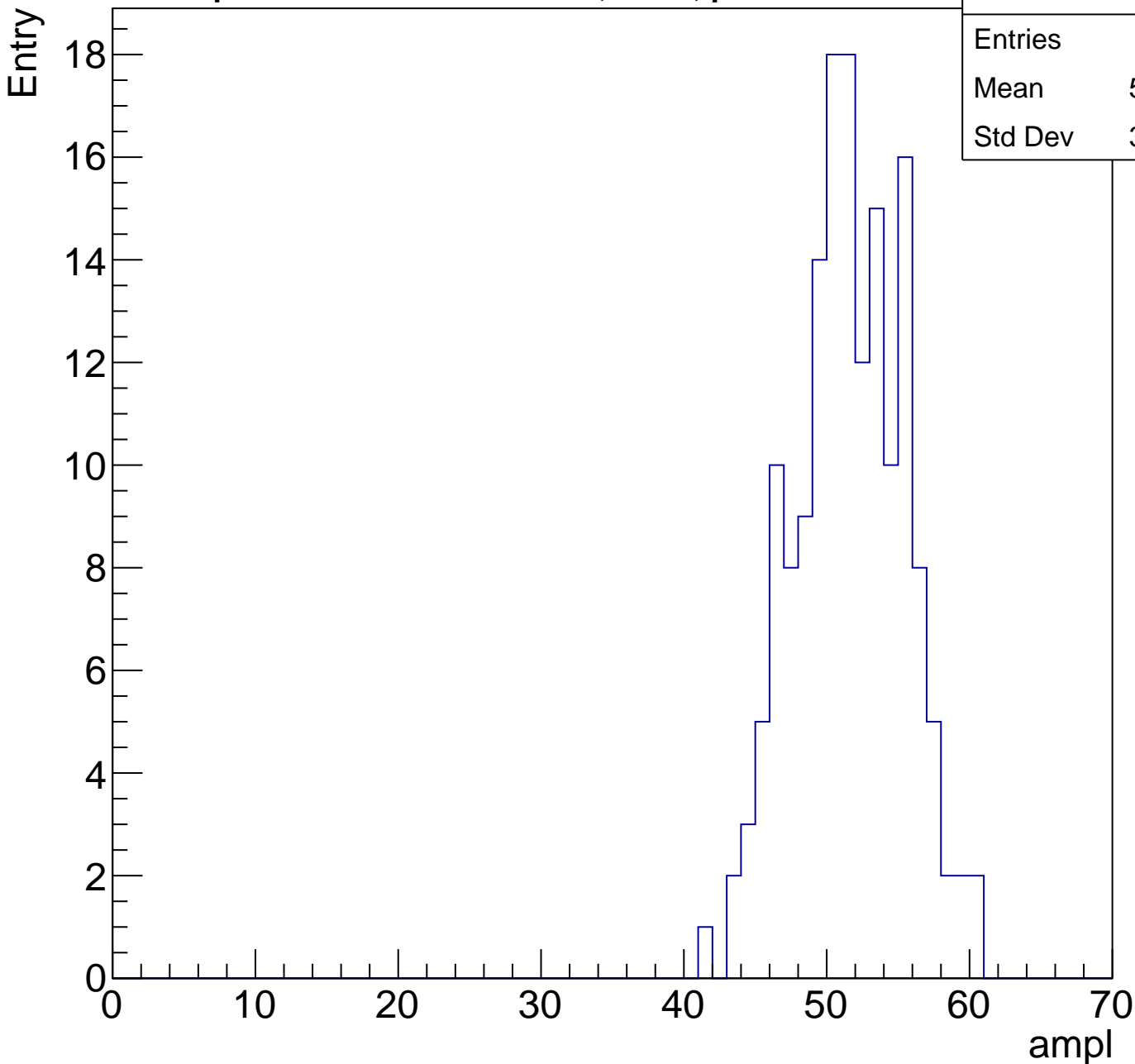
ampl



# B0L101S, U23-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	51.14
Std Dev	3.767



# B0L101S, U23-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

104

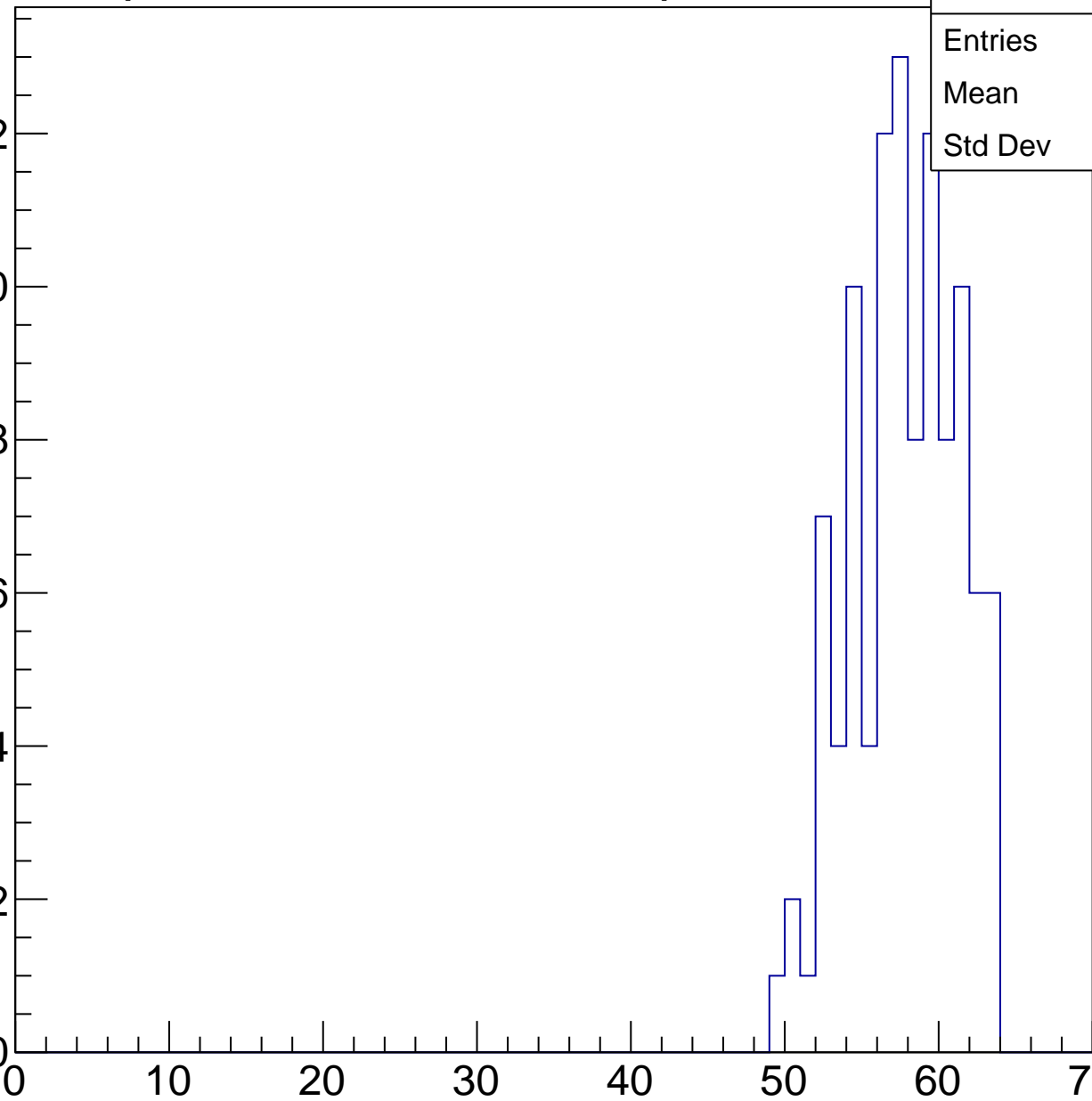
Mean

57.32

Std Dev

3.395

ampl



# B0L101S, U23-ch39, adc5

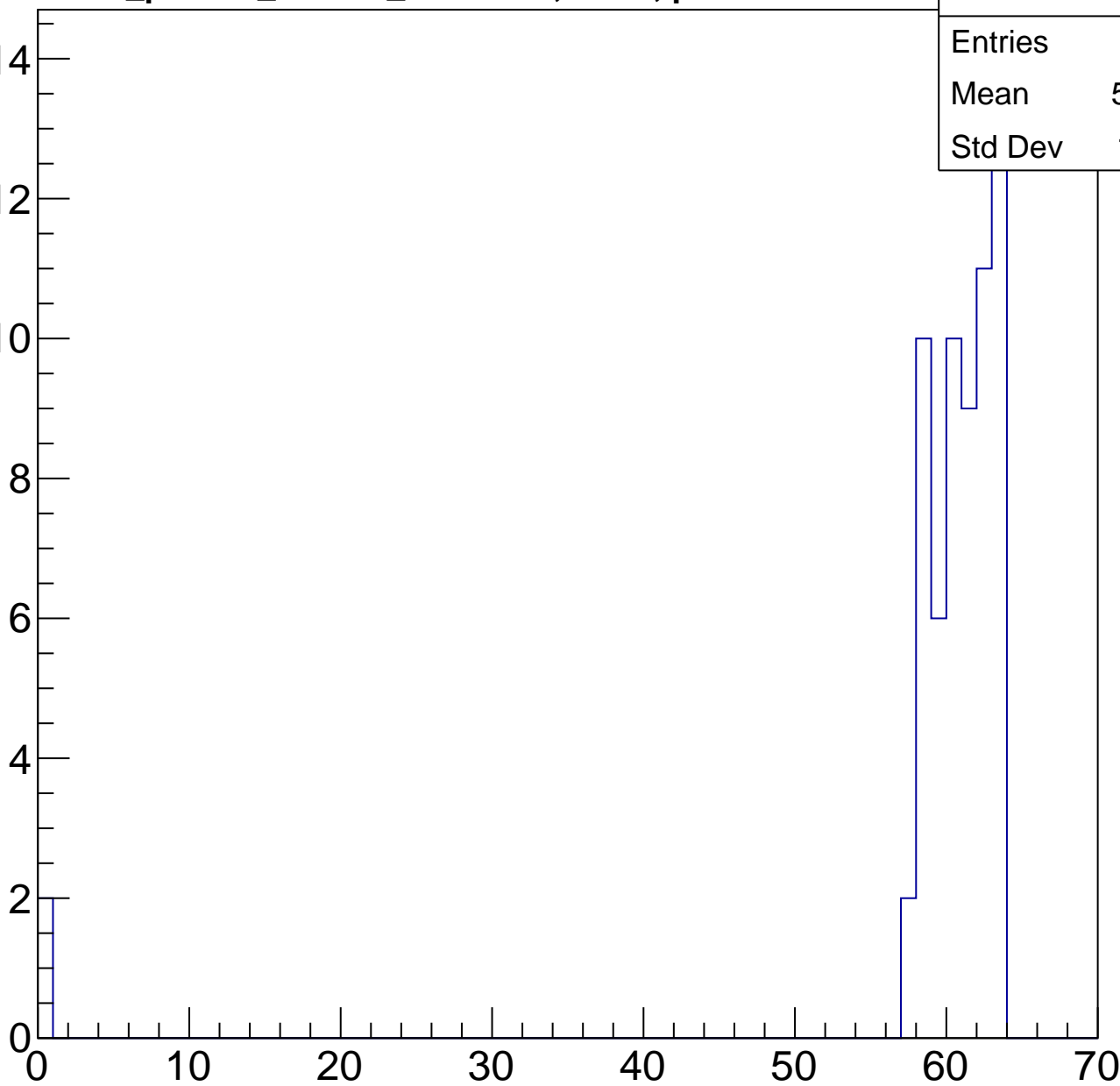
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	64
Mean	58.77
Std Dev	10.71

ampl



# B0L101S, U23-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

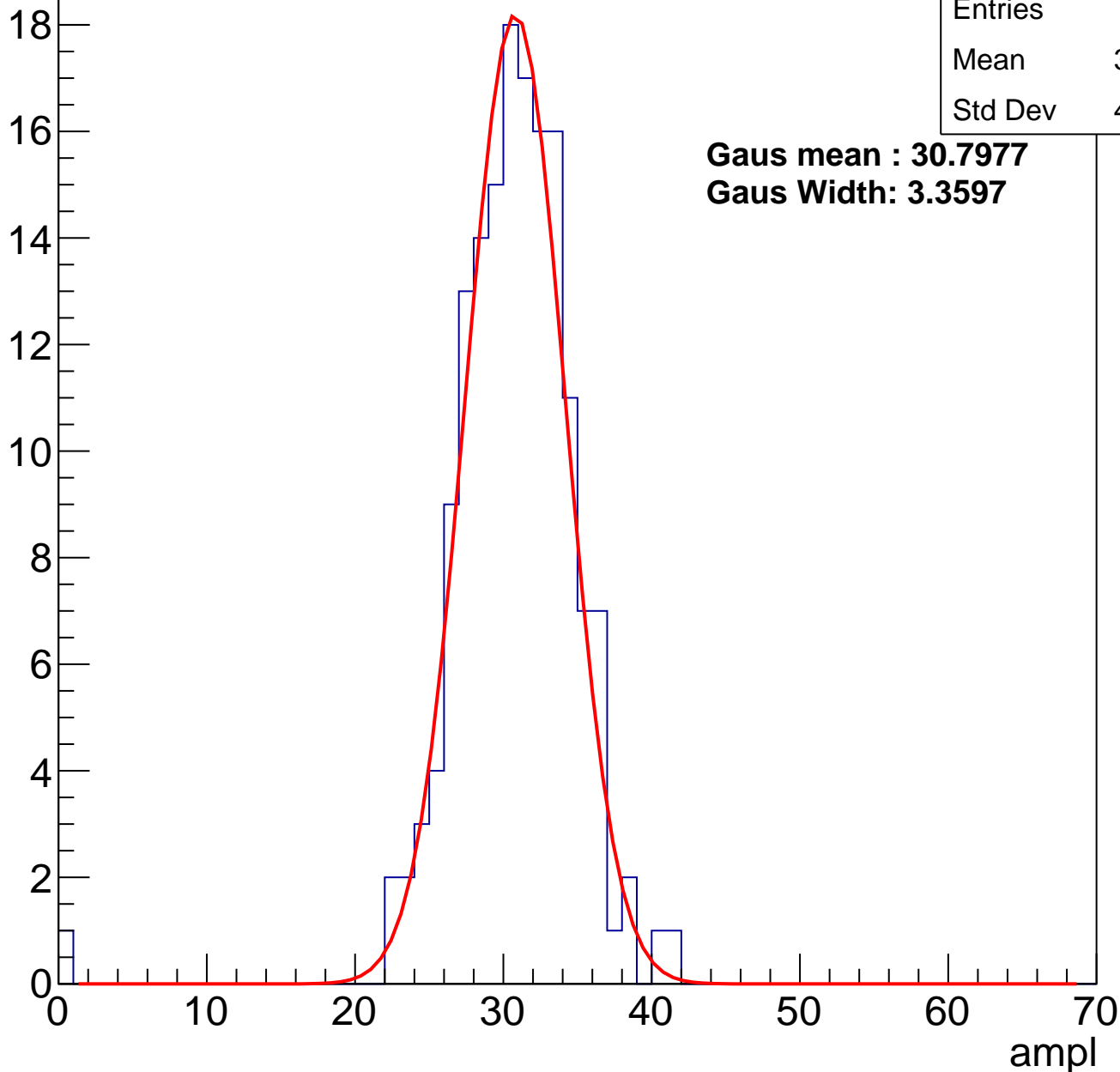
Entry



# B0L101S, U23-ch40, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



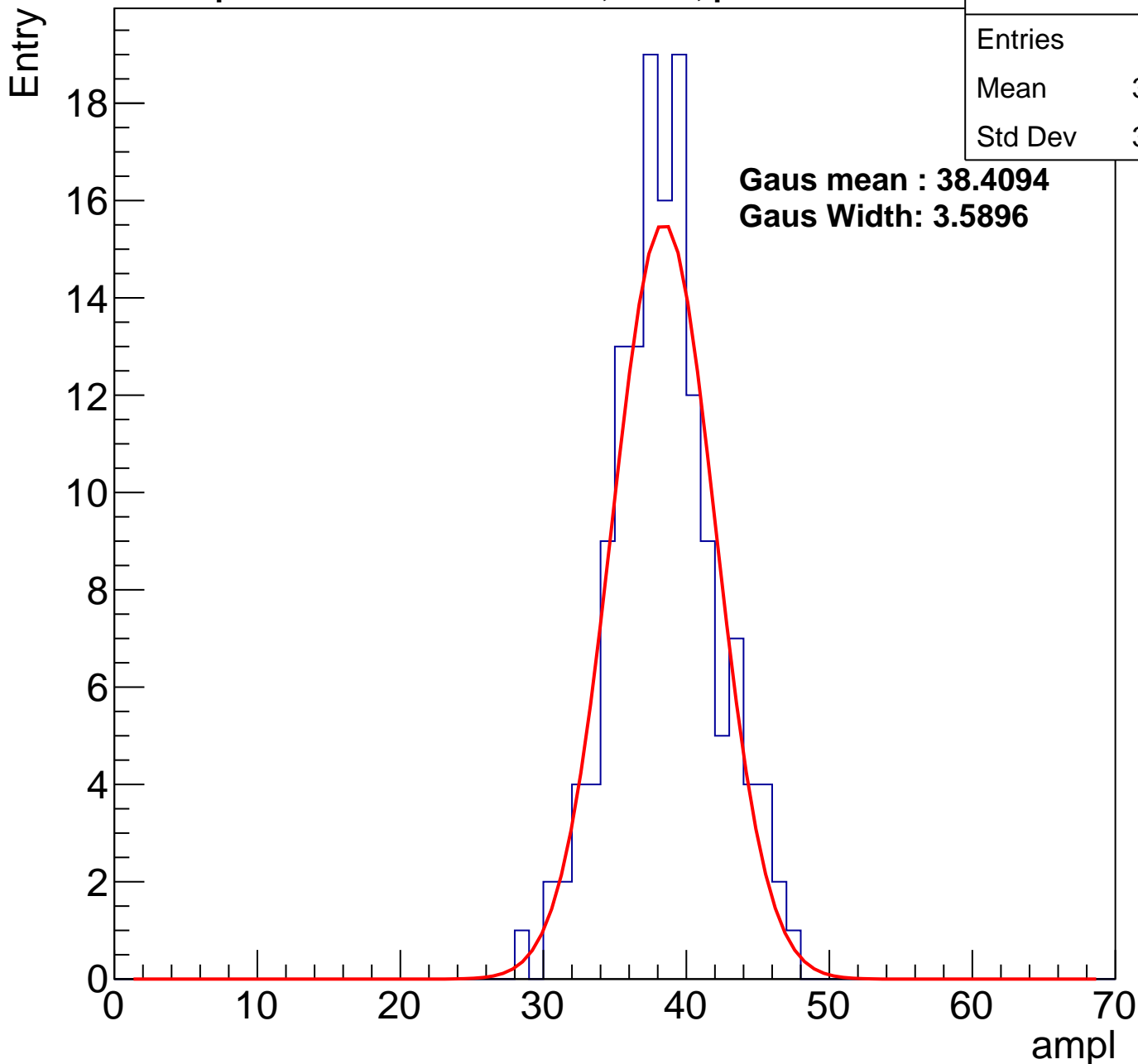
# B0L101S, U23-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	37.99
Std Dev	3.563

**Gaus mean : 38.4094**

**Gaus Width: 3.5896**



# B0L101S, U23-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	44.72
Std Dev	3.138

**Gaus mean : 45.0346**

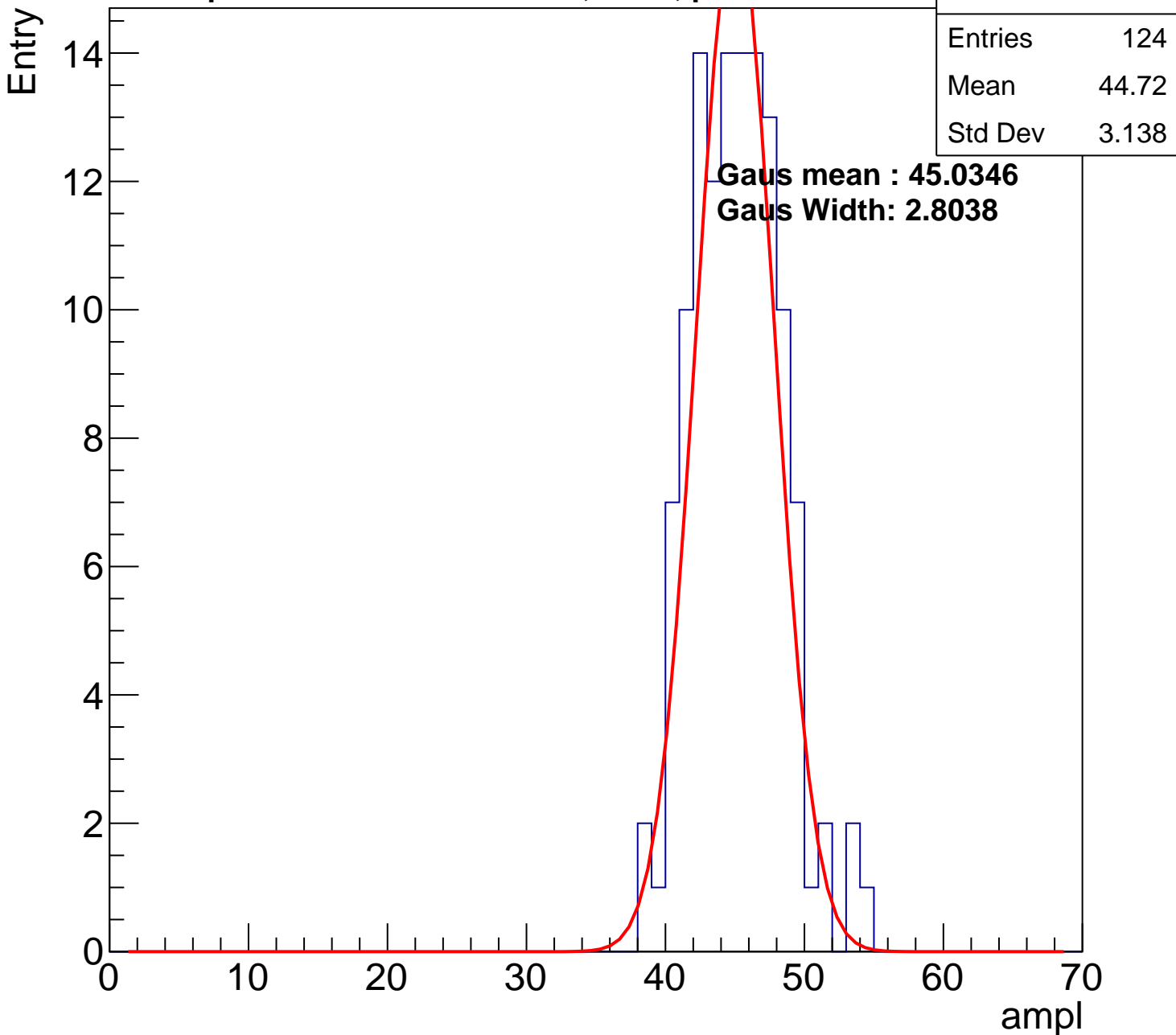
**Gaus Width: 2.8038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

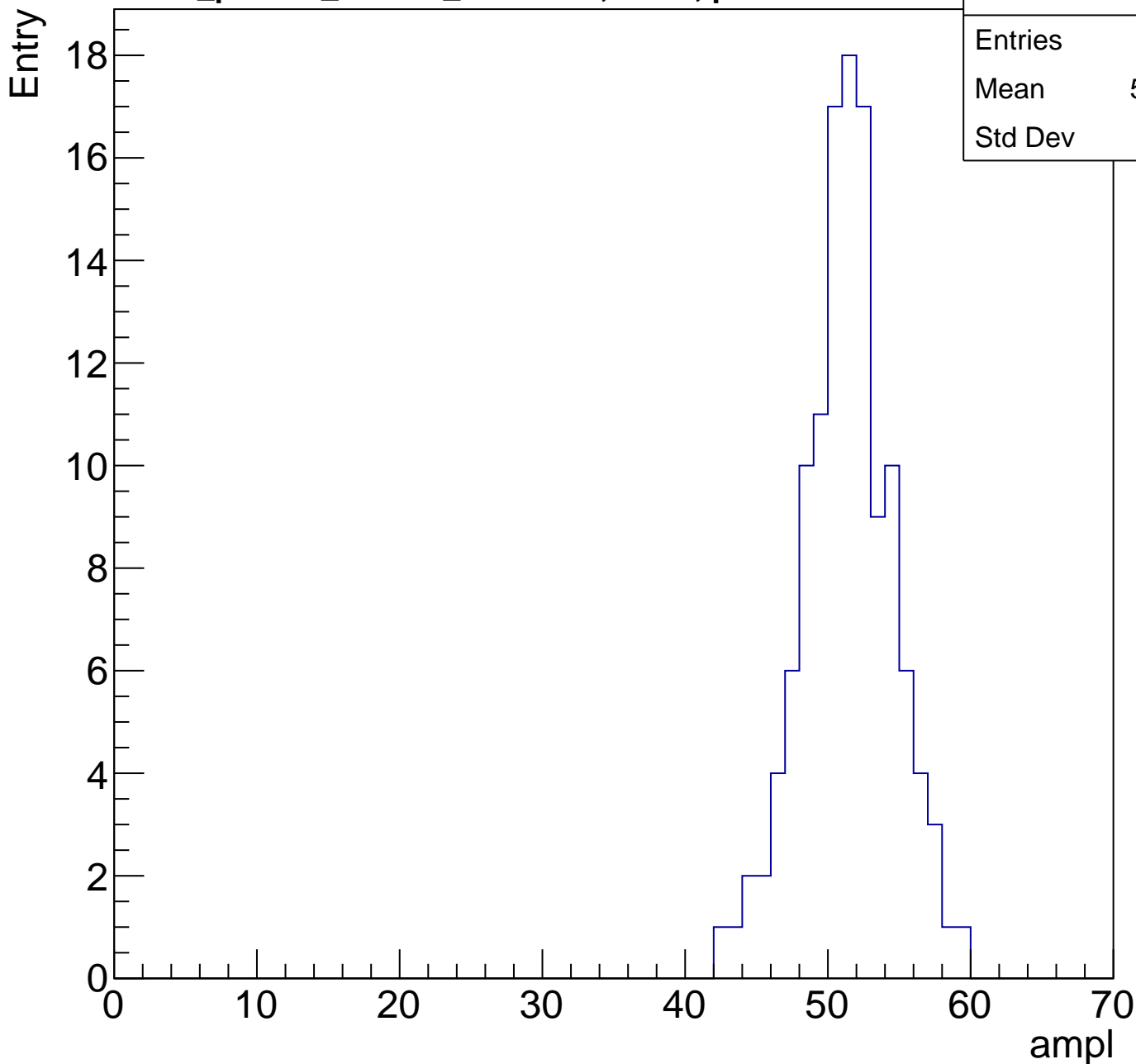
0 10 20 30 40 50 60 70



# B0L101S, U23-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

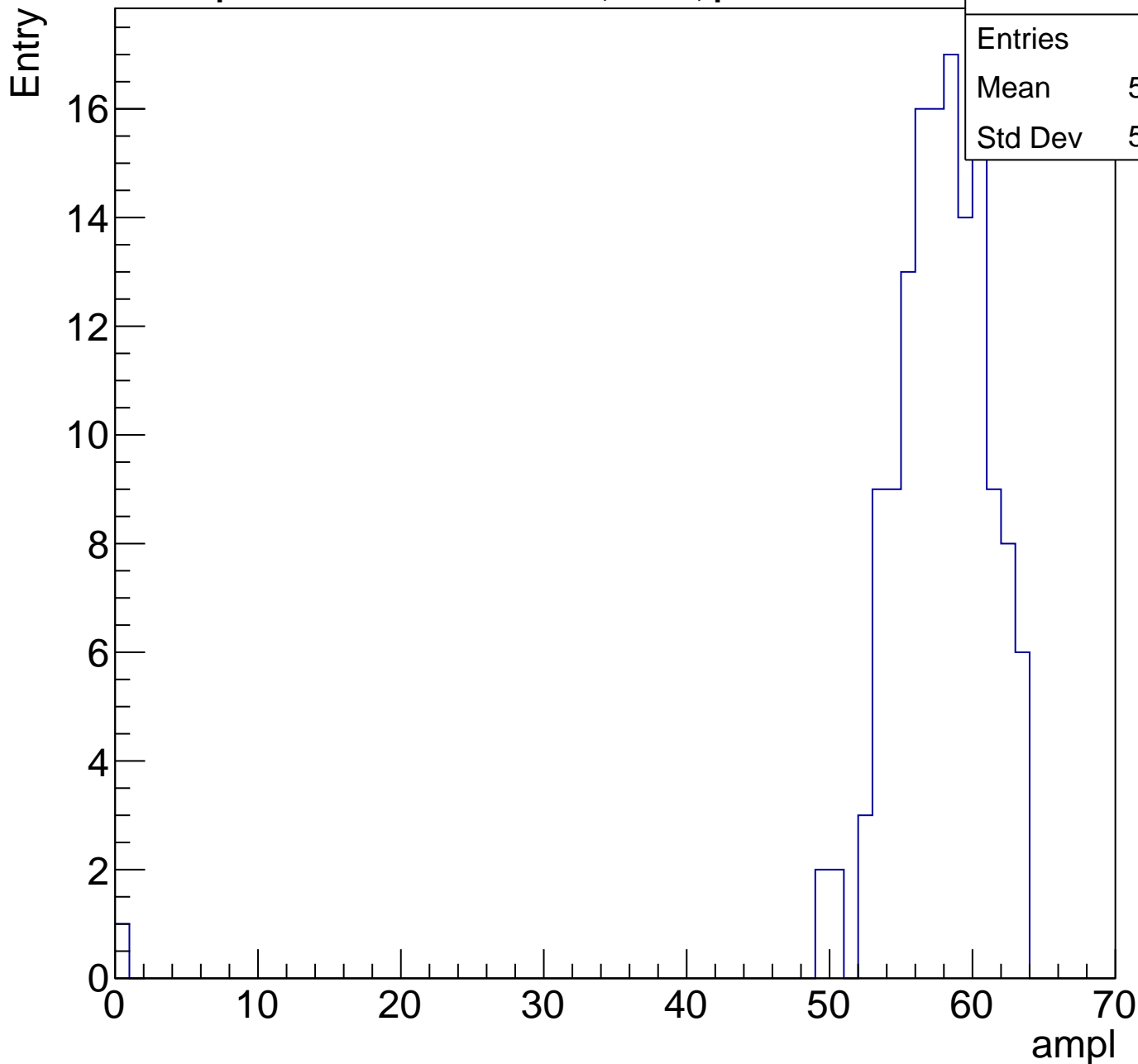
Entries	123
Mean	50.89
Std Dev	3.15



# B0L101S, U23-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	56.99
Std Dev	5.724



# B0L101S, U23-ch40, adc5

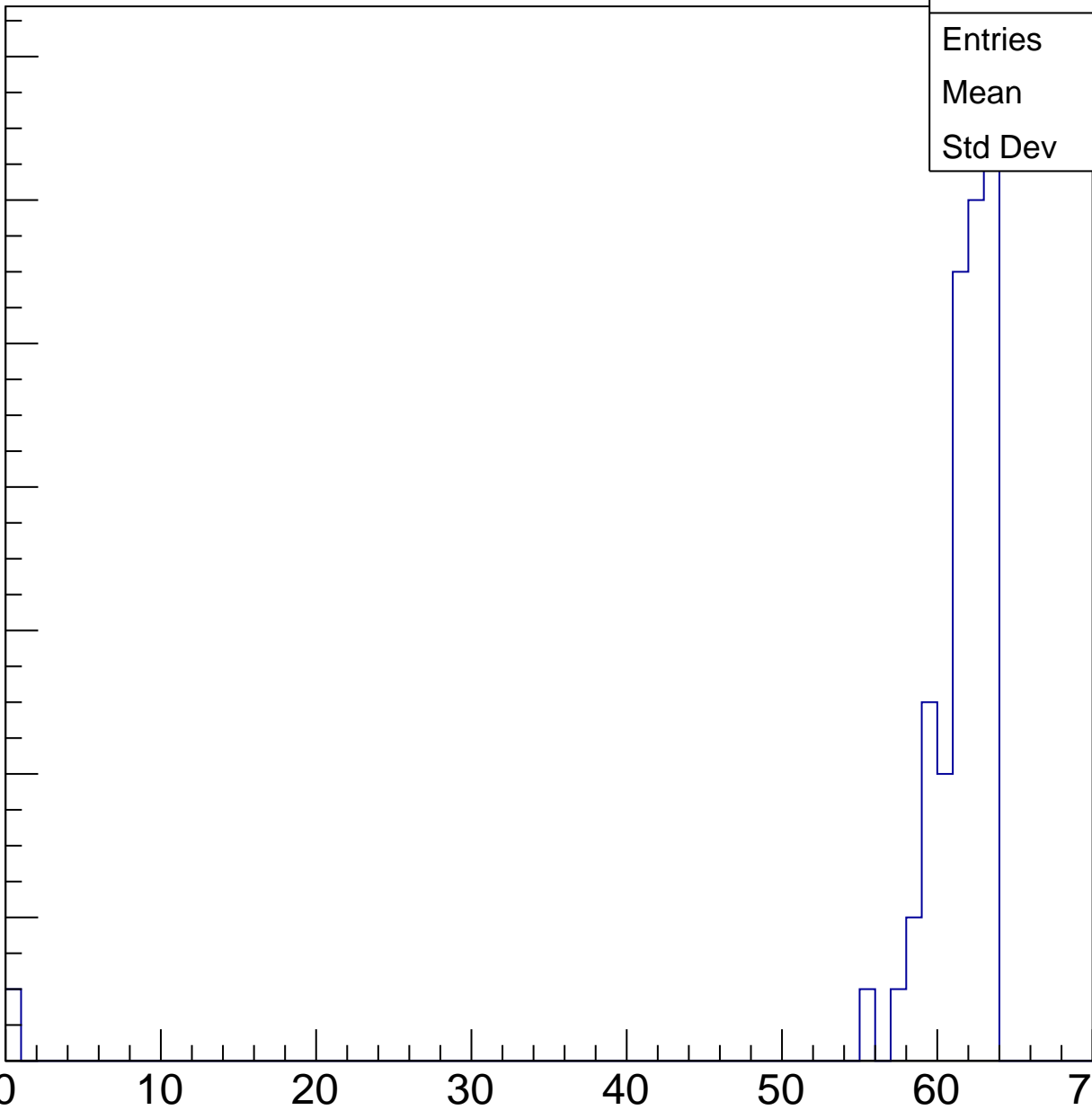
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	51
Mean	60
Std Dev	8.668

ampl



# B0L101S, U23-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch41, adc0

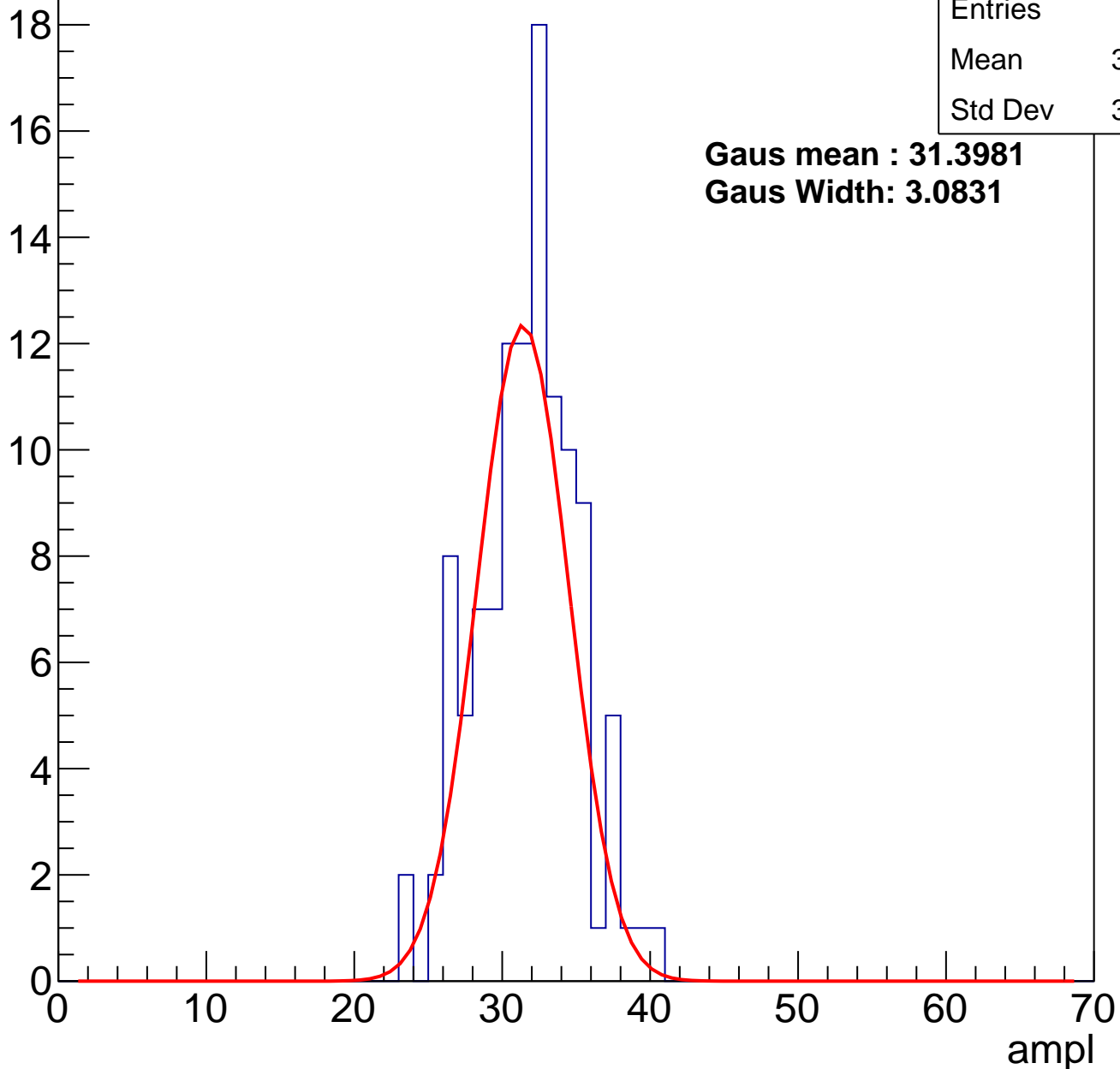
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	31.27
Std Dev	3.367

**Gaus mean : 31.3981**

**Gaus Width: 3.0831**

Entry



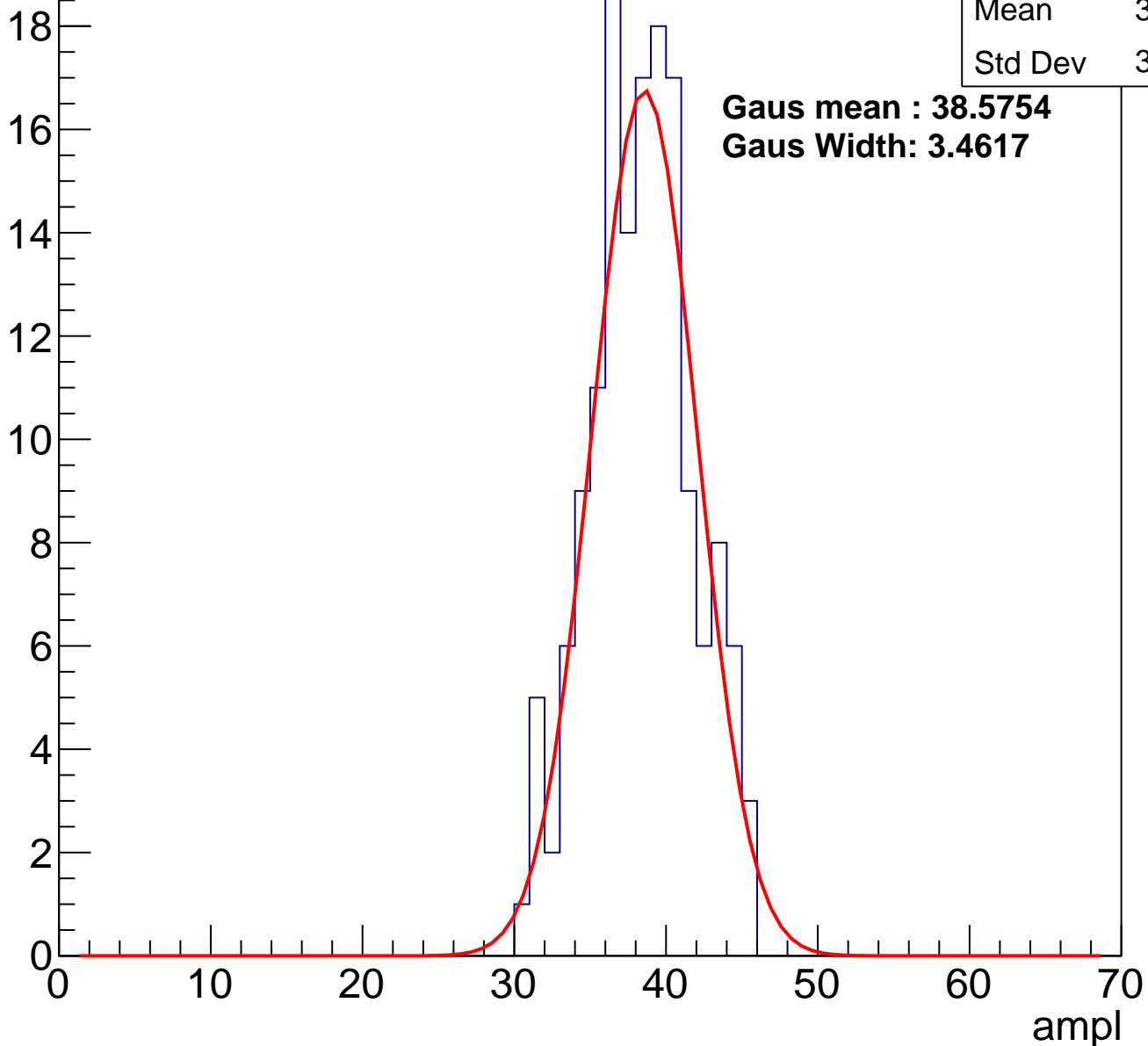
# B0L101S, U23-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	37.96
Std Dev	3.345

**Gaus mean : 38.5754**  
**Gaus Width: 3.4617**

Entry



# B0L101S, U23-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

109

Mean

44.72

Std Dev

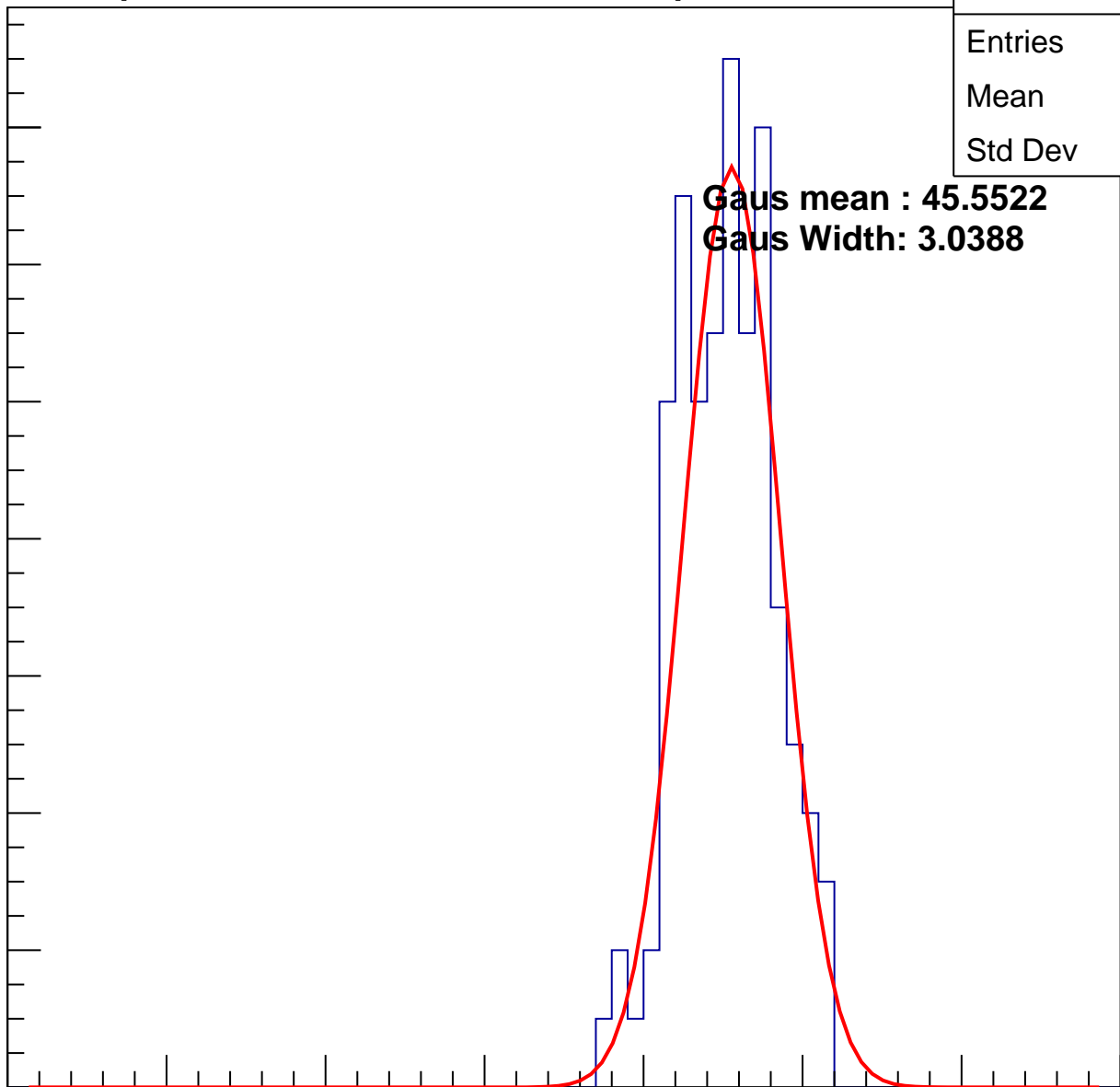
3.007

**Gaus mean : 45.5522**

**Gaus Width: 3.0388**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	51.23
Std Dev	3.807

Entry

16

14

12

10

8

6

4

2

0

0

10

20

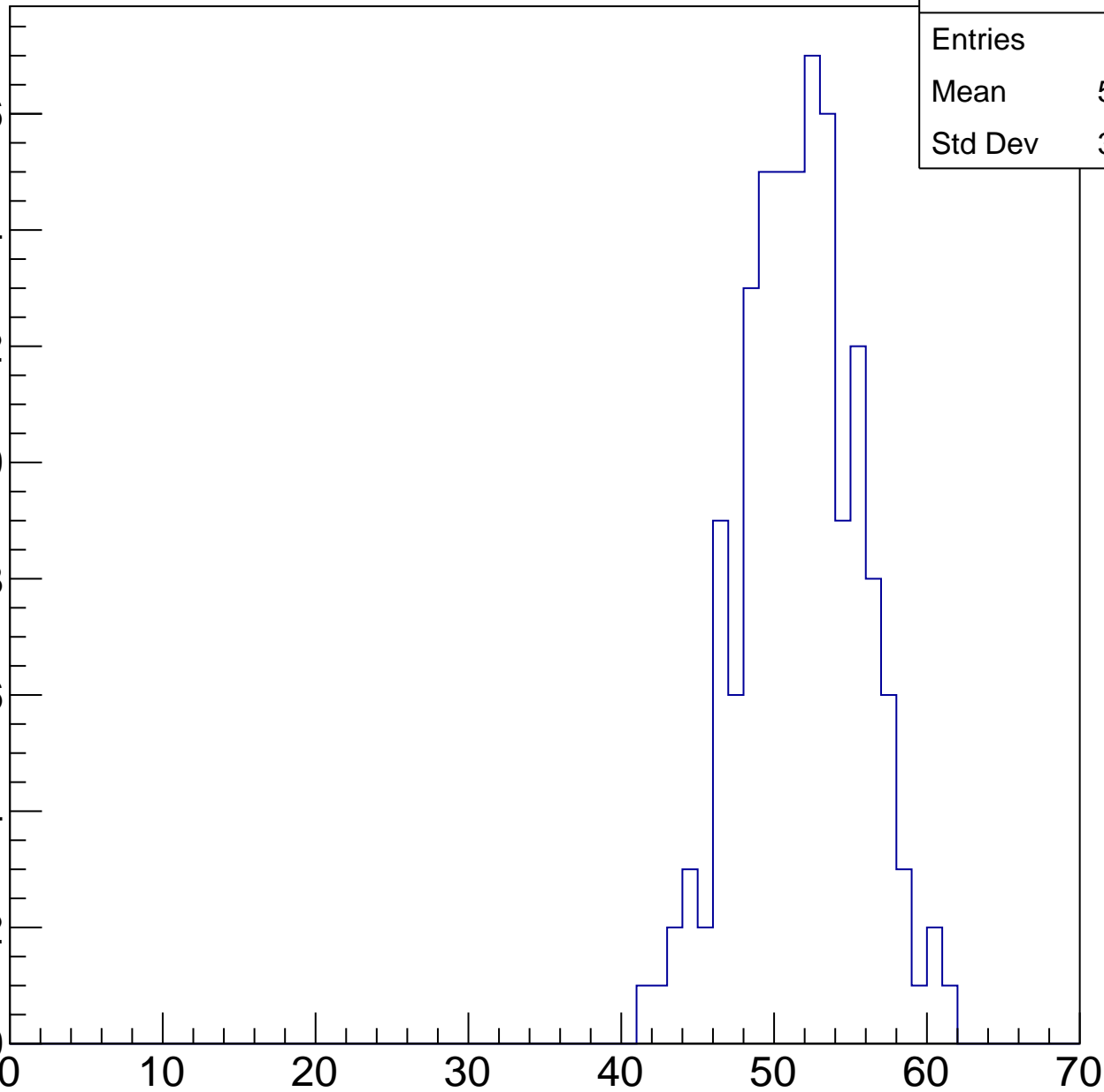
30

40

50

60

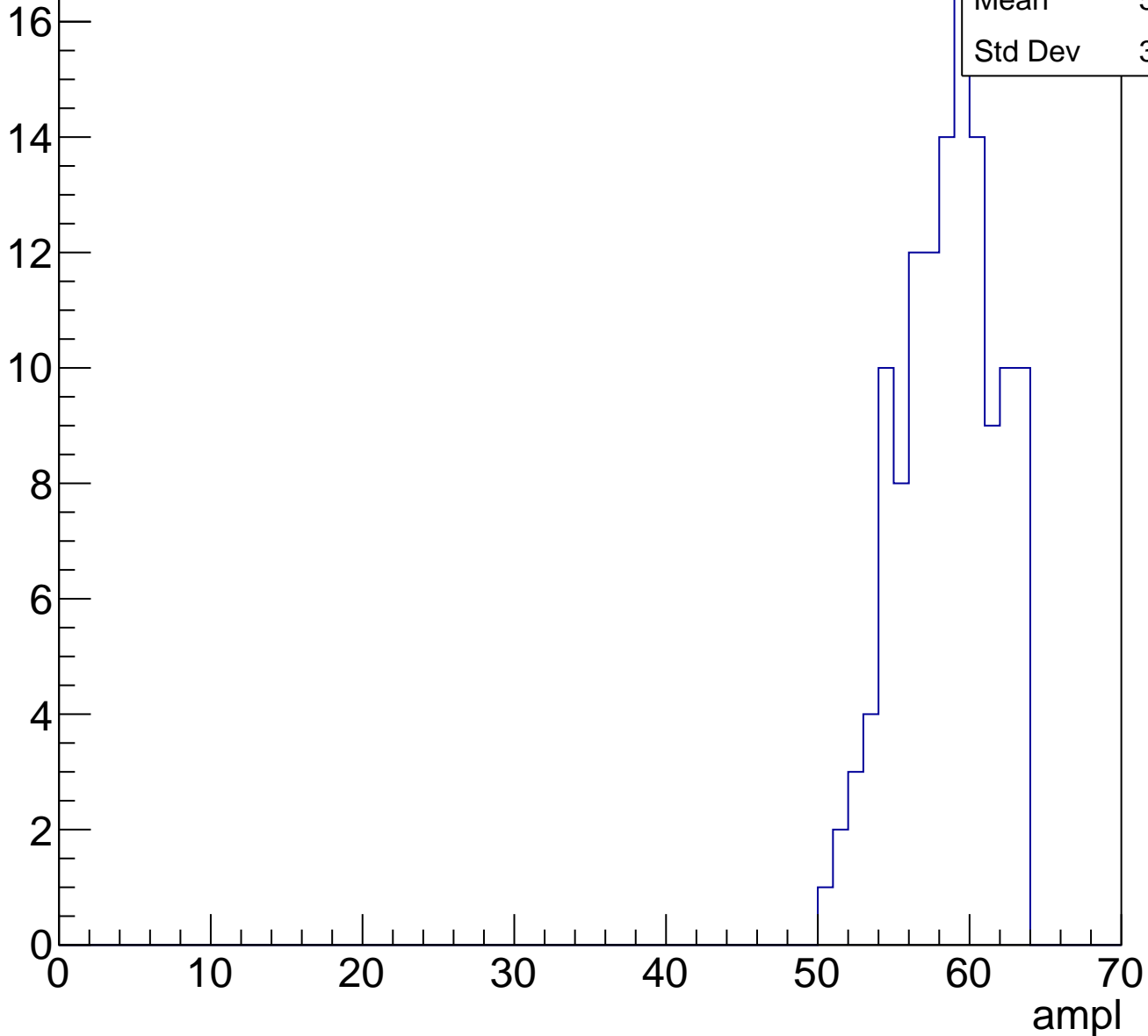
ampl



# B0L101S, U23-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



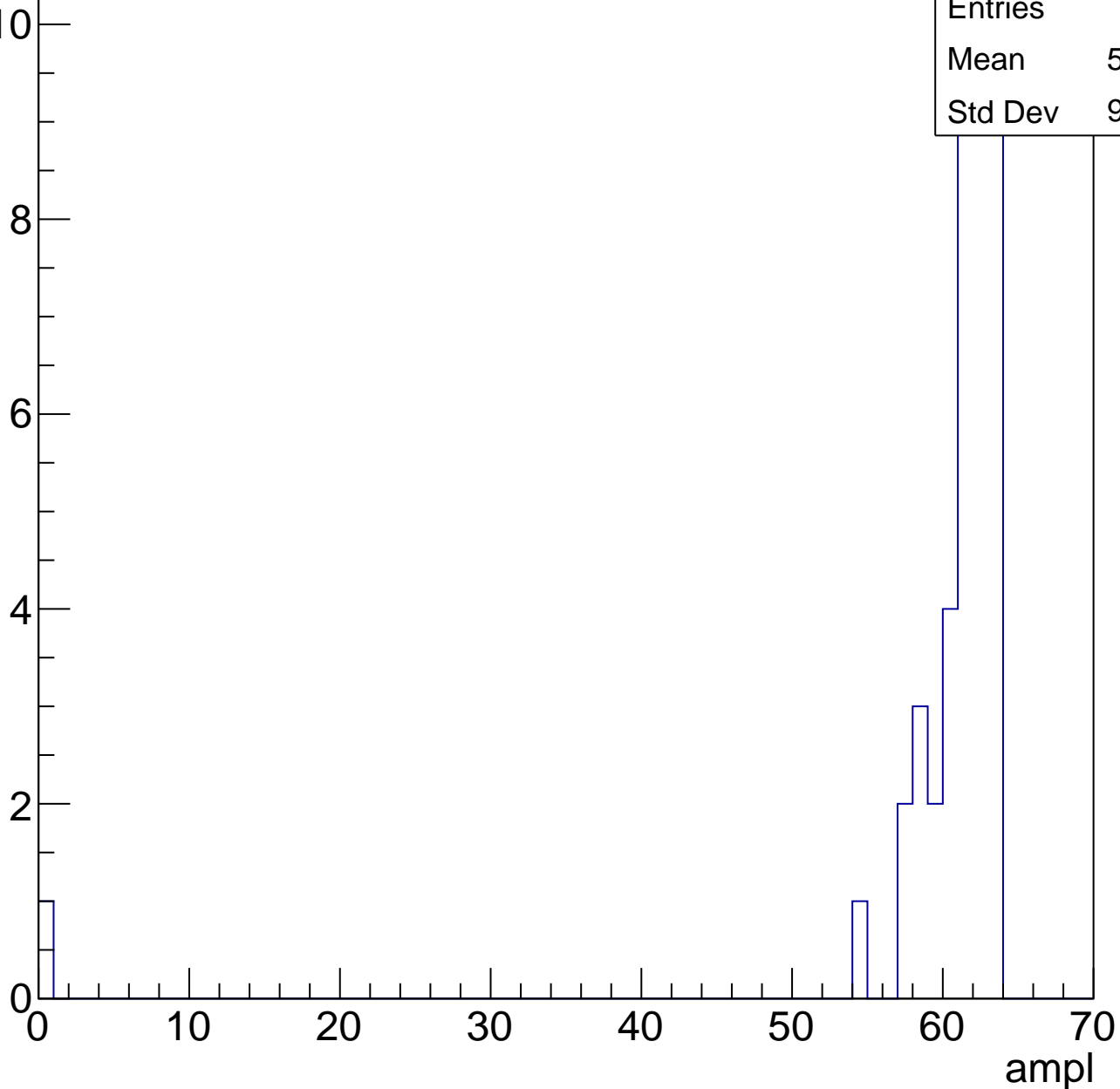
Entries	126
Mean	58.02
Std Dev	3.119

# B0L101S, U23-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	43
Mean	59.53
Std Dev	9.399



# B0L101S, U23-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl



# B0L101S, U23-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

158

Mean

30.31

Std Dev

3.763

**Gaus mean : 30.6378**

**Gaus Width: 3.9516**

0

10

20

30

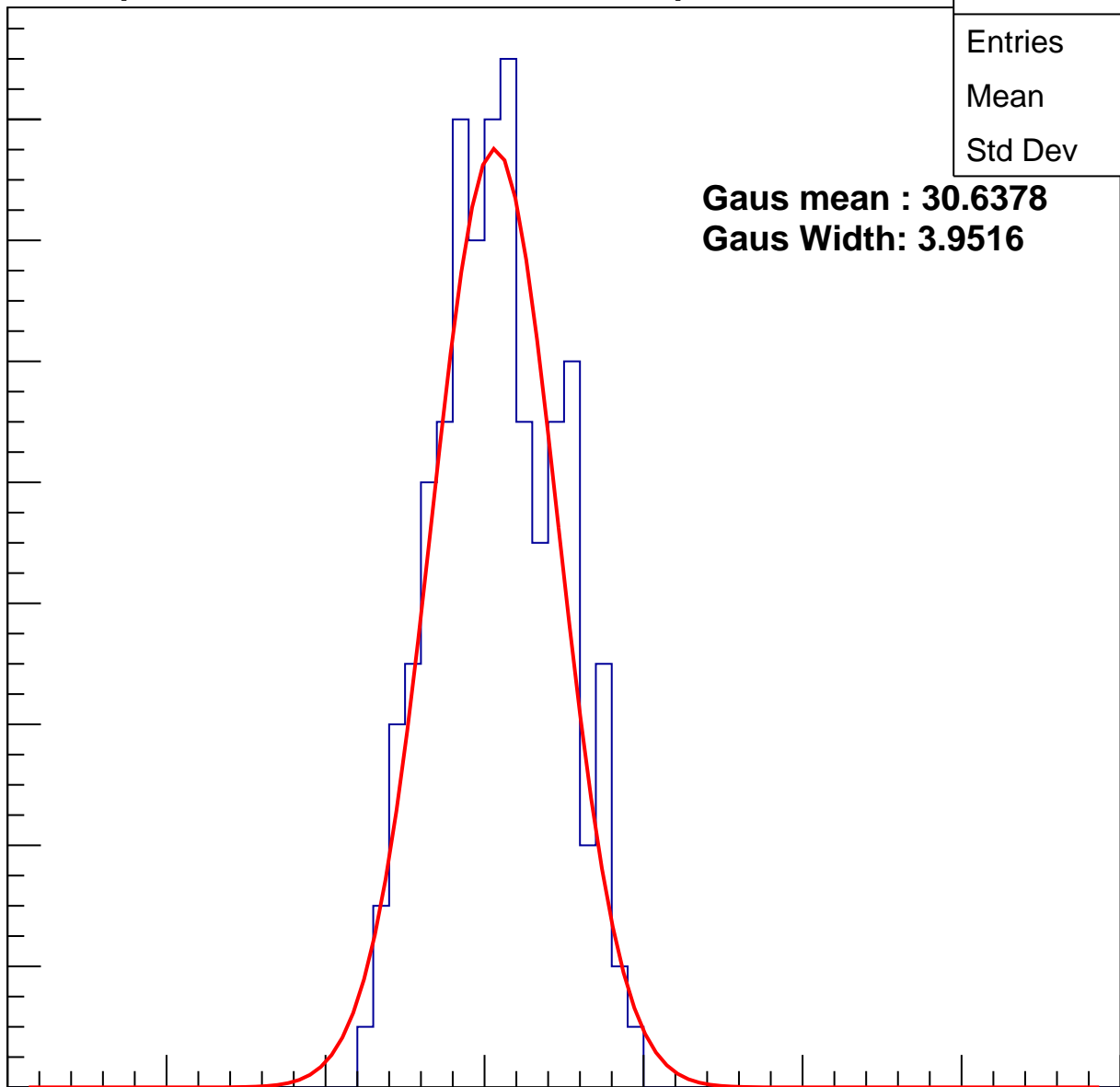
40

50

60

70

ampl

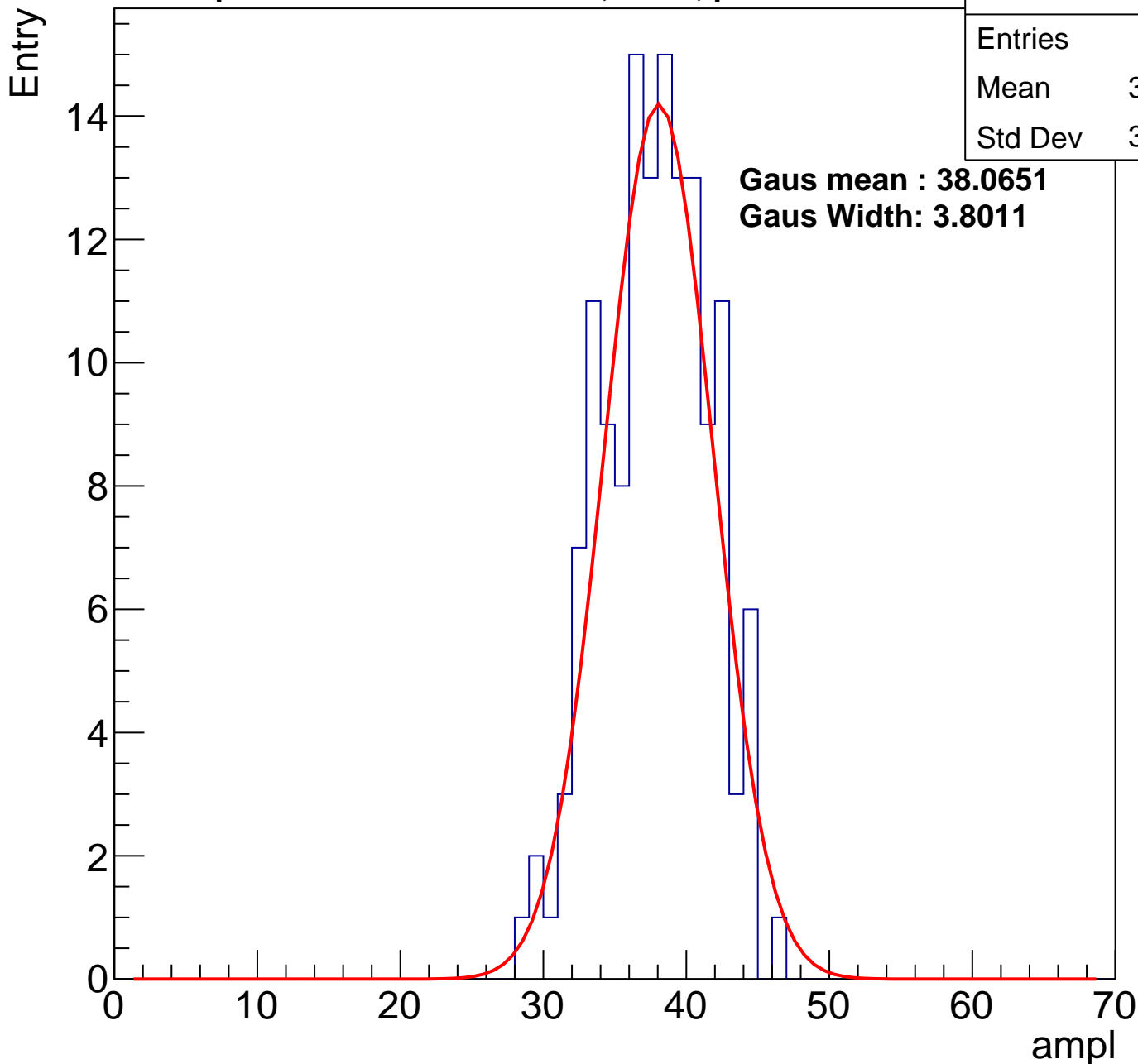


# B0L101S, U23-ch42, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	37.38
Std Dev	3.682

**Gaus mean : 38.0651**  
**Gaus Width: 3.8011**



# B0L101S, U23-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

44.95

Std Dev

3.524

**Gaus mean : 45.2209**

**Gaus Width: 3.7037**

0

10

20

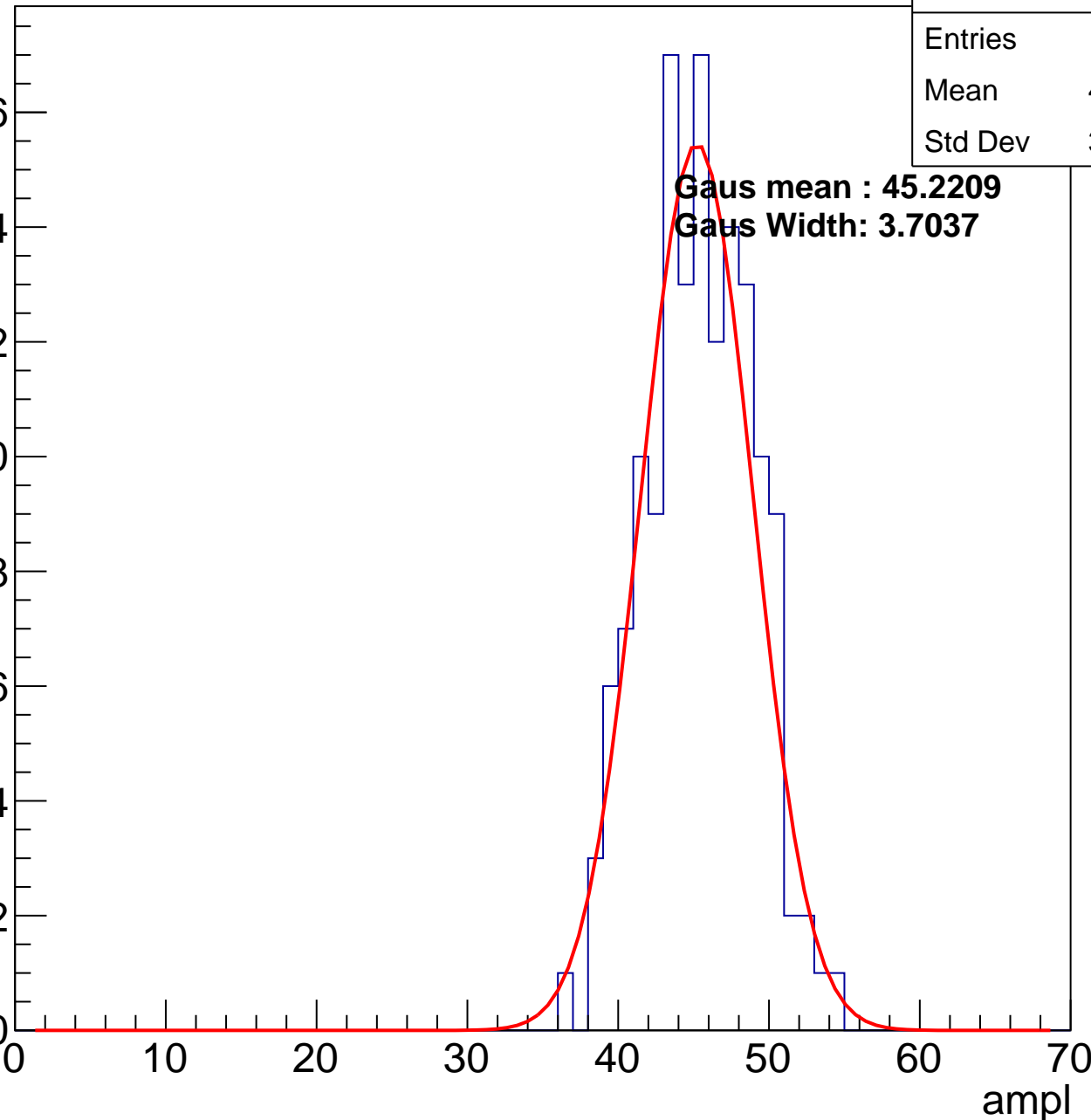
30

40

50

60

ampl



# B0L101S, U23-ch42, adc3

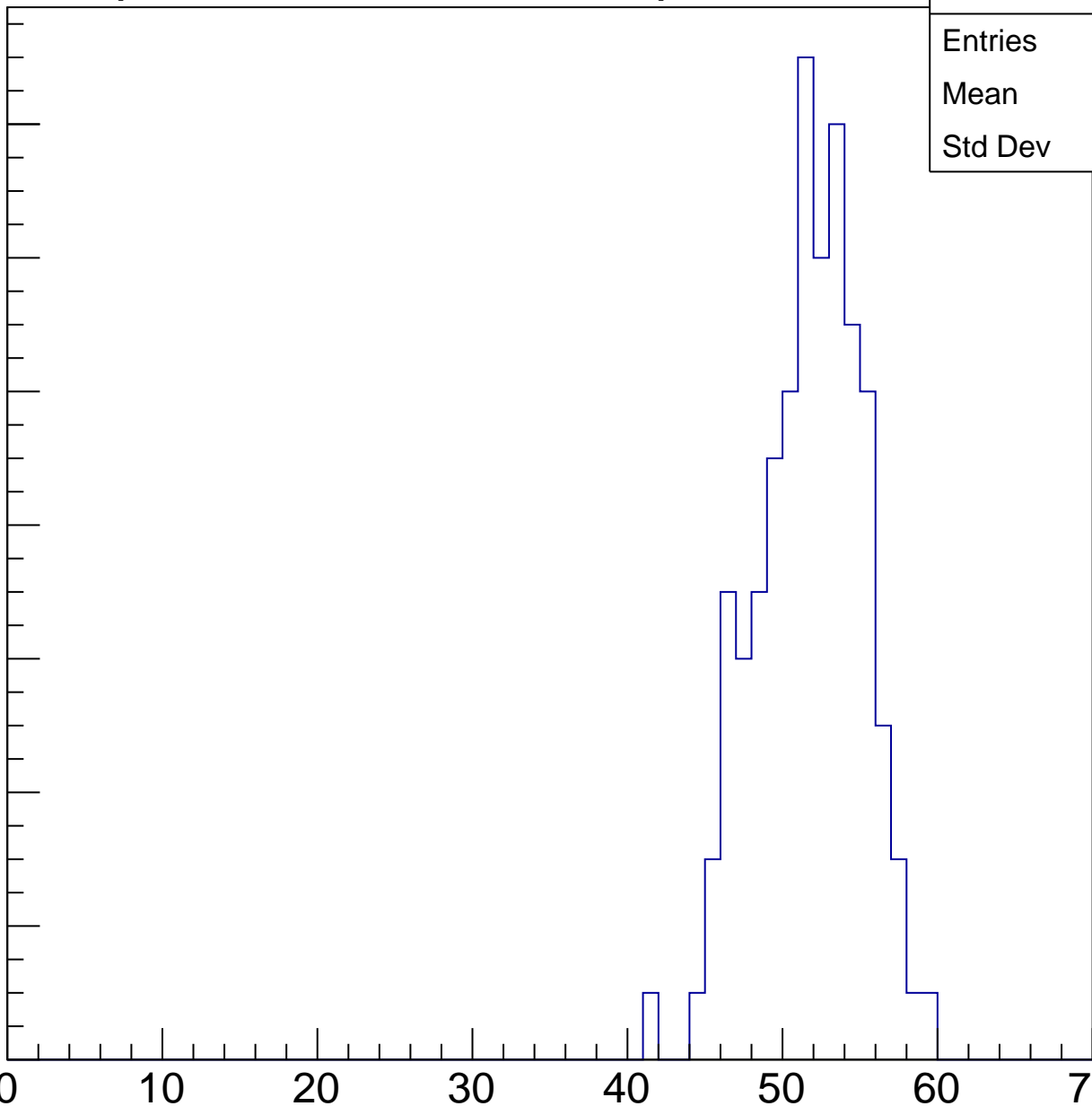
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	116
Mean	51.24
Std Dev	3.352

ampl



# B0L101S, U23-ch42, adc4

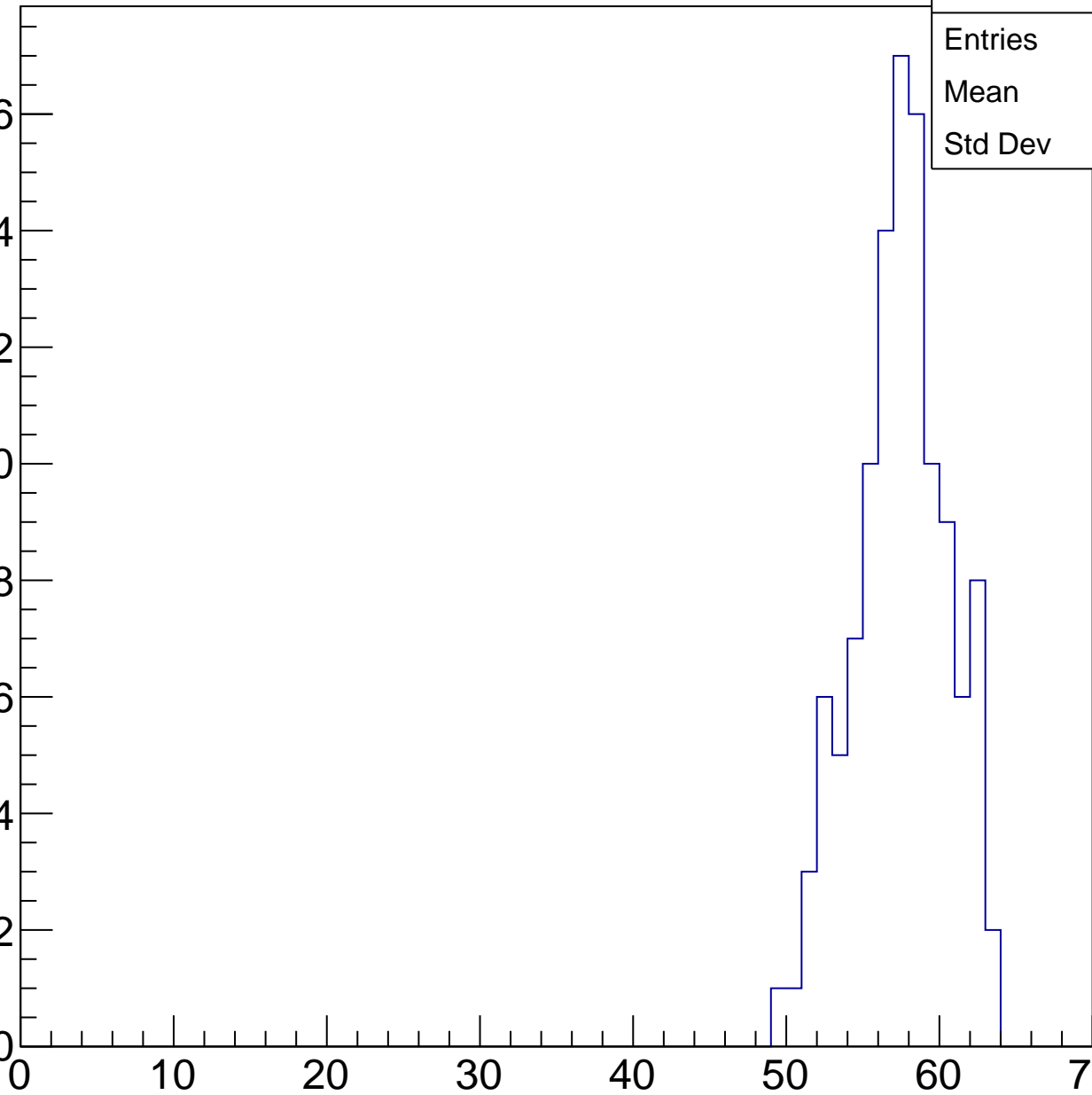
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	57.01
Std Dev	3.055

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch42, adc5

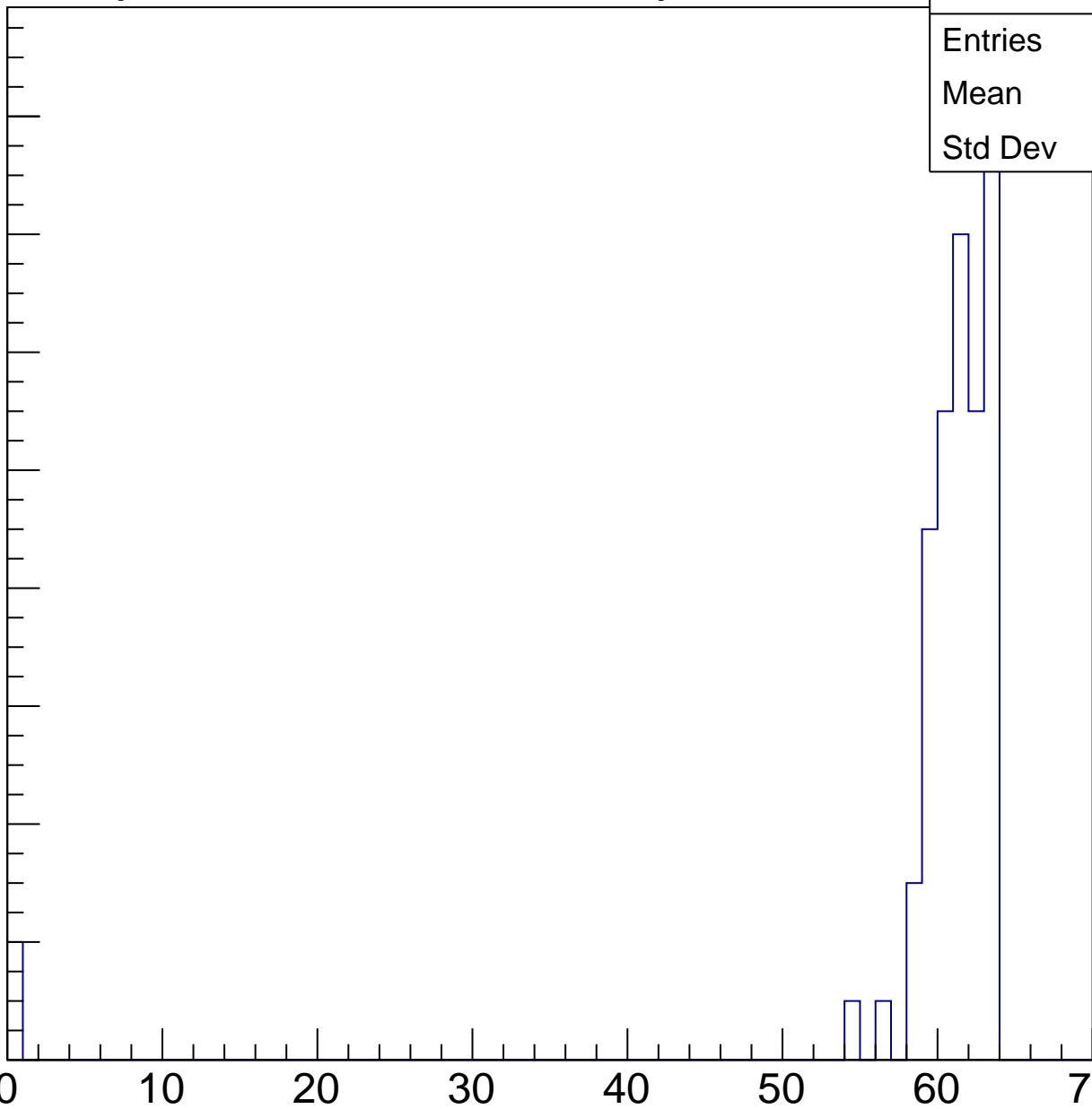
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	59.16
Std Dev	10.38

ampl



# B0L101S, U23-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



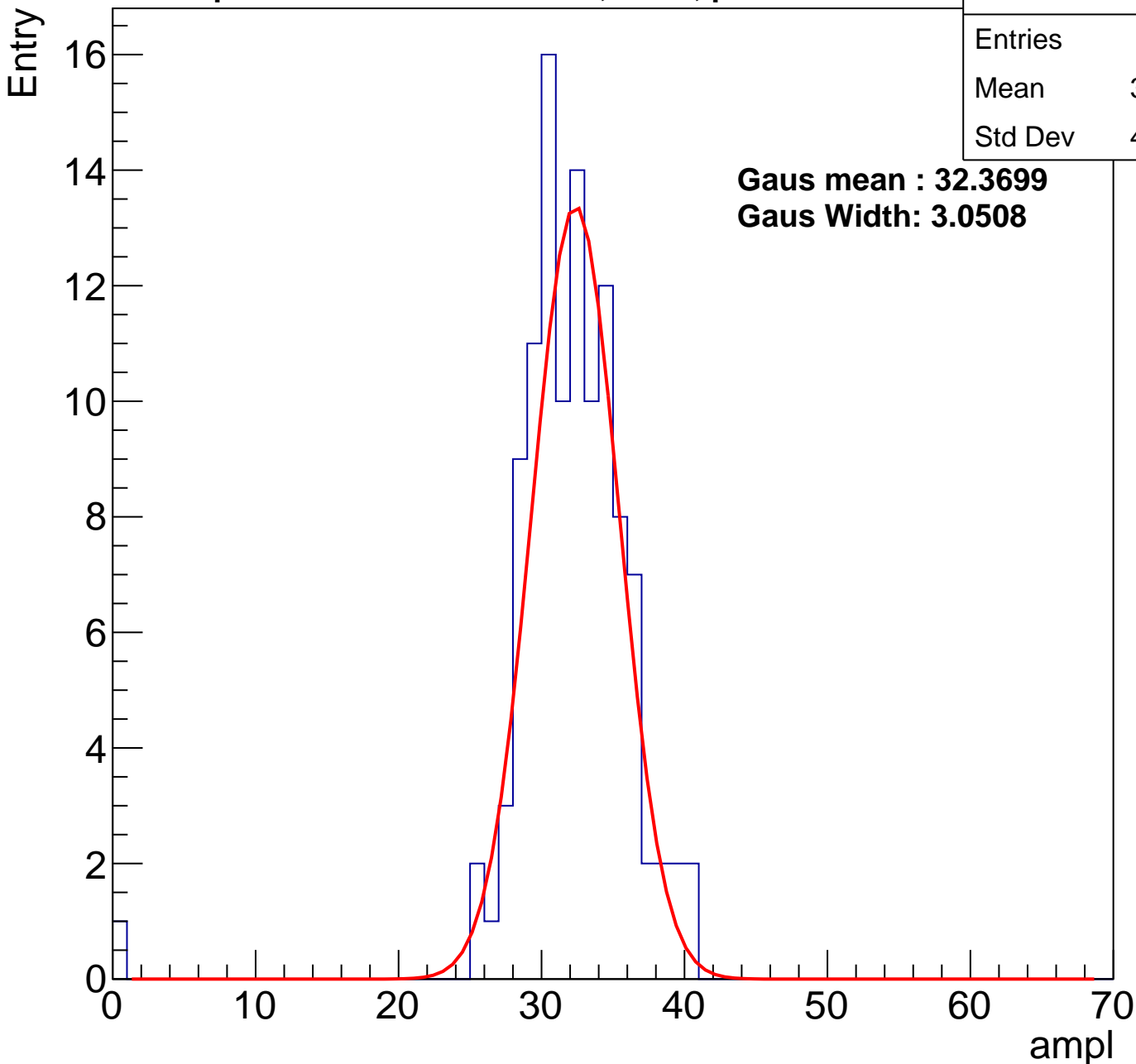
# B0L101S, U23-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	31.64
Std Dev	4.357

**Gaus mean : 32.3699**

**Gaus Width: 3.0508**

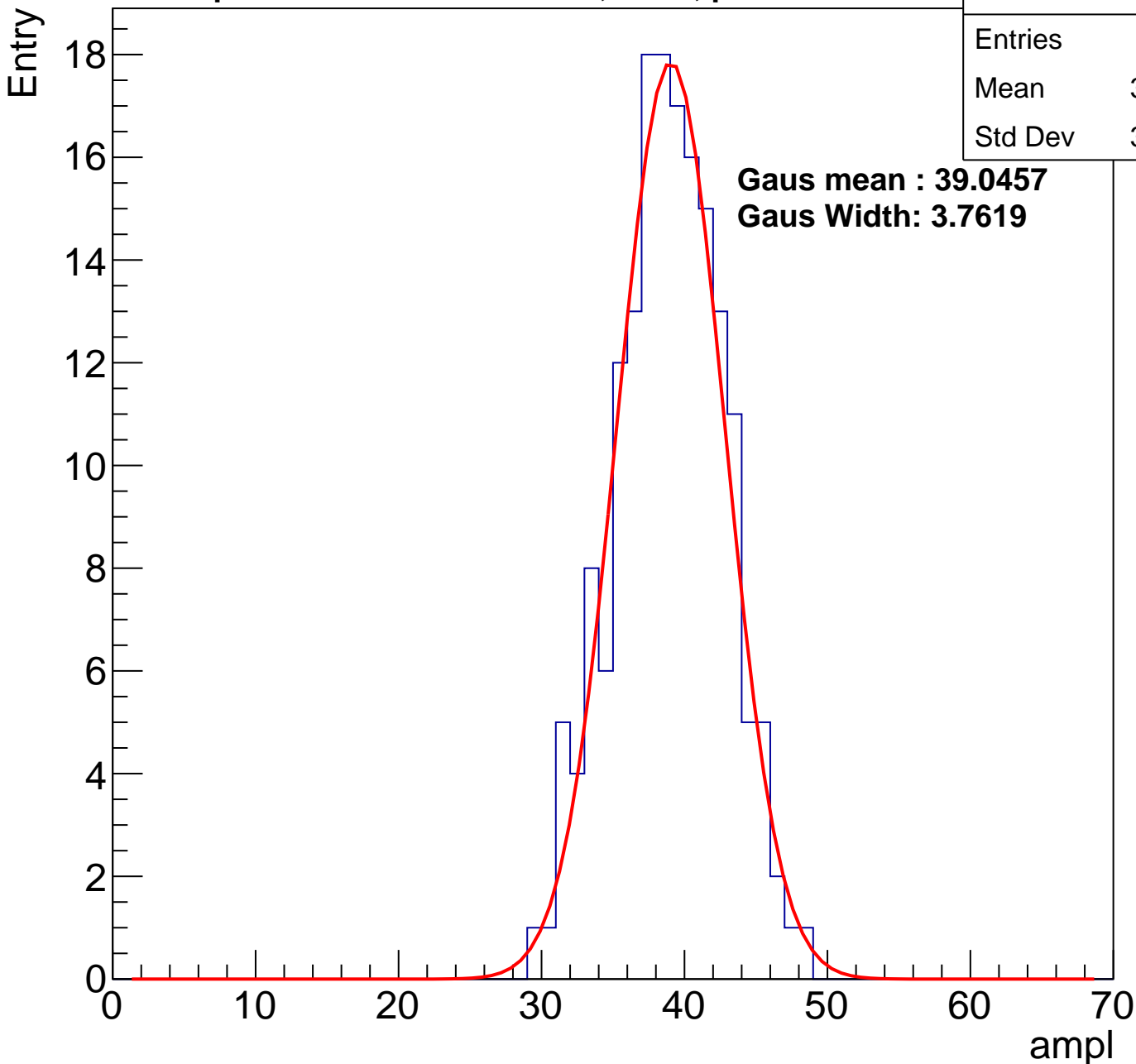


# B0L101S, U23-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	172
Mean	38.47
Std Dev	3.738

**Gaus mean : 39.0457**  
**Gaus Width: 3.7619**



# B0L101S, U23-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

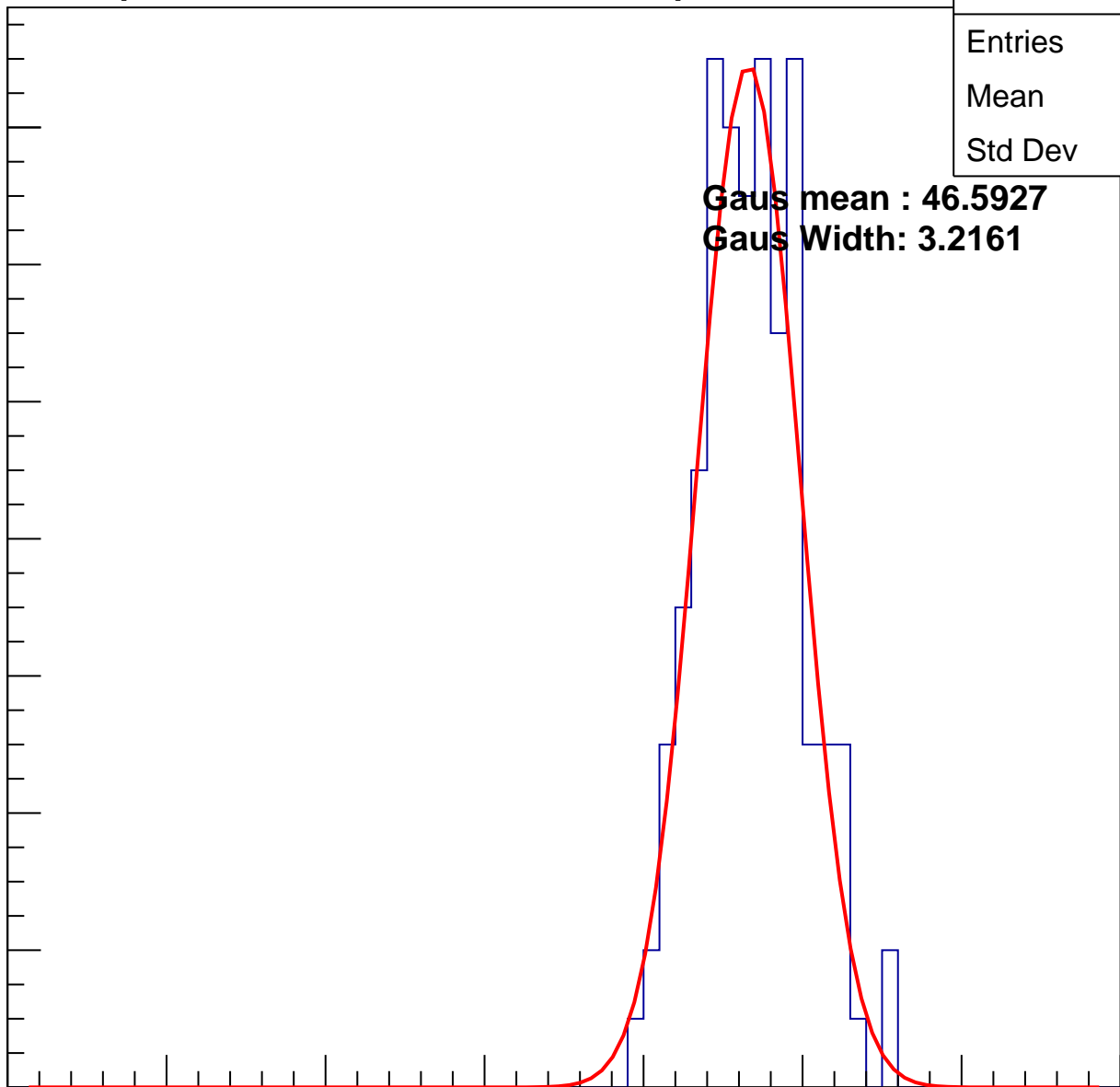
Entries	125
Mean	46.31
Std Dev	3.214

**Gaus mean : 46.5927**

**Gaus Width: 3.2161**

0 10 20 30 40 50 60 70

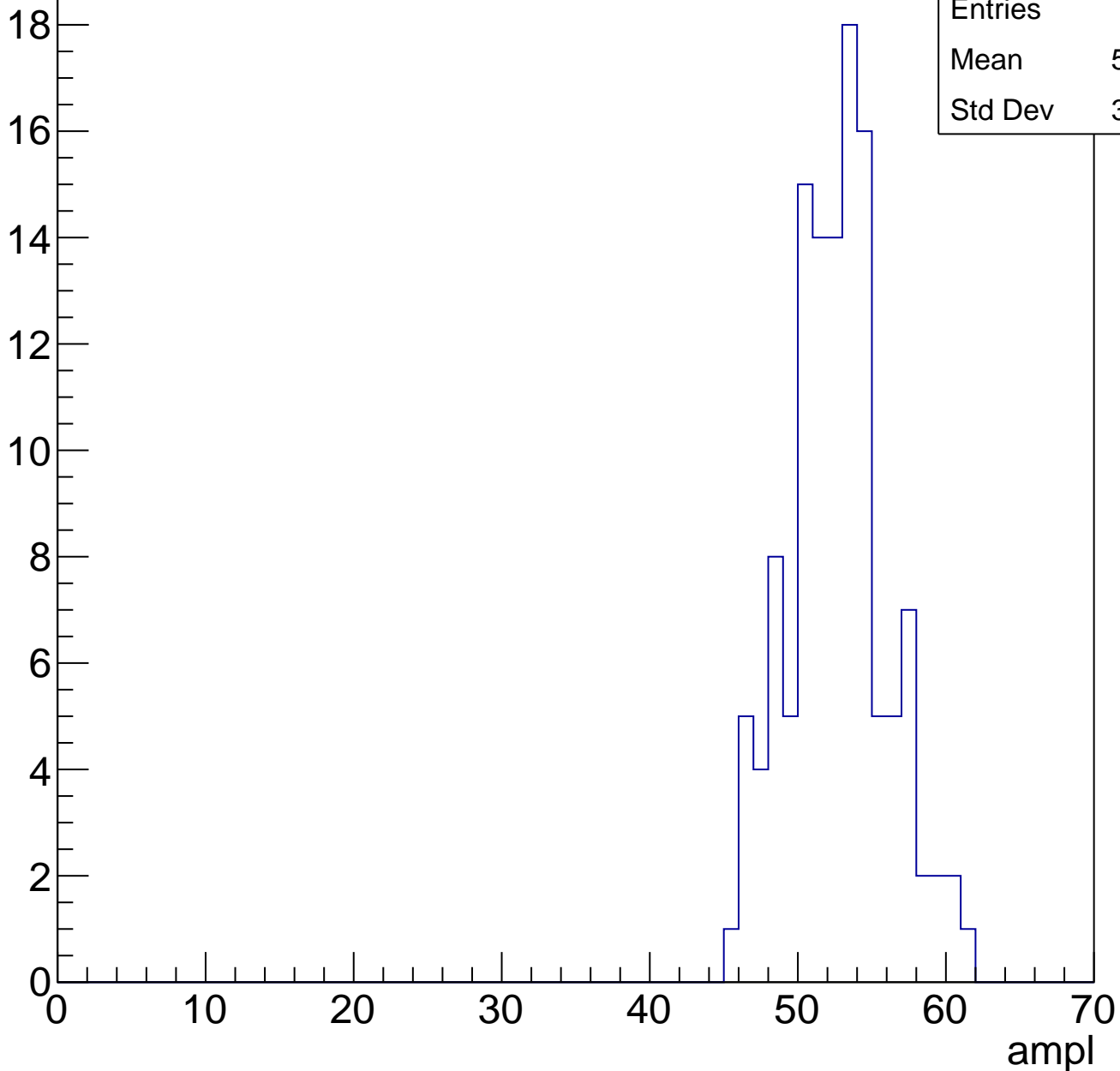
ampl



# B0L101S, U23-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	124
Mean	52.19
Std Dev	3.278

# B0L101S, U23-ch43, adc4

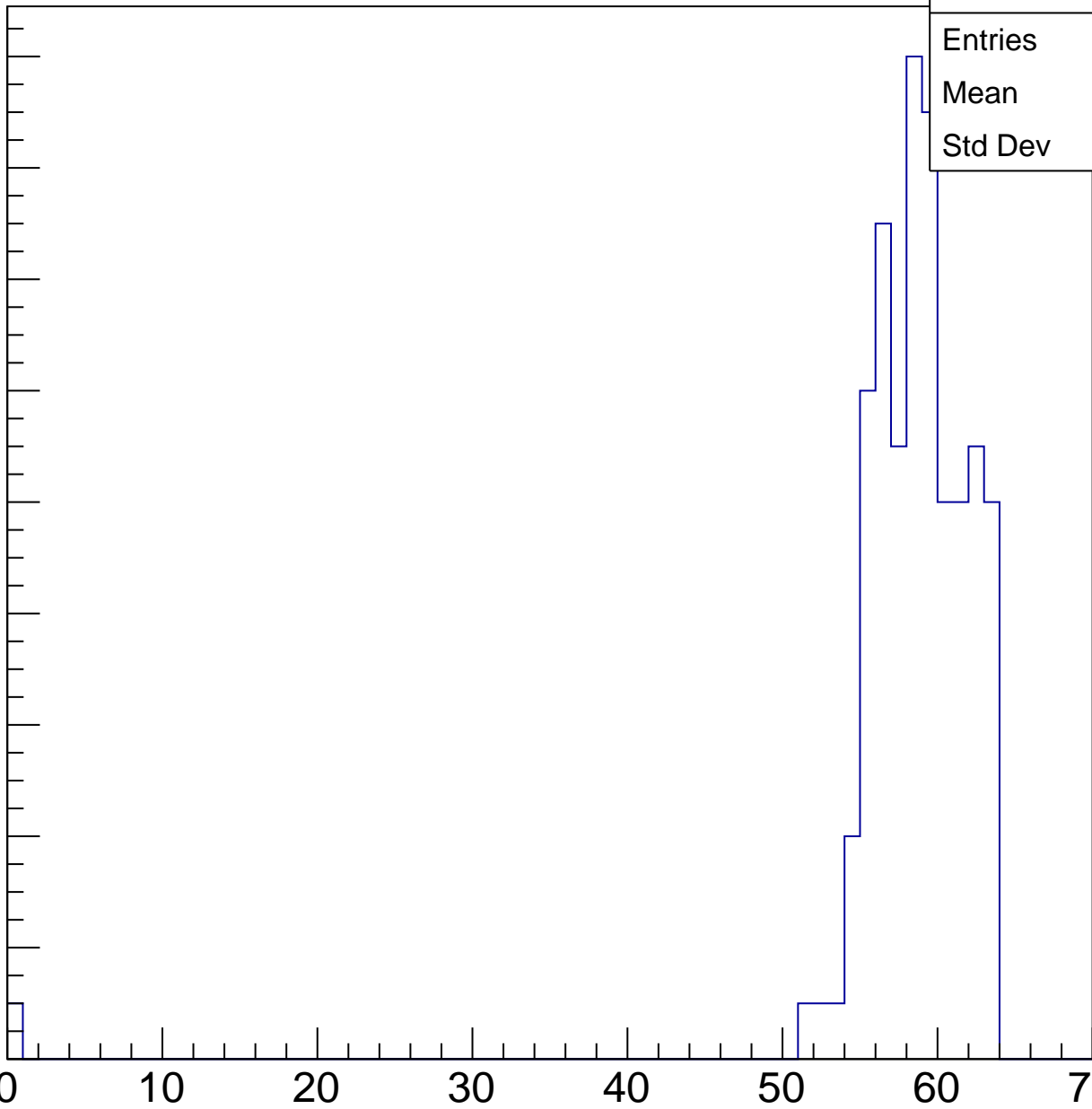
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	57.93
Std Dev	5.93

ampl

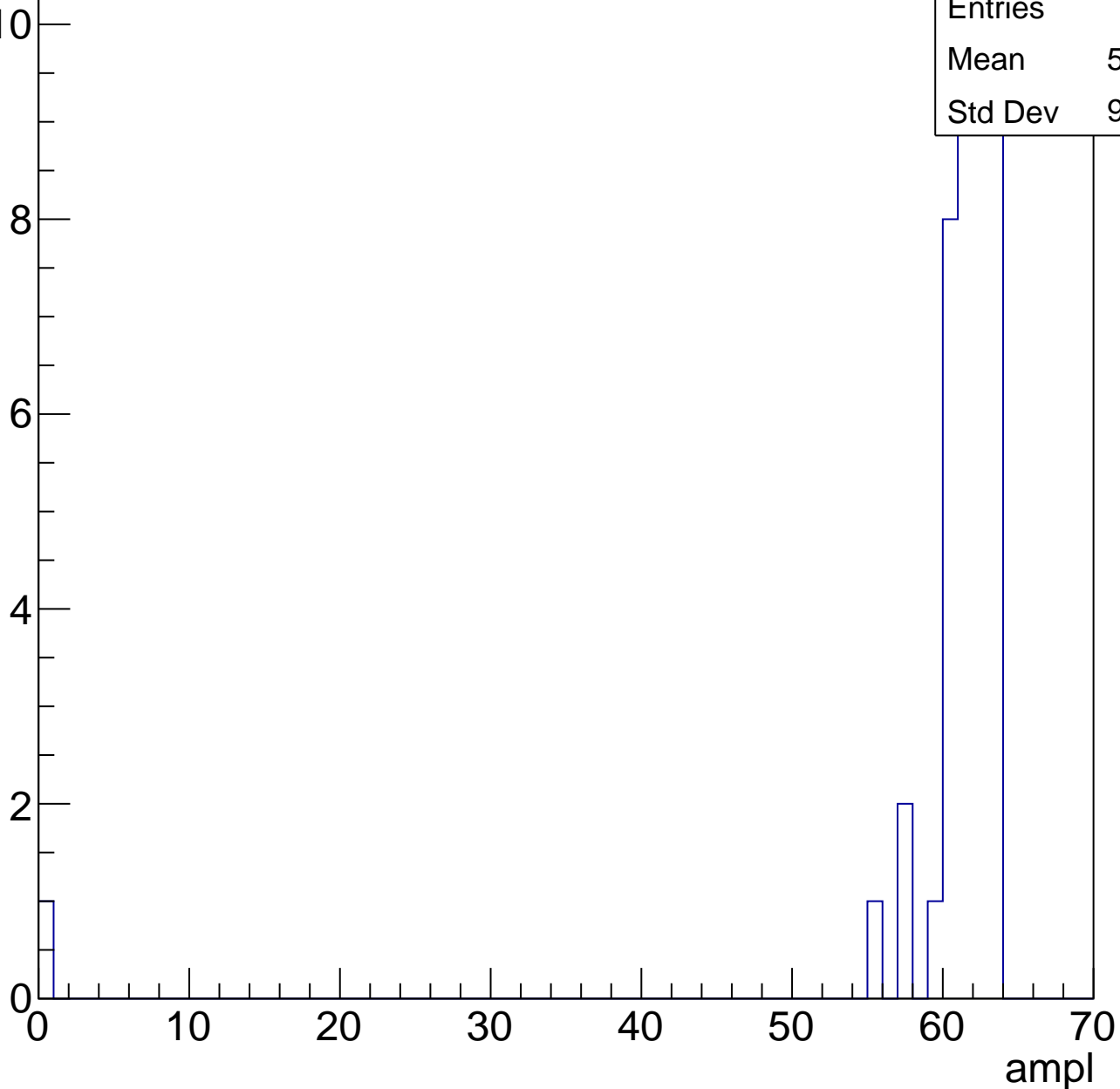


# B0L101S, U23-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	41
Mean	59.63
Std Dev	9.594



# B0L101S, U23-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch44, adc0

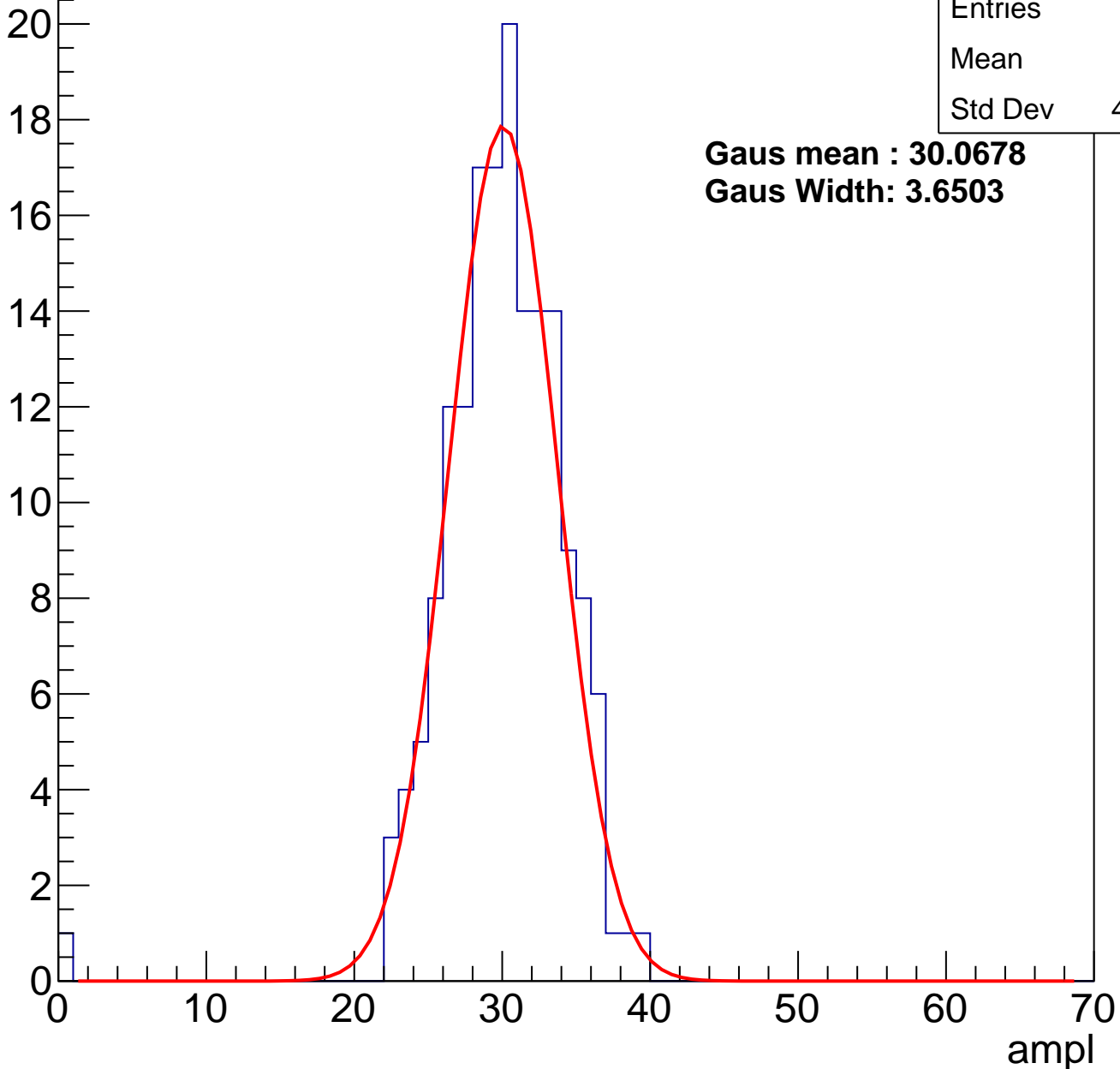
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	167
Mean	29.6
Std Dev	4.222

**Gaus mean : 30.0678**

**Gaus Width: 3.6503**

Entry



# B0L101S, U23-ch44, adc1

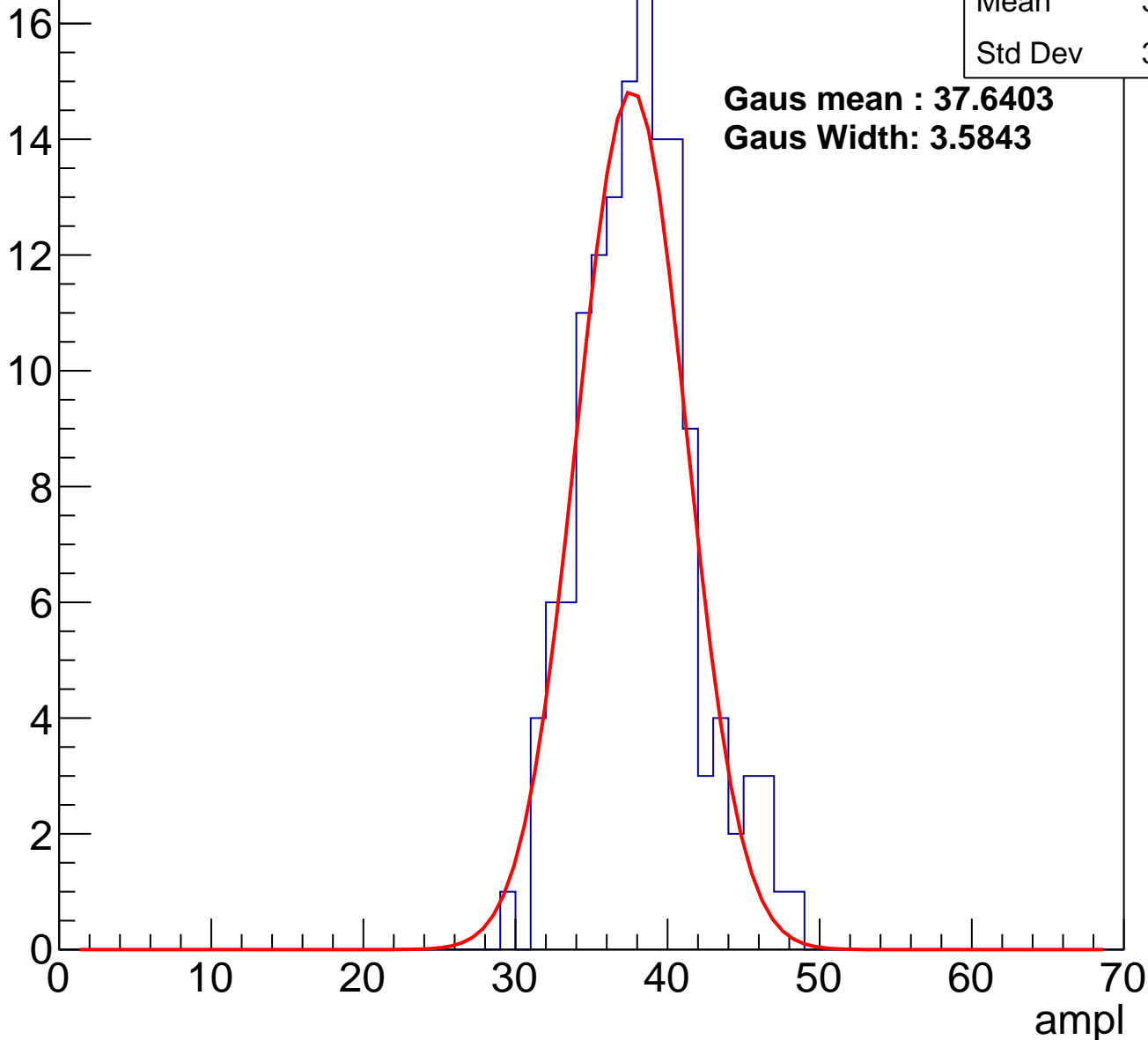
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	37.66
Std Dev	3.667

**Gaus mean : 37.6403**

**Gaus Width: 3.5843**

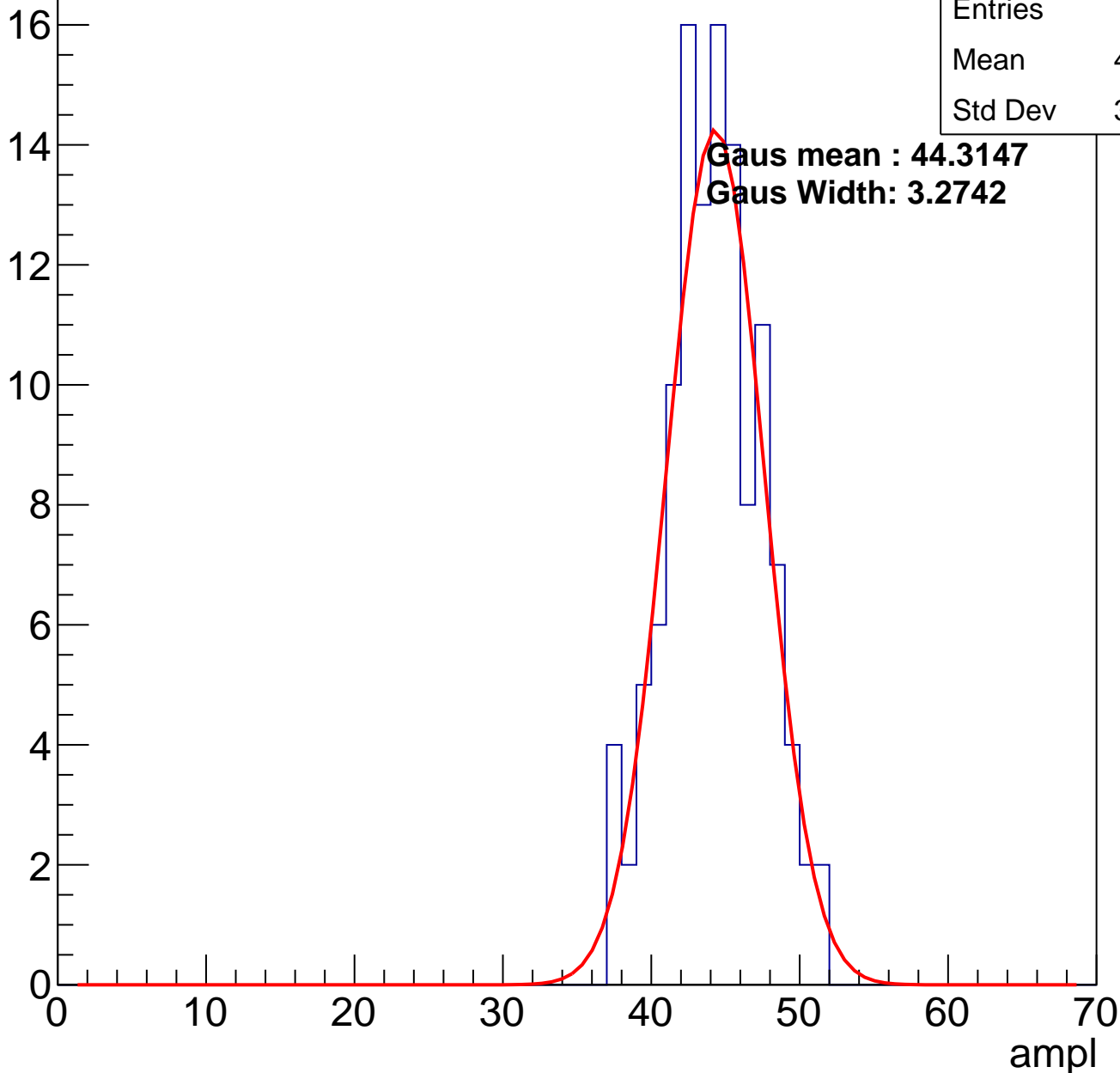
Entry



# B0L101S, U23-ch44, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

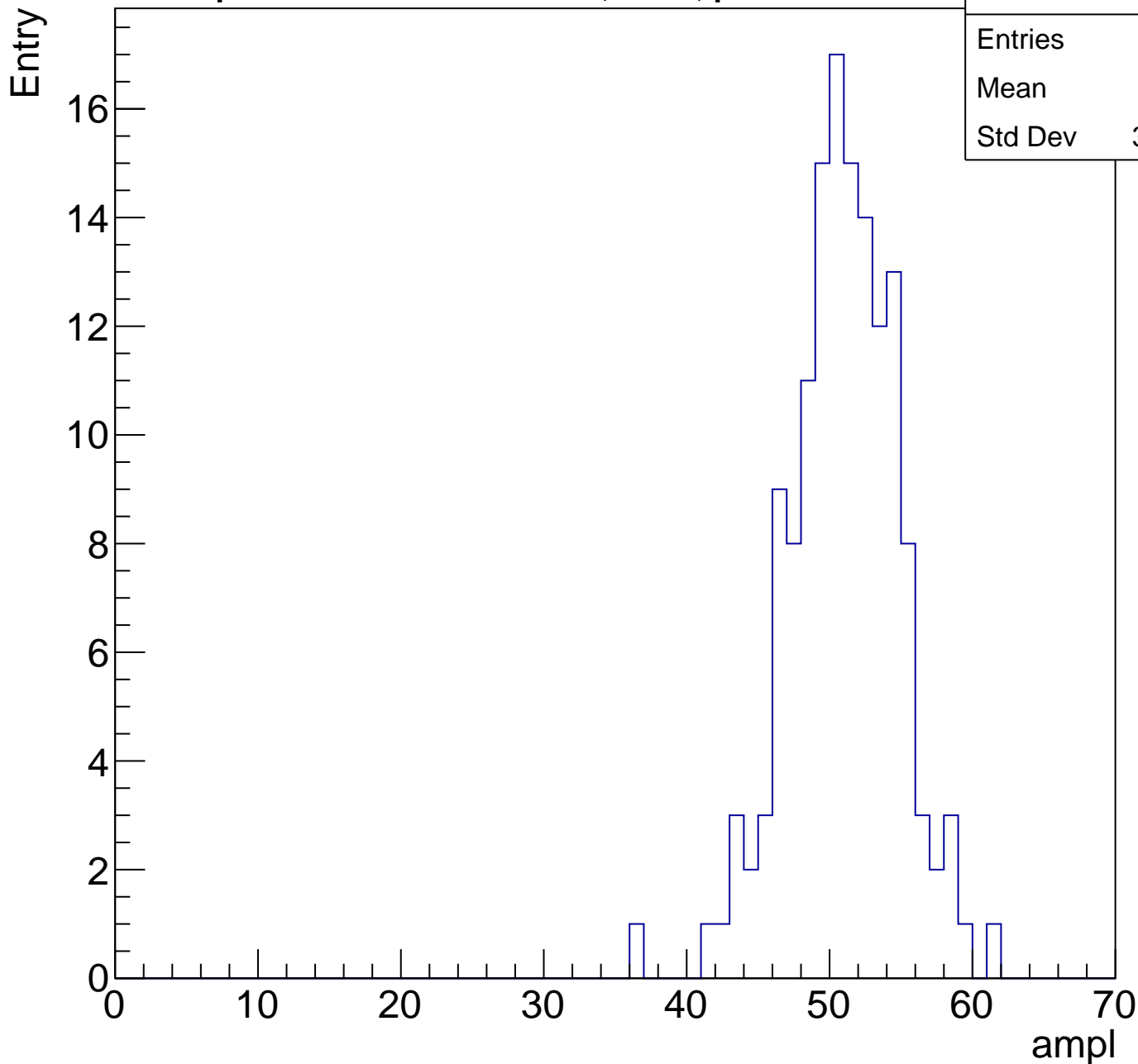
Entry



# B0L101S, U23-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	50.5
Std Dev	3.797



# B0L101S, U23-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

114

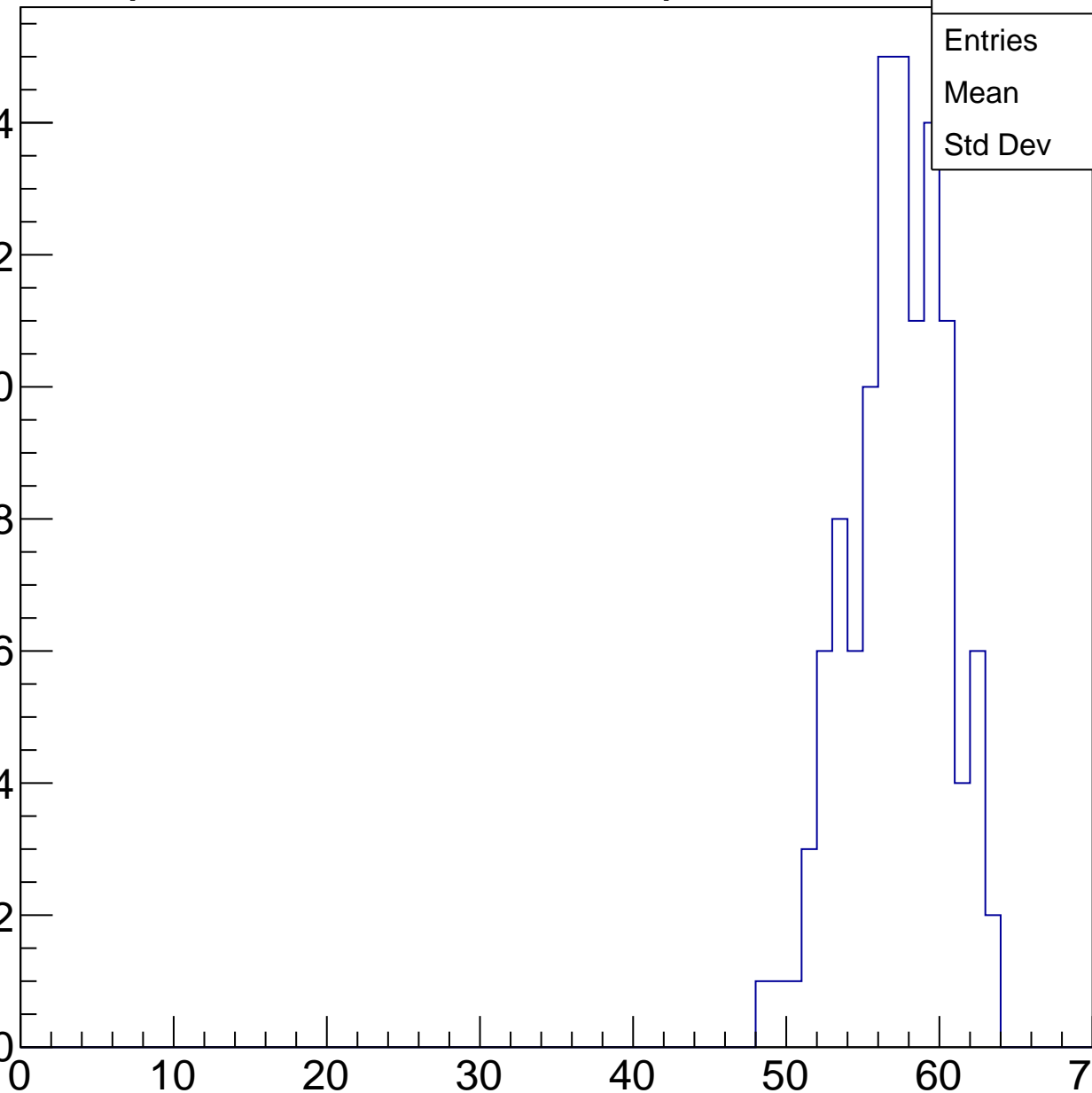
Mean

56.76

Std Dev

3.155

ampl



# B0L101S, U23-ch44, adc5

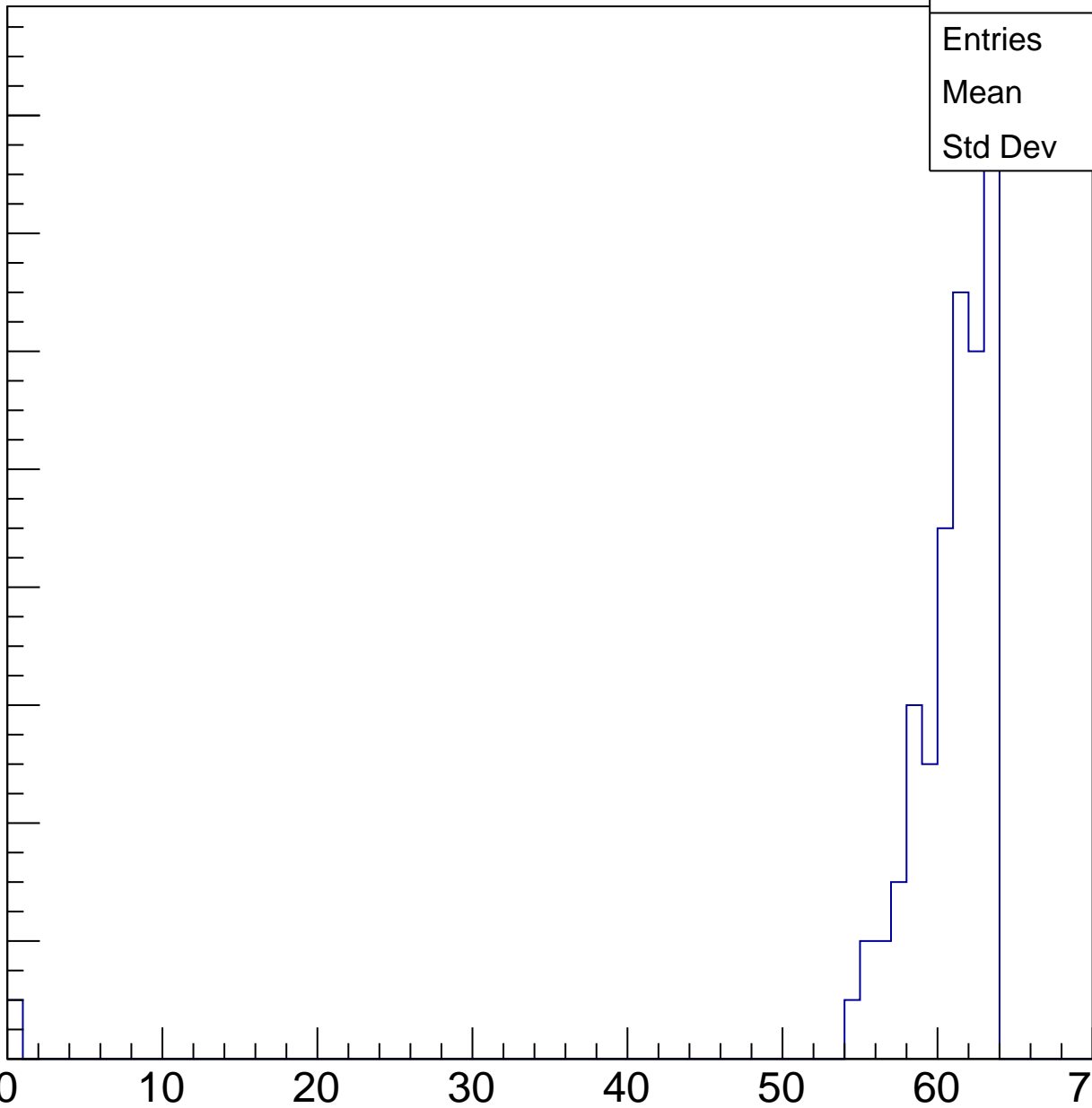
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	71
Mean	59.69
Std Dev	7.488

ampl



# B0L101S, U23-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

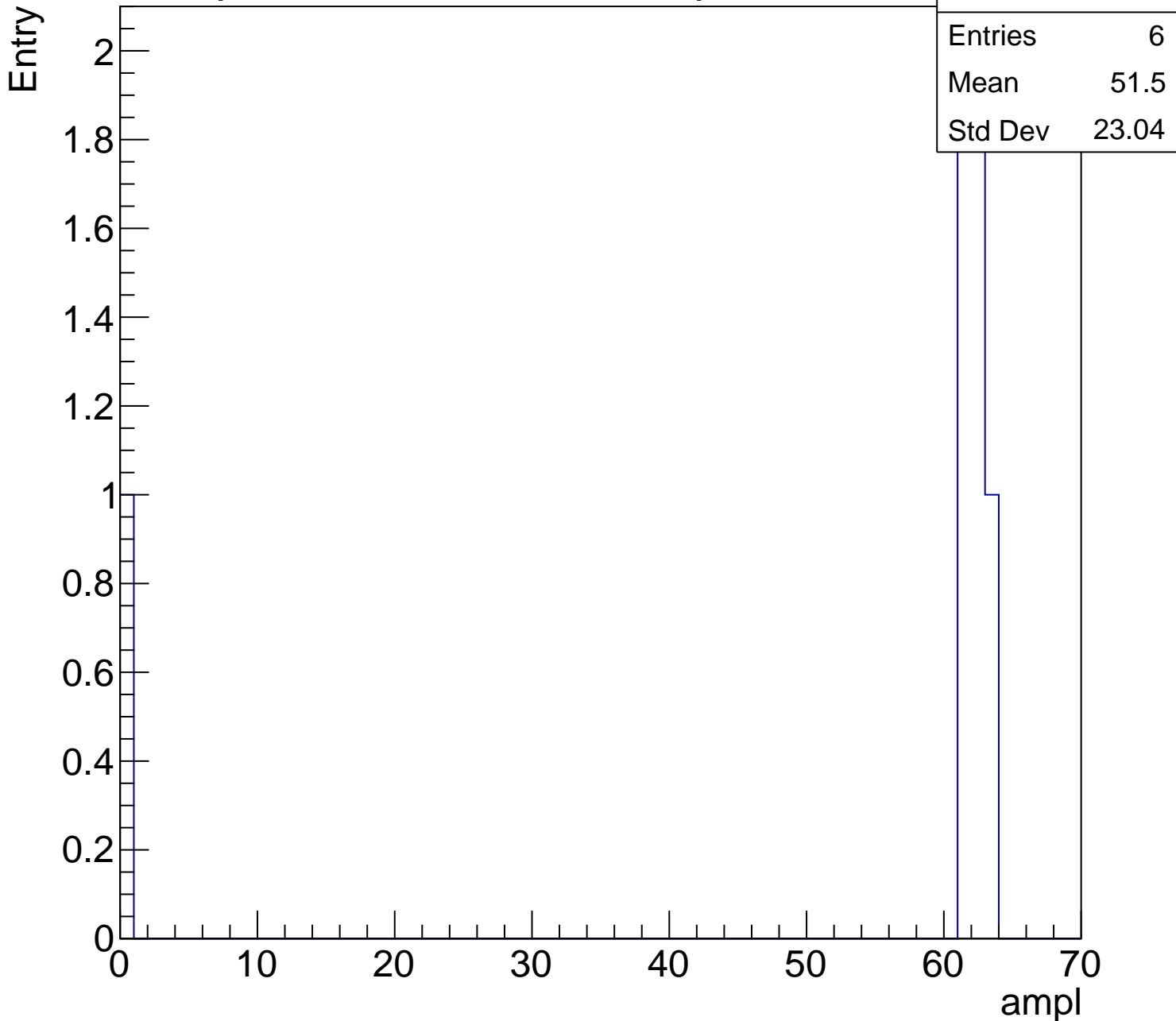
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.04

0 10 20 30 40 50 60 70

ampl





# B0L101S, U23-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

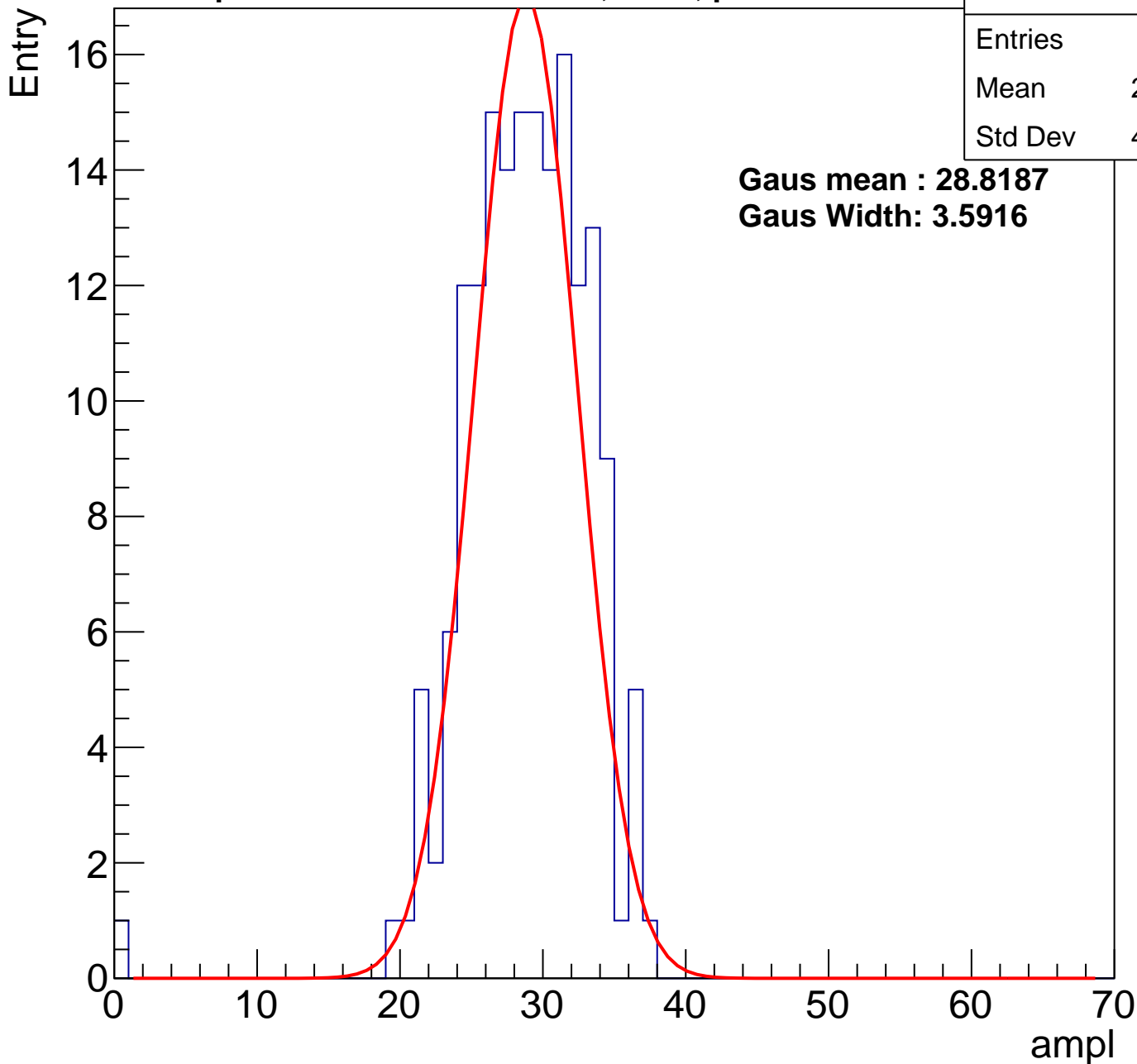
# B0L101S, U23-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	170
Mean	28.38
Std Dev	4.374

**Gaus mean : 28.8187**

**Gaus Width: 3.5916**



# B0L101S, U23-ch45, adc1

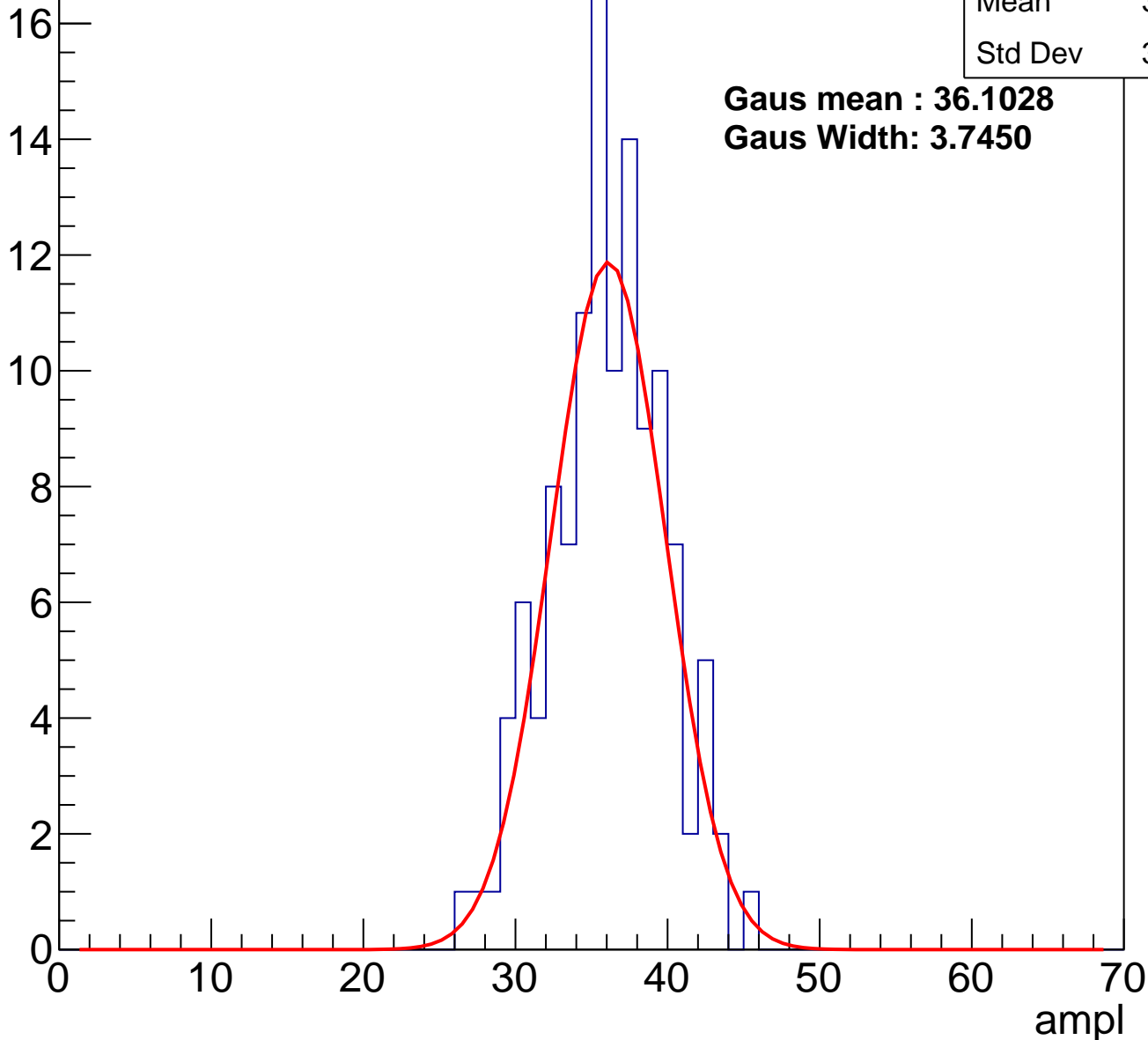
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	35.58
Std Dev	3.707

**Gaus mean : 36.1028**

**Gaus Width: 3.7450**

Entry



# B0L101S, U23-ch45, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

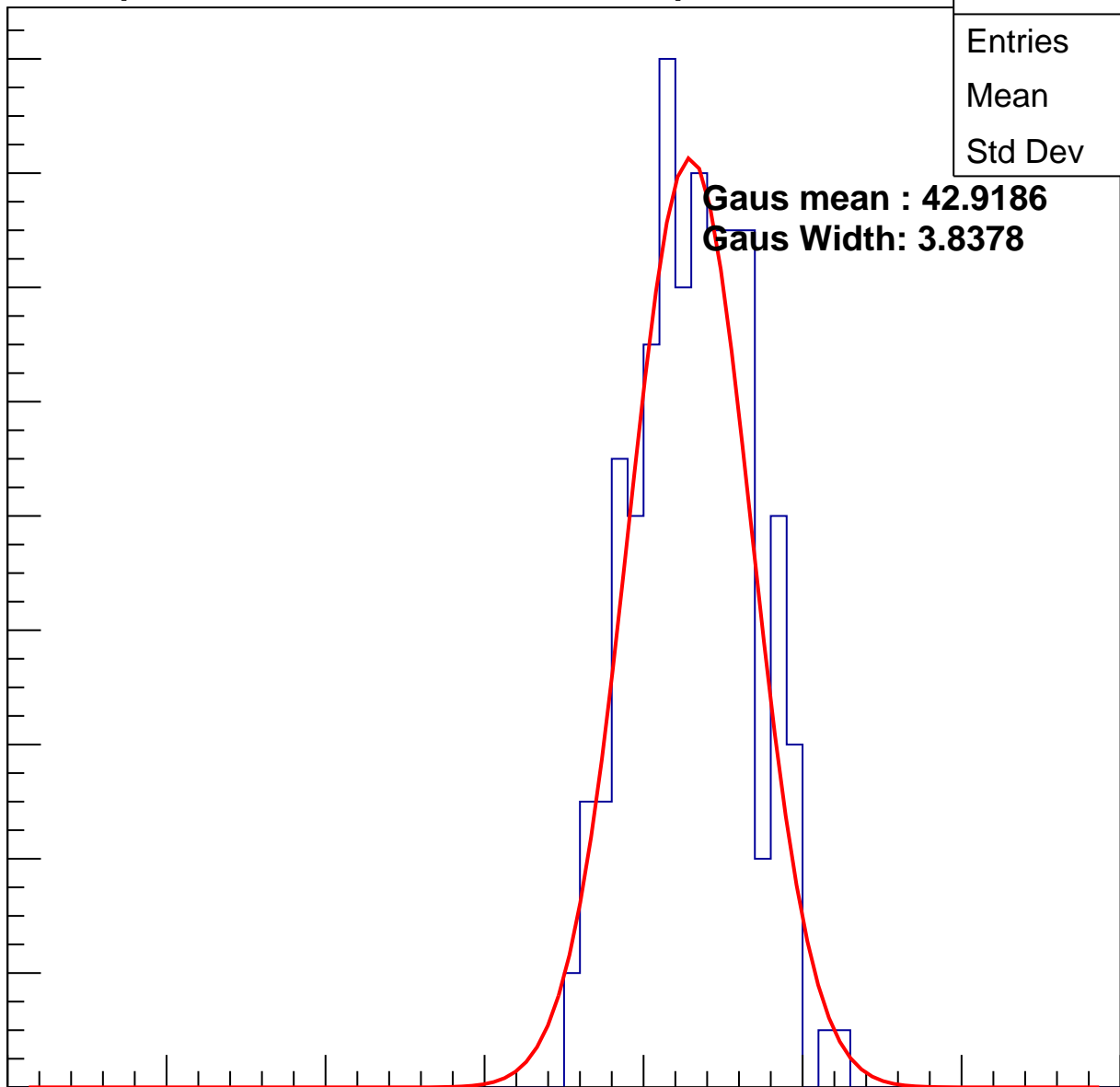
Entries	161
Mean	42.65
Std Dev	3.574

**Gaus mean : 42.9186**

**Gaus Width: 3.8378**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

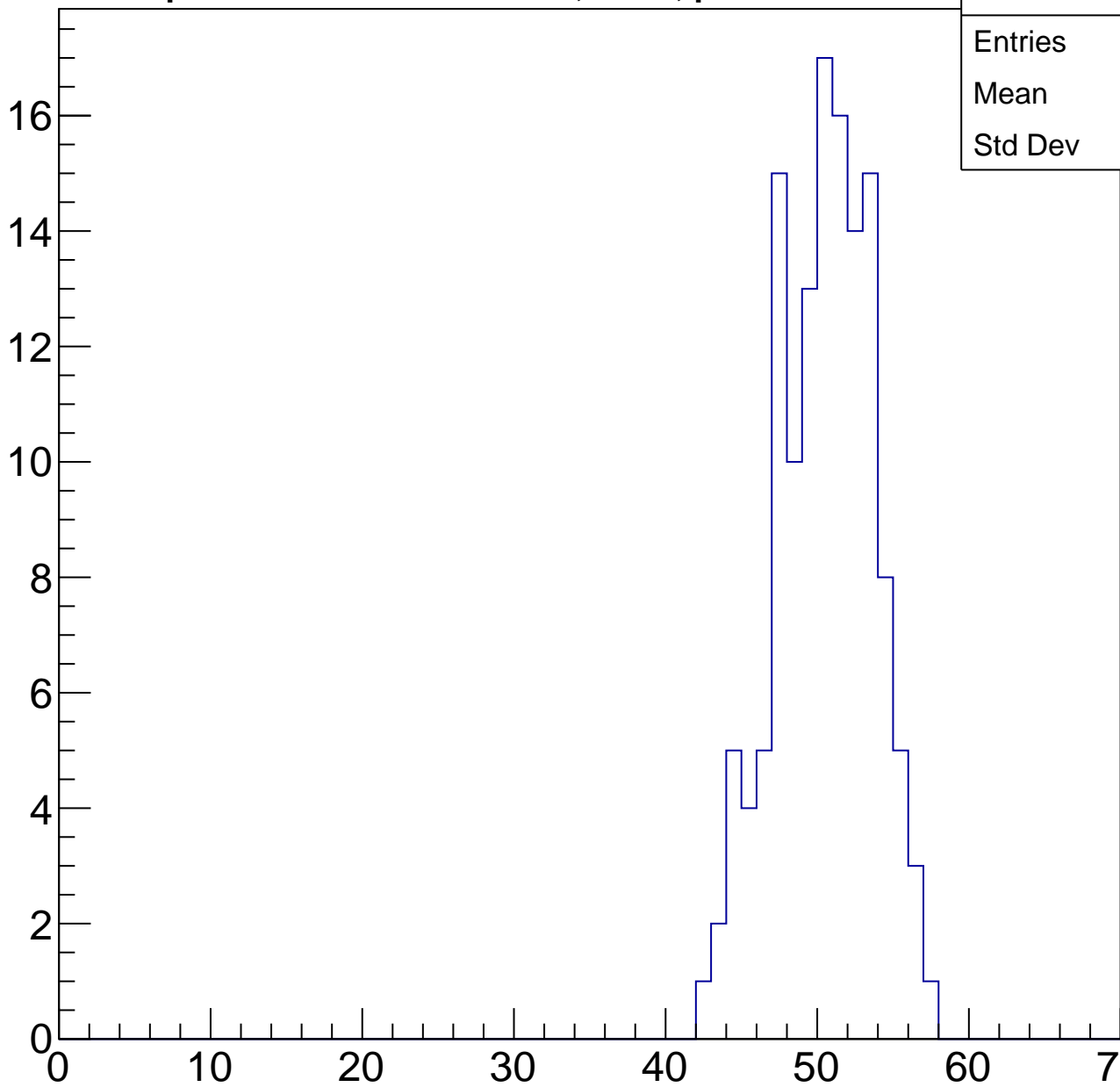
Entries	134
Mean	50.01
Std Dev	3.147

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

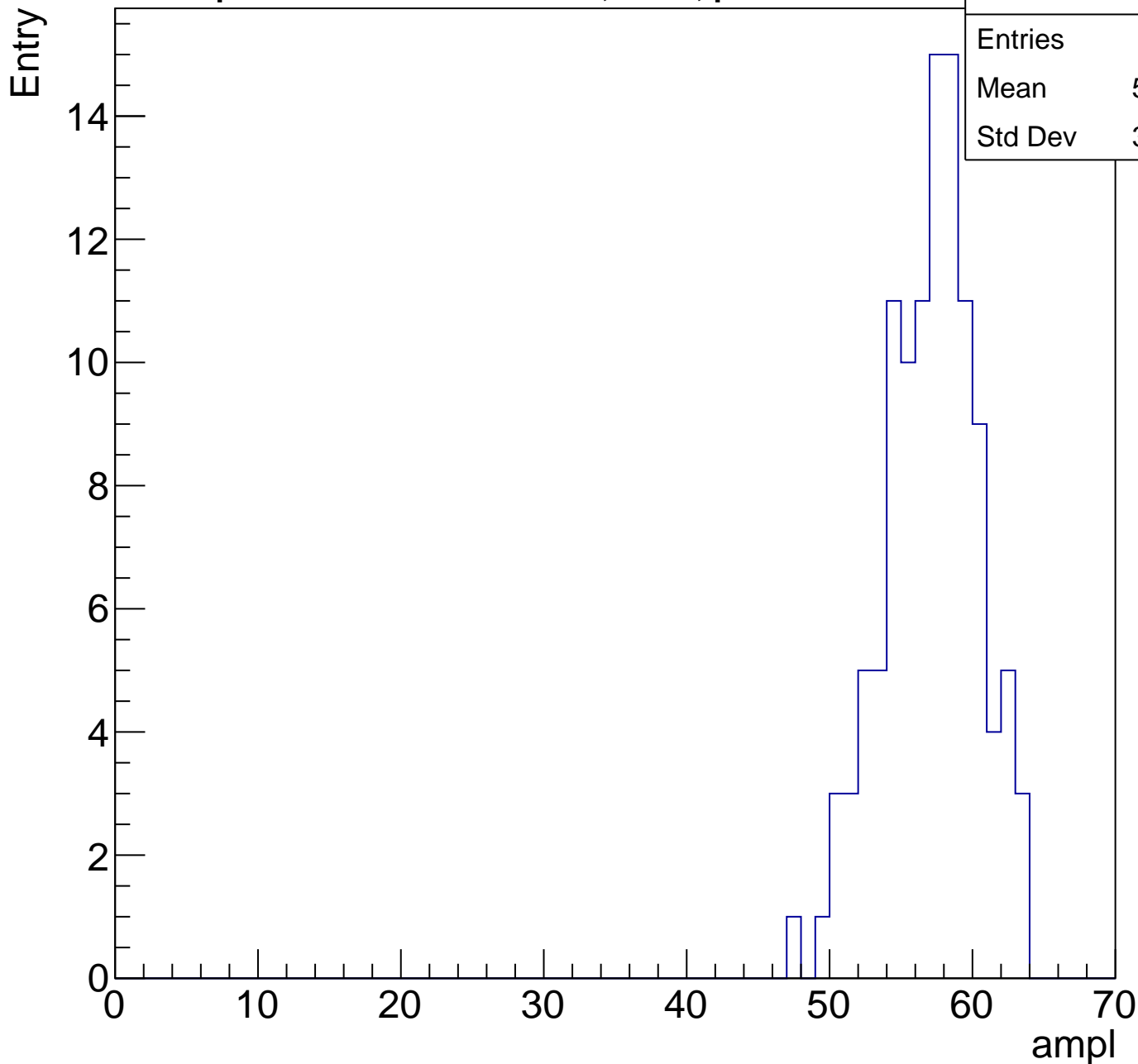
ampl



# B0L101S, U23-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	56.62
Std Dev	3.266



# B0L101S, U23-ch45, adc5

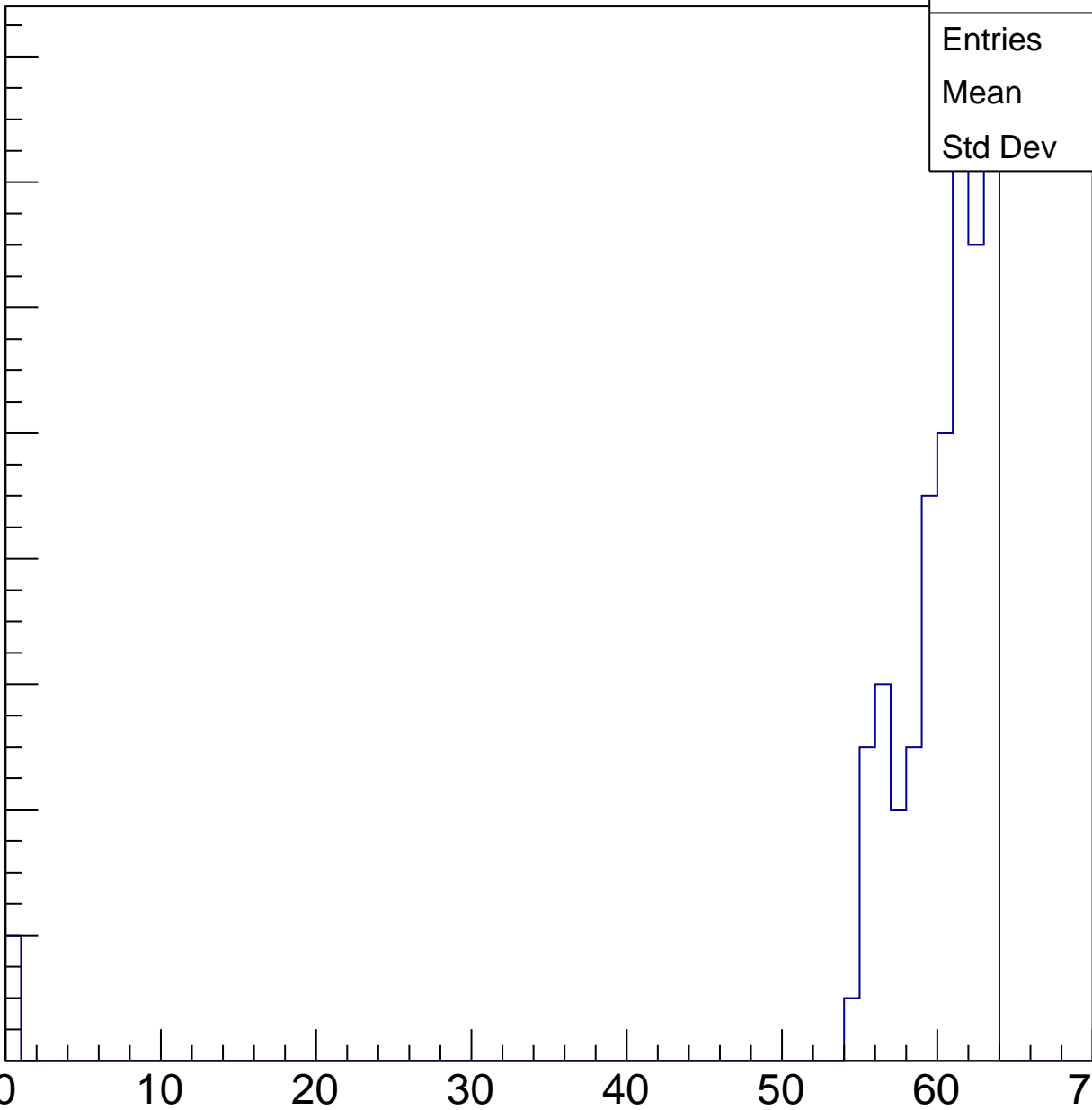
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	86
Mean	58.64
Std Dev	9.384

ampl



# B0L101S, U23-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	5
Mean	61.6
Std Dev	1.02



# B0L101S, U23-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	28.53
Std Dev	3.557

**Gaus mean : 29.4086**

**Gaus Width: 3.6649**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

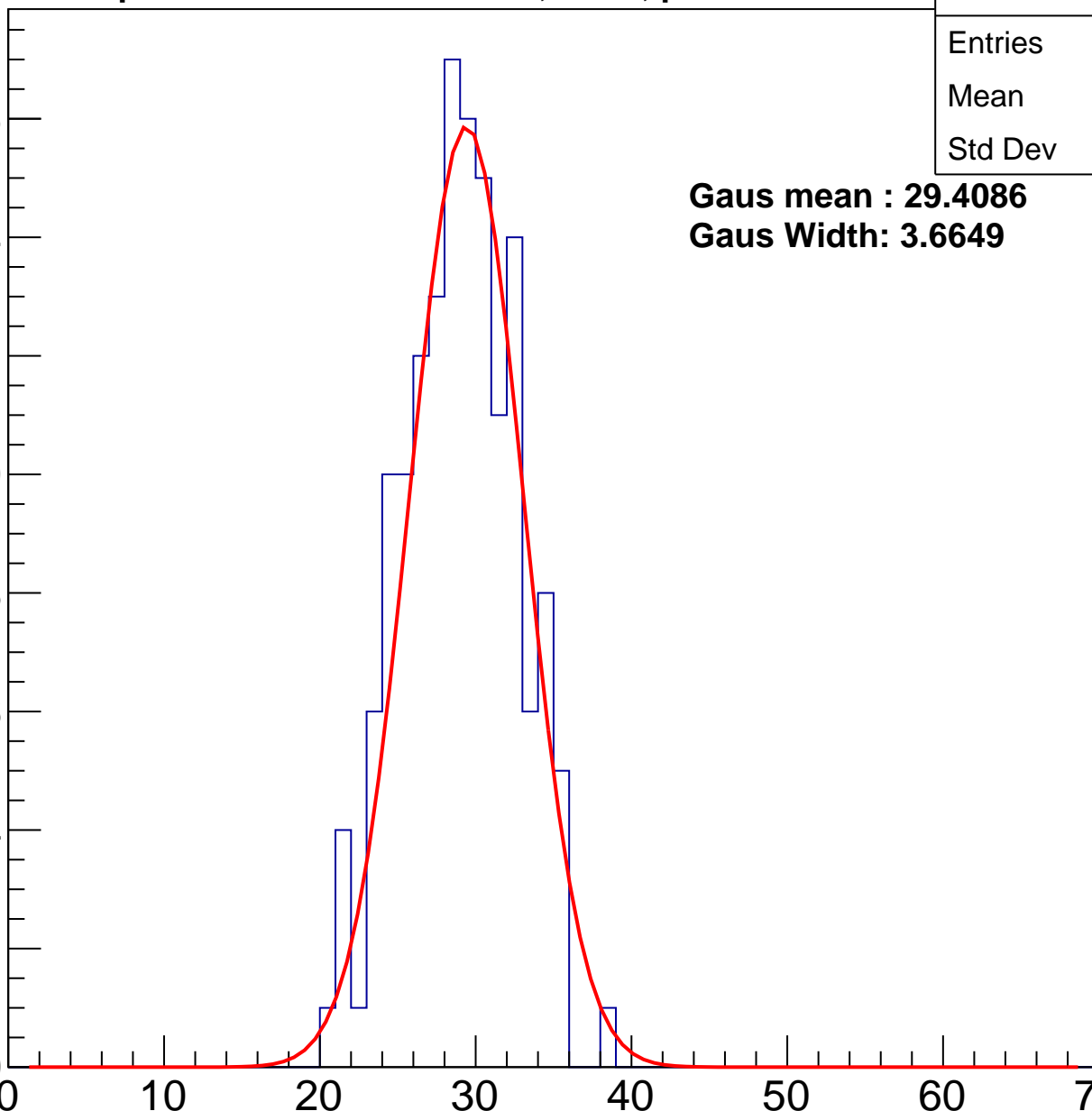
40

50

60

70

ampl



# B0L101S, U23-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	35.21
Std Dev	3.492

**Gaus mean : 35.7063**

**Gaus Width: 2.9802**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

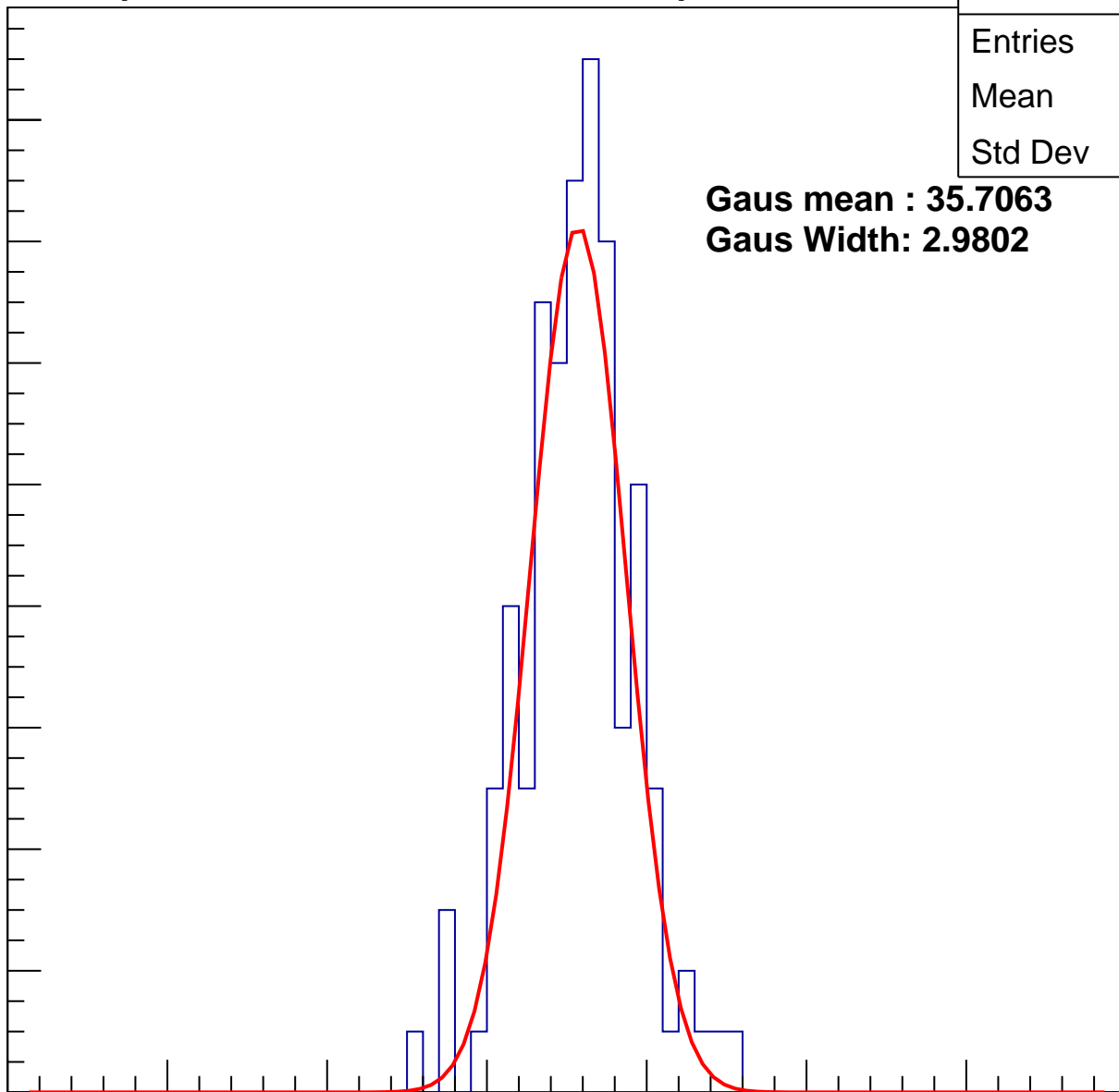
40

50

60

70

ampl



# B0L101S, U23-ch46, adc2

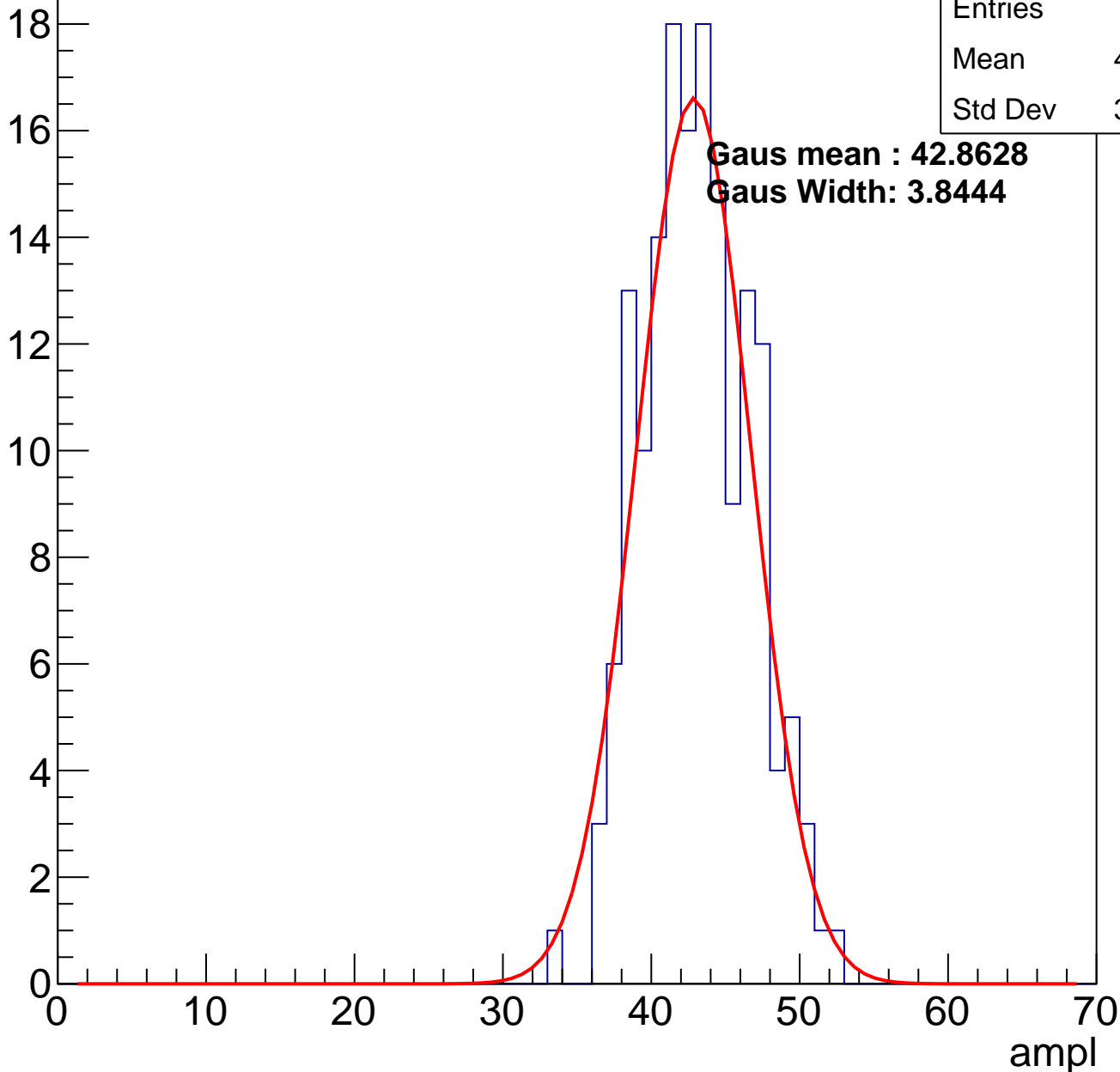
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	42.64
Std Dev	3.577

**Gaus mean : 42.8628**

**Gaus Width: 3.8444**

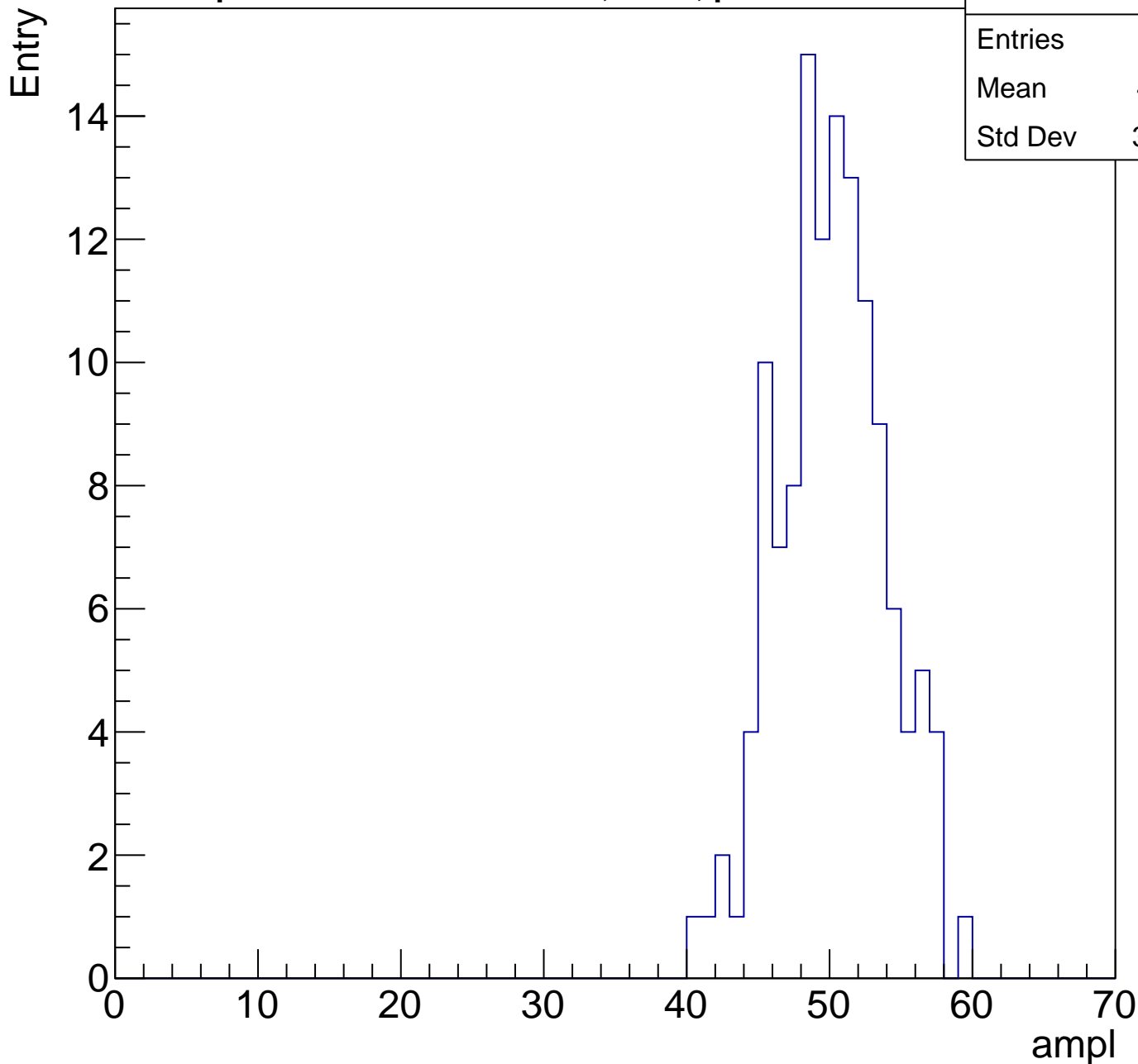
Entry



# B0L101S, U23-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	49.71
Std Dev	3.738



# B0L101S, U23-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

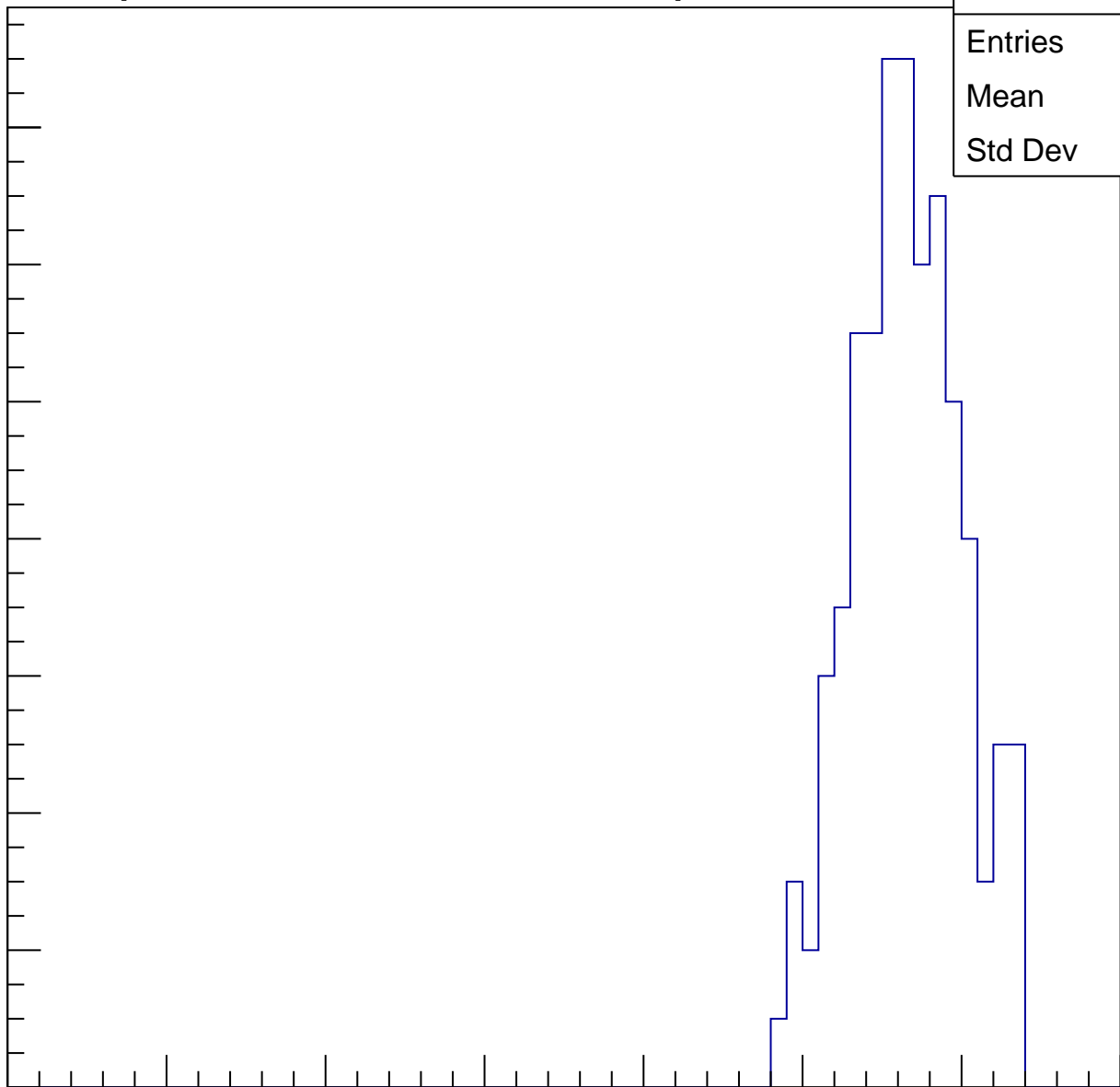
Entries	127
Mean	56.09
Std Dev	3.418

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch46, adc5

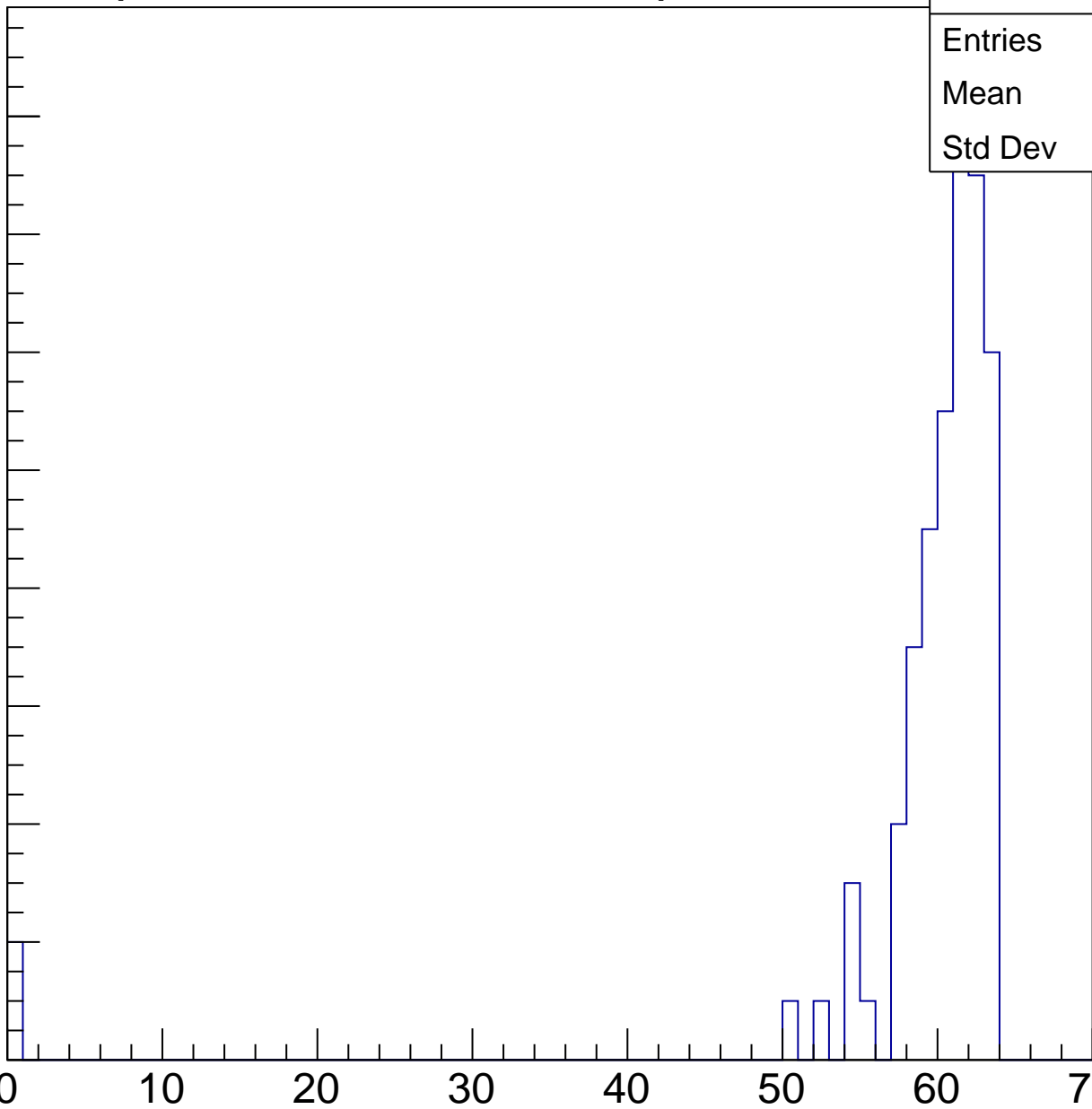
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	58.64
Std Dev	9.57

ampl



# B0L101S, U23-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

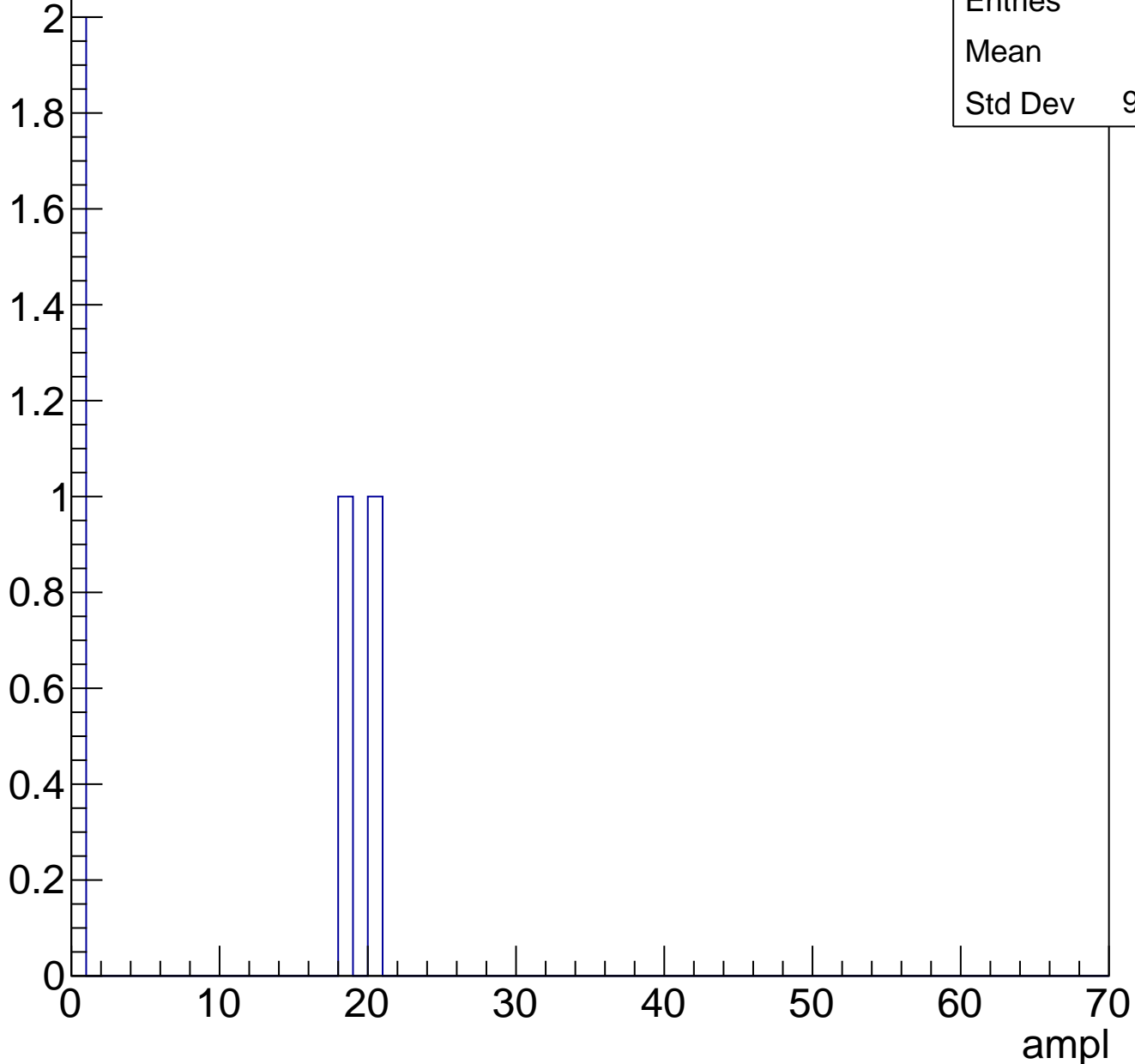




# B0L101S, U23-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	9.5
Std Dev	9.526

# B0L101S, U23-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

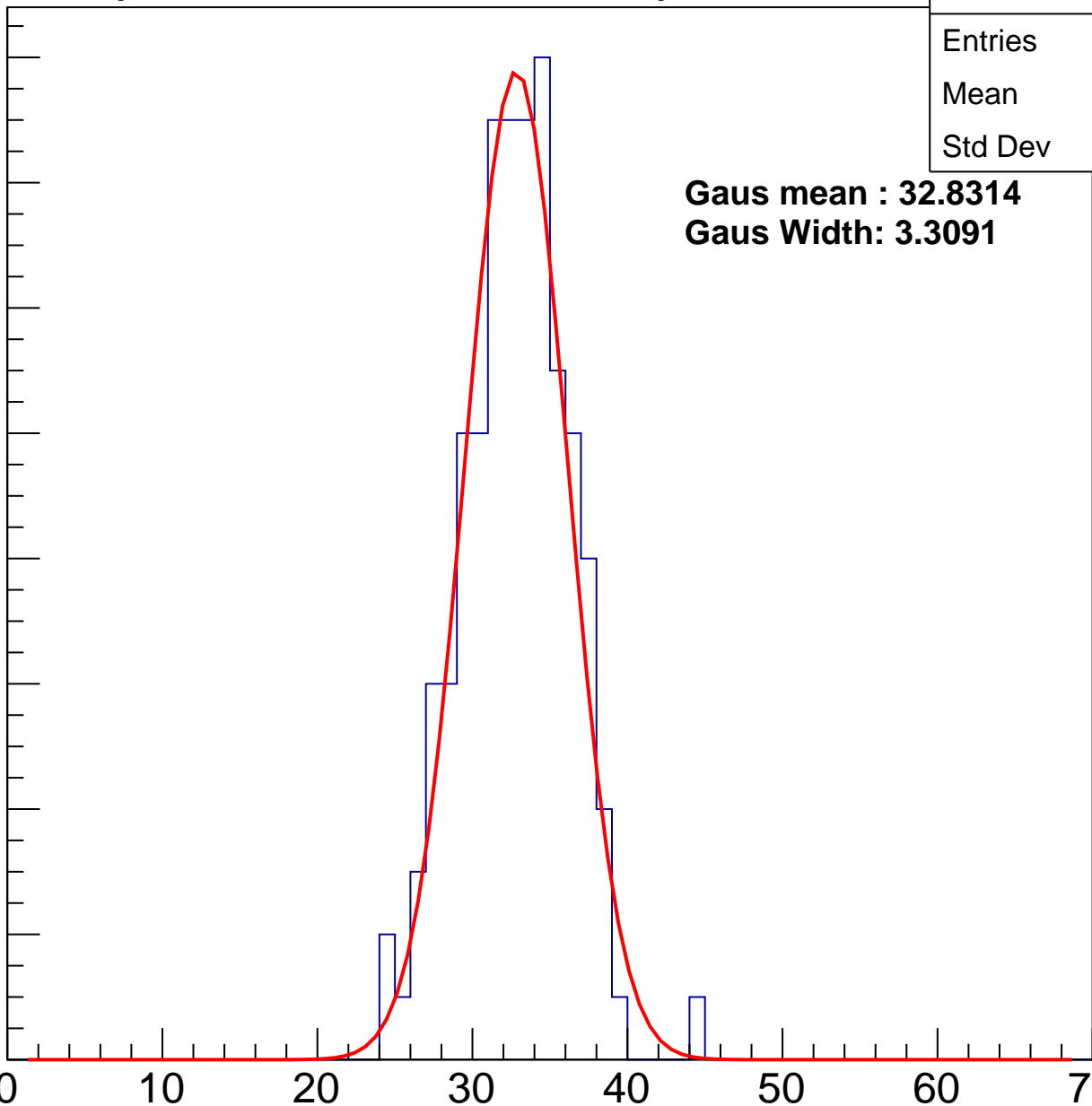
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	134
Mean	32.32
Std Dev	3.387

**Gaus mean : 32.8314**

**Gaus Width: 3.3091**

ampl



# B0L101S, U23-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

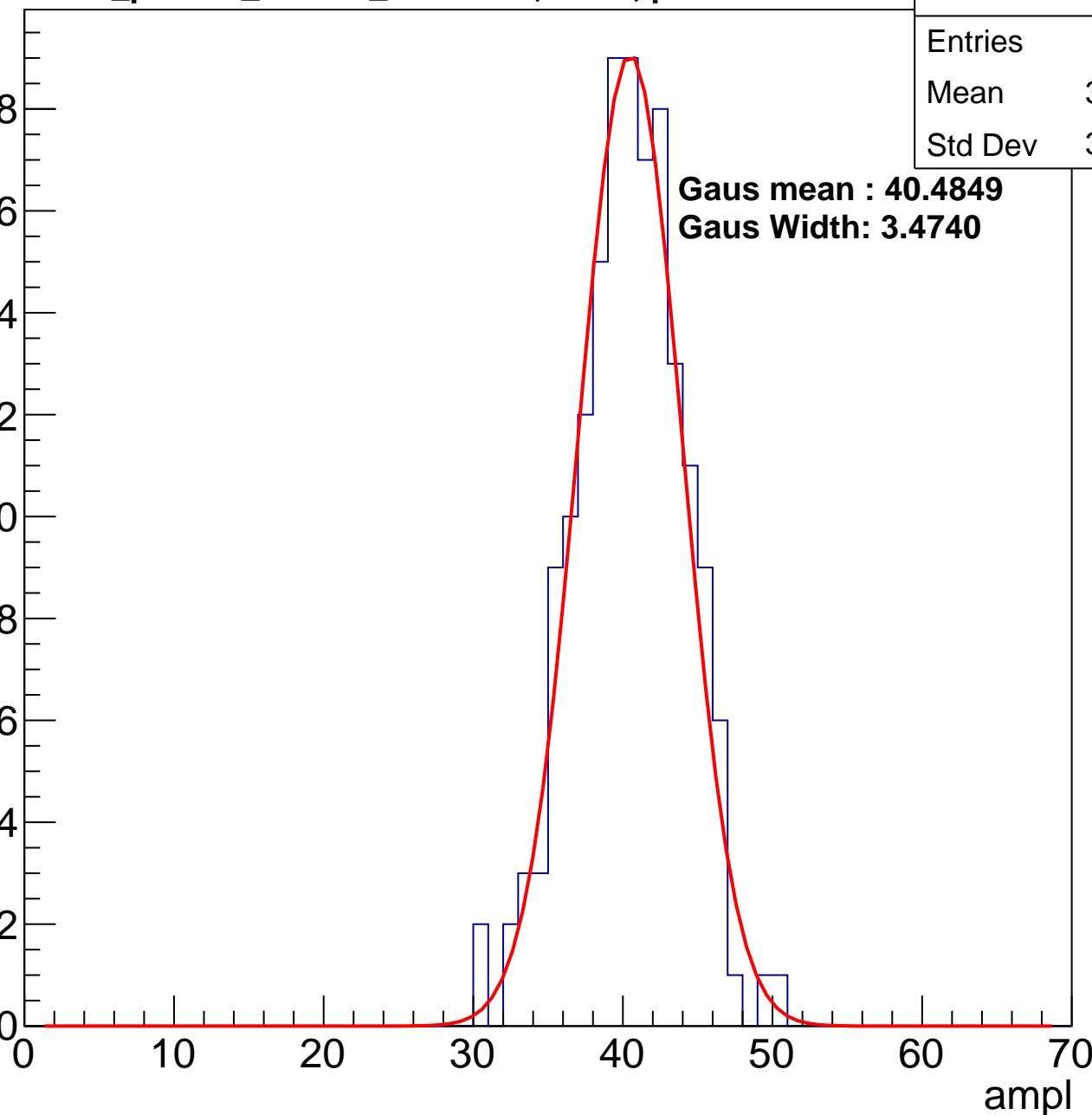
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	171
Mean	39.99
Std Dev	3.593

**Gaus mean : 40.4849**

**Gaus Width: 3.4740**



# B0L101S, U23-ch47, adc2

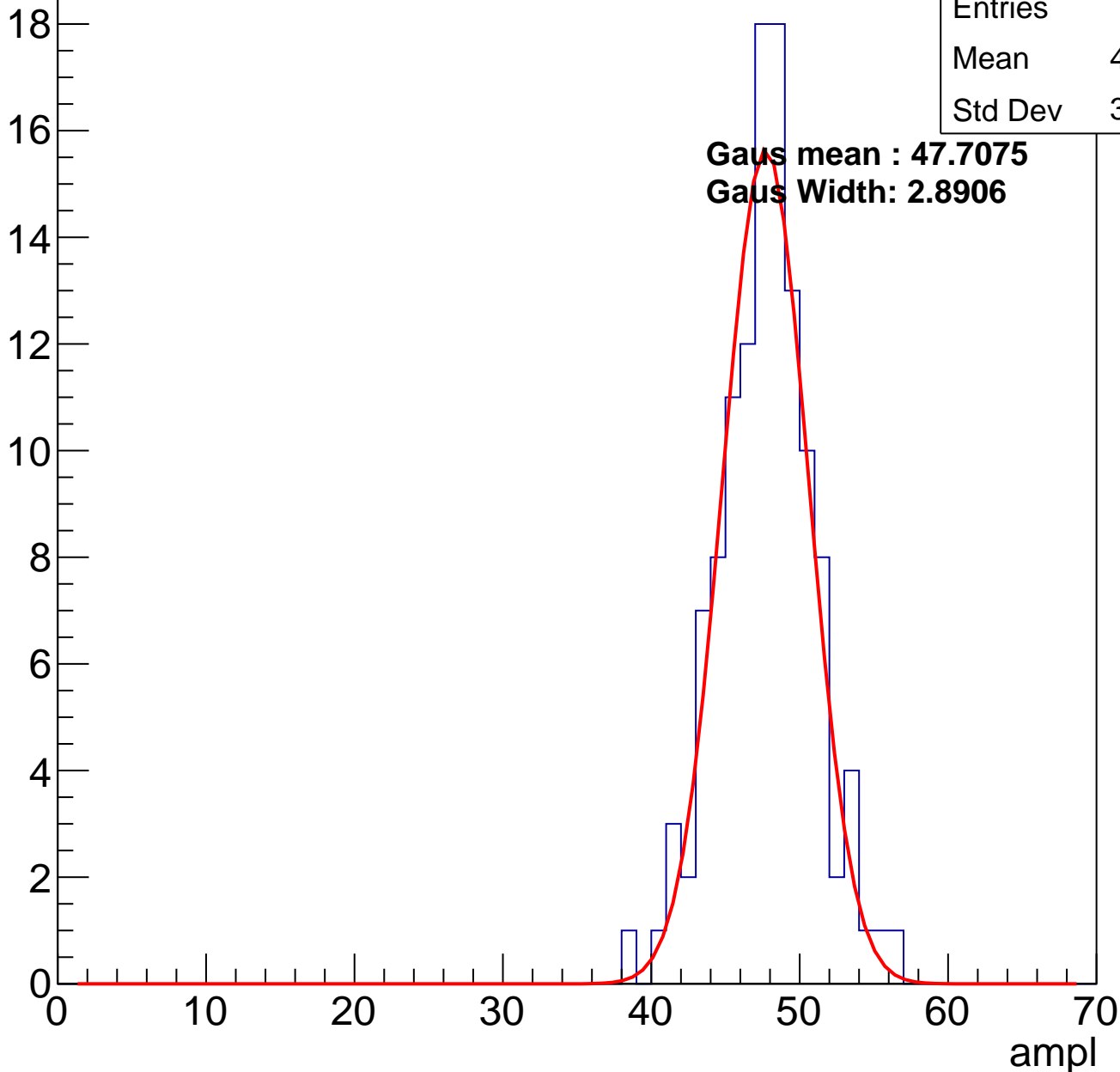
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	47.28
Std Dev	3.142

**Gaus mean : 47.7075**

**Gaus Width: 2.8906**

Entry



# B0L101S, U23-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

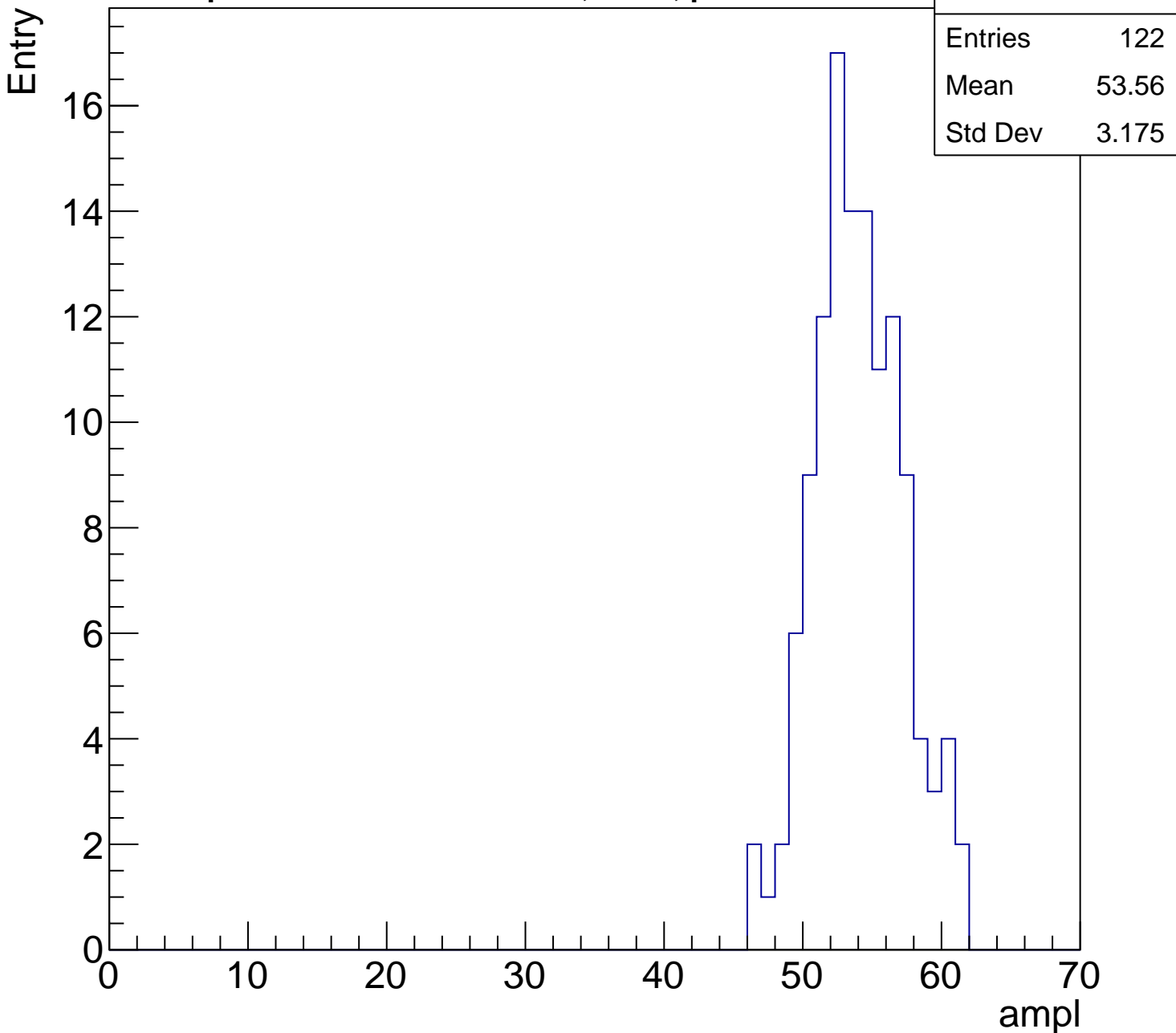
Entries	122
Mean	53.56
Std Dev	3.175

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch47, adc4

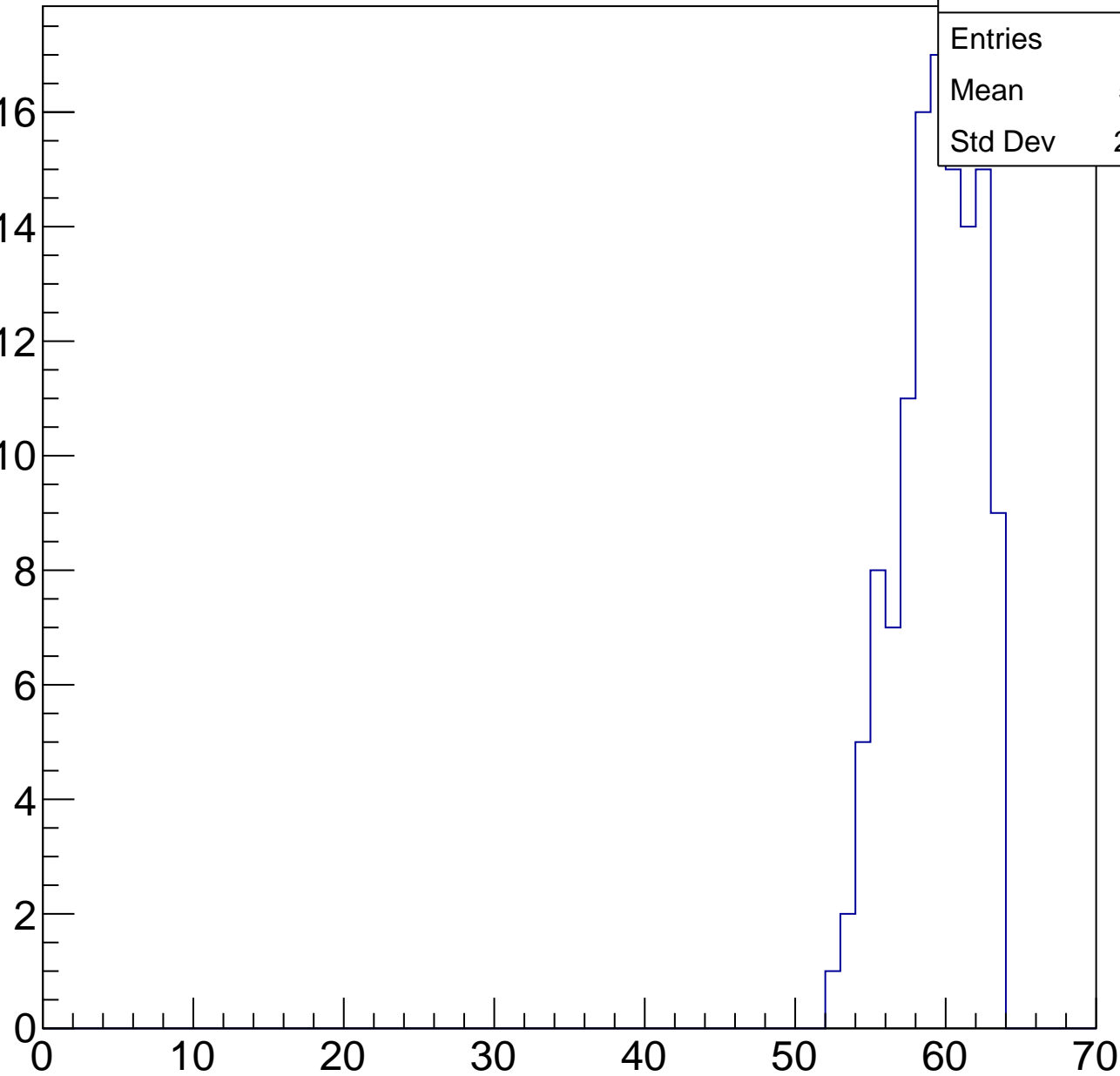
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	120
Mean	58.91
Std Dev	2.655

ampl



# B0L101S, U23-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	23
Mean	56.83
Std Dev	17.56

# B0L101S, U23-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B0L101S, U23-ch48, adc0

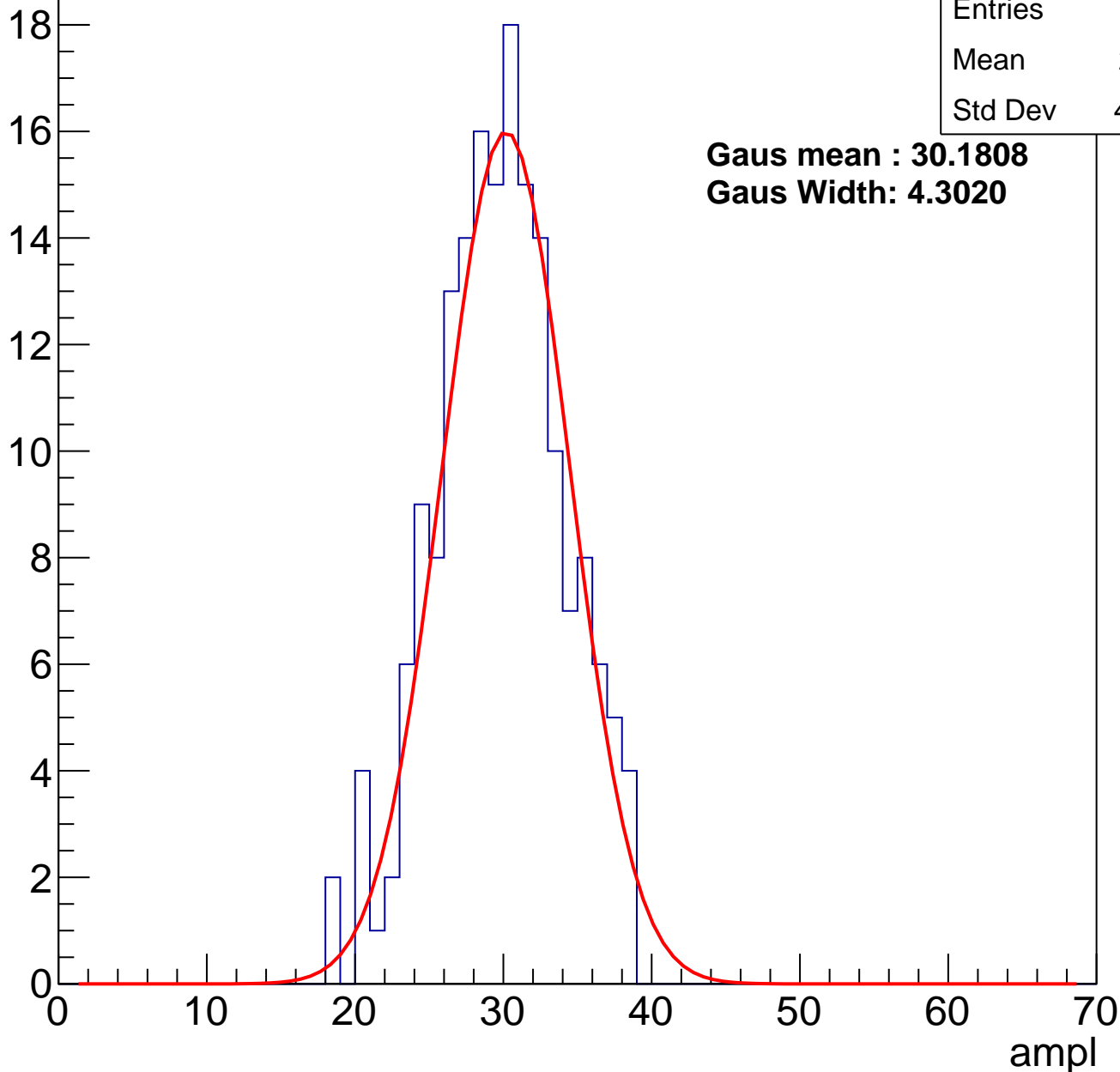
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	177
Mean	29.31
Std Dev	4.274

**Gaus mean : 30.1808**

**Gaus Width: 4.3020**

Entry

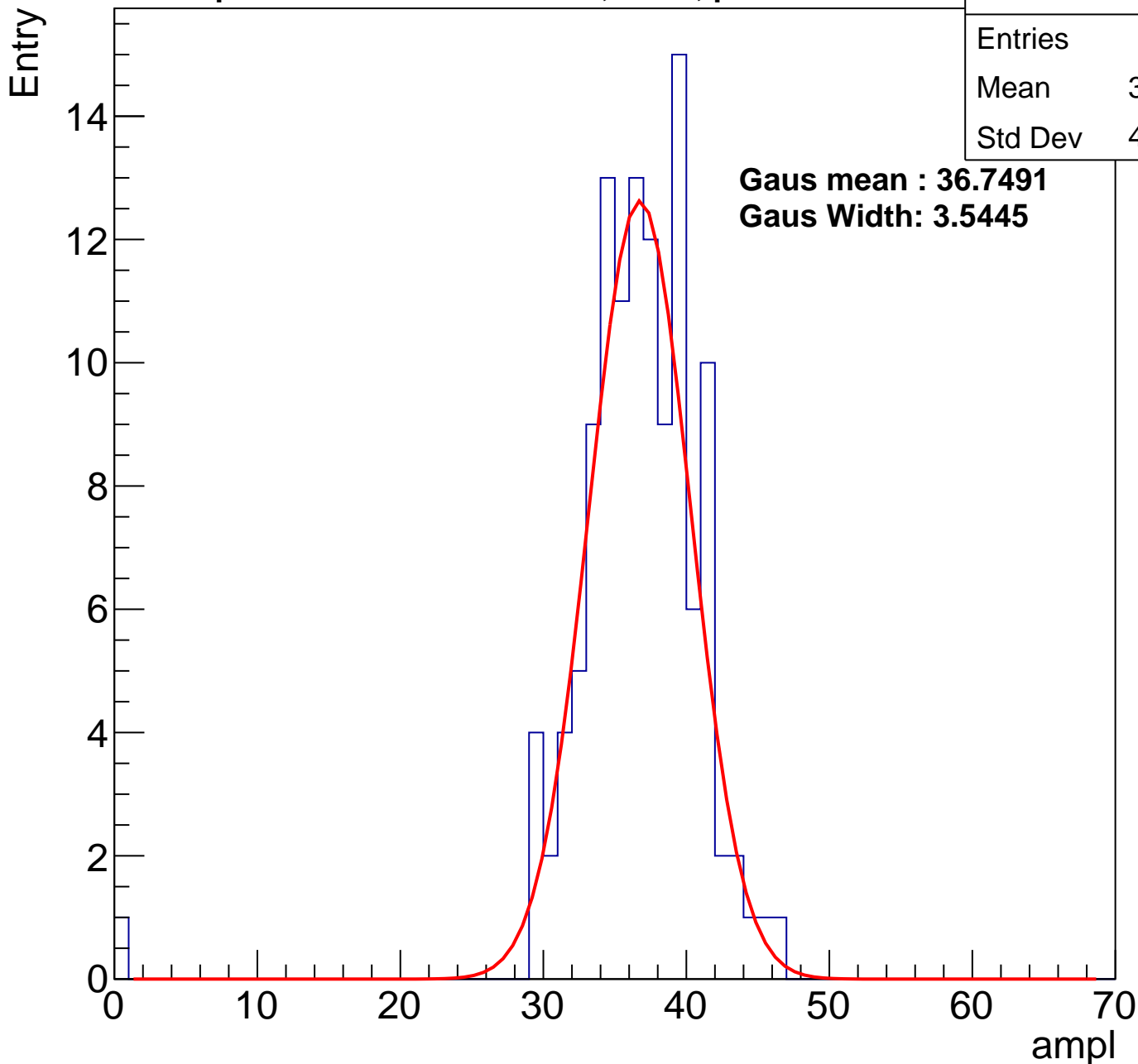


# B0L101S, U23-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	36.18
Std Dev	4.834

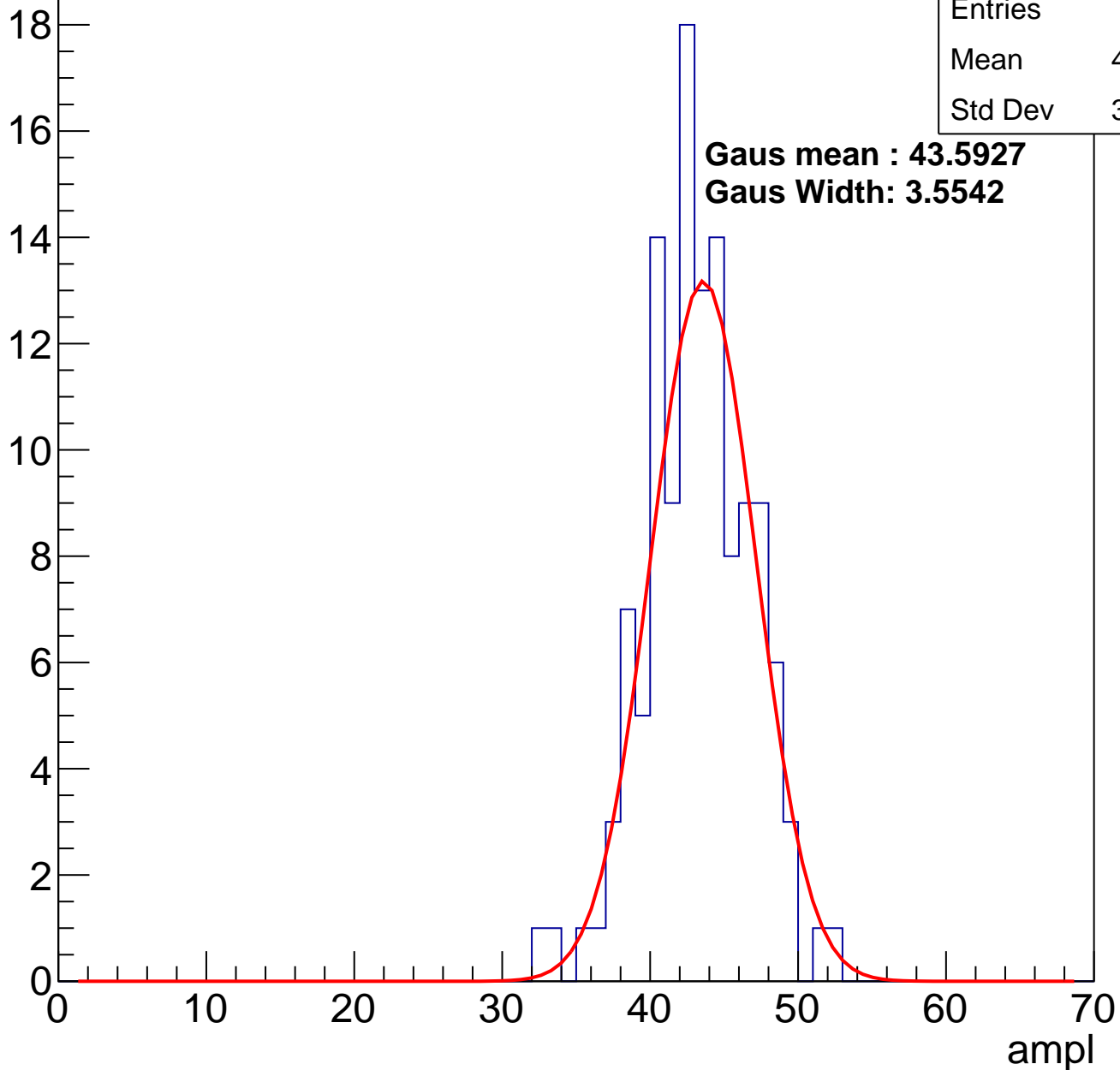
**Gaus mean : 36.7491**  
**Gaus Width: 3.5445**



# B0L101S, U23-ch48, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

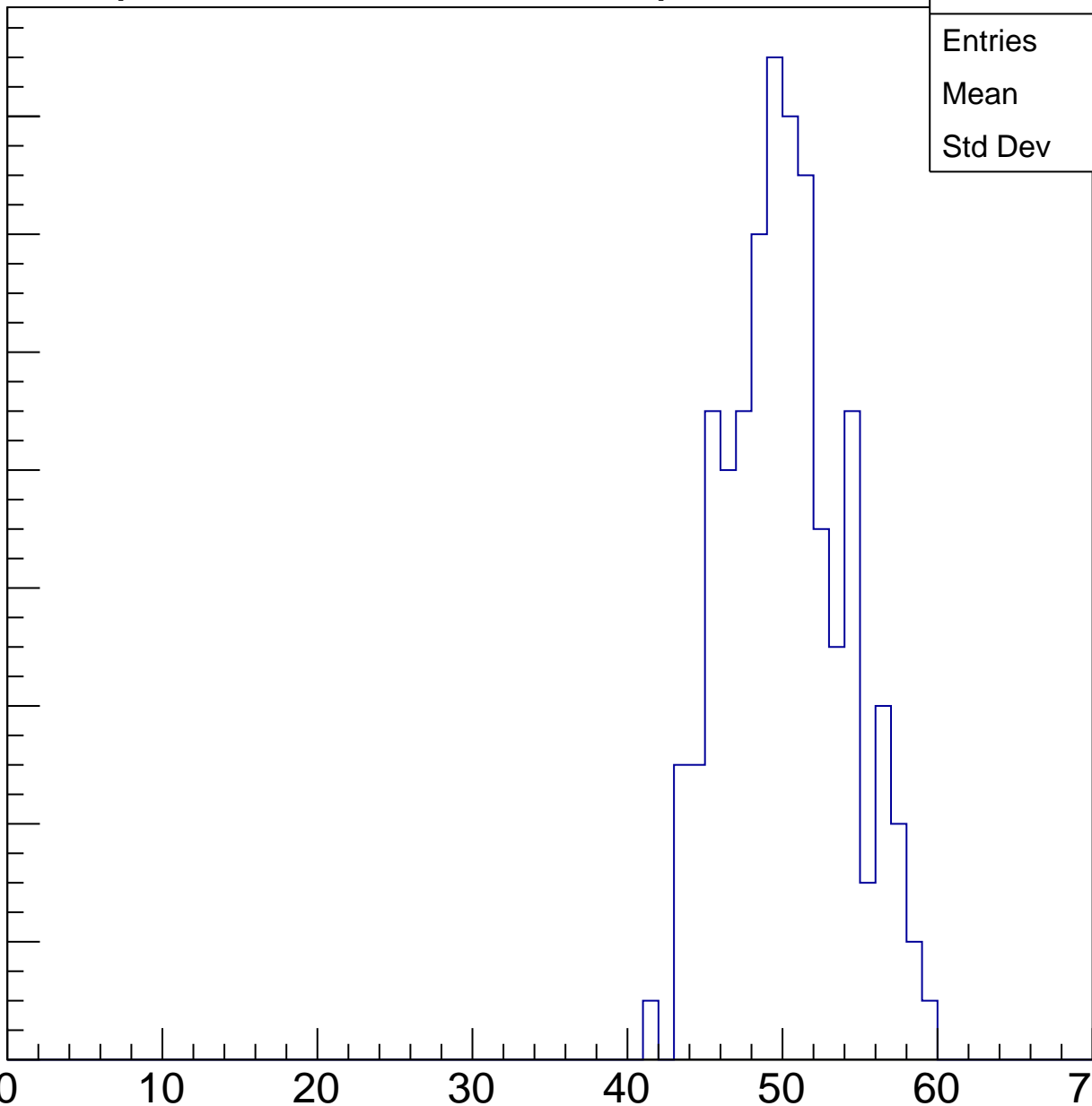
Entries	148
Mean	49.7
Std Dev	3.754

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

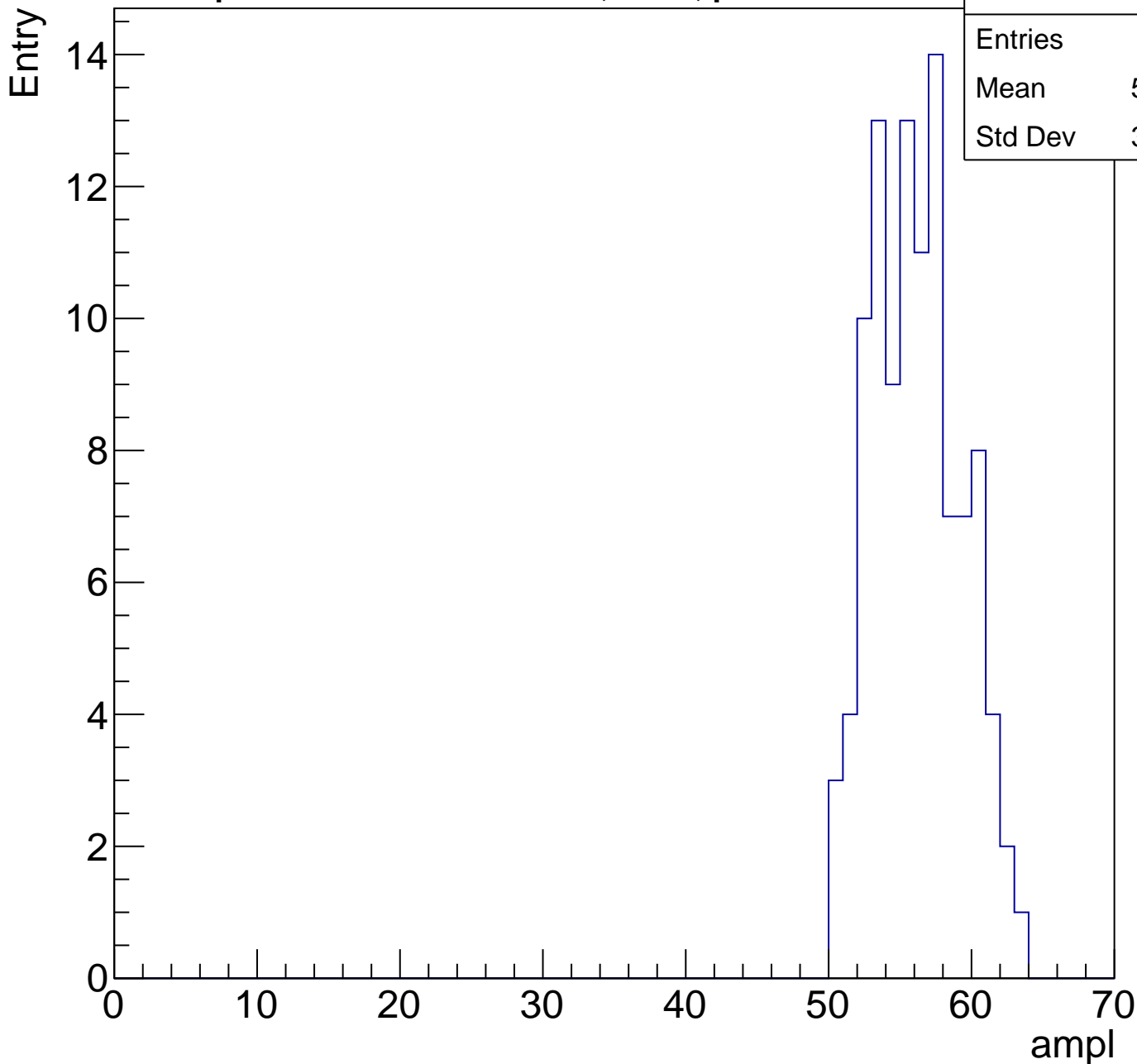
ampl



# B0L101S, U23-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	55.74
Std Dev	3.042



# B0L101S, U23-ch48, adc5

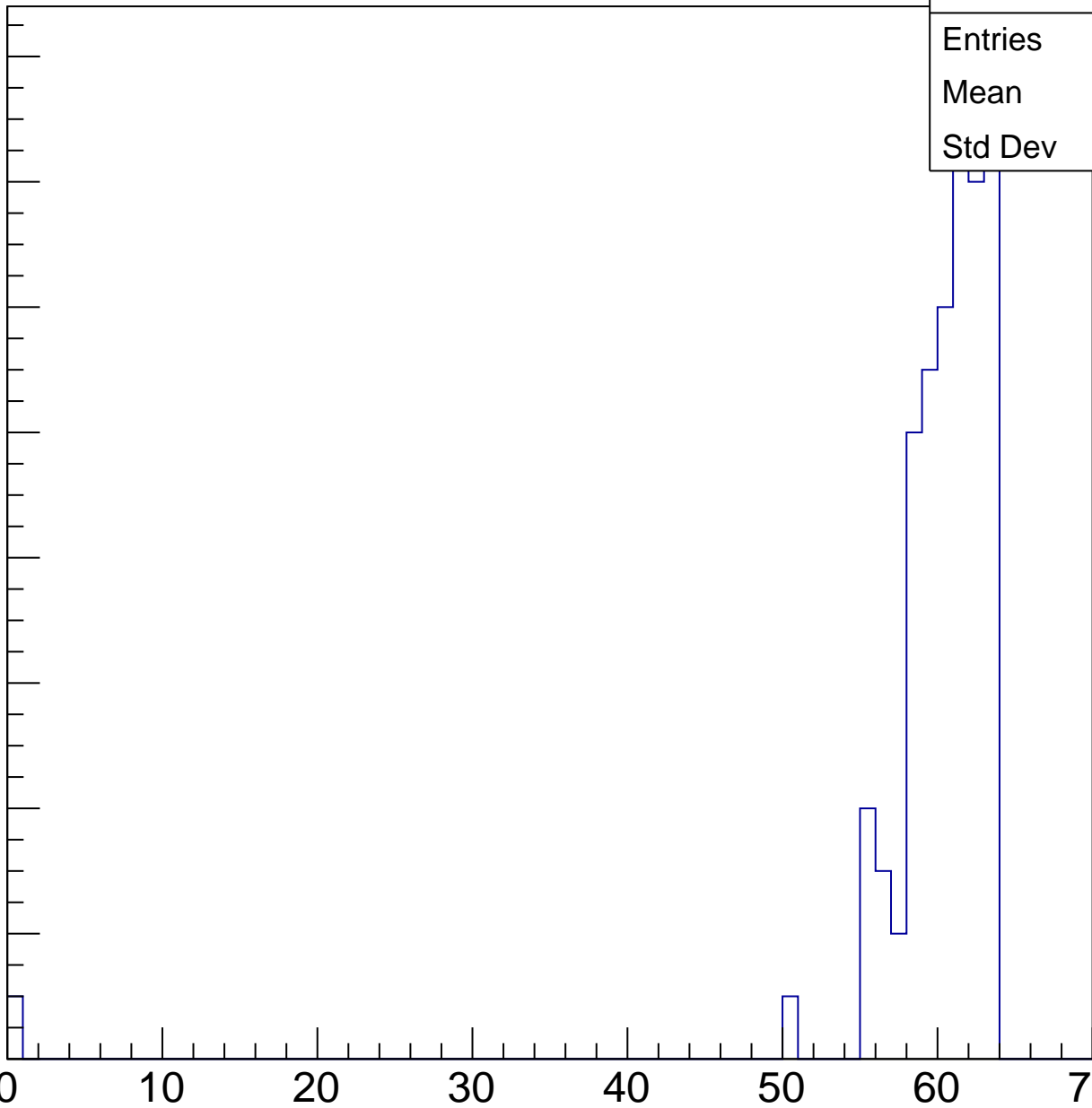
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	90
Mean	59.48
Std Dev	6.756

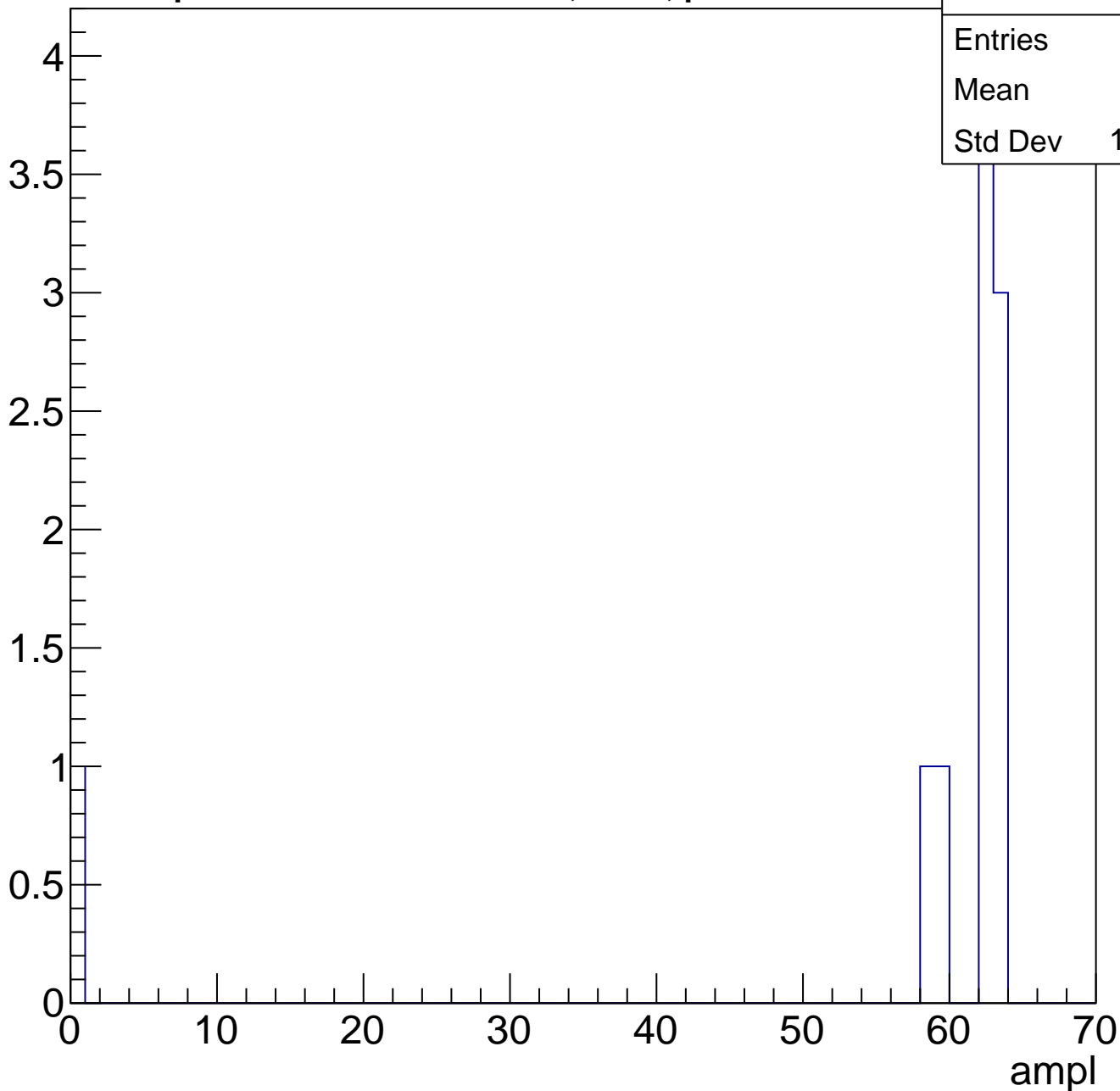
ampl



# B0L101S, U23-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U23-ch49, adc0

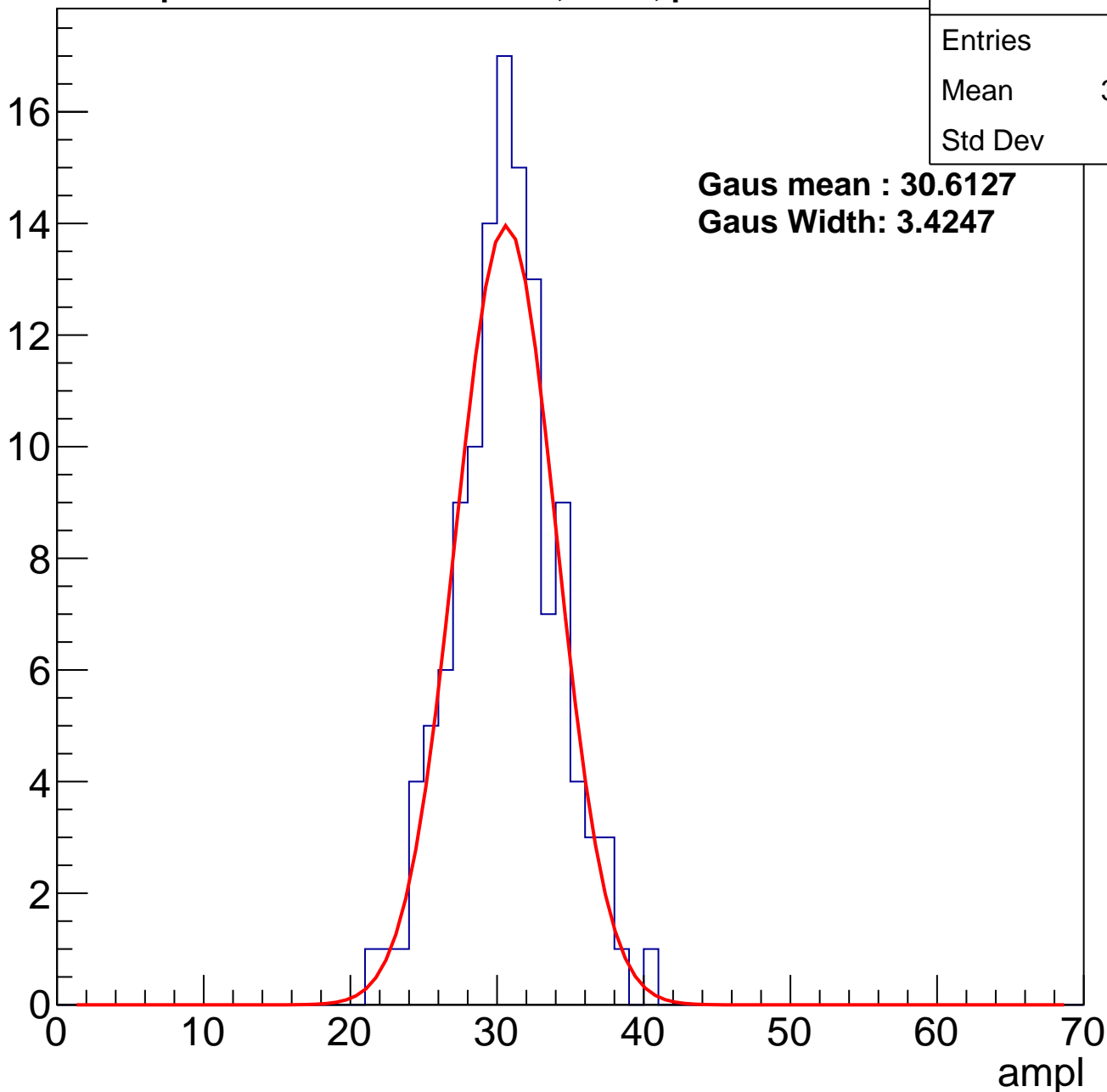
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	30.14
Std Dev	3.46

**Gaus mean : 30.6127**

**Gaus Width: 3.4247**

Entry



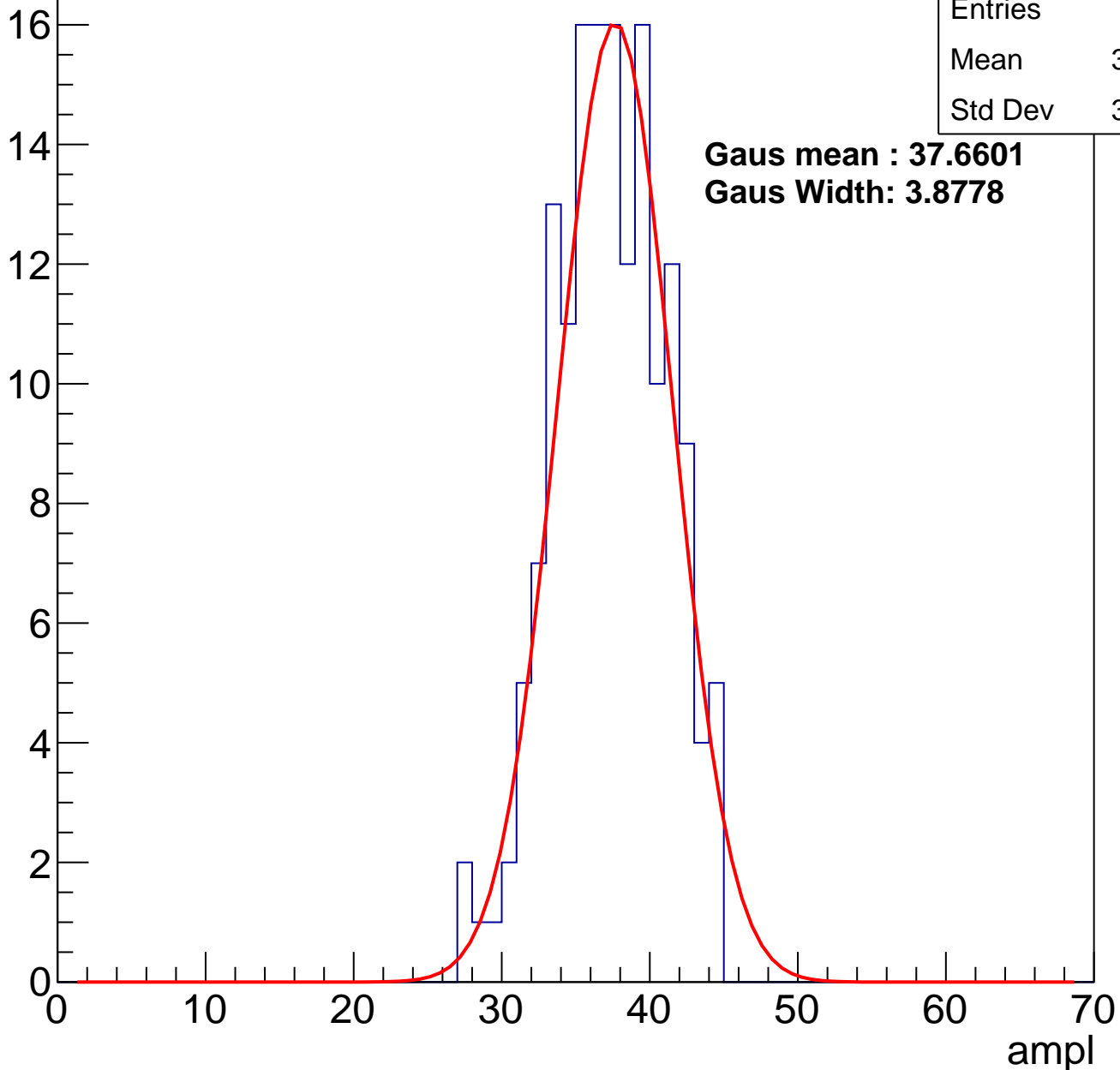
# B0L101S, U23-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	36.85
Std Dev	3.692

**Gaus mean : 37.6601**  
**Gaus Width: 3.8778**

Entry



# B0L101S, U23-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

116

Mean

43.83

Std Dev

3.325

**Gaus mean : 44.3394**

**Gaus Width: 3.2814**

0

10

20

30

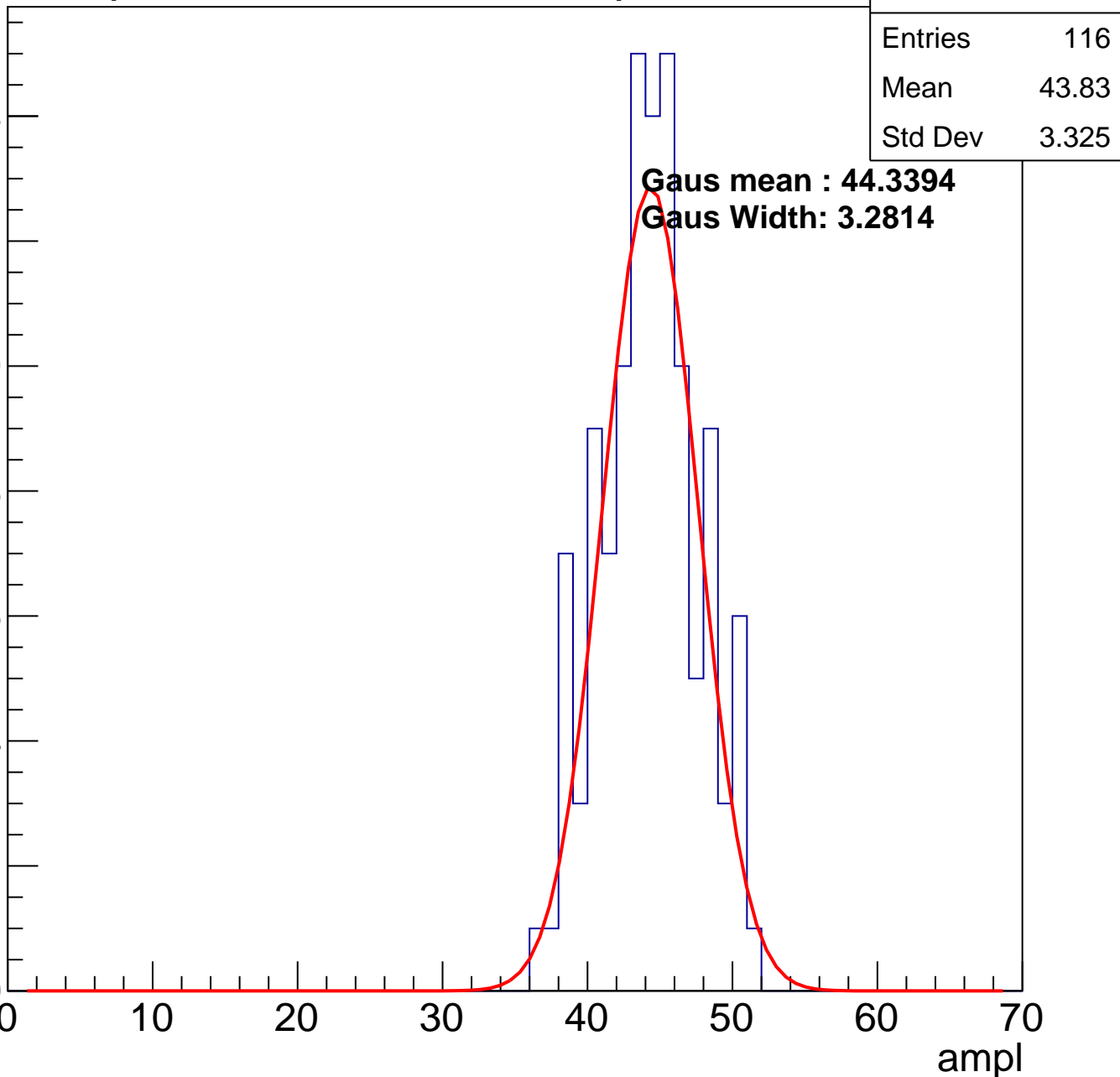
40

50

60

70

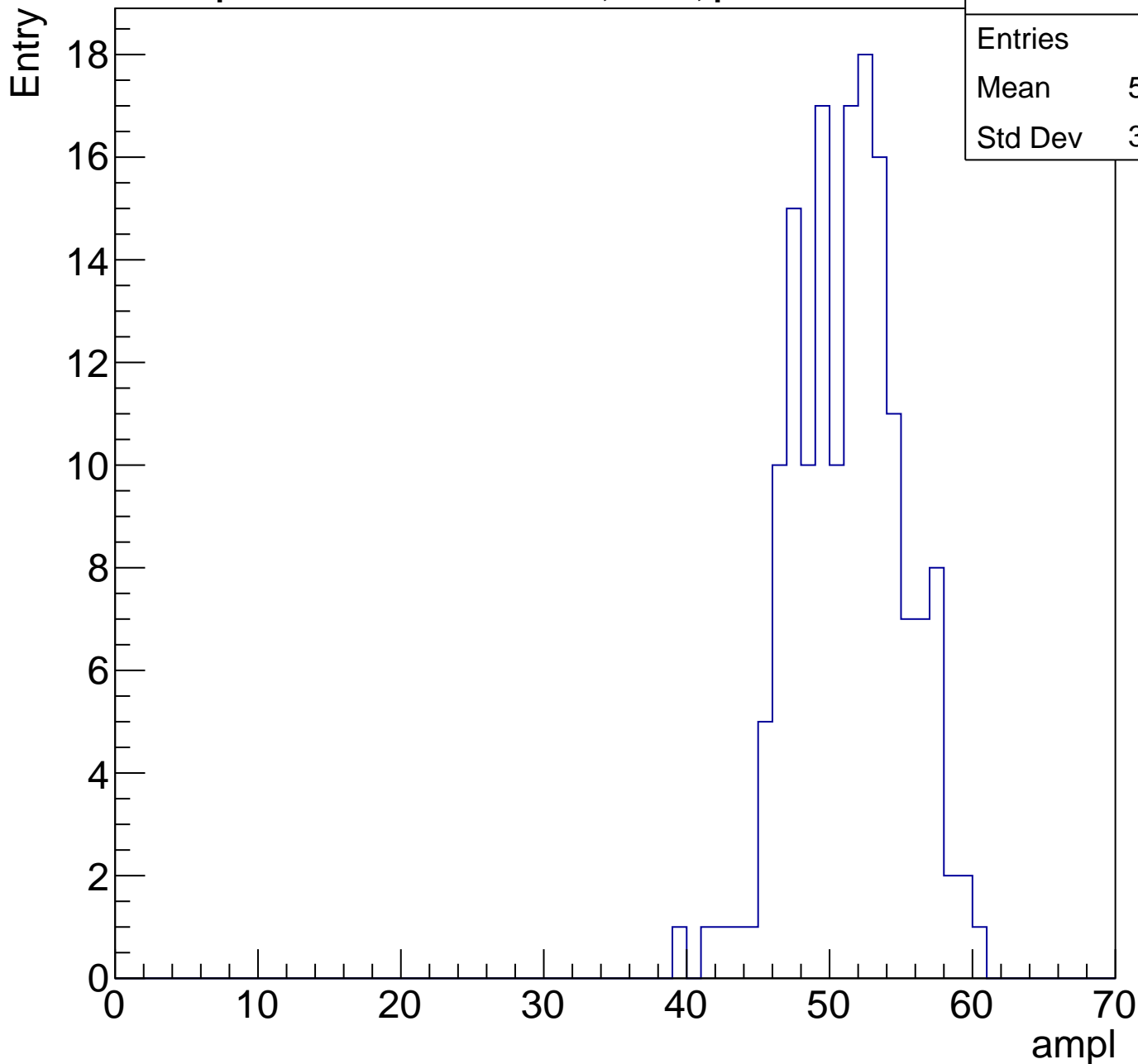
ampl



# B0L101S, U23-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	50.83
Std Dev	3.808



# B0L101S, U23-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

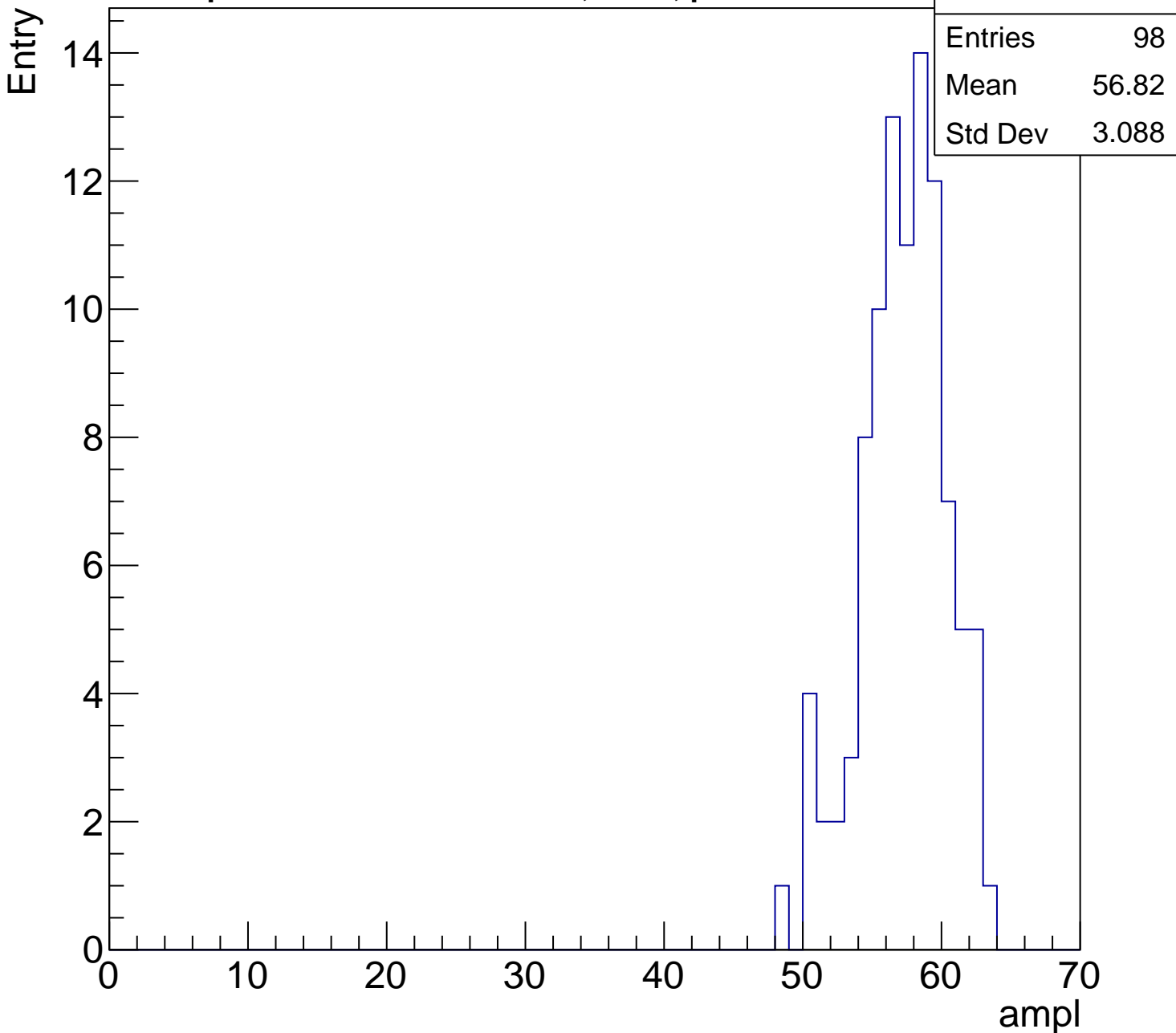
Entries	98
Mean	56.82
Std Dev	3.088

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

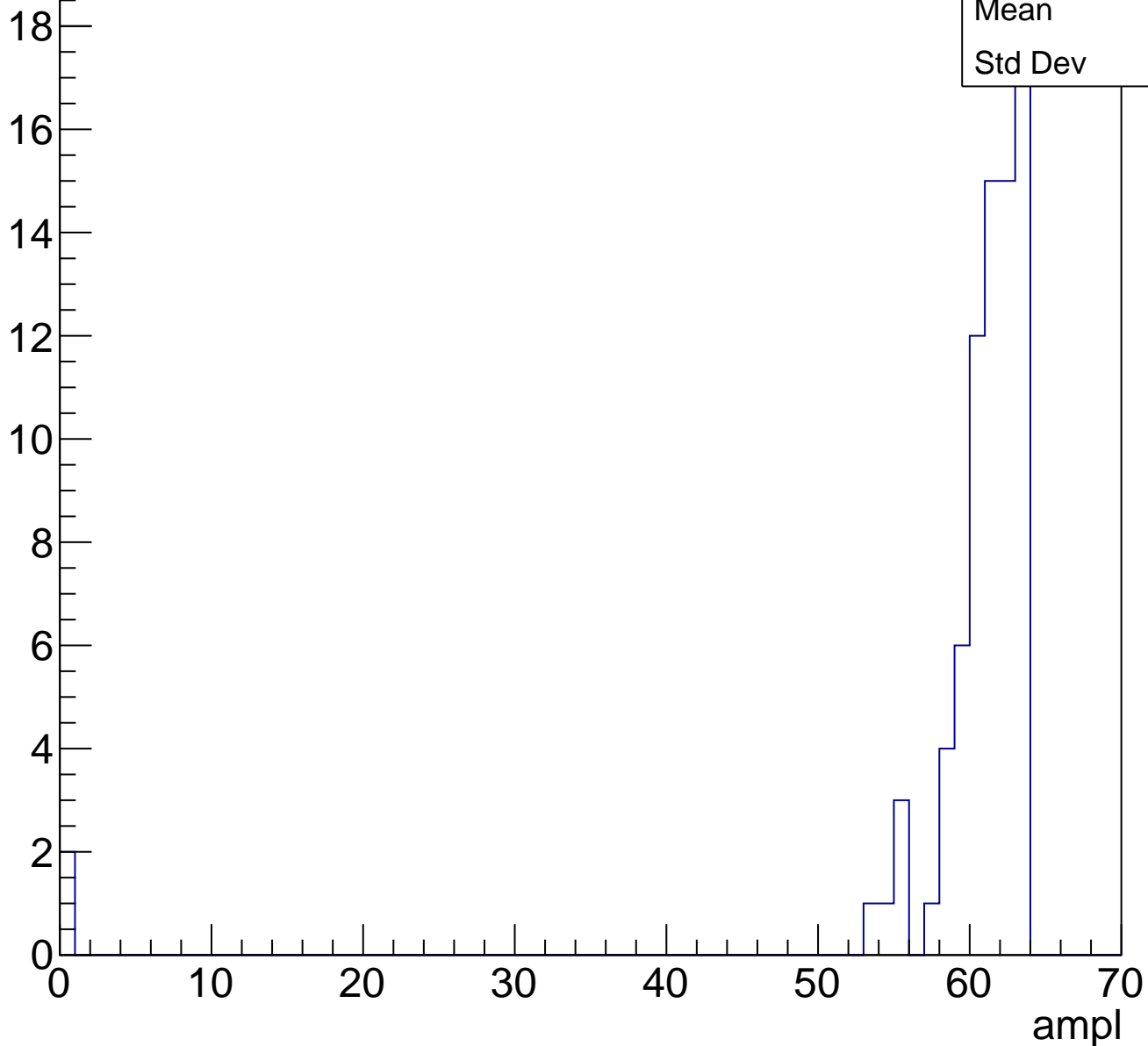
ampl



# B0L101S, U23-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

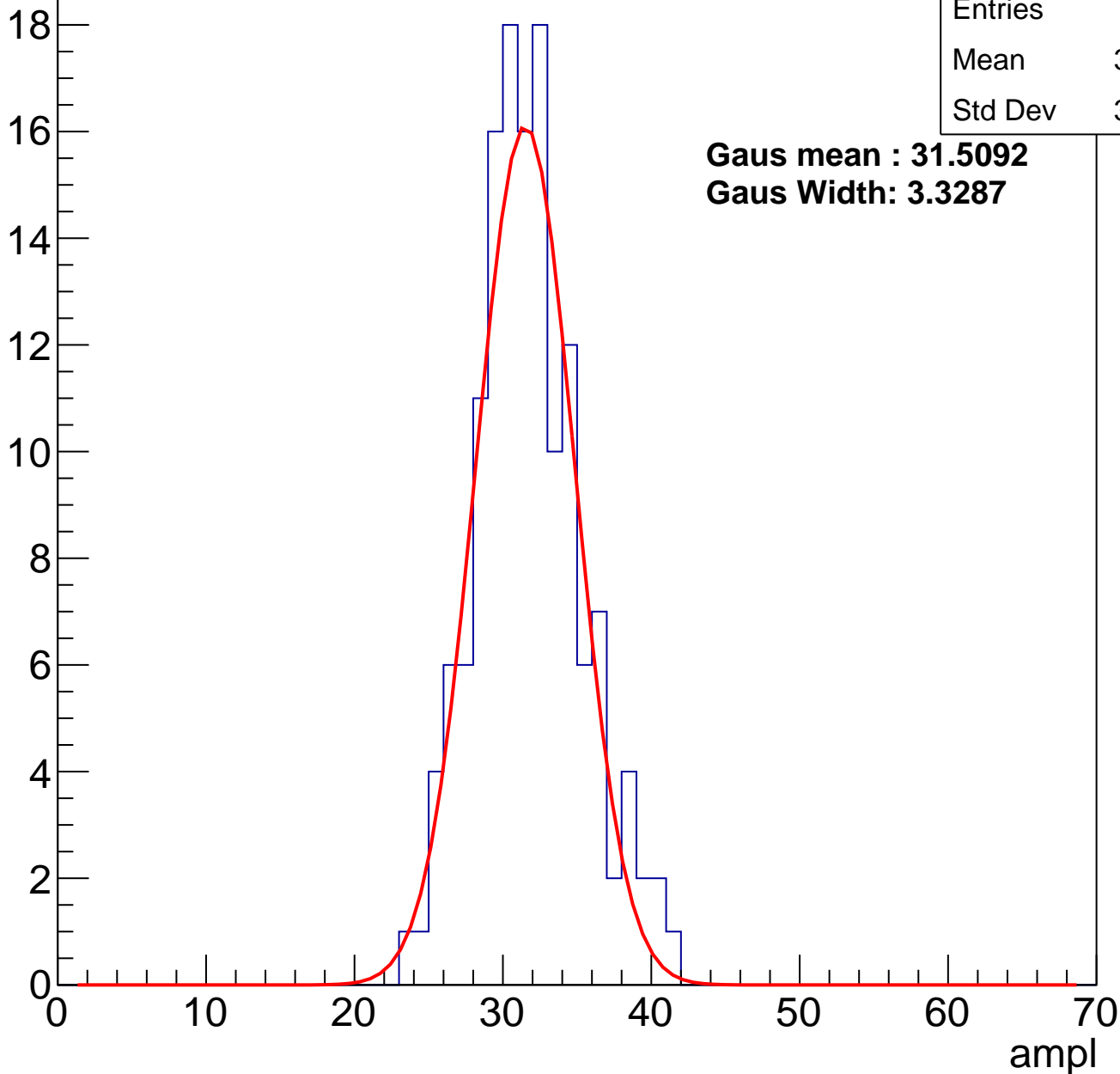


Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch50, adc1

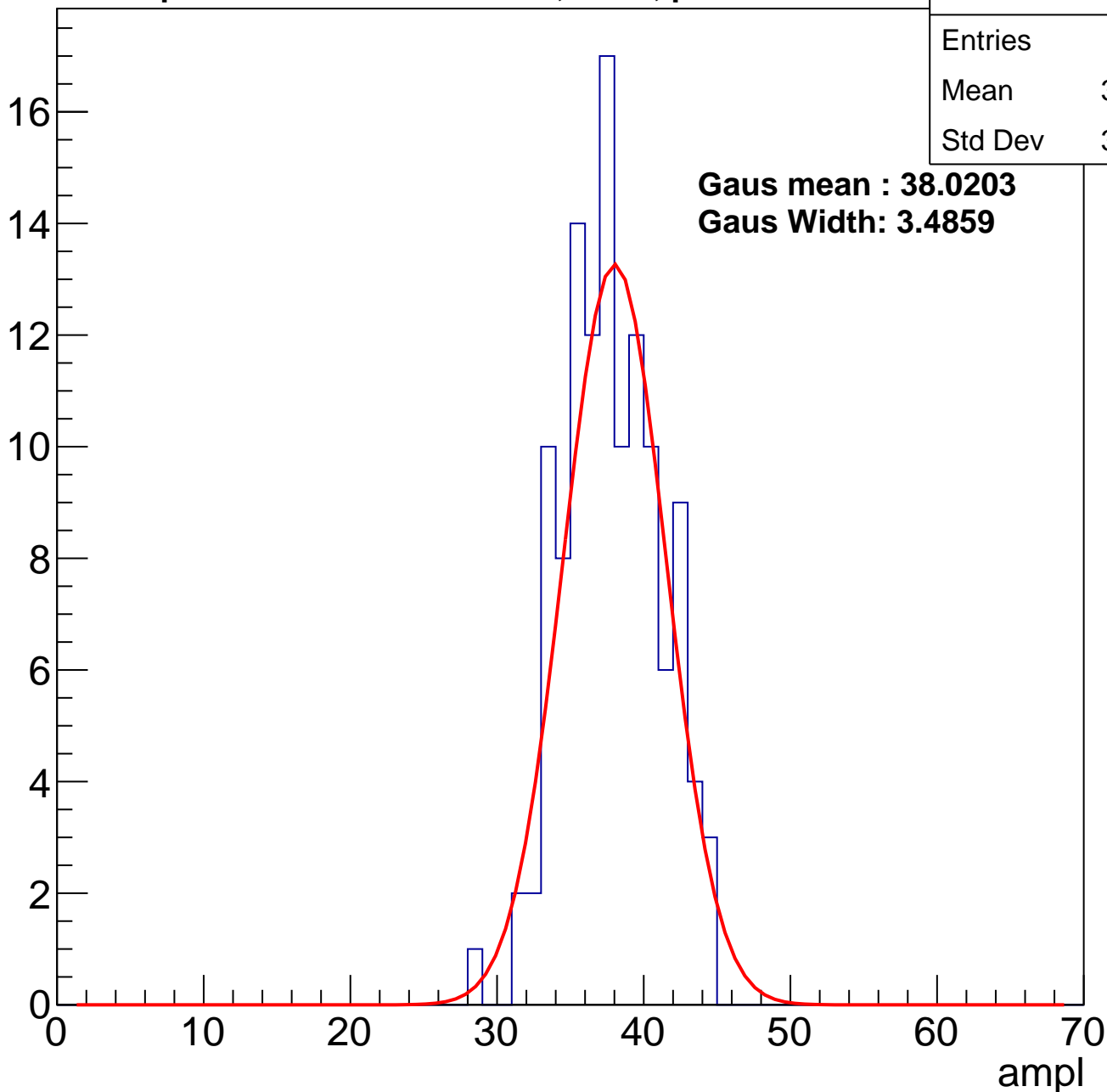
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	37.36
Std Dev	3.224

**Gaus mean : 38.0203**

**Gaus Width: 3.4859**

Entry



# B0L101S, U23-ch50, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

44.15

Std Dev

3.284

**Gaus mean : 44.8545**

**Gaus Width: 3.4626**

0

10

20

30

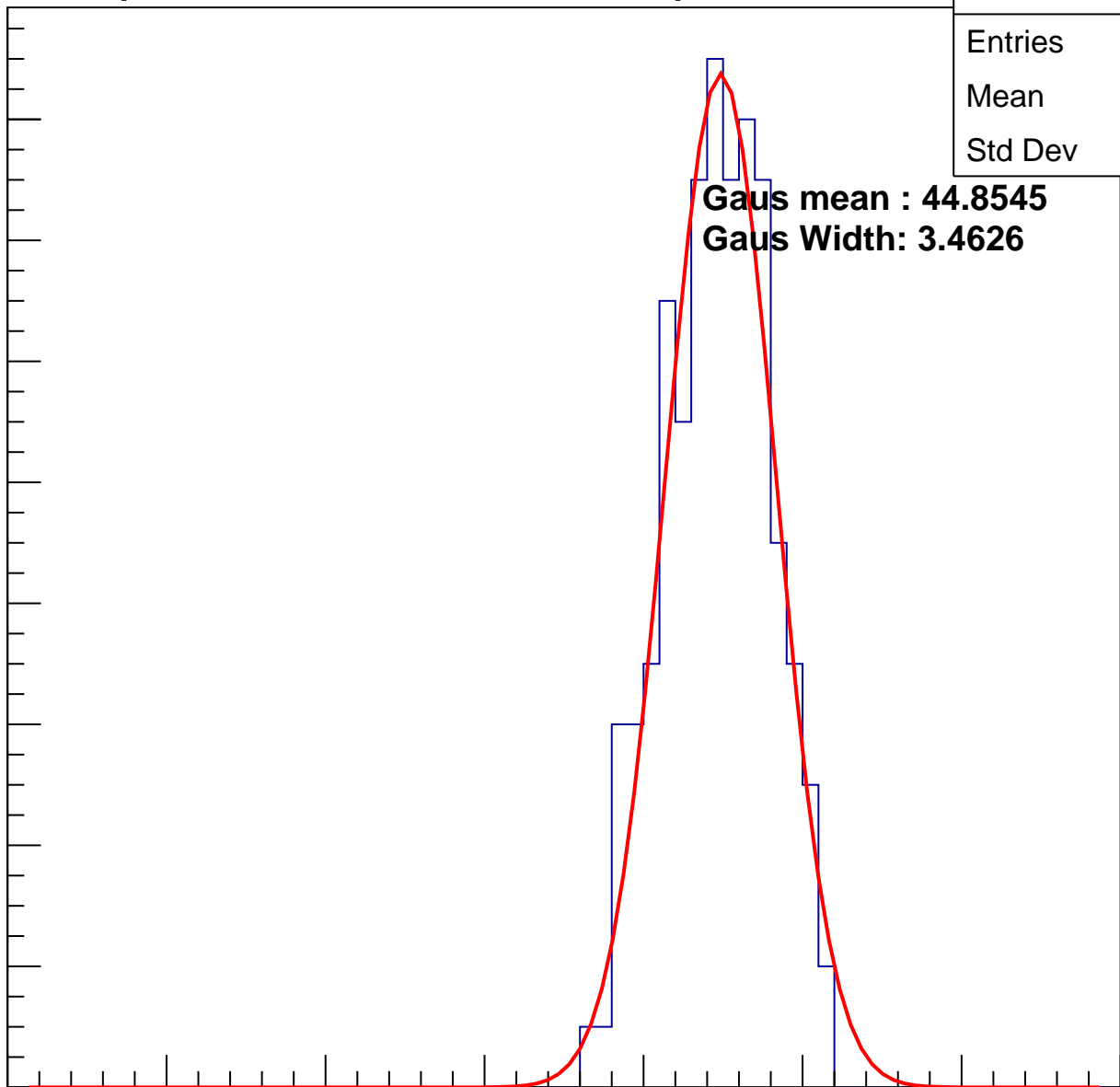
40

50

60

70

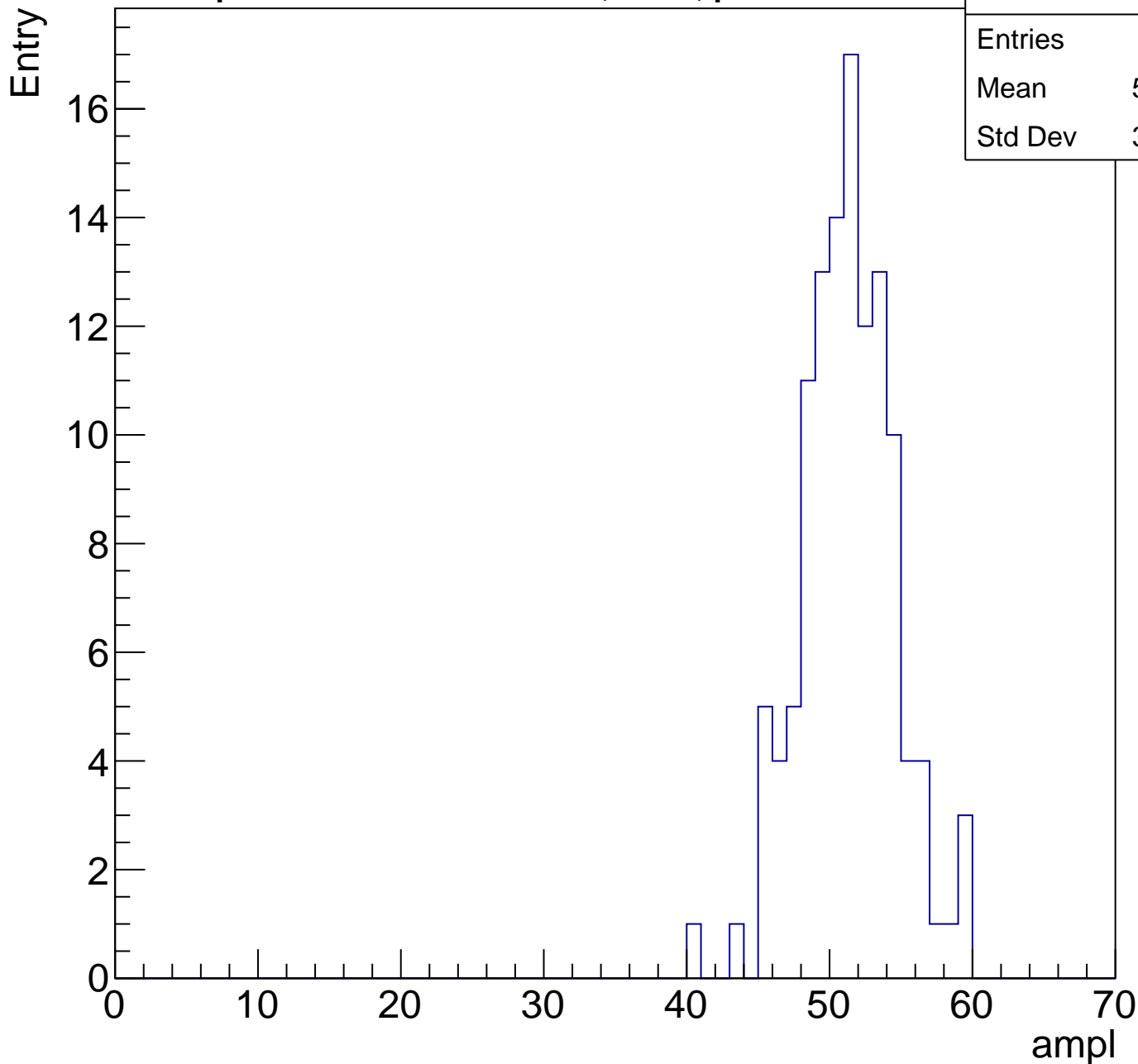
ampl



# B0L101S, U23-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	50.82
Std Dev	3.307



# B0L101S, U23-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	56.22
Std Dev	5.746

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

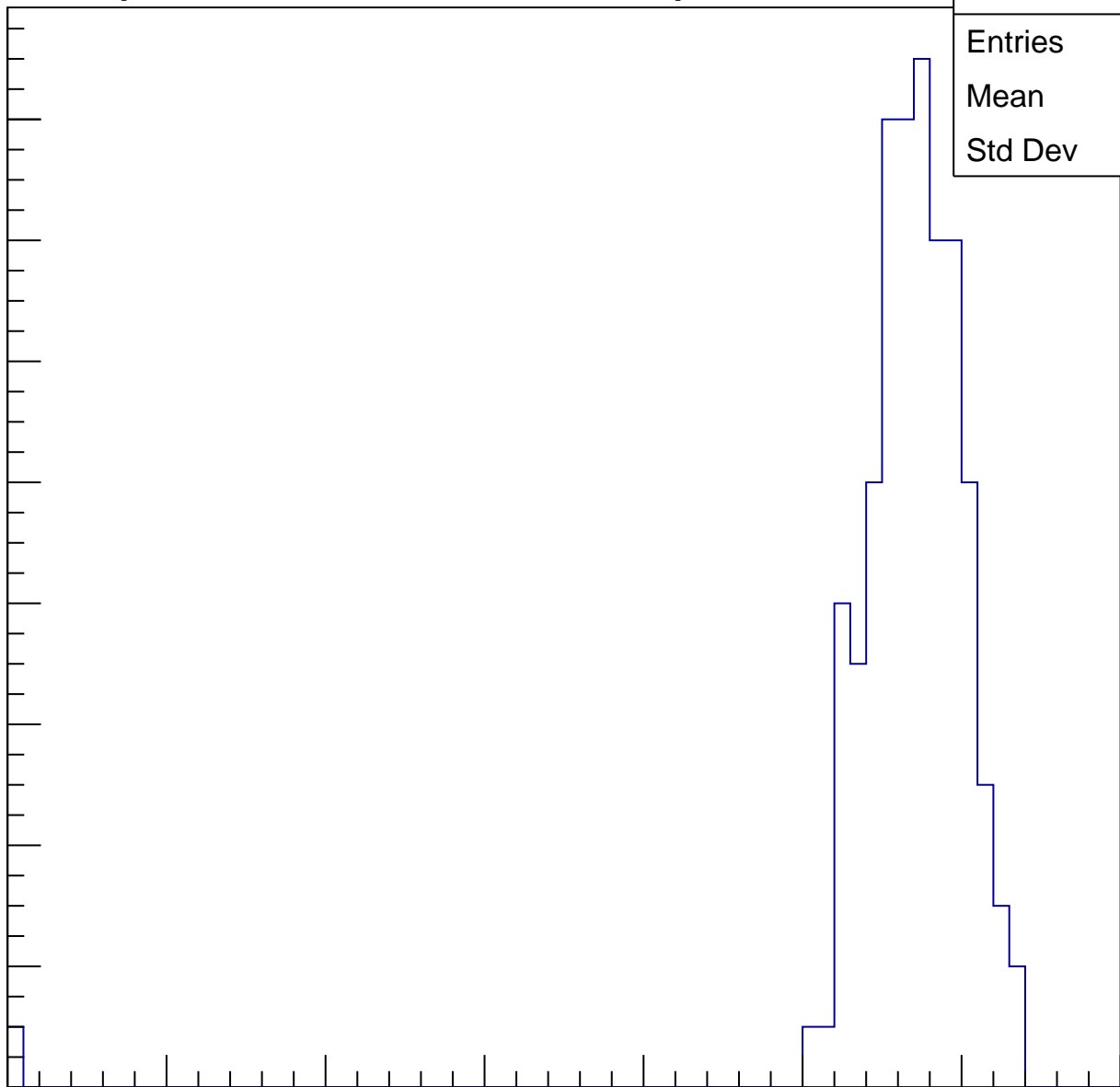
40

50

60

70

ampl



# B0L101S, U23-ch50, adc5

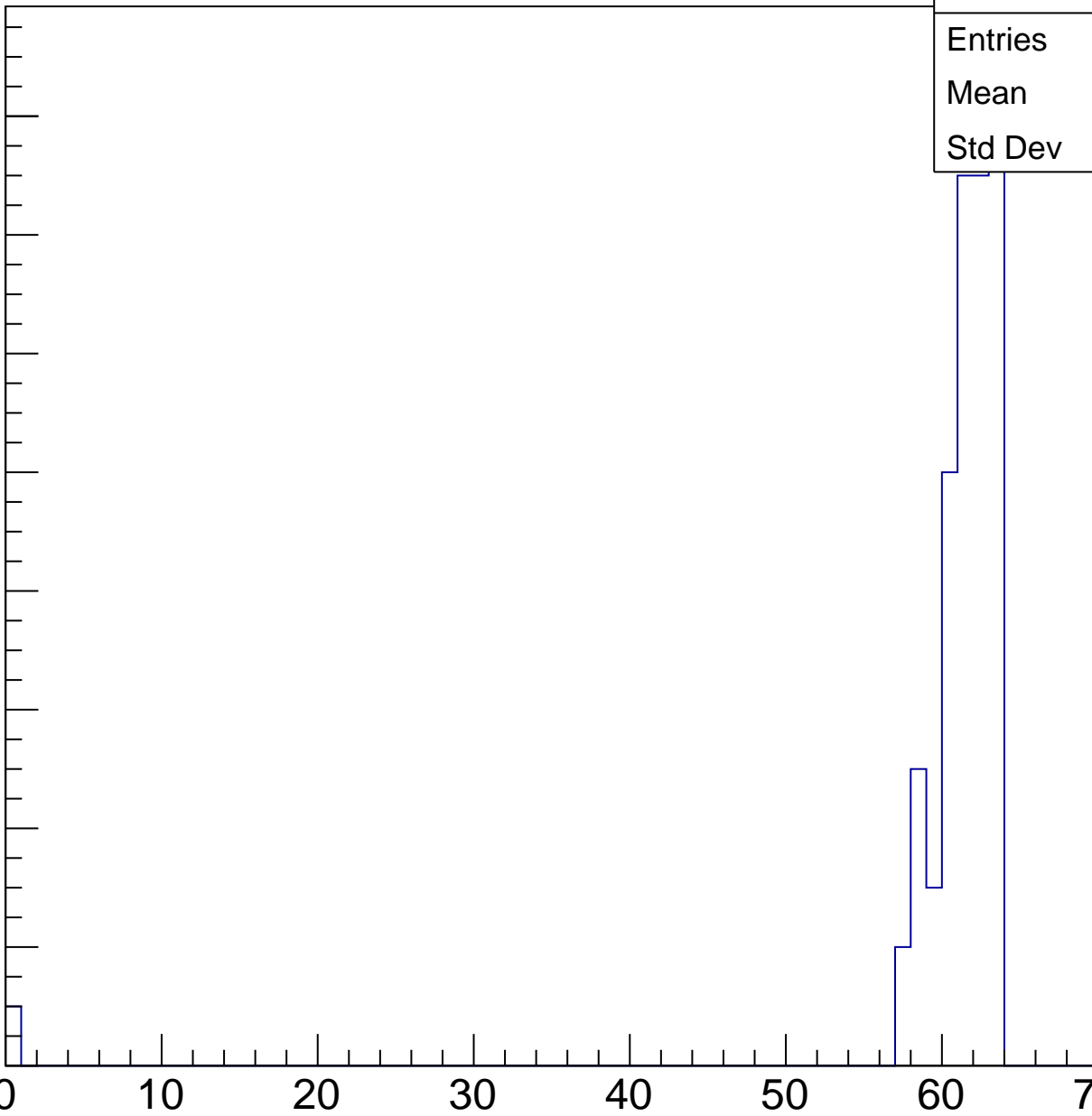
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	68
Mean	60.25
Std Dev	7.539

ampl



# B0L101S, U23-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl





# B0L101S, U23-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch51, adc0

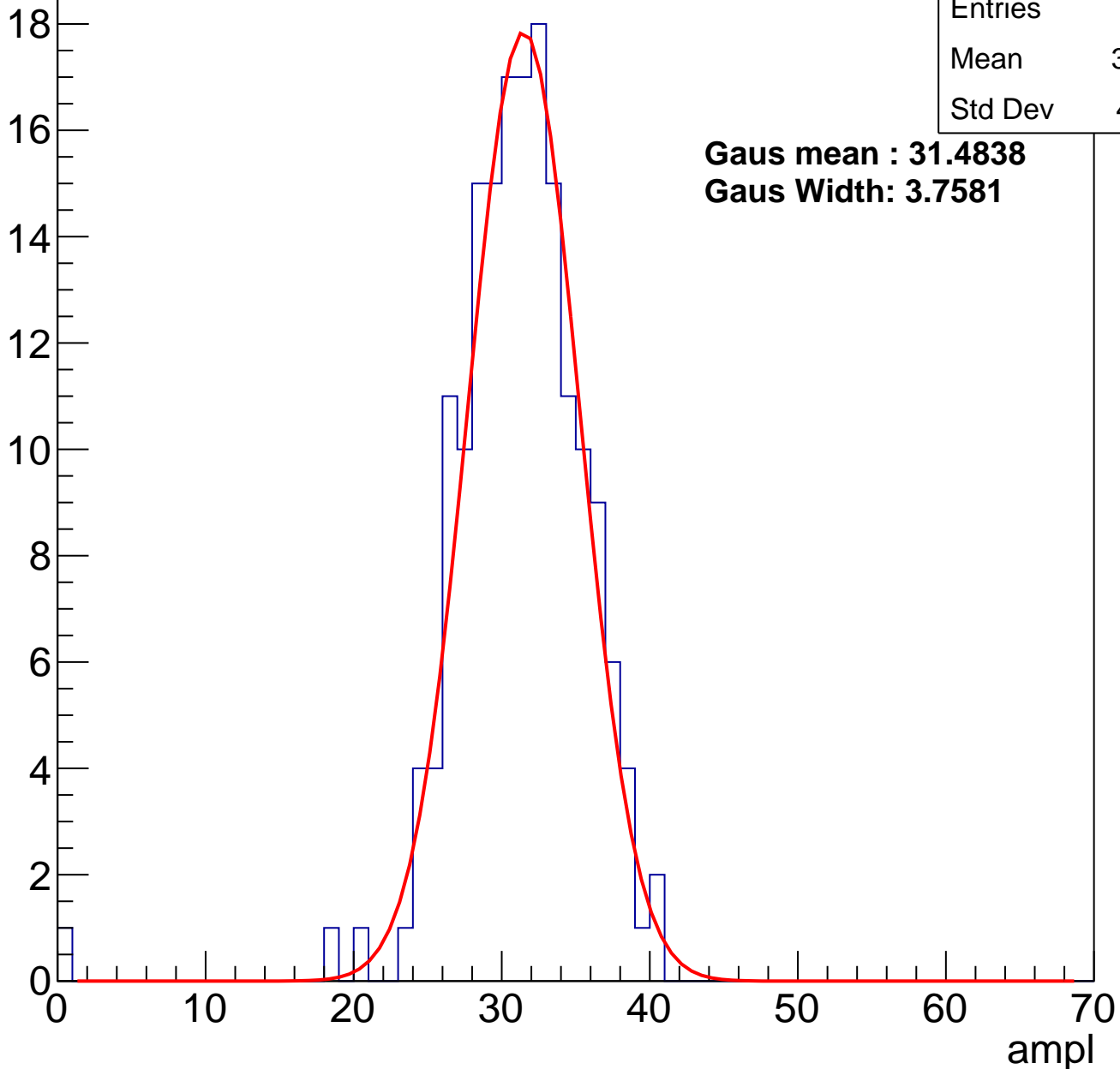
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	173
Mean	30.73
Std Dev	4.491

**Gaus mean : 31.4838**

**Gaus Width: 3.7581**

Entry



# B0L101S, U23-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

159

Mean

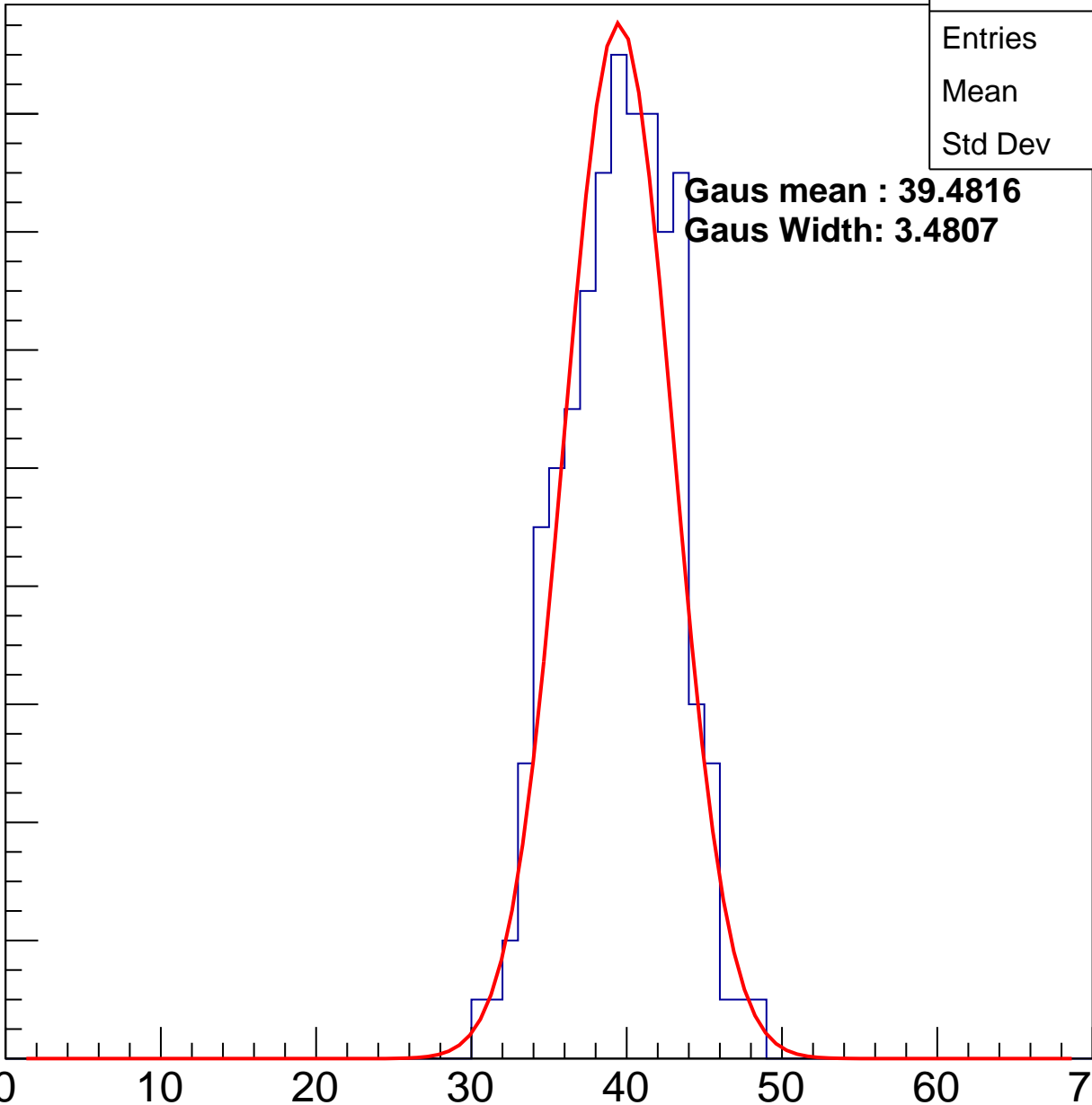
39.09

Std Dev

3.496

**Gaus mean : 39.4816**

**Gaus Width: 3.4807**



# B0L101S, U23-ch51, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	46.17
Std Dev	3.084

**Gaus mean : 46.7407**

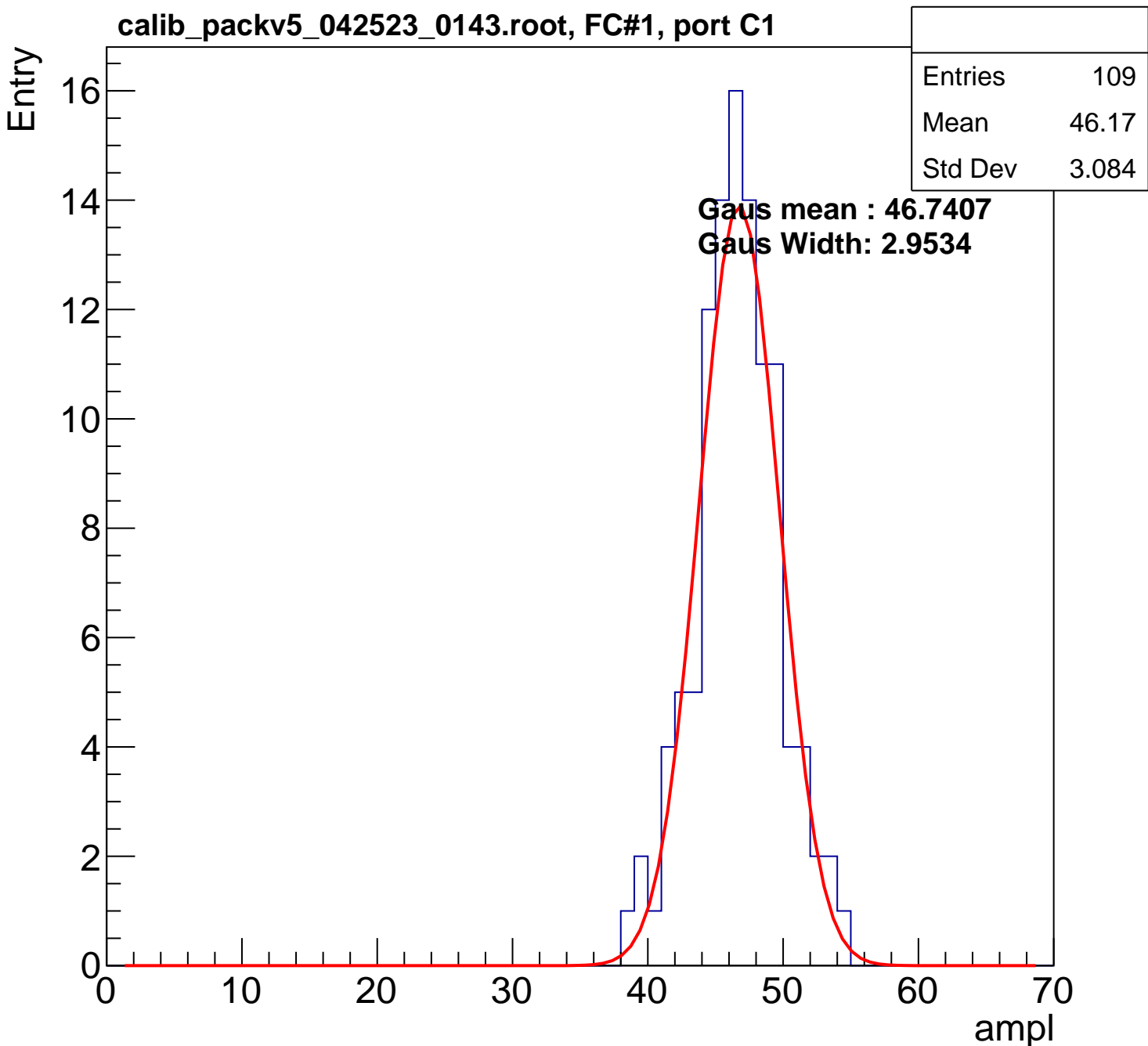
**Gaus Width: 2.9534**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

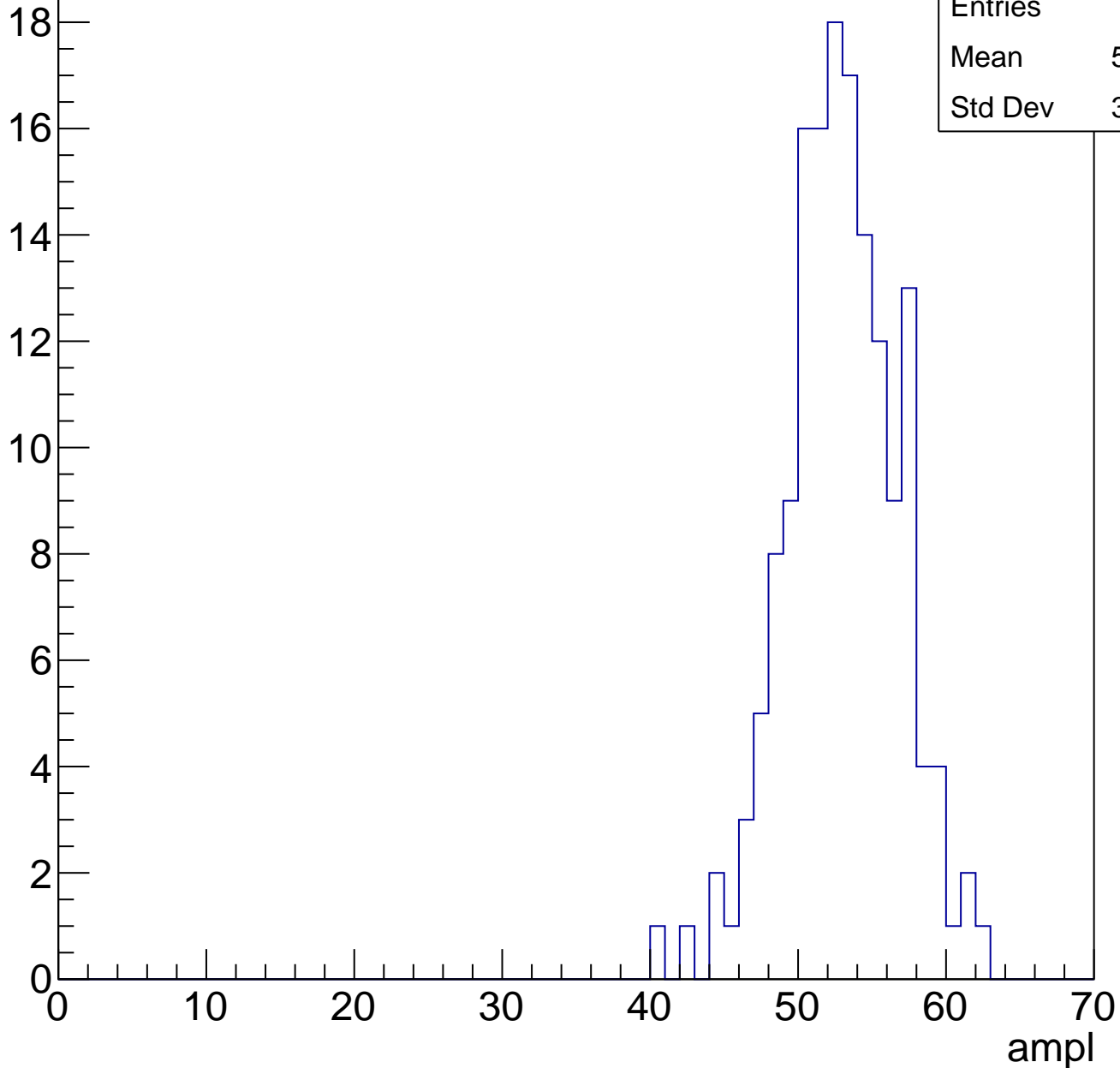


# B0L101S, U23-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	52.48
Std Dev	3.763

Entry



# B0L101S, U23-ch51, adc4

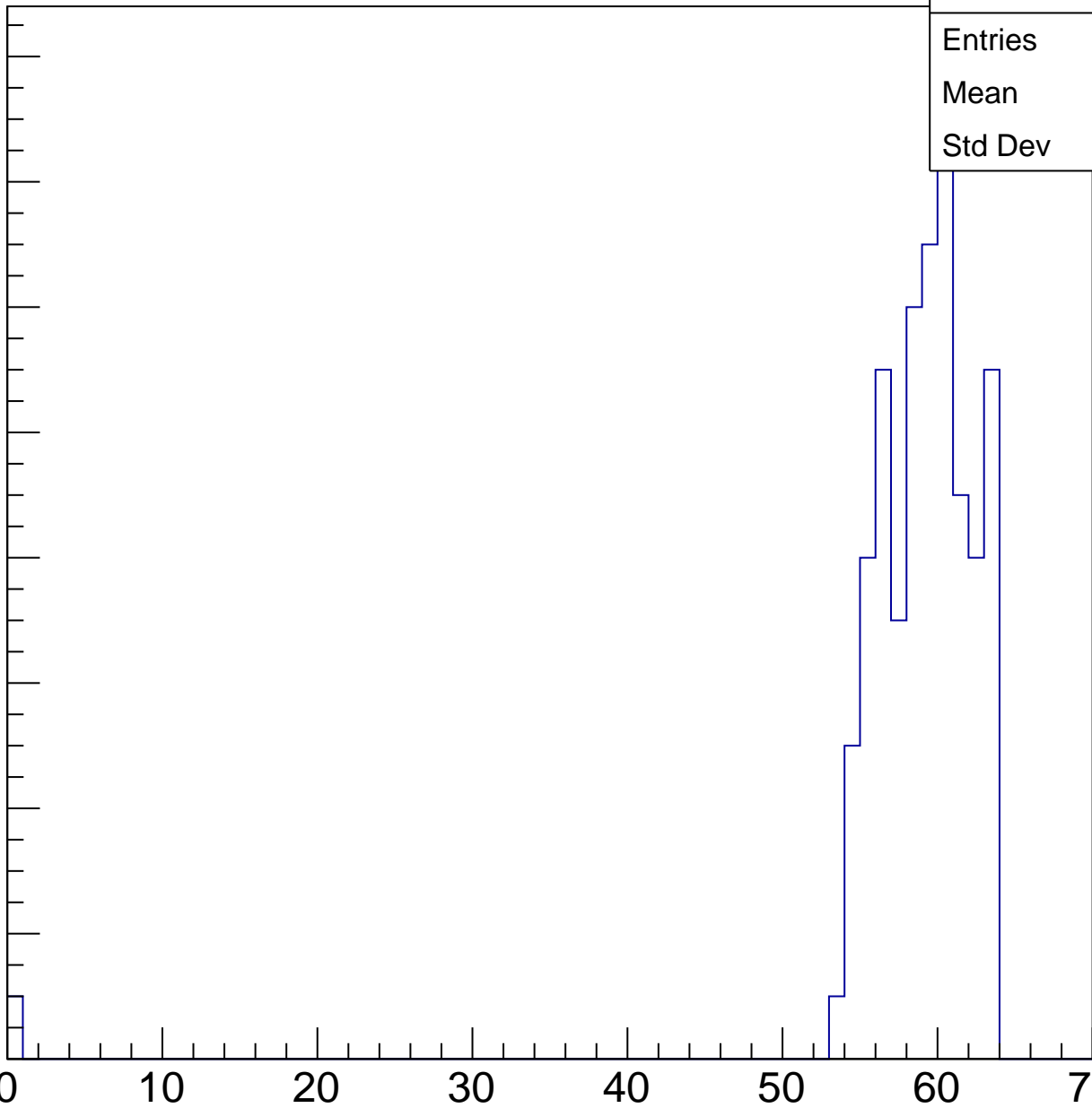
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	102
Mean	58.23
Std Dev	6.377

ampl



# B0L101S, U23-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

Entries	40
Mean	59.67
Std Dev	9.681

ampl

0 10 20 30 40 50 60 70

# B0L101S, U23-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U23-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L101S, U23-ch52, adc0

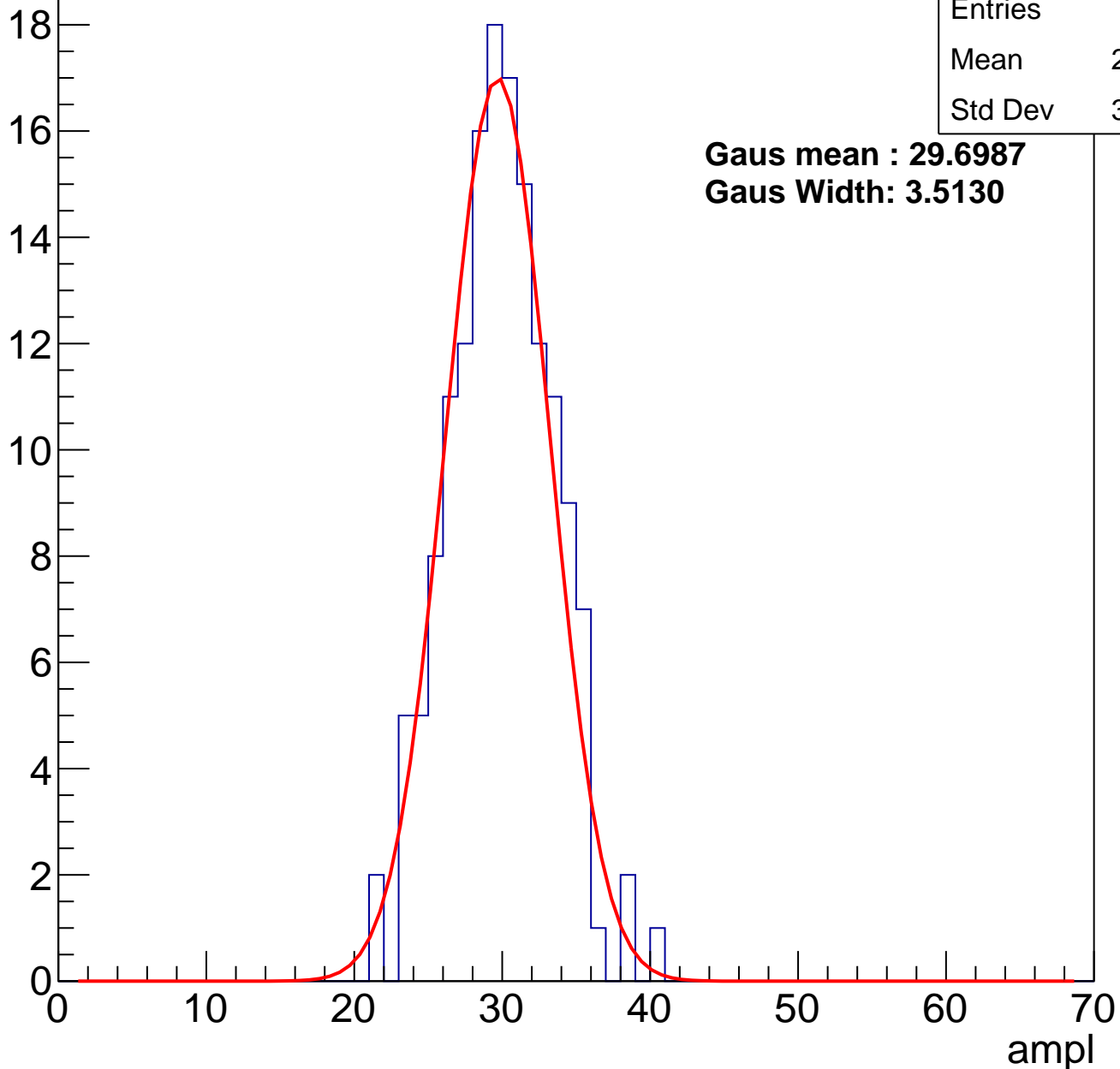
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	29.49
Std Dev	3.496

**Gaus mean : 29.6987**

**Gaus Width: 3.5130**

Entry



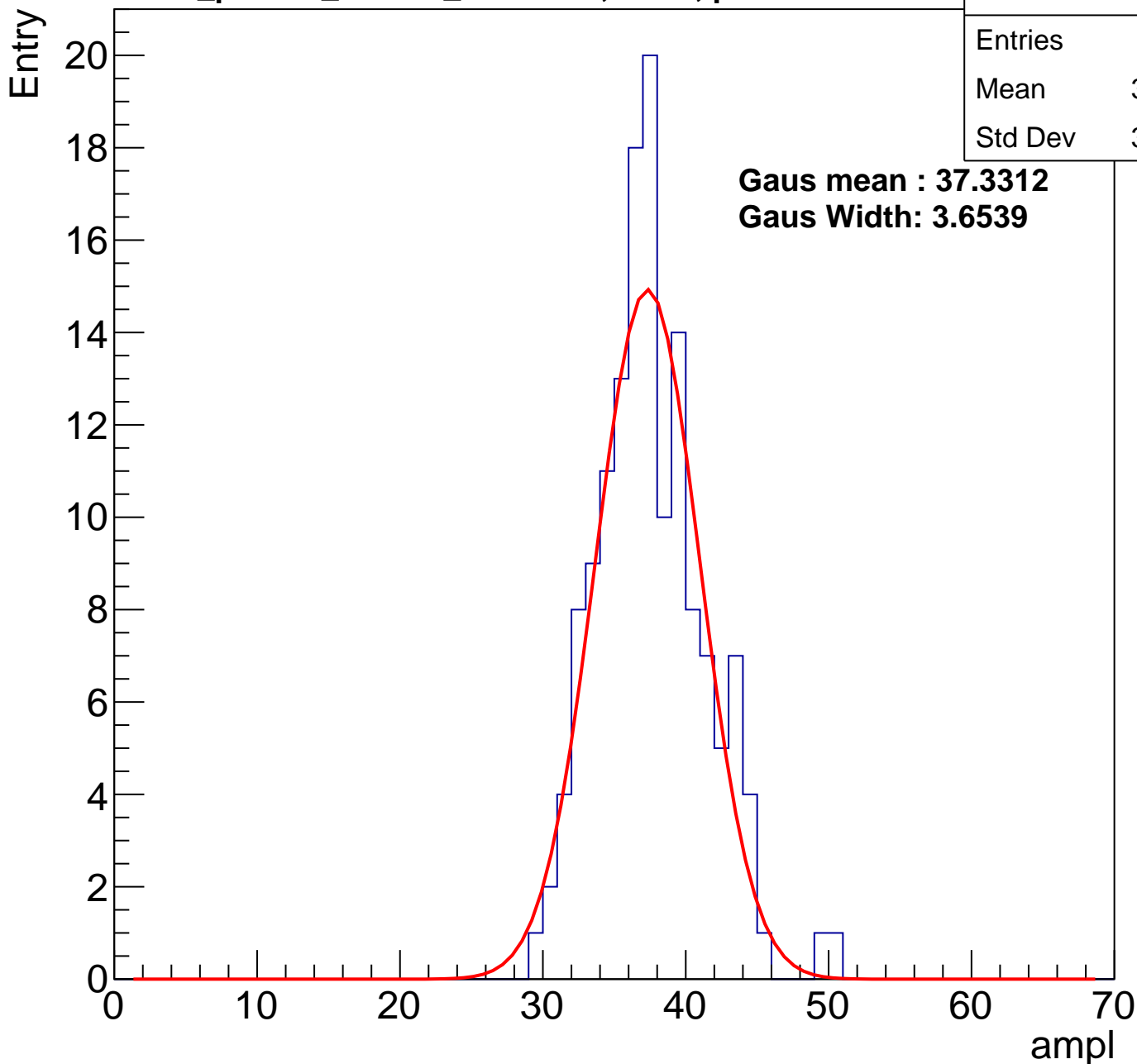
# B0L101S, U23-ch52, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	37.13
Std Dev	3.742

**Gaus mean : 37.3312**

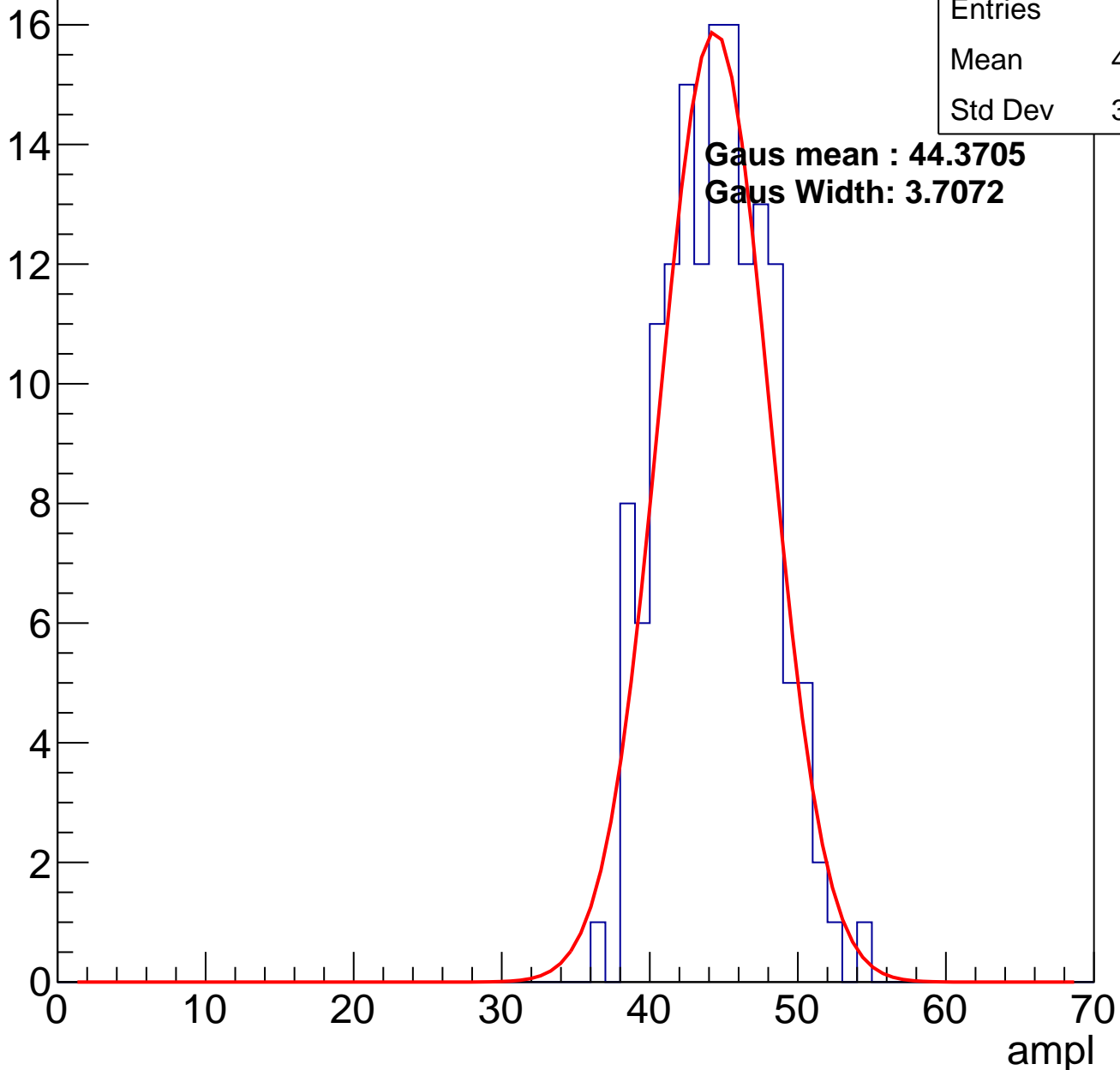
**Gaus Width: 3.6539**



# B0L101S, U23-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

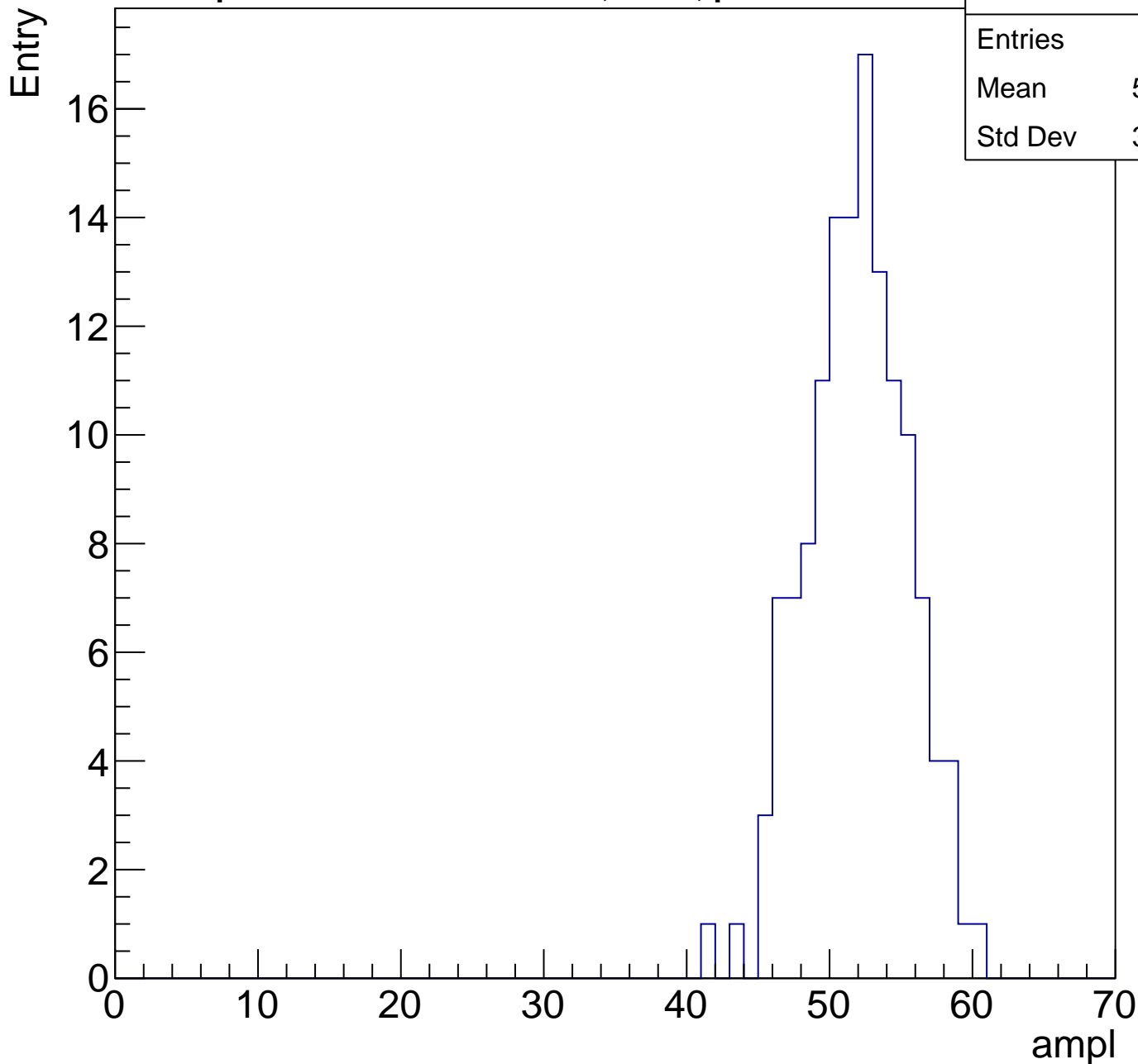
Entry



# B0L101S, U23-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	51.46
Std Dev	3.508



# B0L101S, U23-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

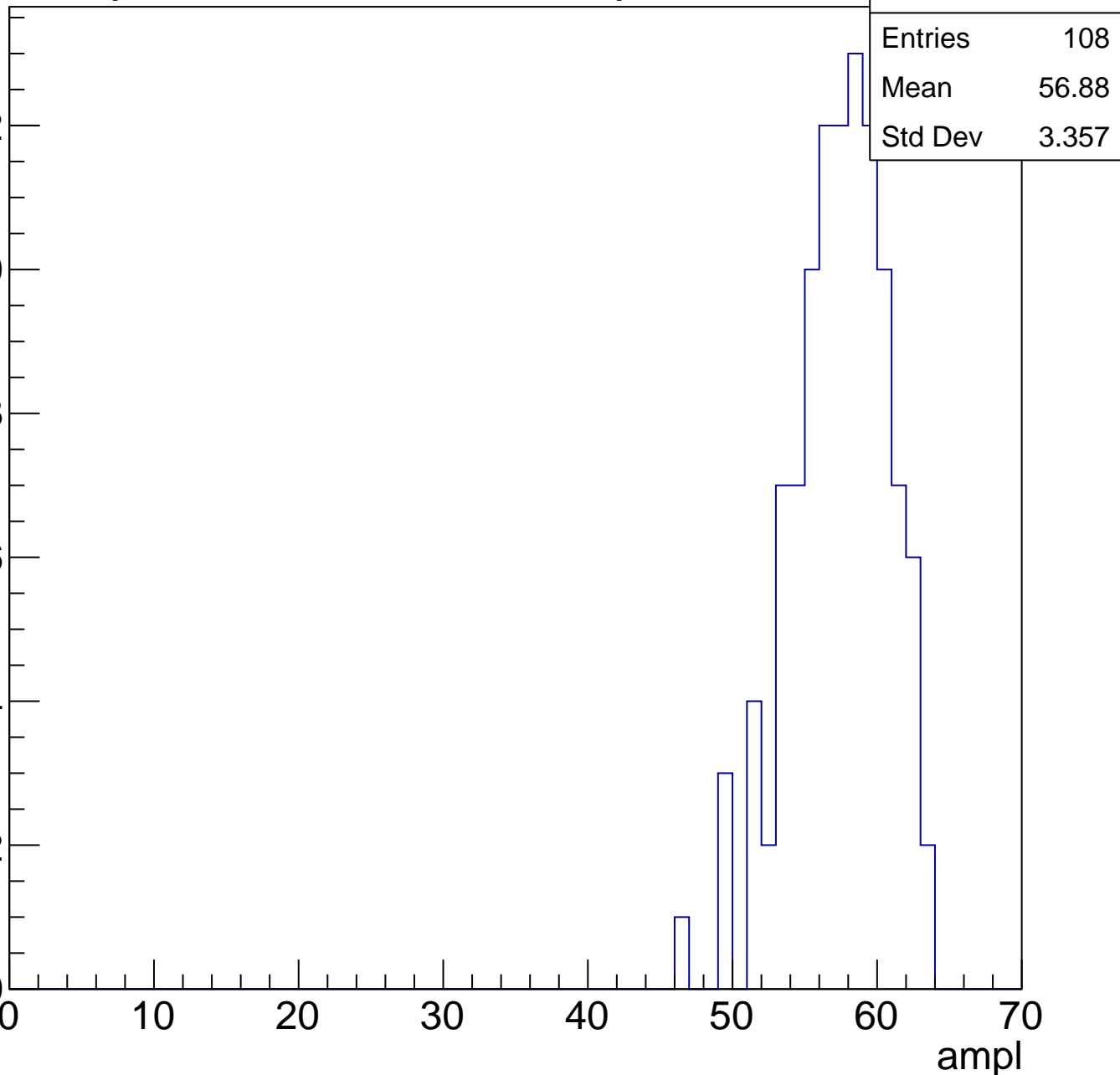
50

60

70

ampl

Entries	108
Mean	56.88
Std Dev	3.357



# B0L101S, U23-ch52, adc5

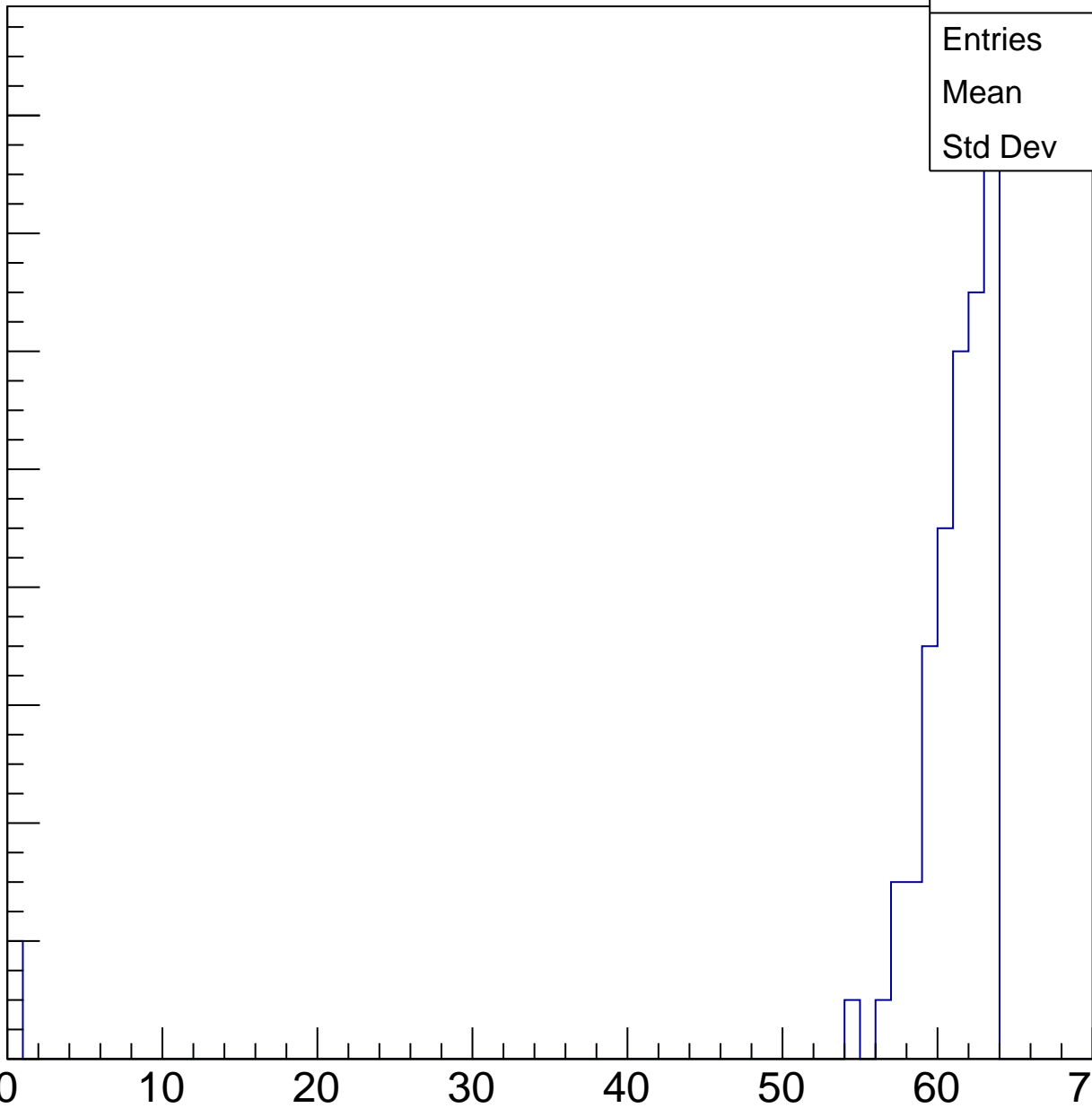
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	68
Mean	59.07
Std Dev	10.47

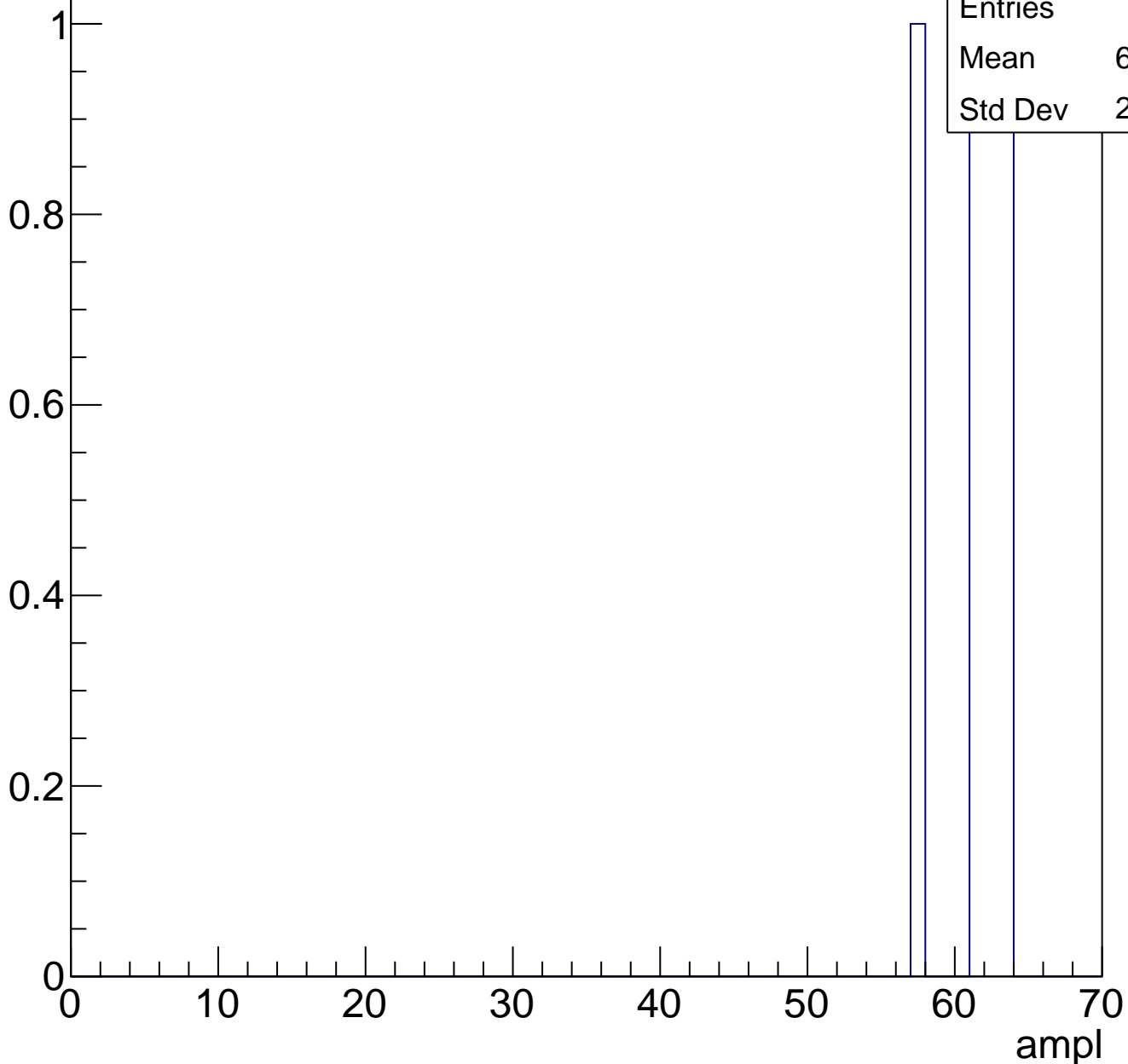
ampl



# B0L101S, U23-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch53, adc0

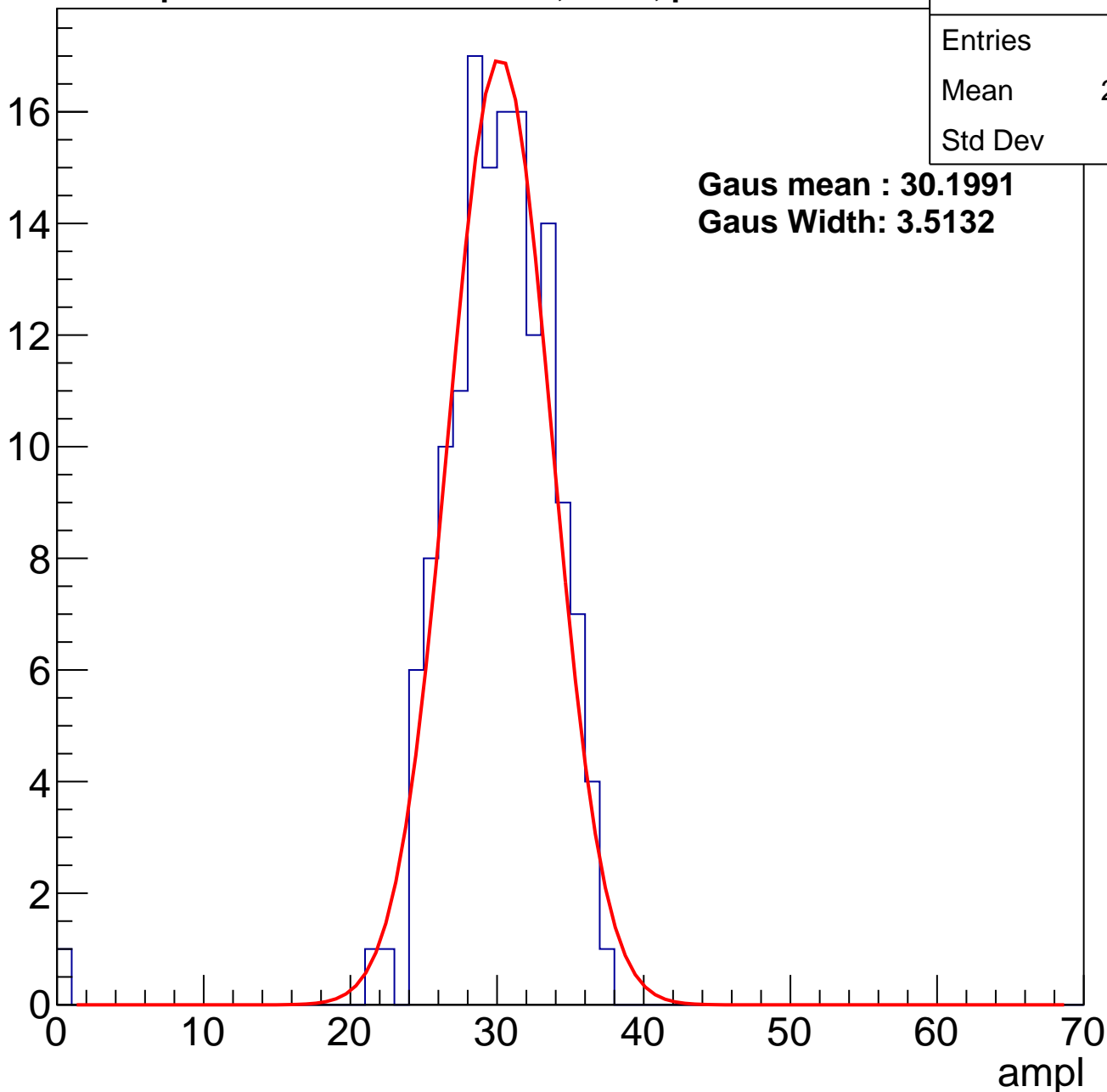
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	29.59
Std Dev	4.08

**Gaus mean : 30.1991**

**Gaus Width: 3.5132**

Entry



# B0L101S, U23-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

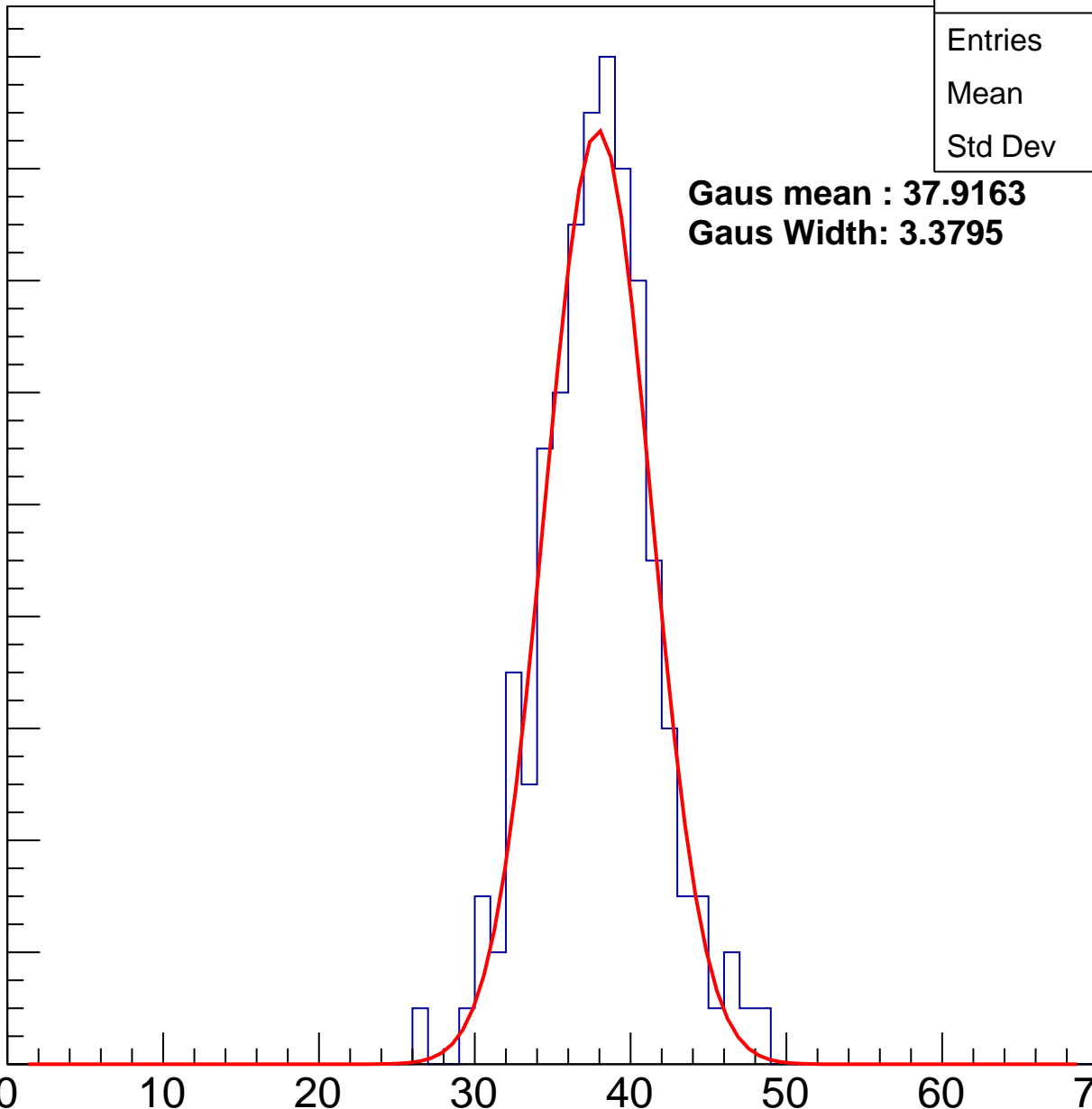
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	148
Mean	37.44
Std Dev	3.673

**Gaus mean : 37.9163**  
**Gaus Width: 3.3795**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch53, adc2

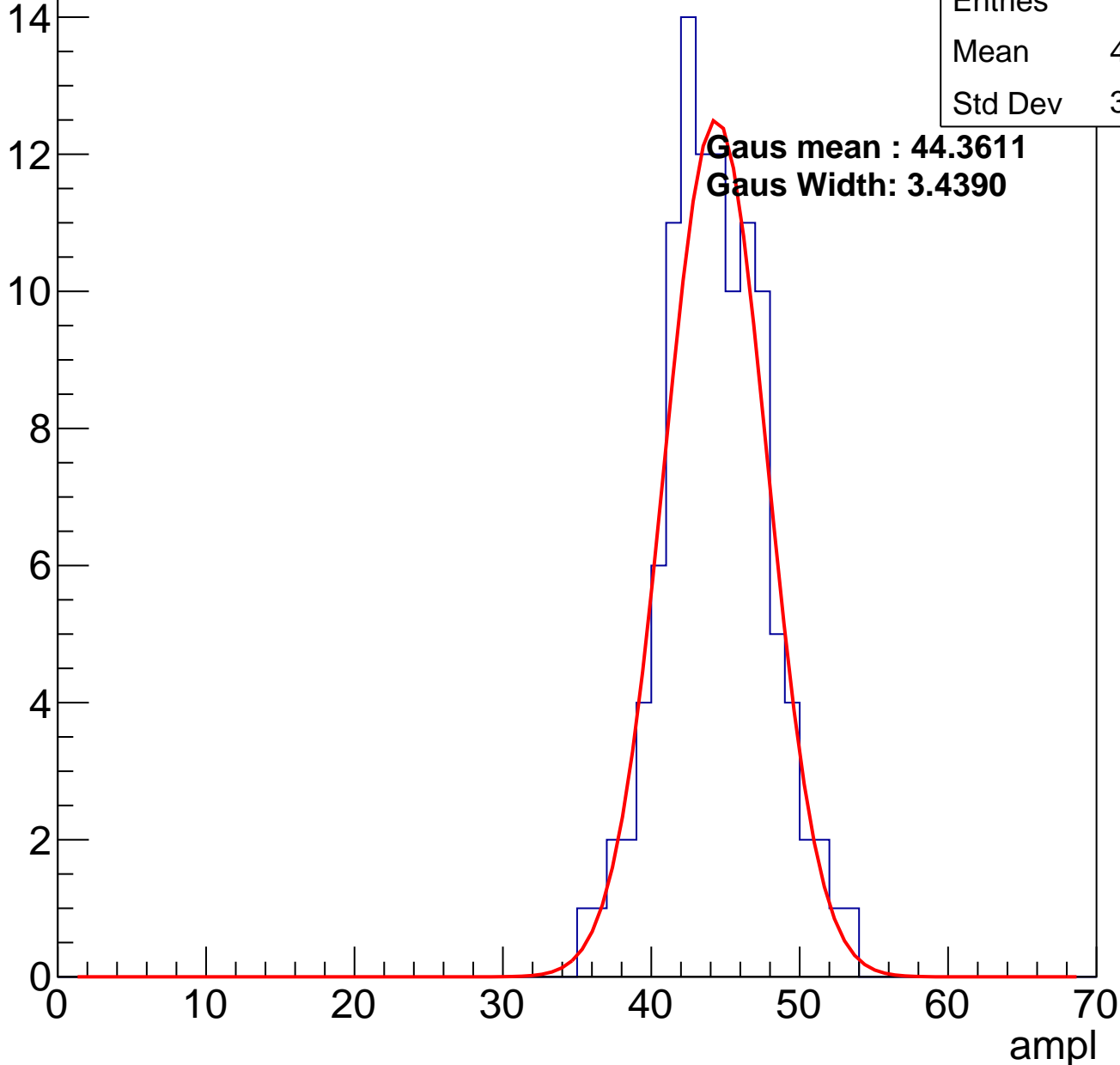
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	111
Mean	43.86
Std Dev	3.442

**Gaus mean : 44.3611**

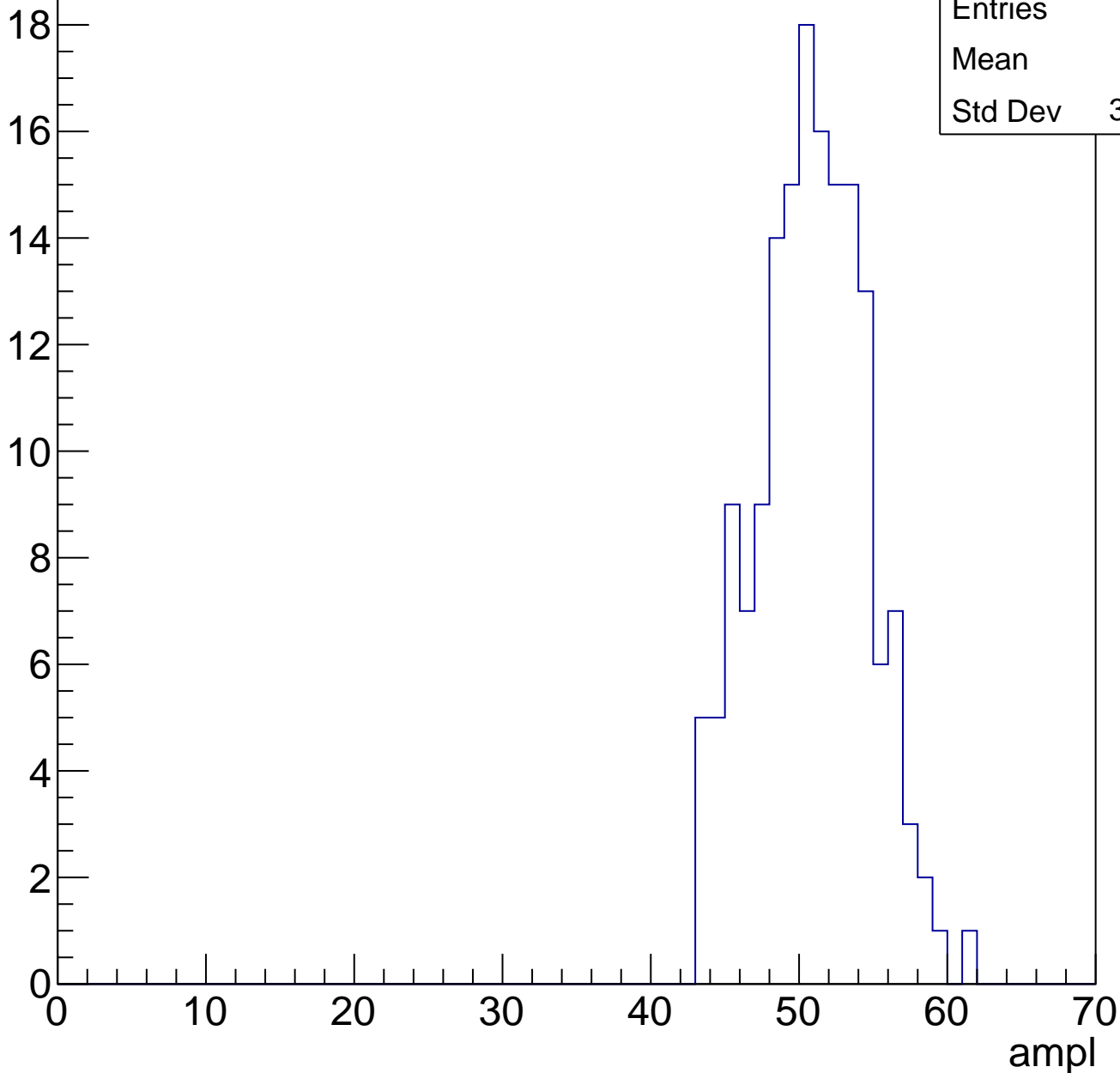
**Gaus Width: 3.4390**



# B0L101S, U23-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

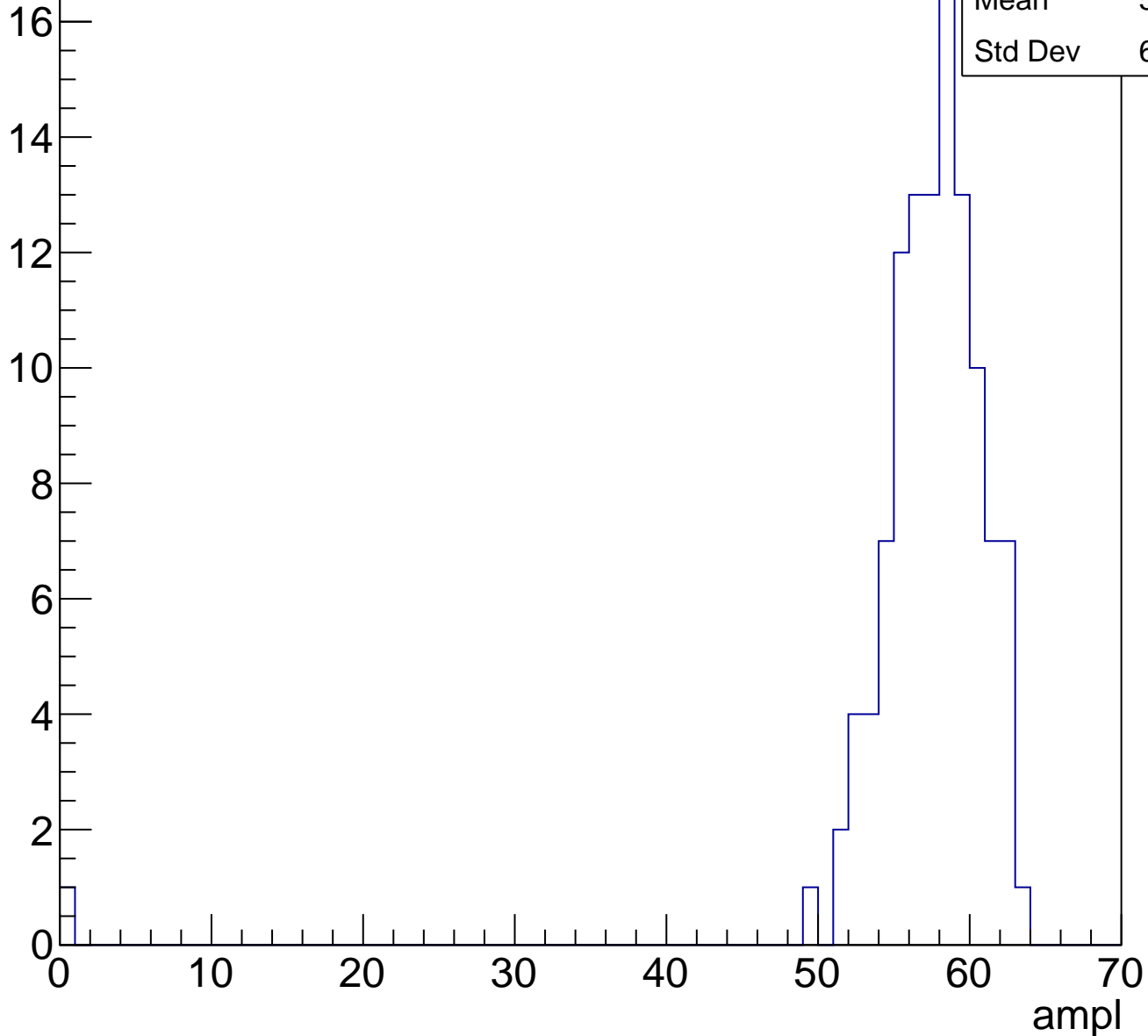
Entry



# B0L101S, U23-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

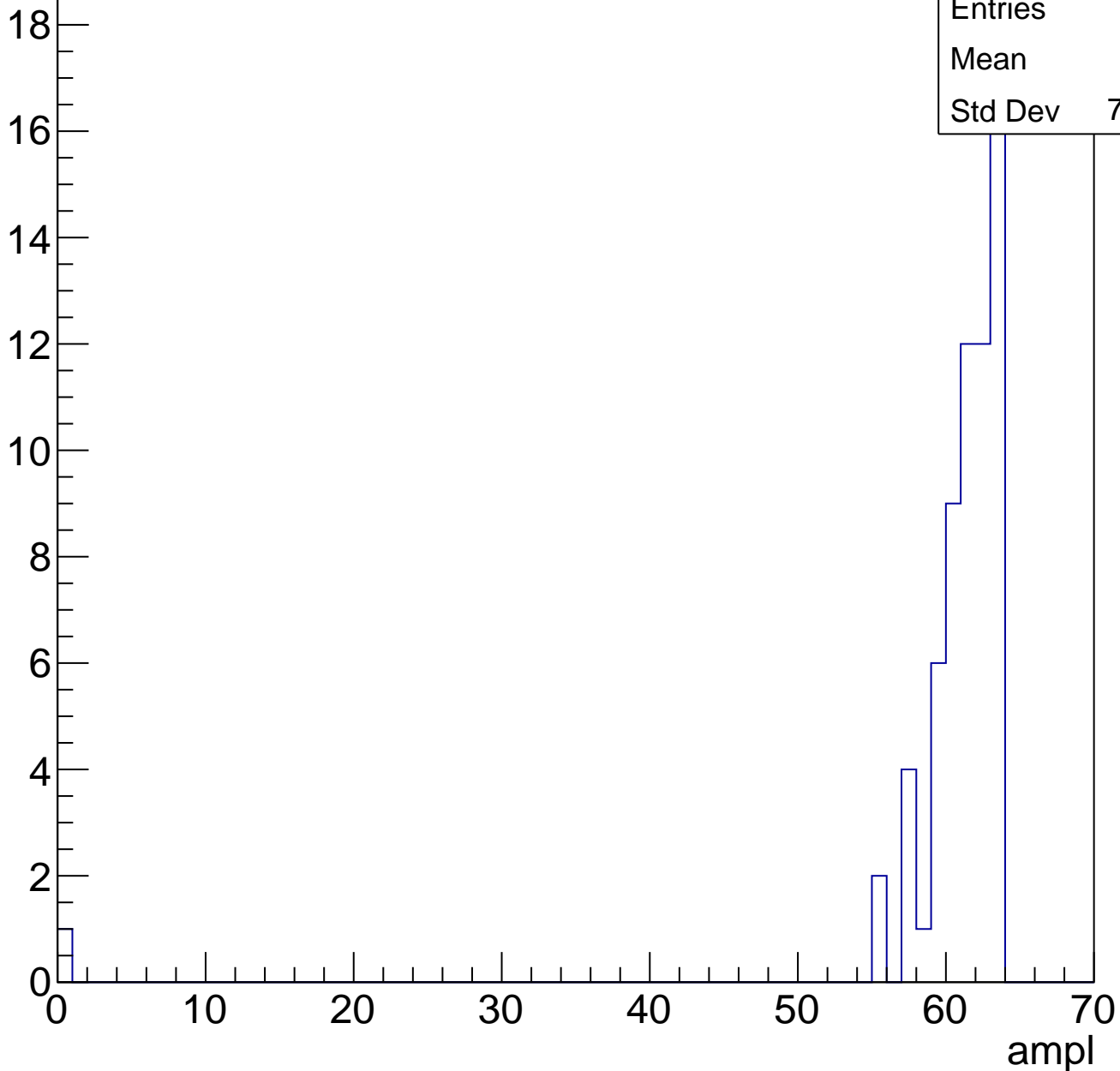
Entry



# B0L101S, U23-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch54, adc0

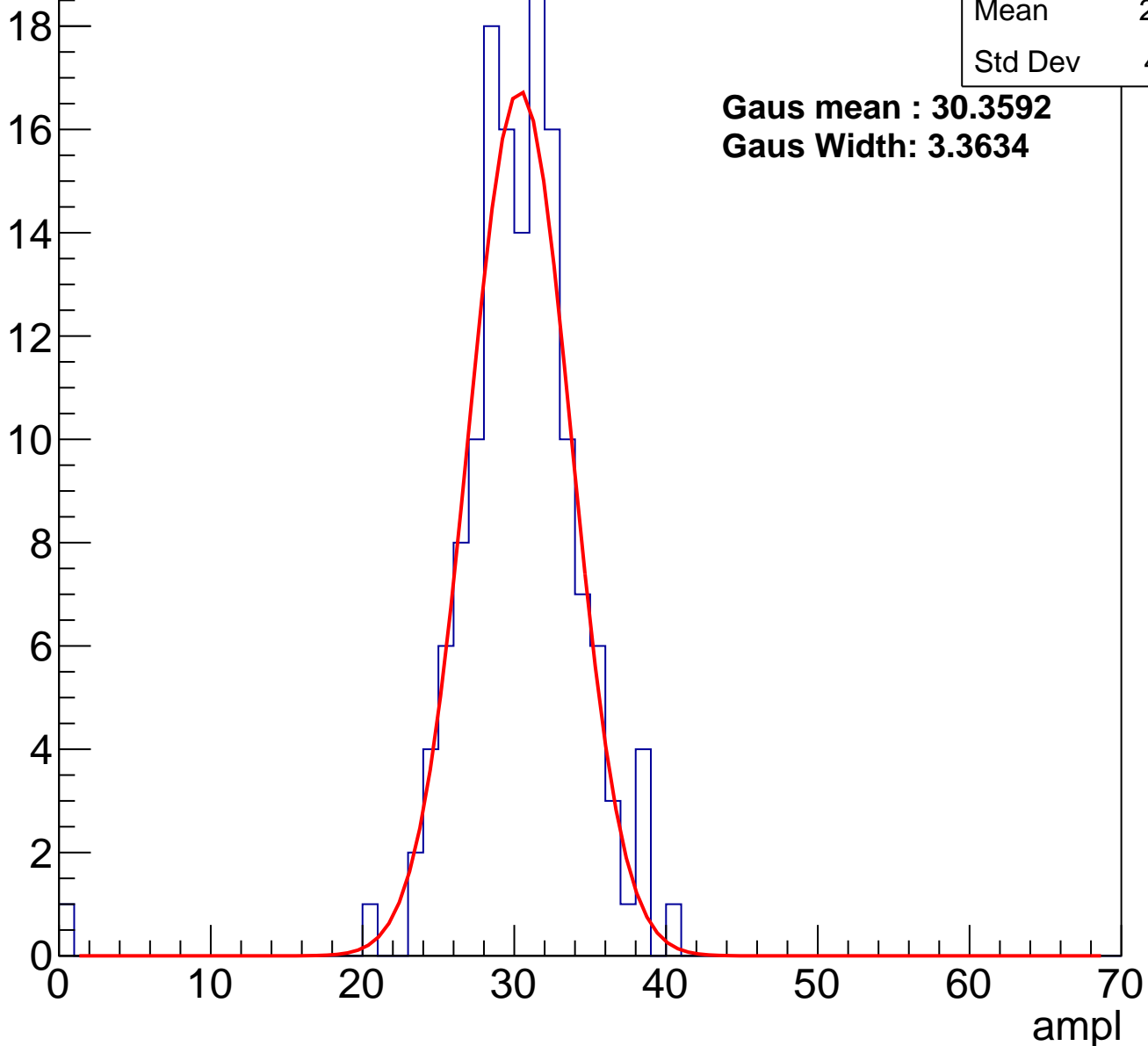
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	29.89
Std Dev	4.241

**Gaus mean : 30.3592**

**Gaus Width: 3.3634**

Entry



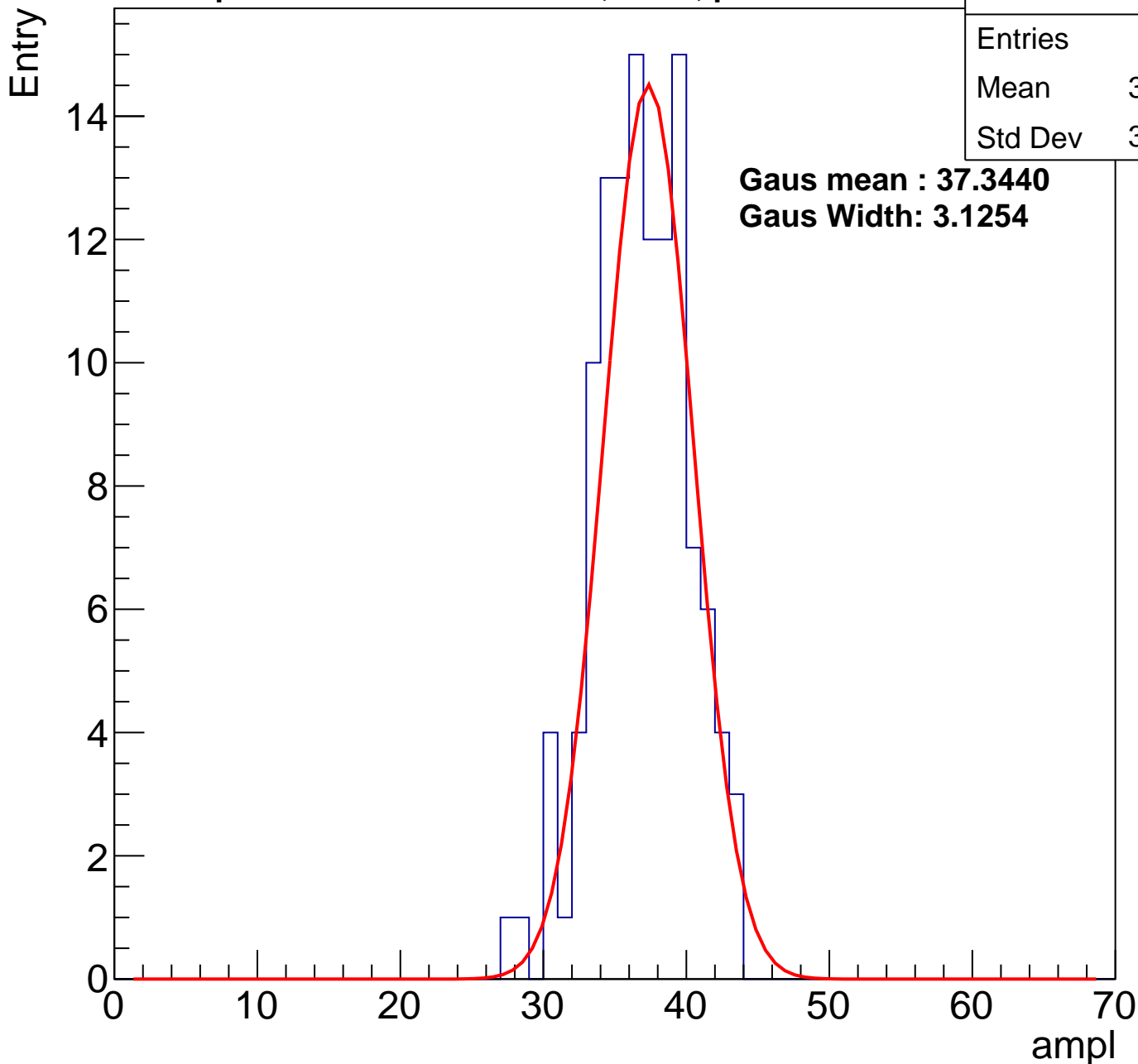
# B0L101S, U23-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	36.44
Std Dev	3.224

**Gaus mean : 37.3440**

**Gaus Width: 3.1254**



# B0L101S, U23-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	42.92
Std Dev	3.398

**Gaus mean : 43.6056**

**Gaus Width: 3.6417**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

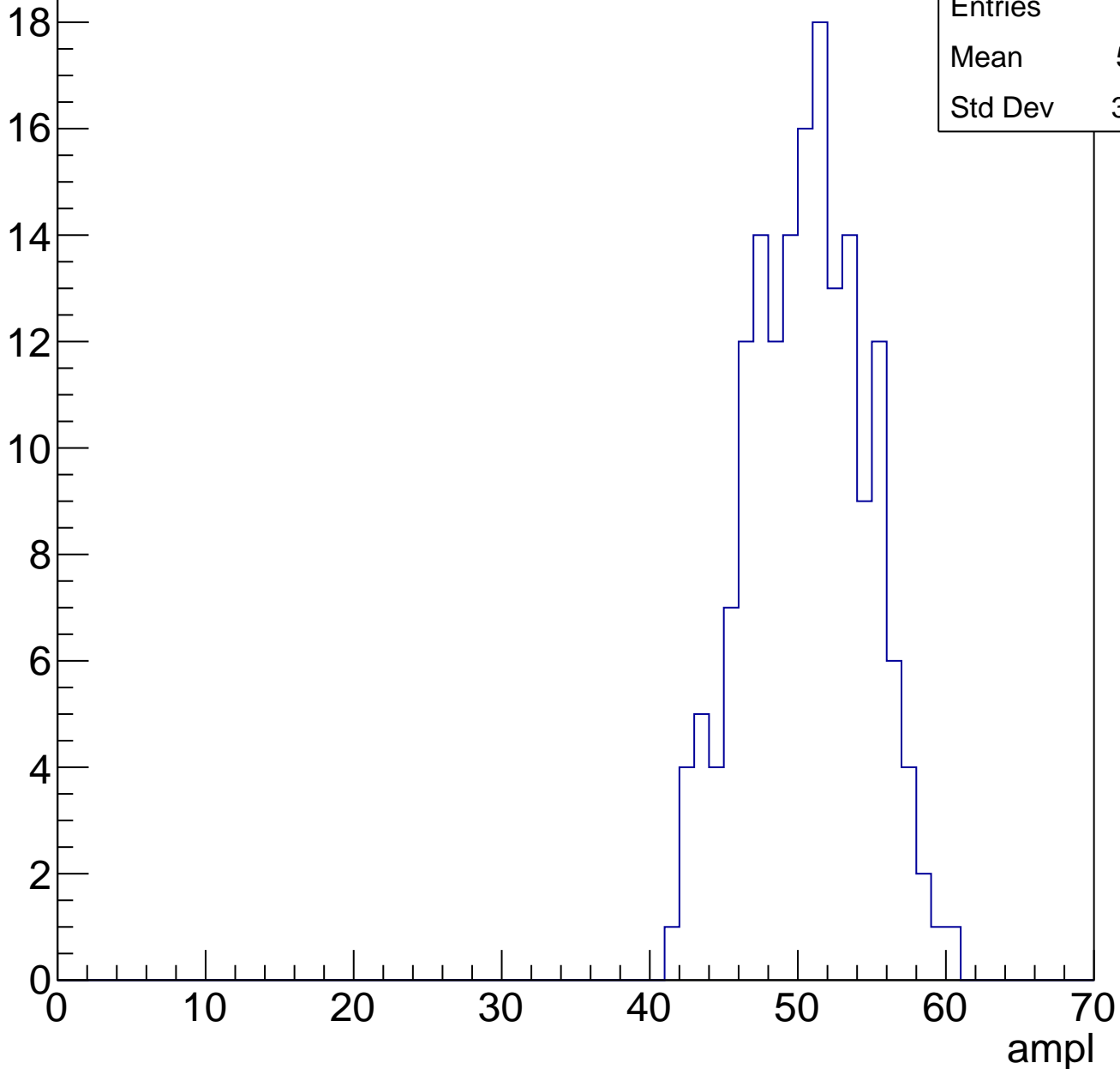
70

# B0L101S, U23-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	50.11
Std Dev	3.952

Entry



# B0L101S, U23-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

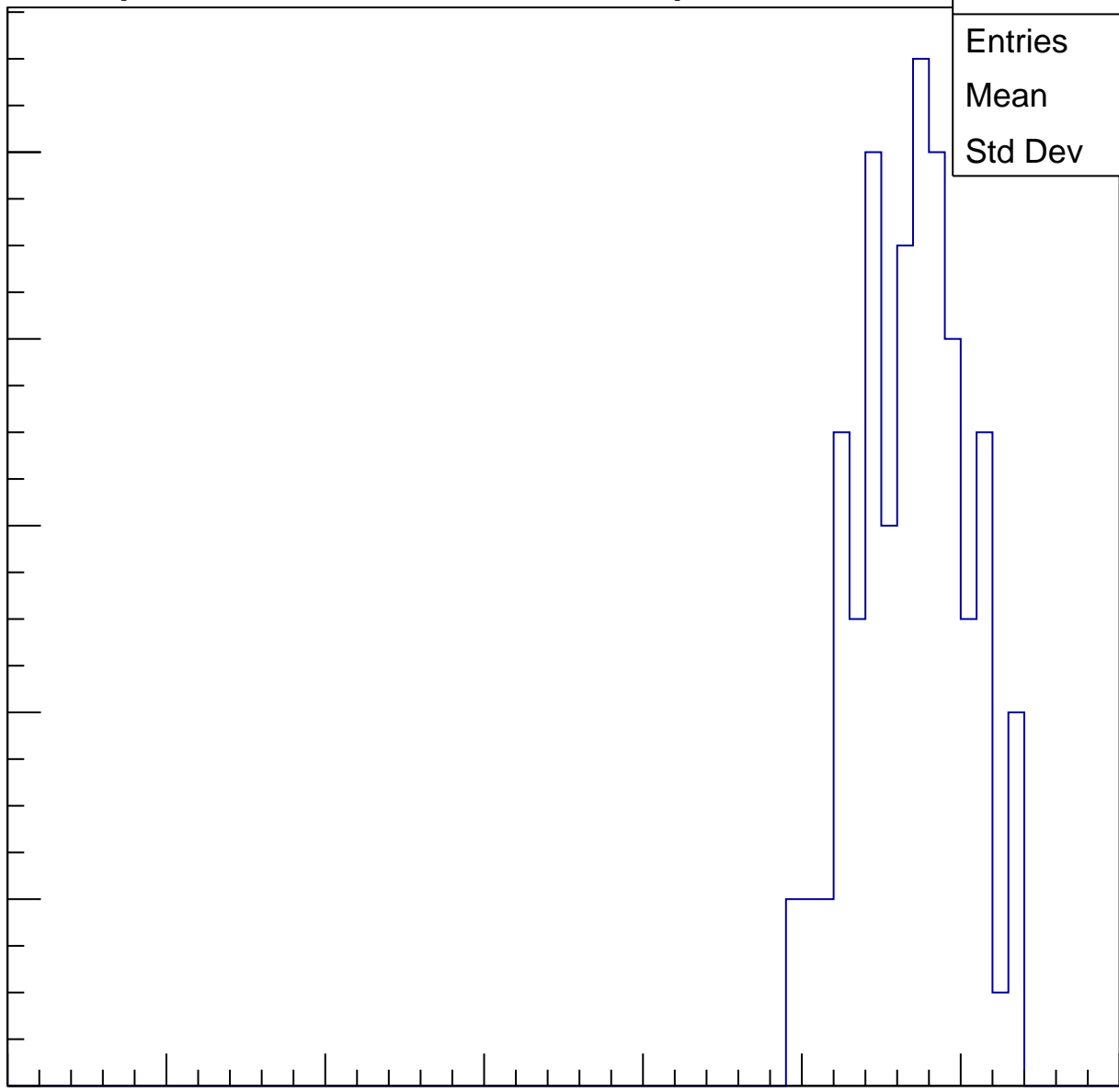
40

50

60

ampl

Entries	89
Mean	56.44
Std Dev	3.379



# B0L101S, U23-ch54, adc5

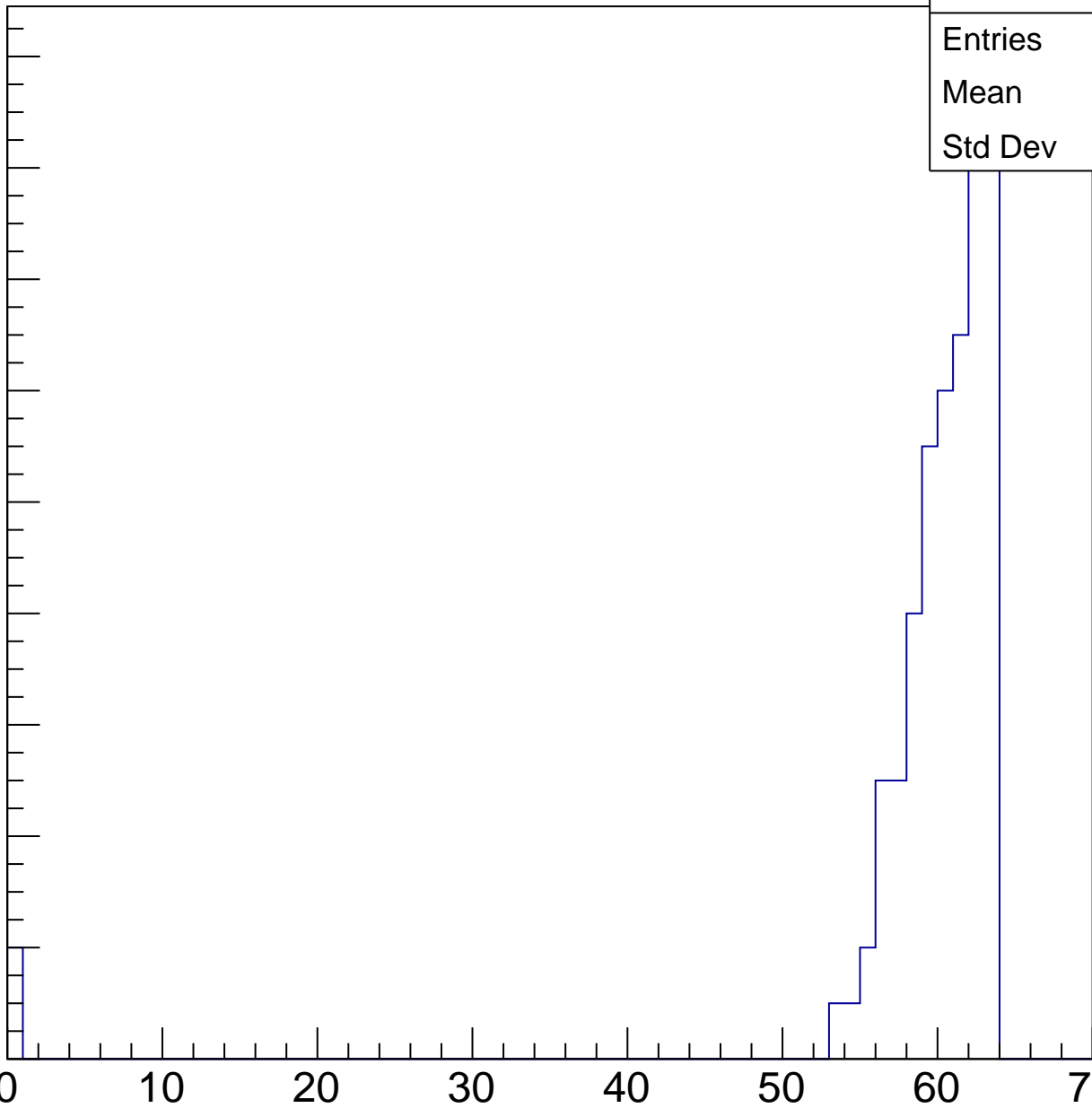
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	94
Mean	58.85
Std Dev	8.996

ampl



# B0L101S, U23-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	60.67
Std Dev	0.9428

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch55, adc0

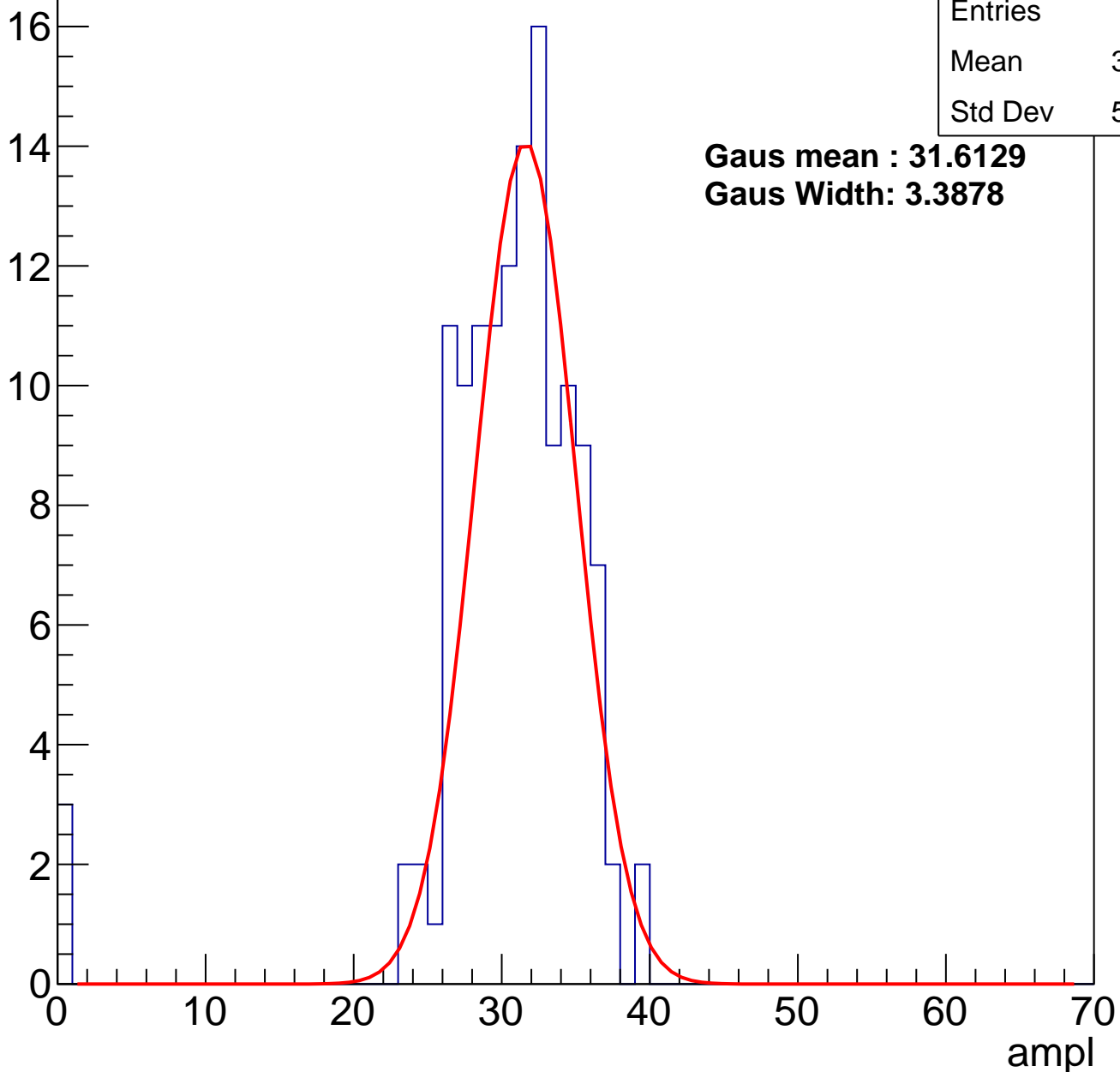
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	30.03
Std Dev	5.689

**Gaus mean : 31.6129**

**Gaus Width: 3.3878**

Entry



# B0L101S, U23-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	36.93
Std Dev	4.017

**Gaus mean : 37.3682**

**Gaus Width: 3.8390**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

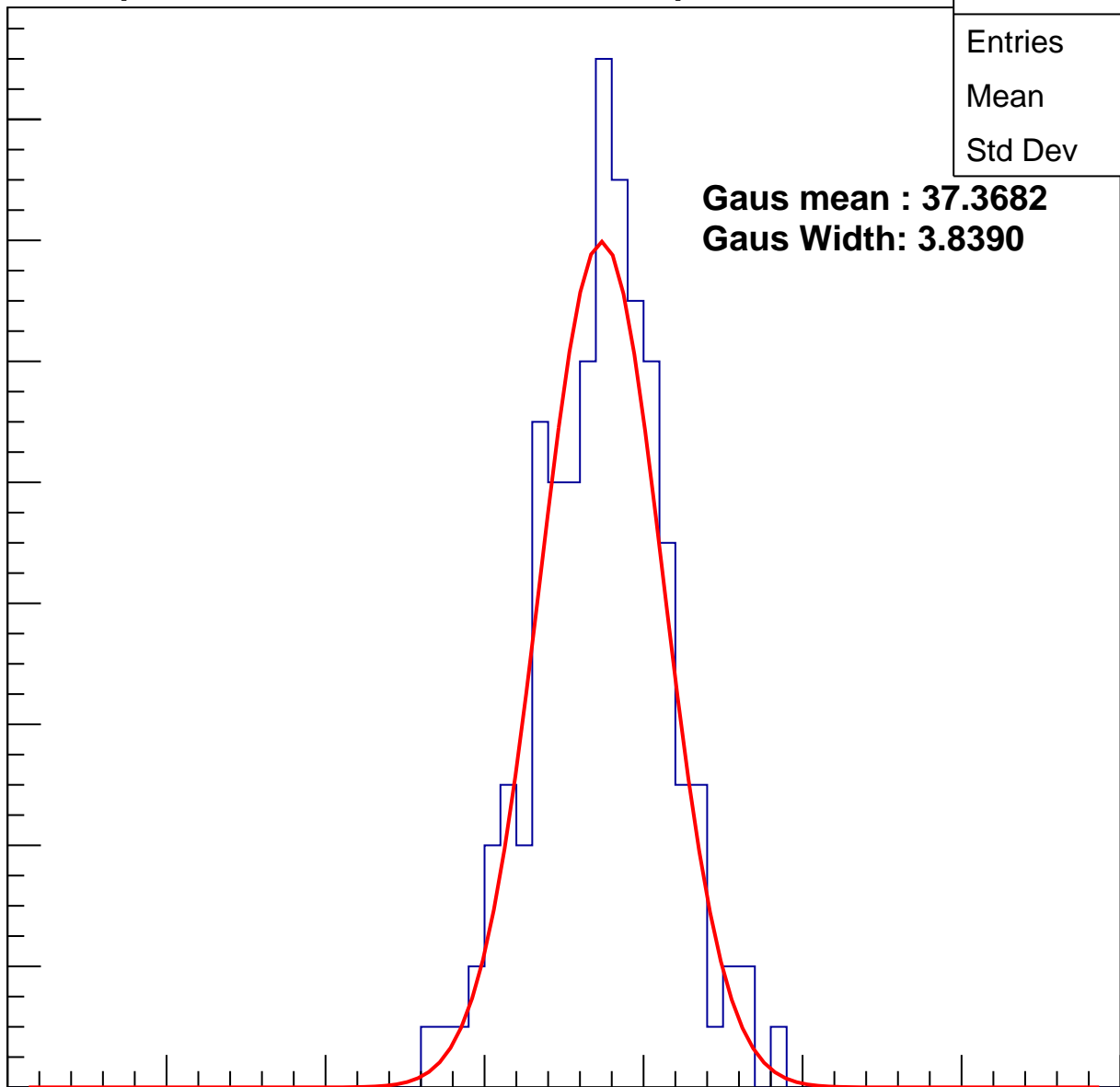
40

50

60

70

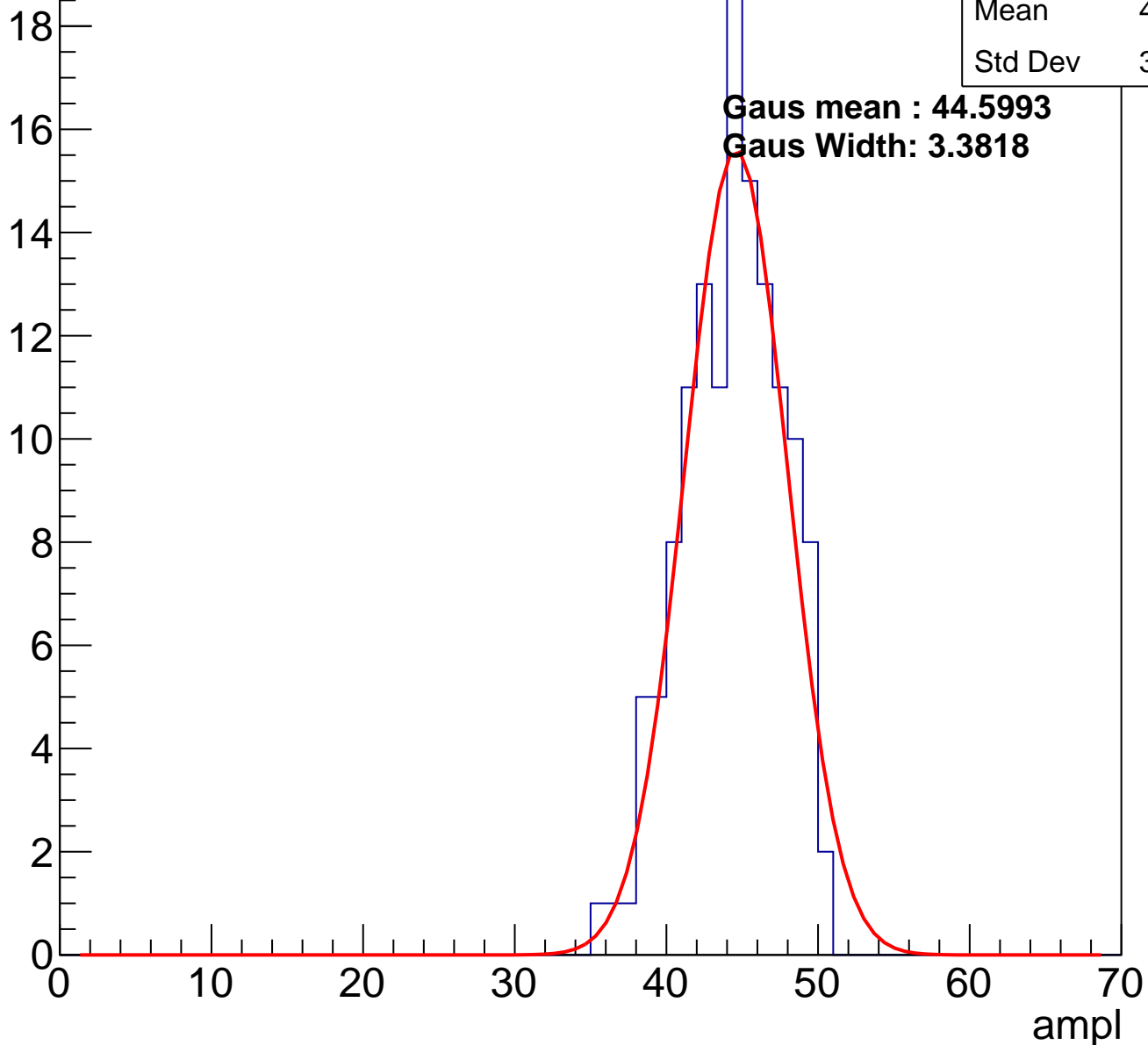
ampl



# B0L101S, U23-ch55, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

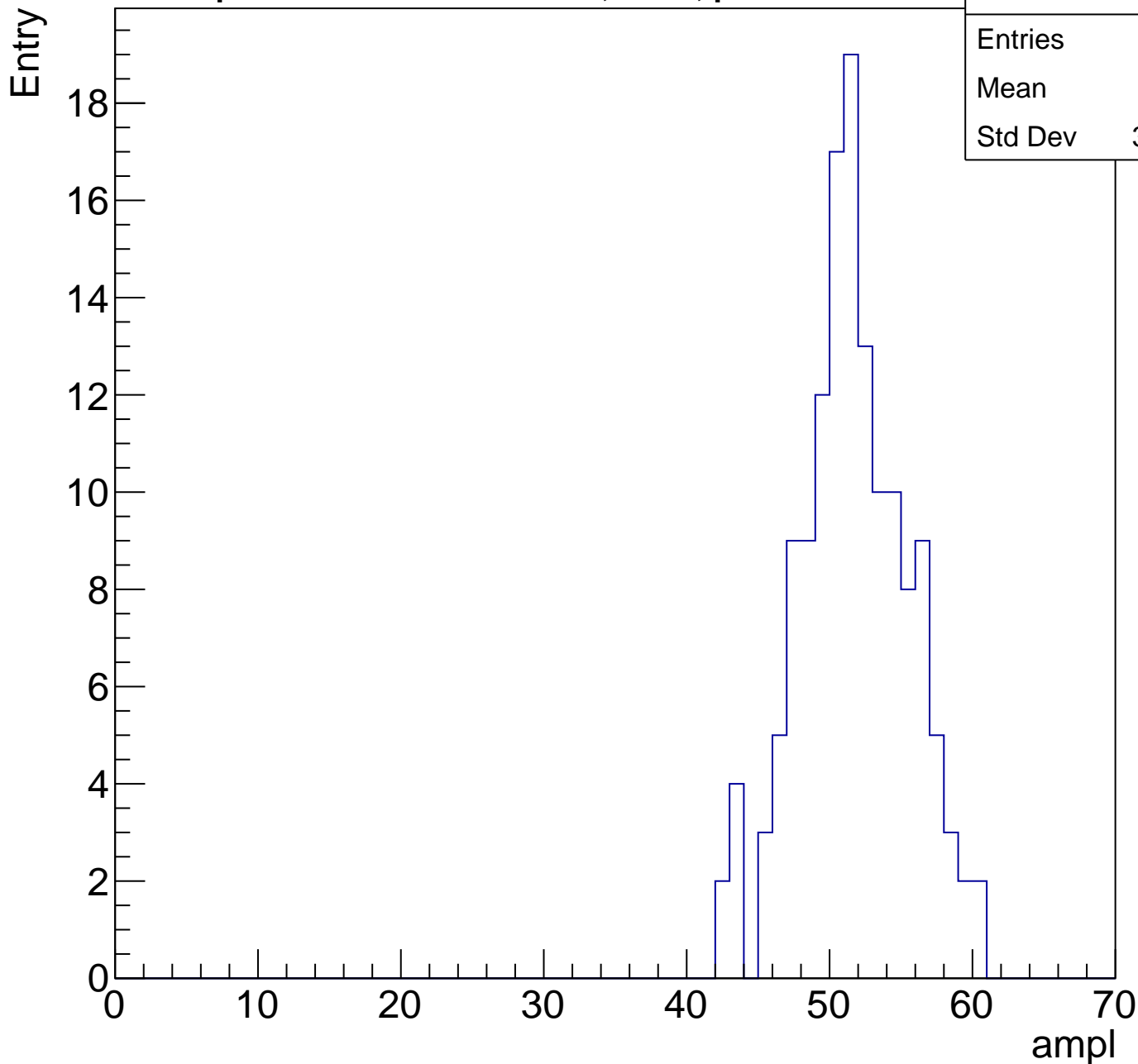
Entry



# B0L101S, U23-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	51.2
Std Dev	3.798



# B0L101S, U23-ch55, adc4

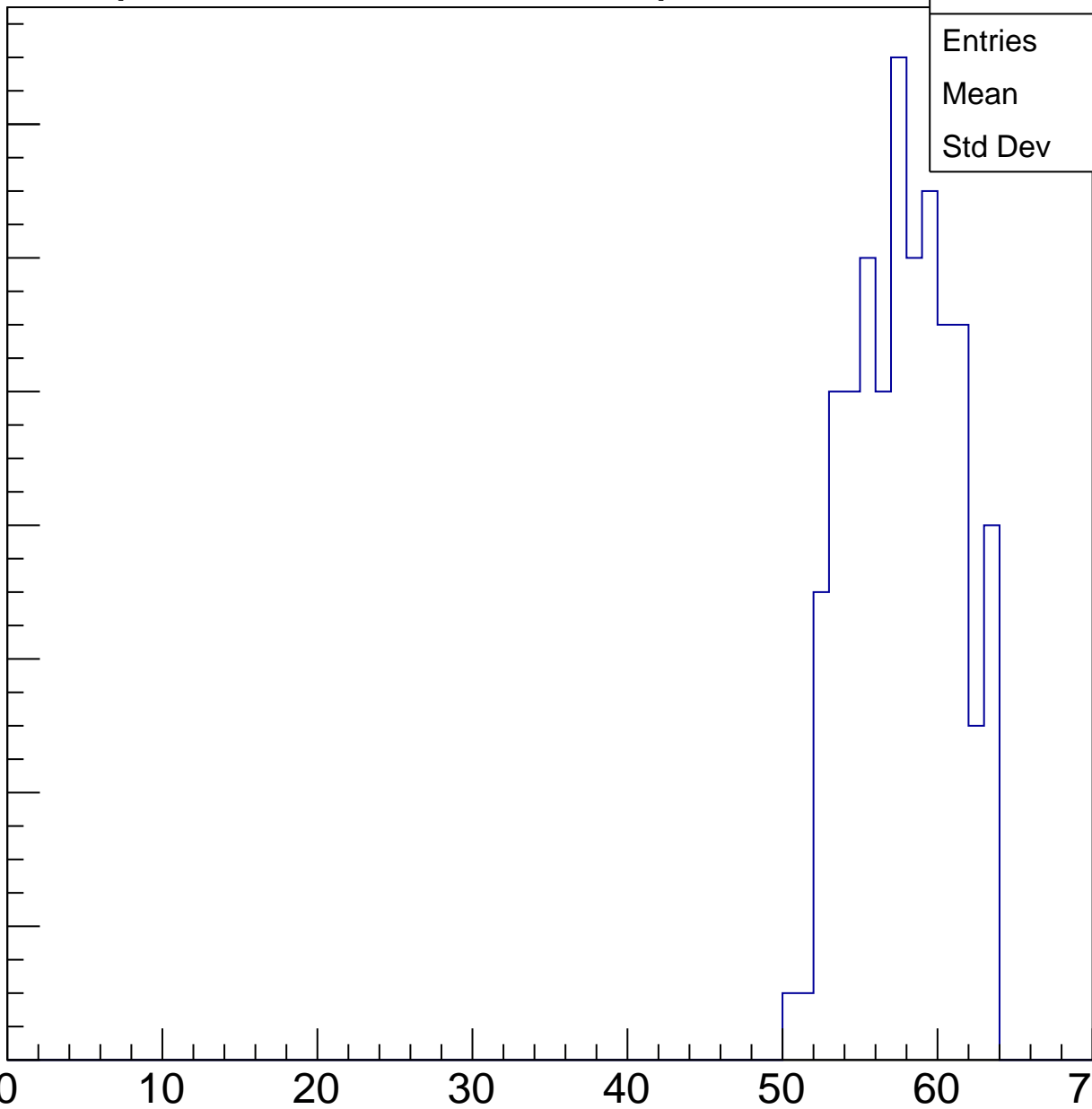
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	126
Mean	57.29
Std Dev	3.224

ampl



# B0L101S, U23-ch55, adc5

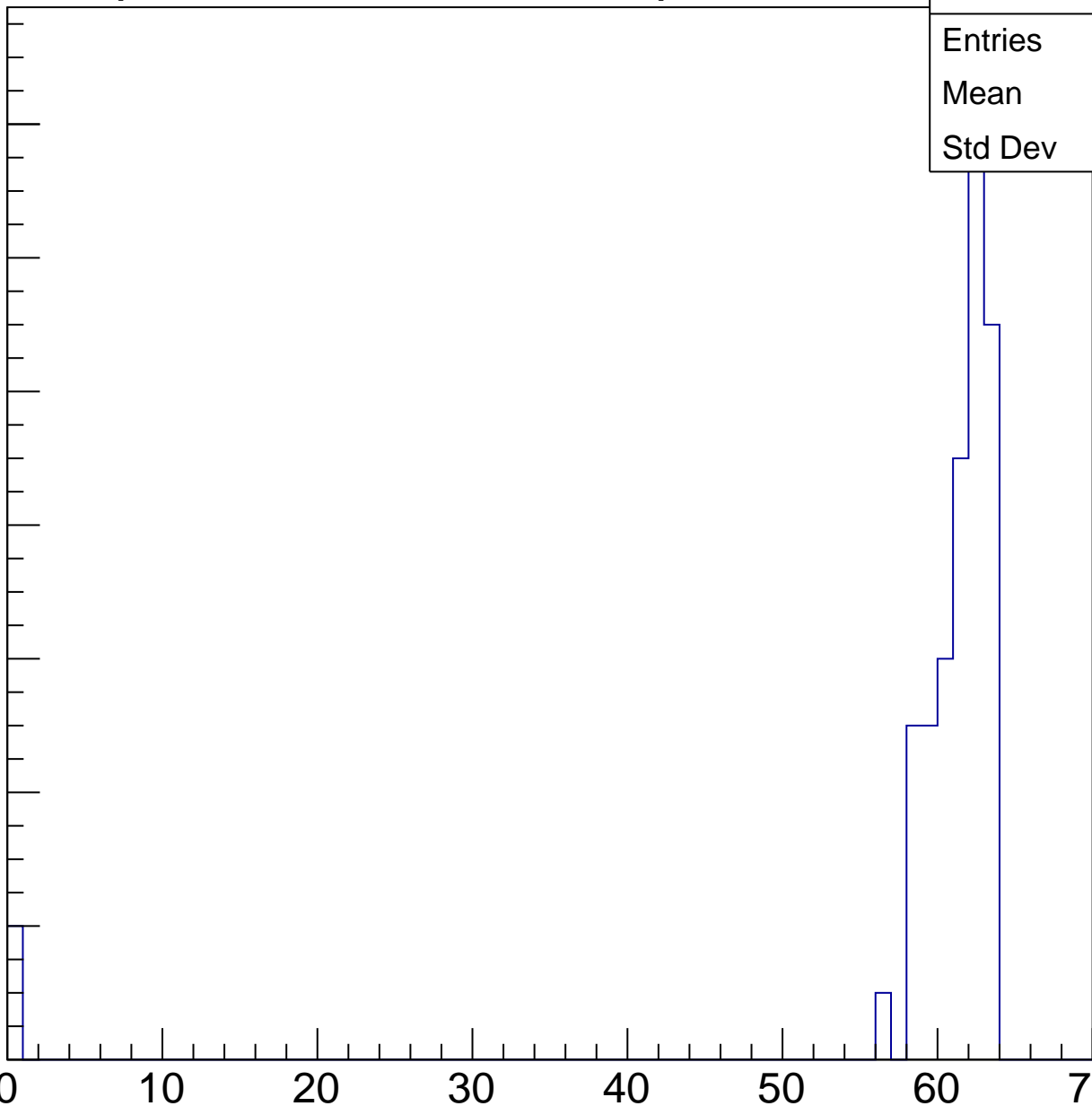
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	54
Mean	58.76
Std Dev	11.65

ampl



# B0L101S, U23-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch56, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	27.98
Std Dev	3.683

**Gaus mean : 28.5281**

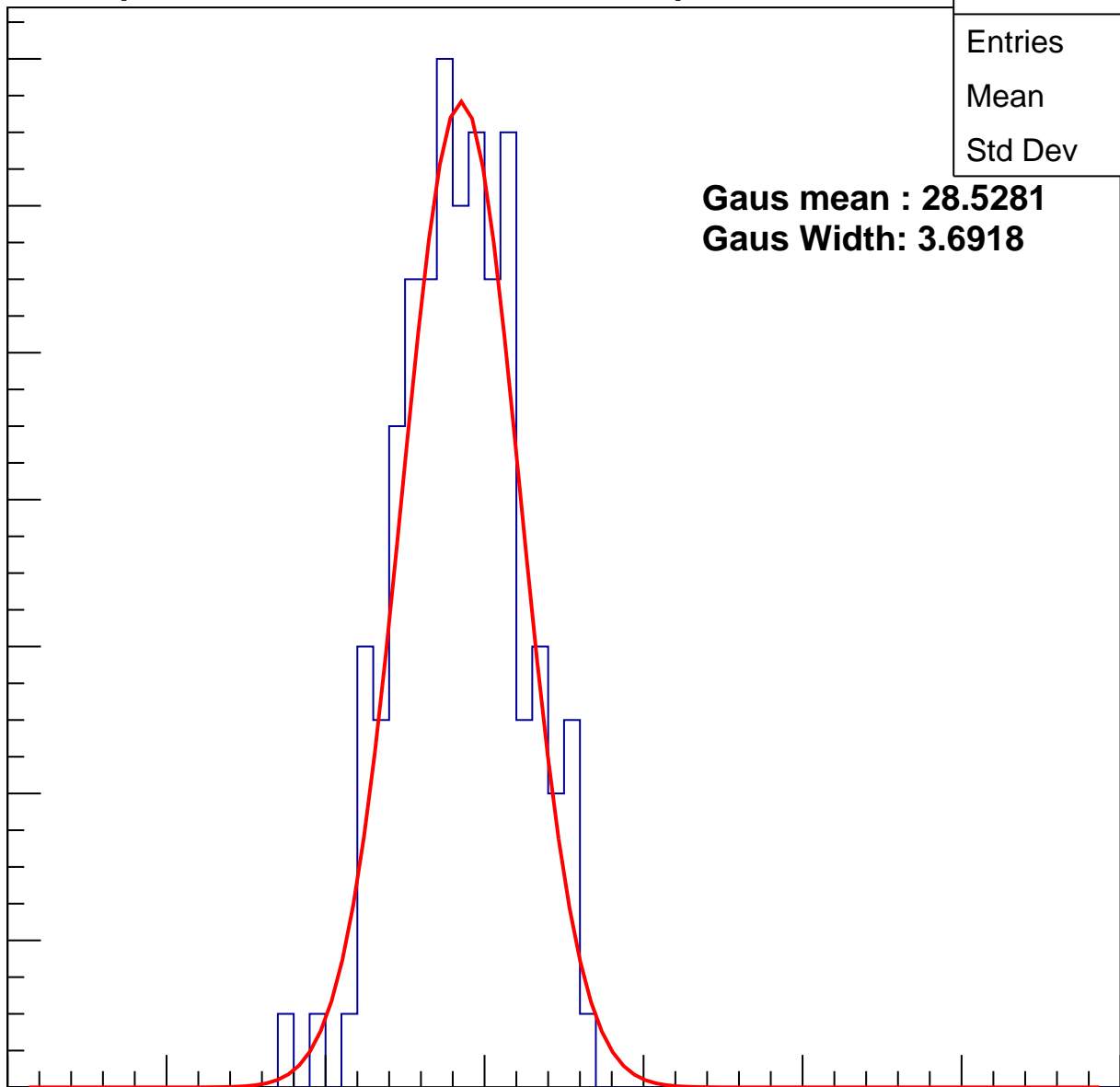
**Gaus Width: 3.6918**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch56, adc1

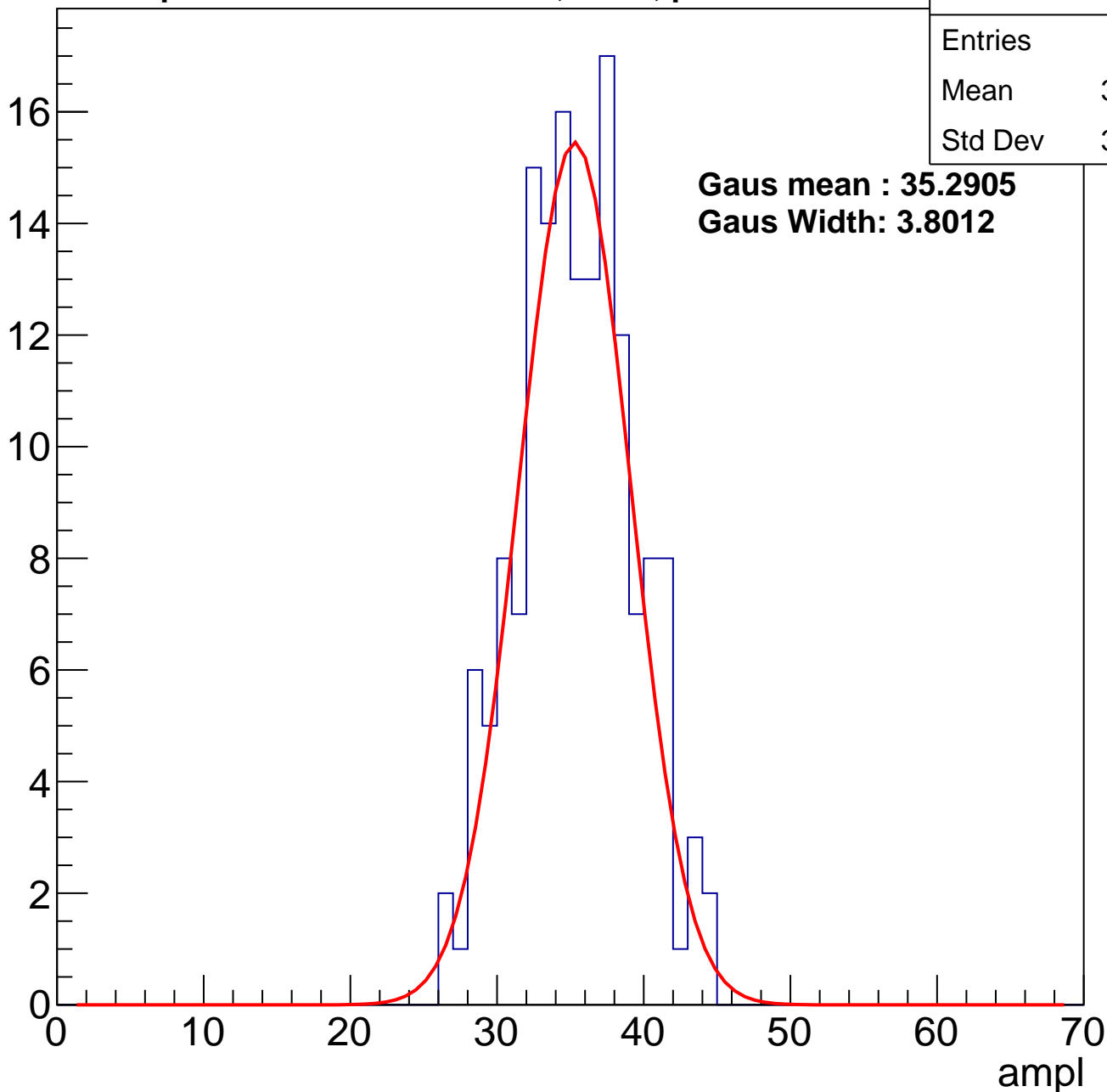
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	34.96
Std Dev	3.906

**Gaus mean : 35.2905**

**Gaus Width: 3.8012**

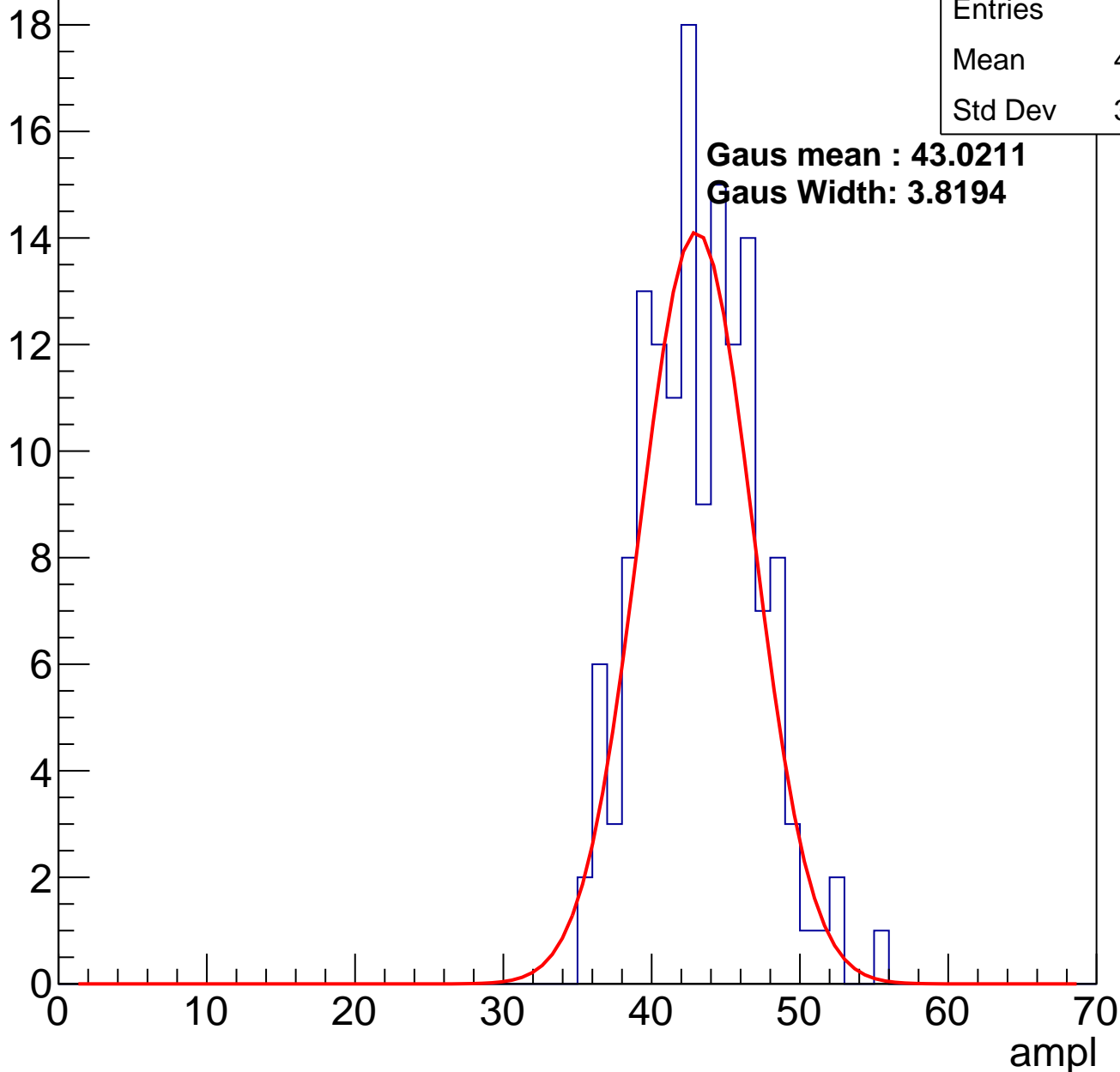
Entry



# B0L101S, U23-ch56, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

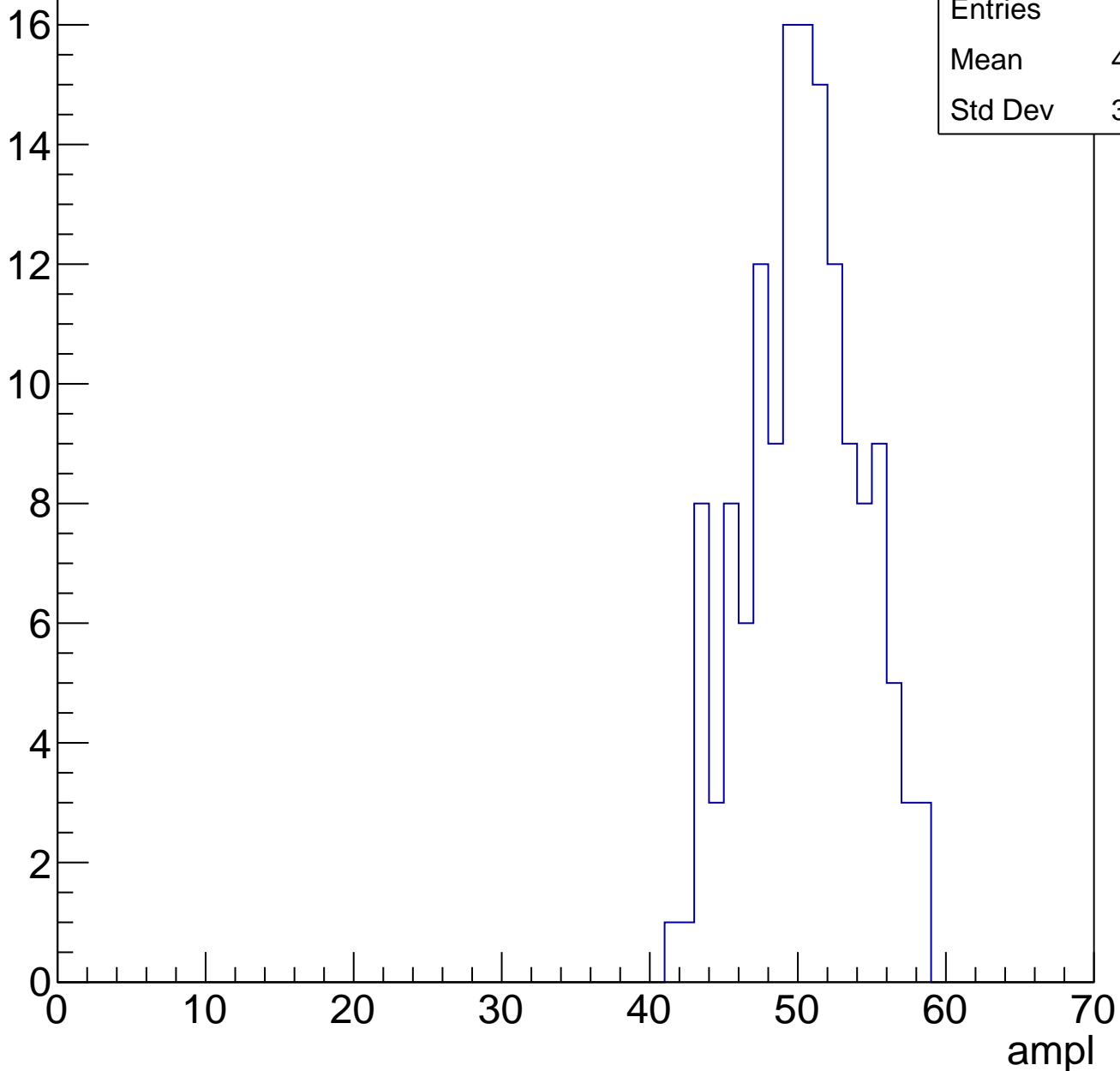
Entry



# B0L101S, U23-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	102
Mean	55.45
Std Dev	3.371

Entry

12

10

8

6

4

2

0

0

10

20

30

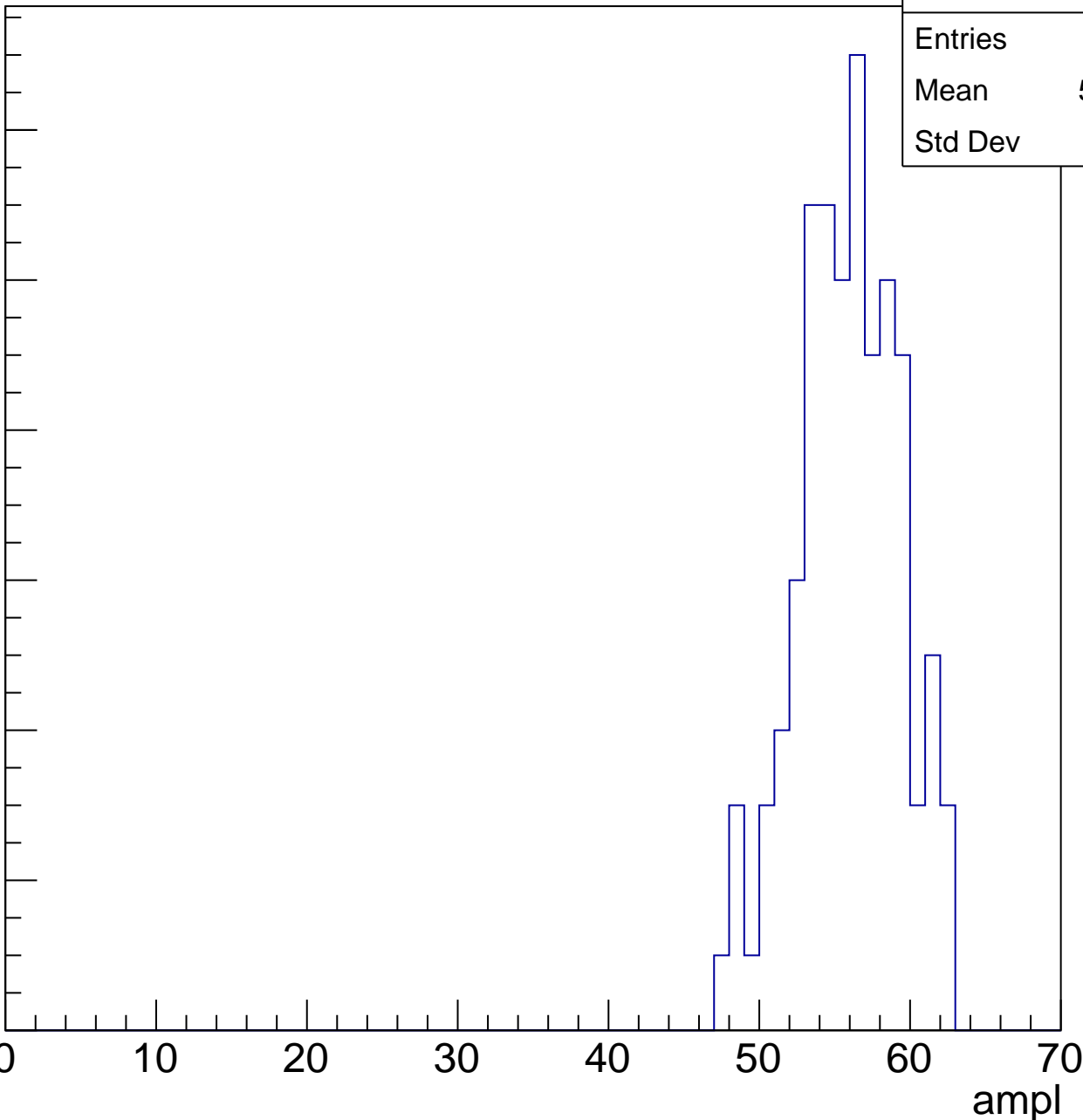
40

50

60

ampl

70



# B0L101S, U23-ch56, adc5

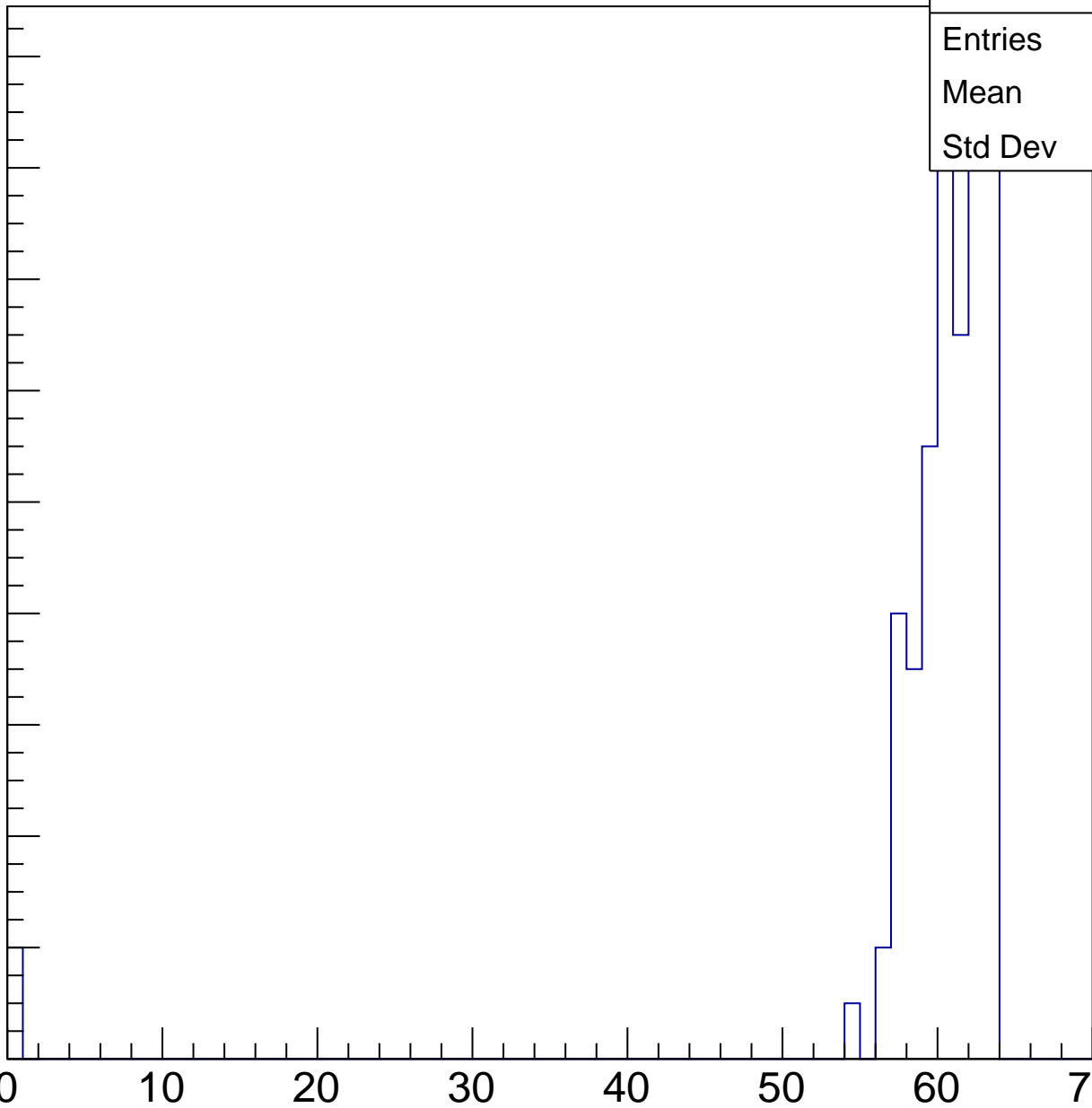
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	95
Mean	59.14
Std Dev	8.916

ampl



# B0L101S, U23-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.6
Std Dev	1.2

ampl

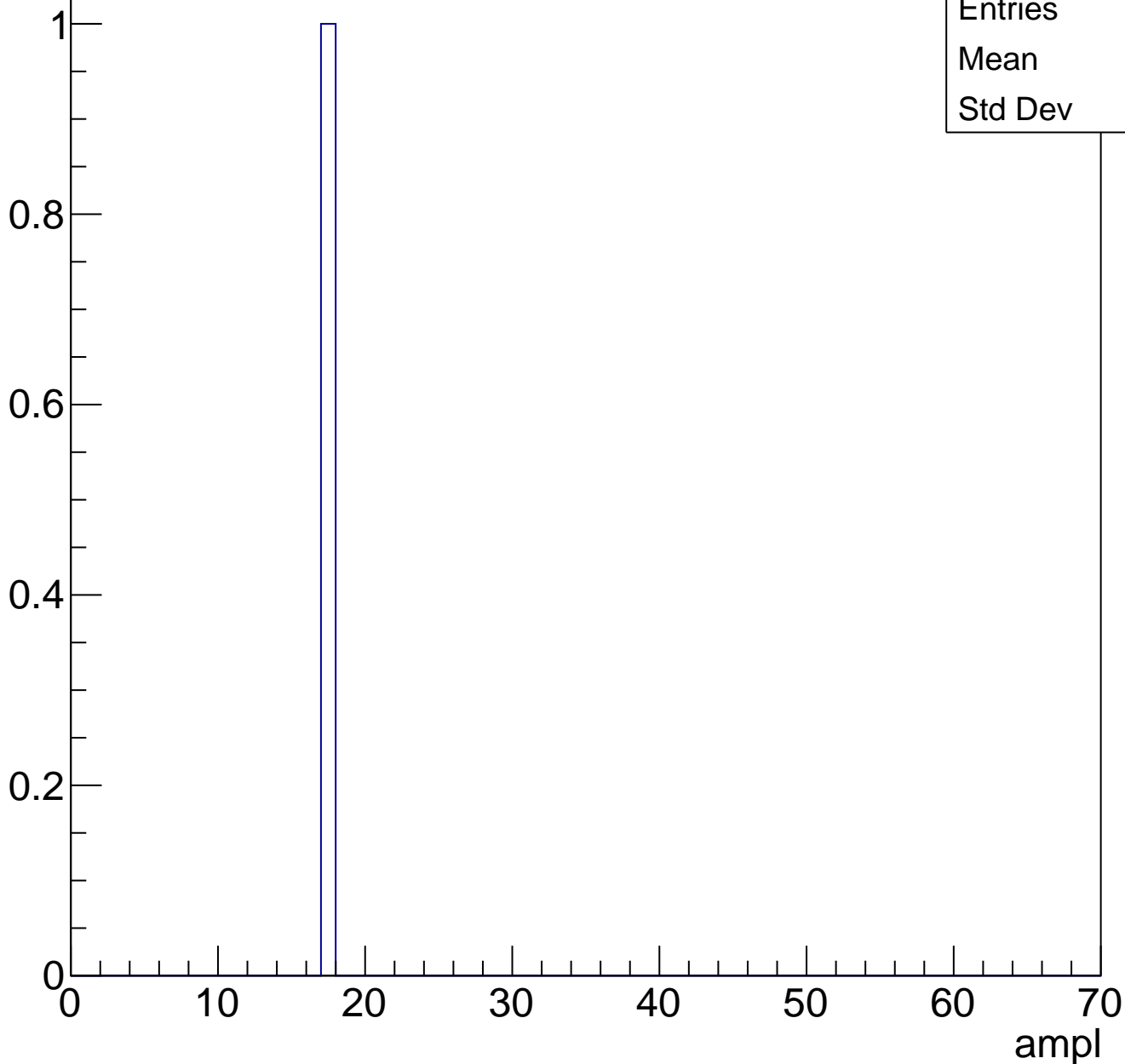
0 10 20 30 40 50 60 70



# B0L101S, U23-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



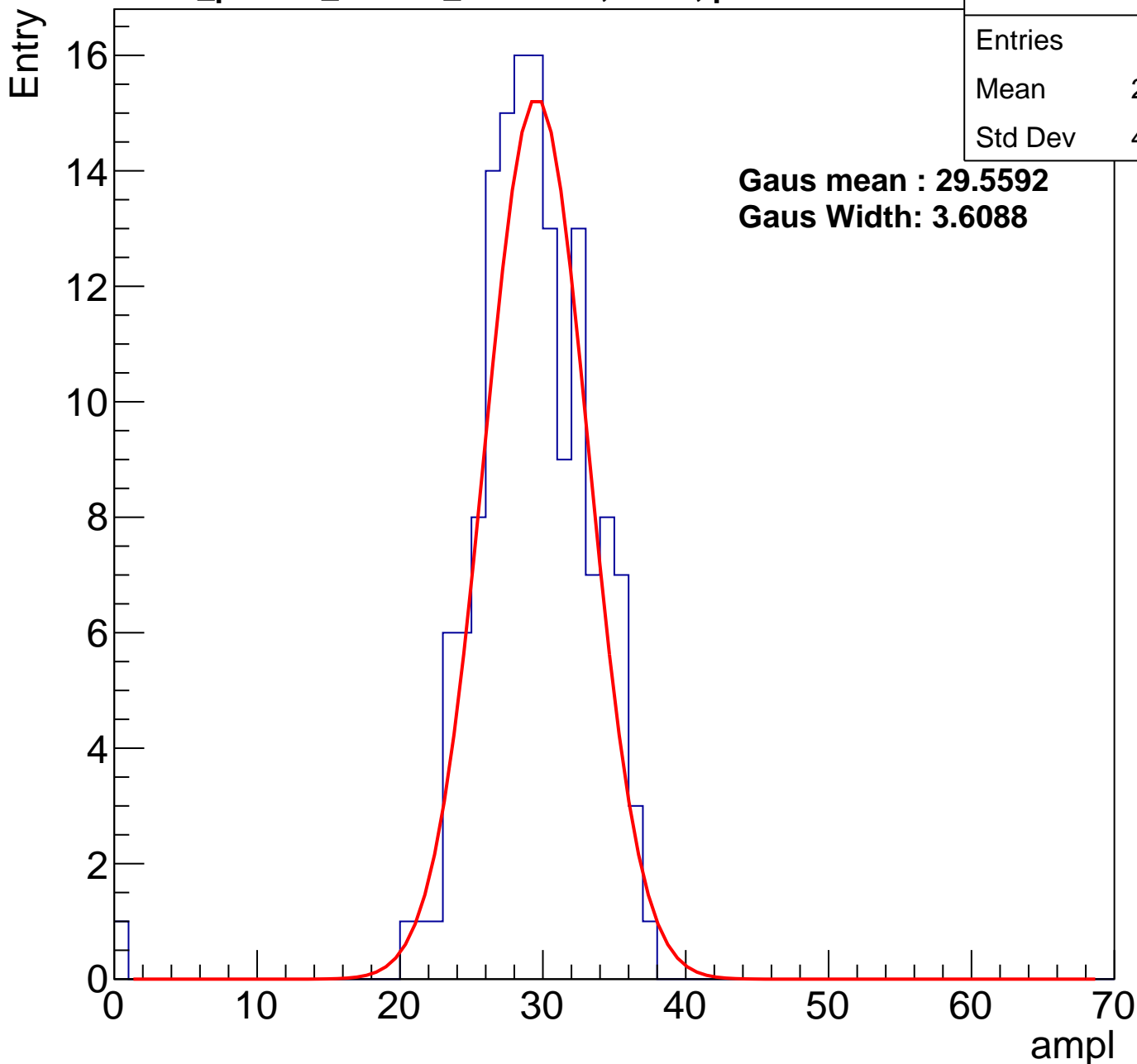
# B0L101S, U23-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	28.79
Std Dev	4.273

**Gaus mean : 29.5592**

**Gaus Width: 3.6088**



# B0L101S, U23-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	35.9
Std Dev	4.696

**Gaus mean : 36.5254**

**Gaus Width: 3.5669**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

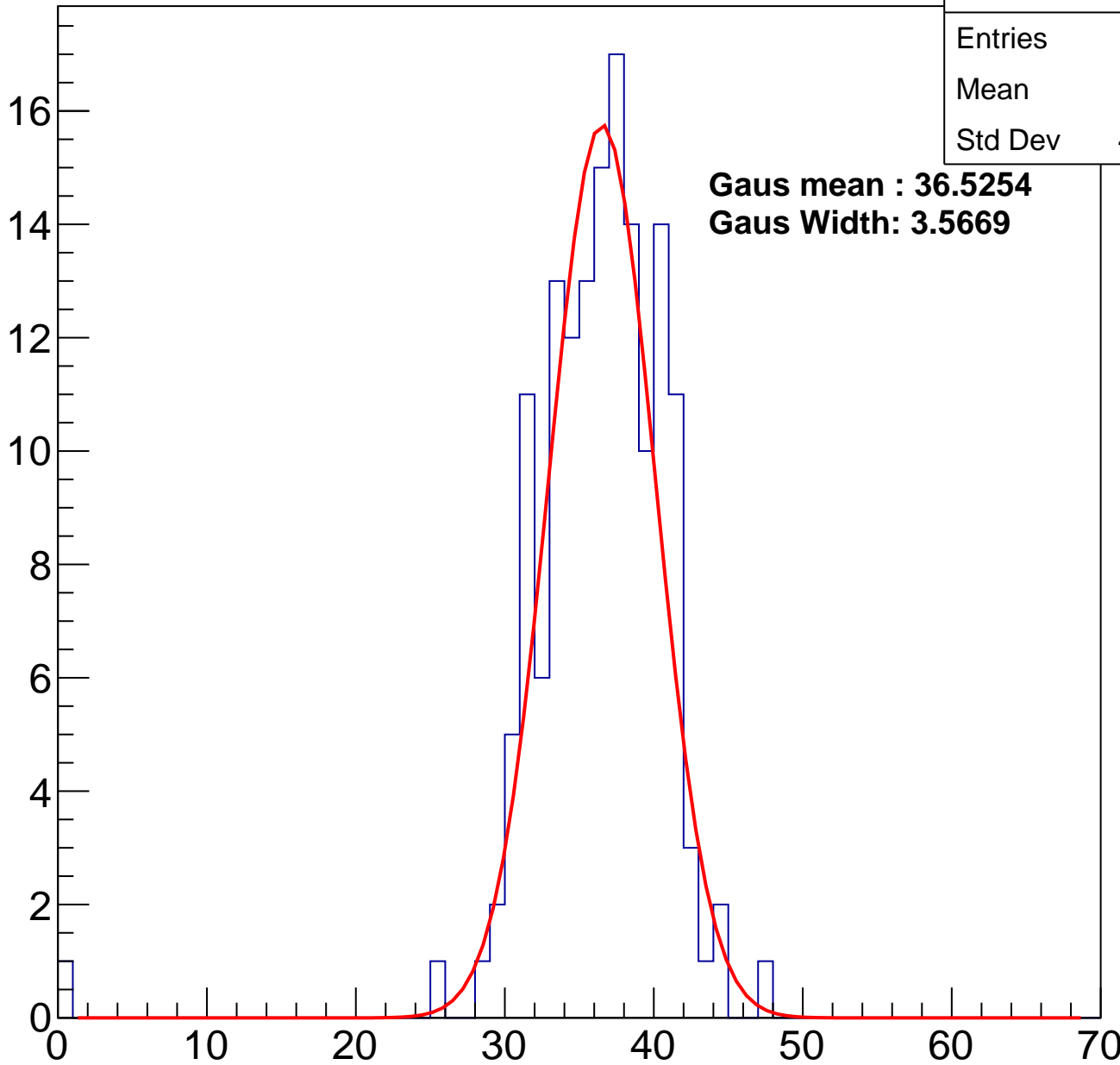
40

50

60

70

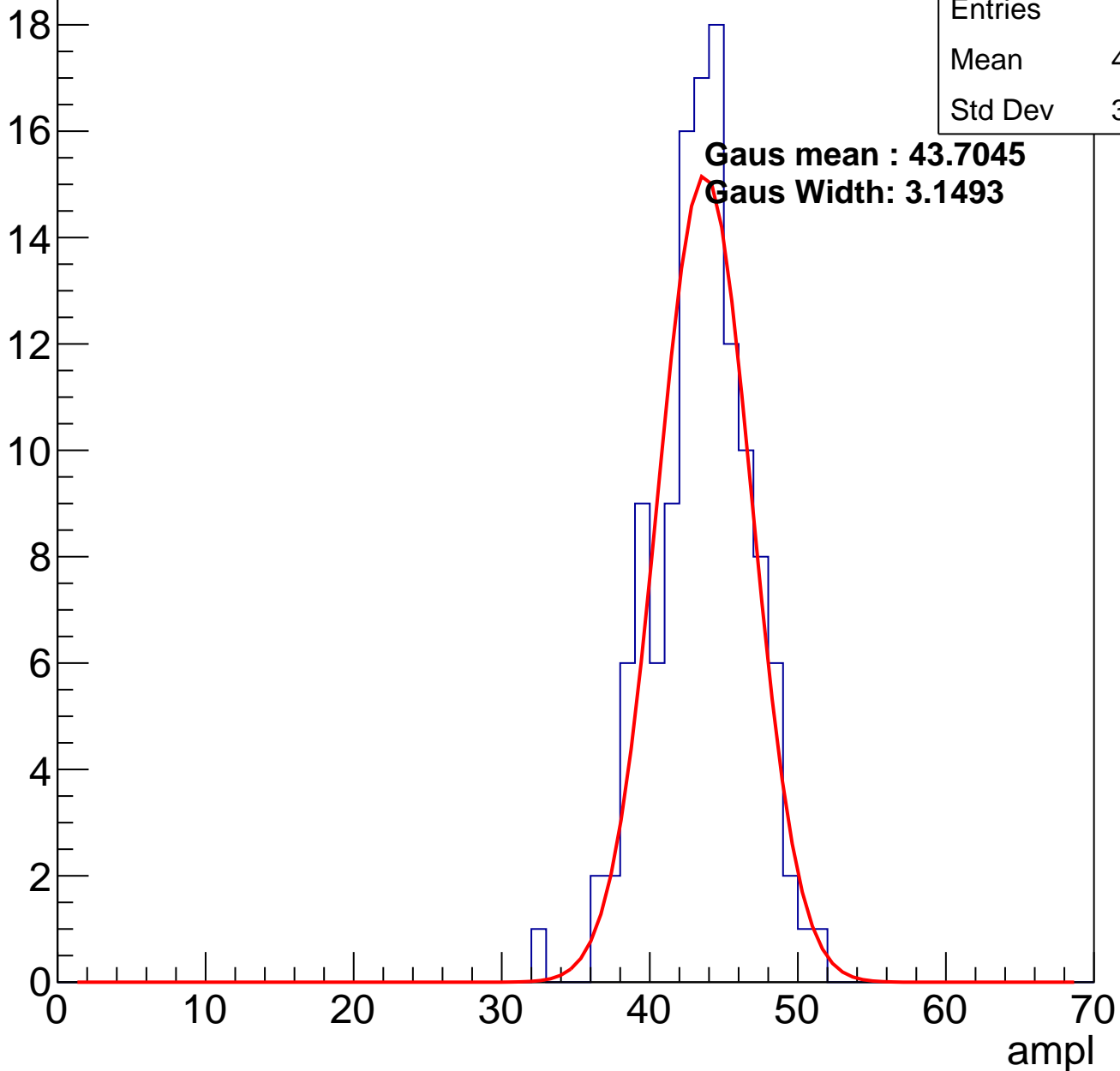
ampl



# B0L101S, U23-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

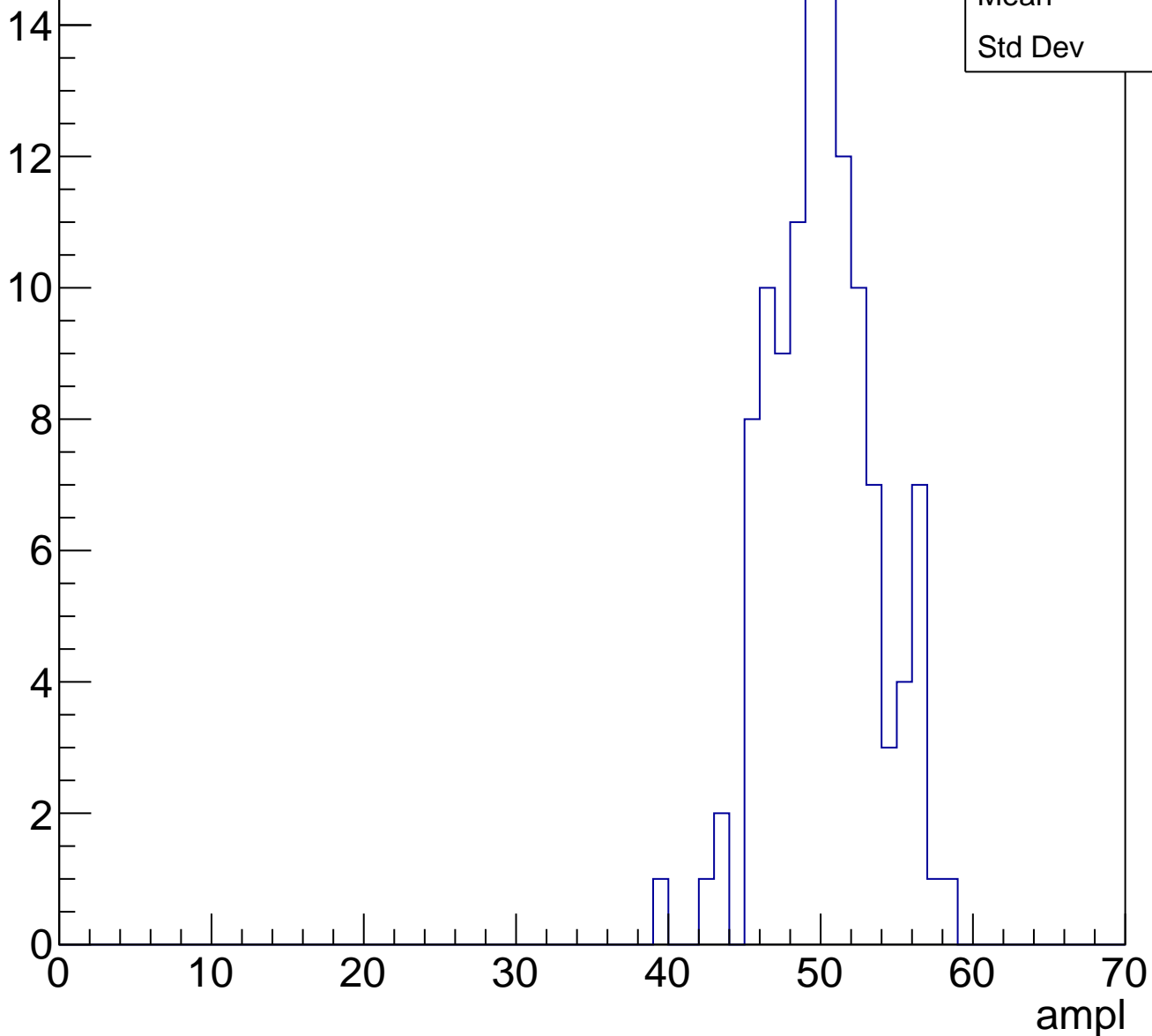


# B0L101S, U23-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	49.7
Std Dev	3.46

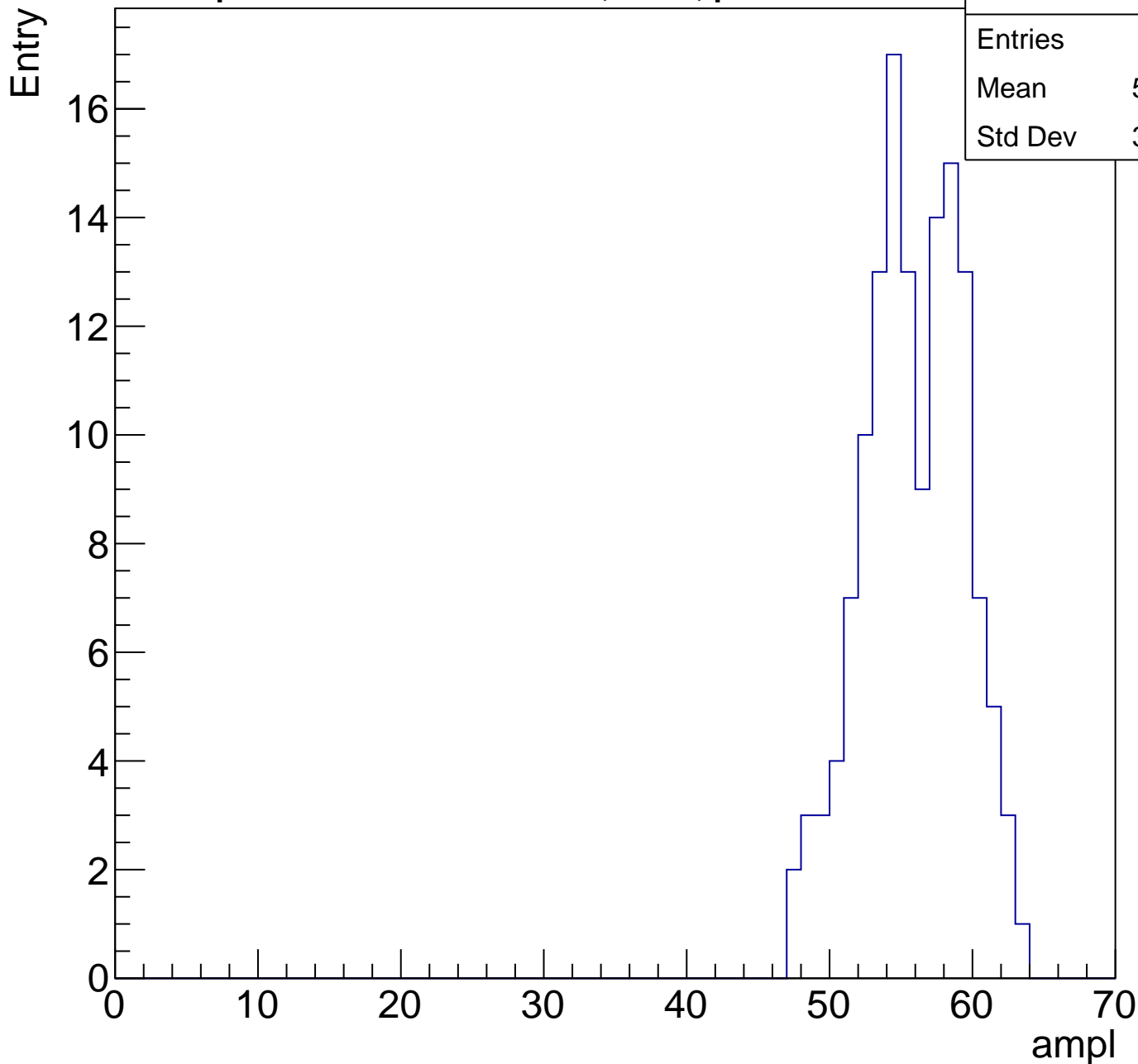
Entry



# B0L101S, U23-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

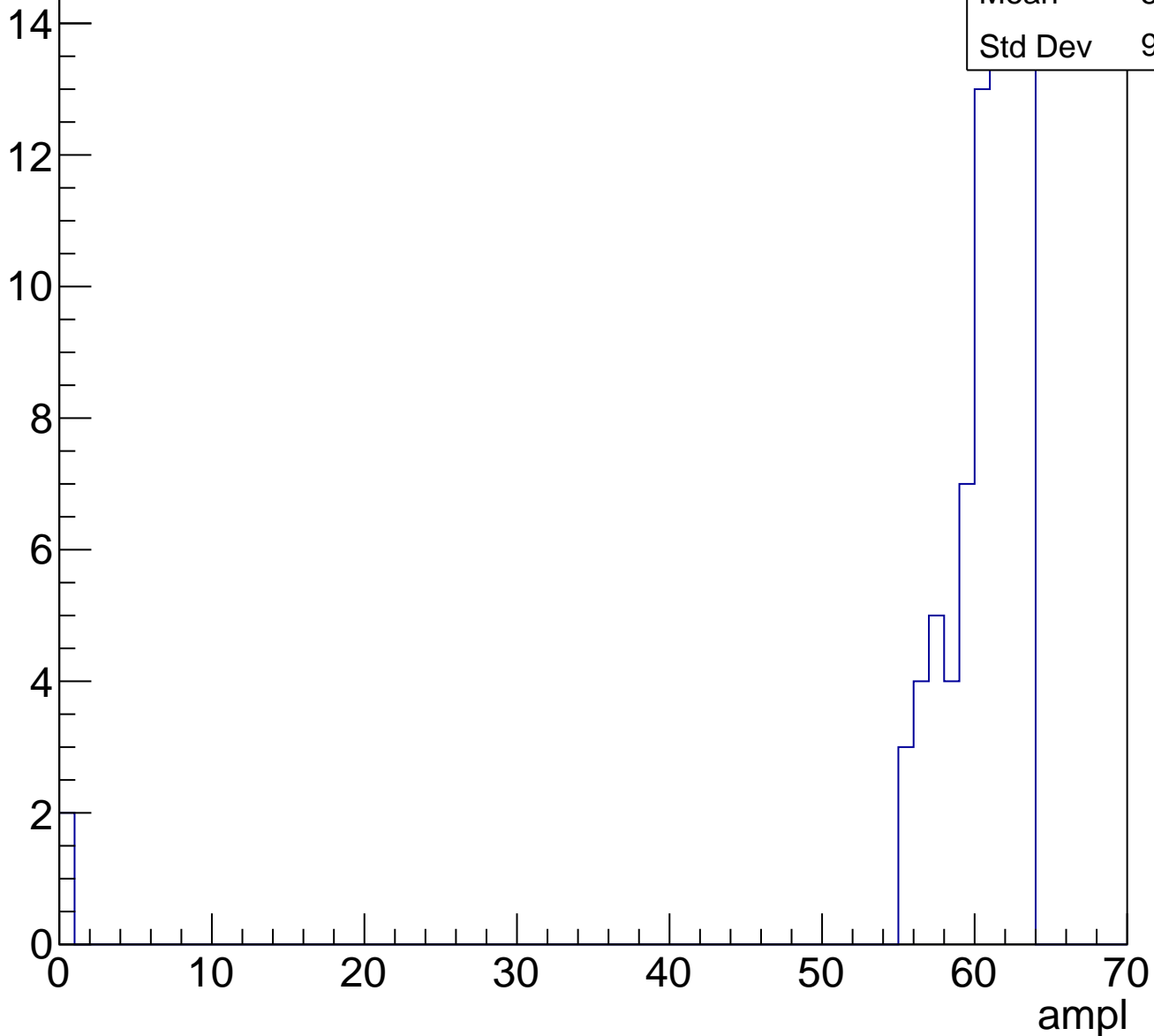
Entries	139
Mean	55.37
Std Dev	3.506



# B0L101S, U23-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B0L101S, U23-ch58, adc0

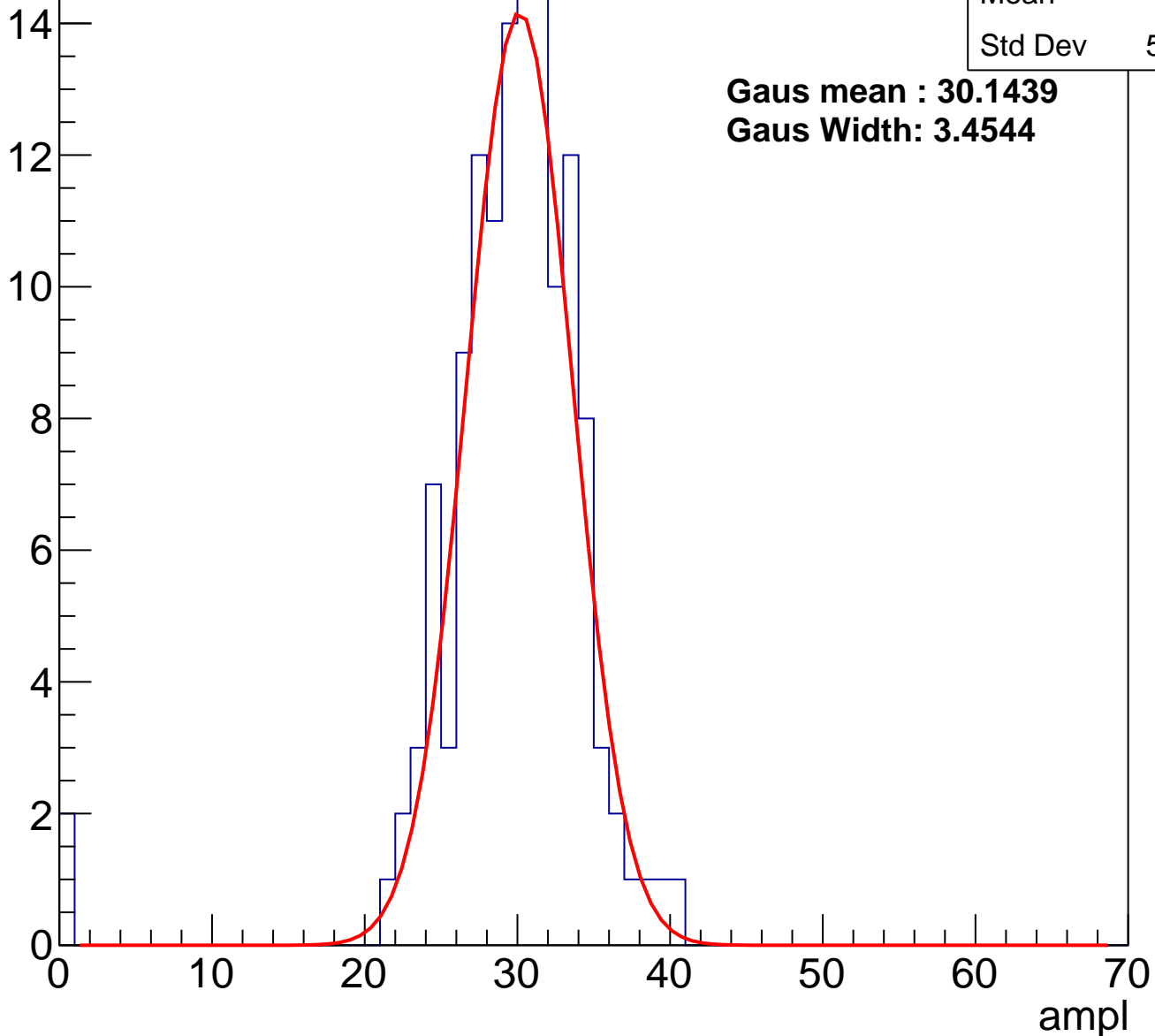
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	29.2
Std Dev	5.085

**Gaus mean : 30.1439**

**Gaus Width: 3.4544**

Entry



# B0L101S, U23-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	36.14
Std Dev	3.689

**Gaus mean : 36.7715**

**Gaus Width: 3.3236**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

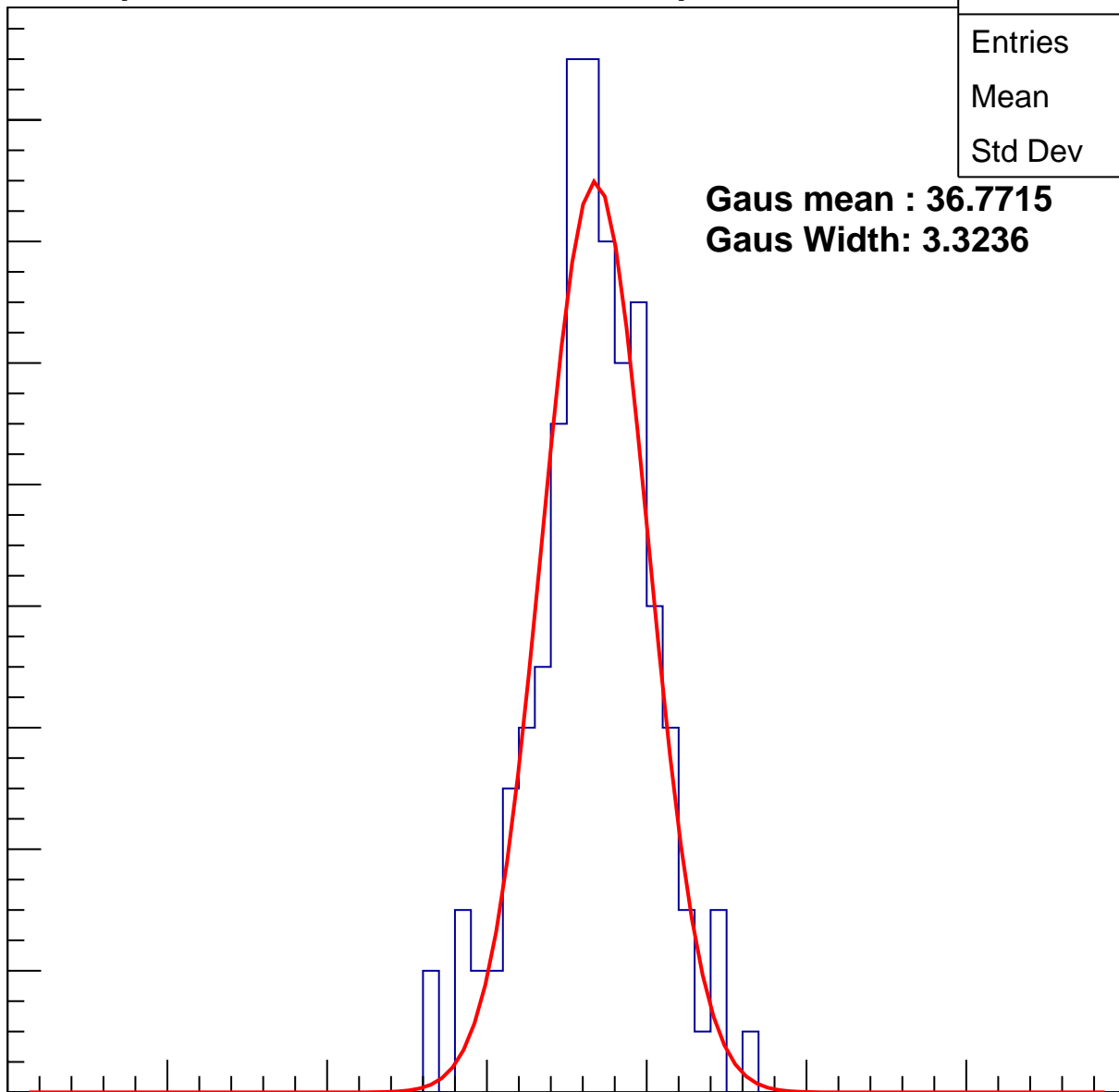
40

50

60

70

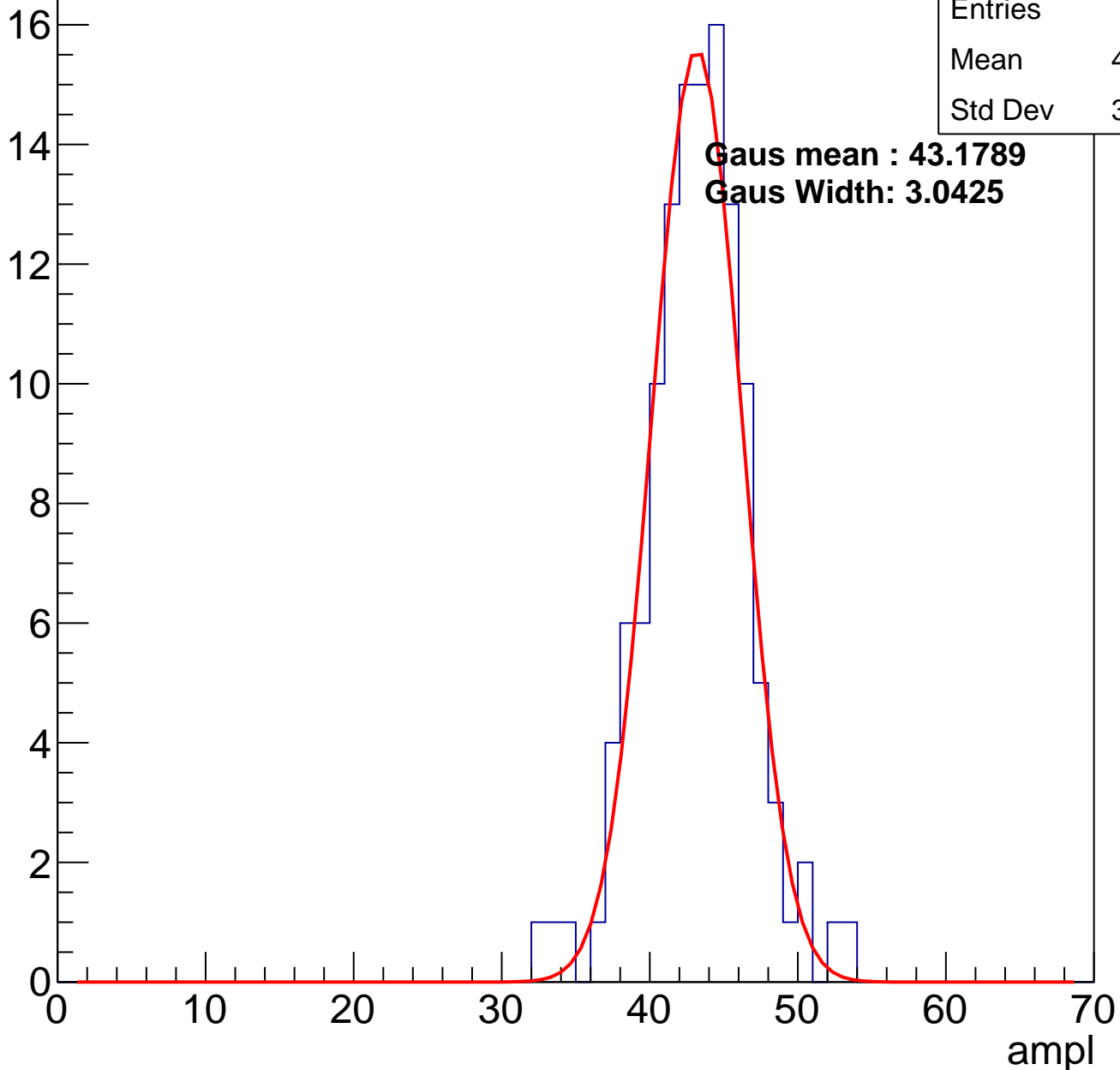
ampl



# B0L101S, U23-ch58, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

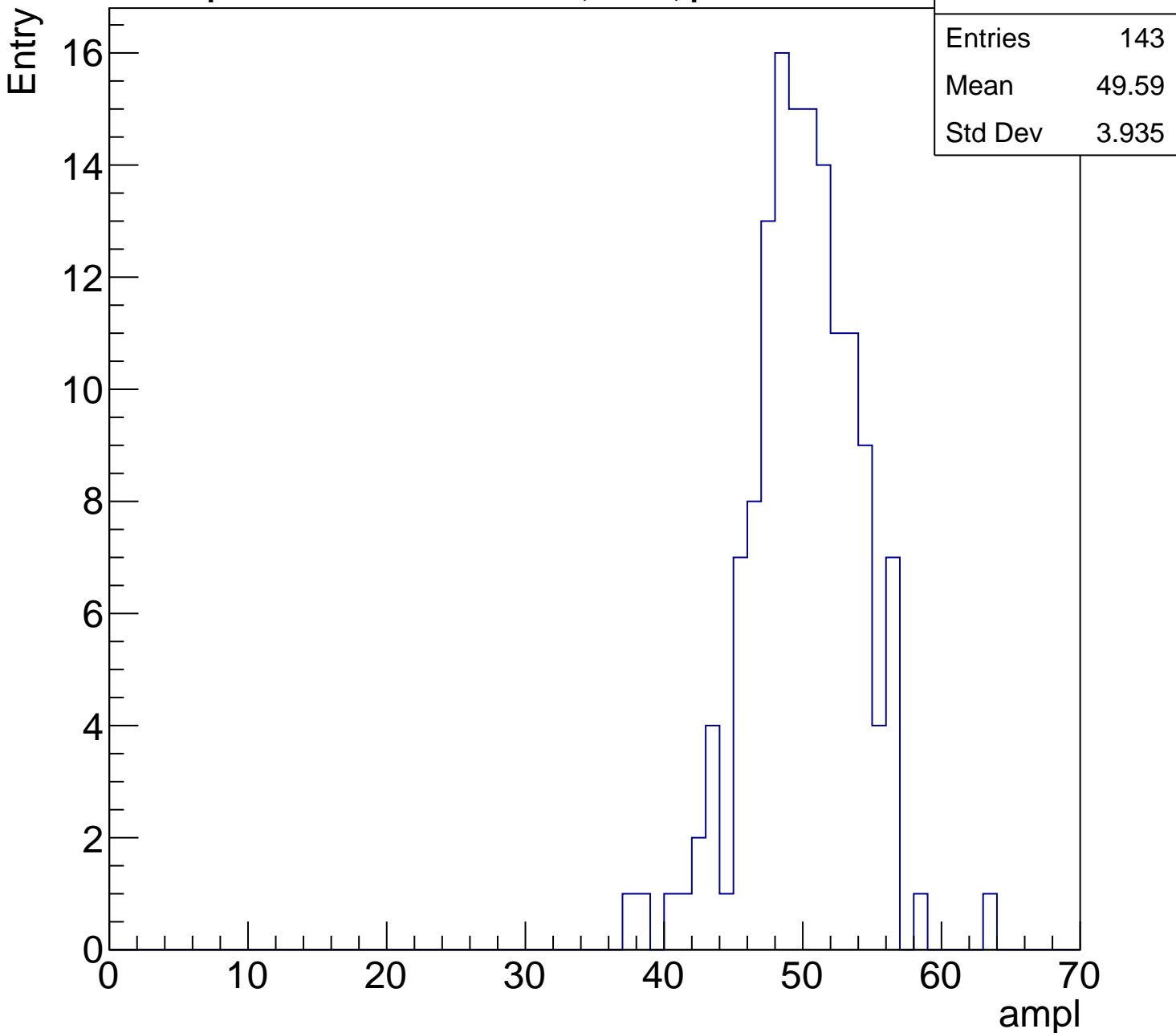
Entries	143
Mean	49.59
Std Dev	3.935

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	55.41
Std Dev	3.59

Entry

12

10

8

6

4

2

0

0

10

20

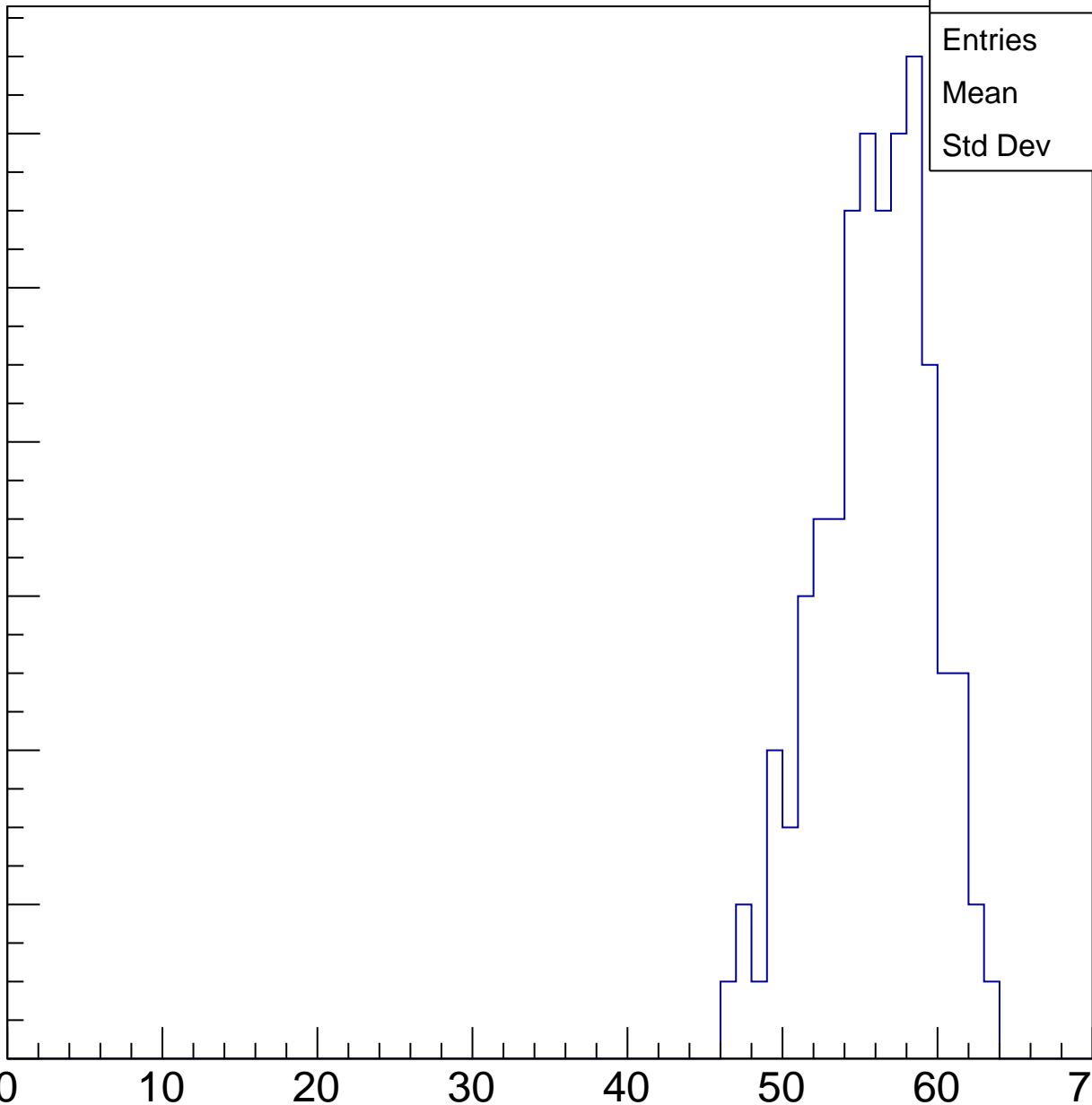
30

40

50

60

ampl



# B0L101S, U23-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

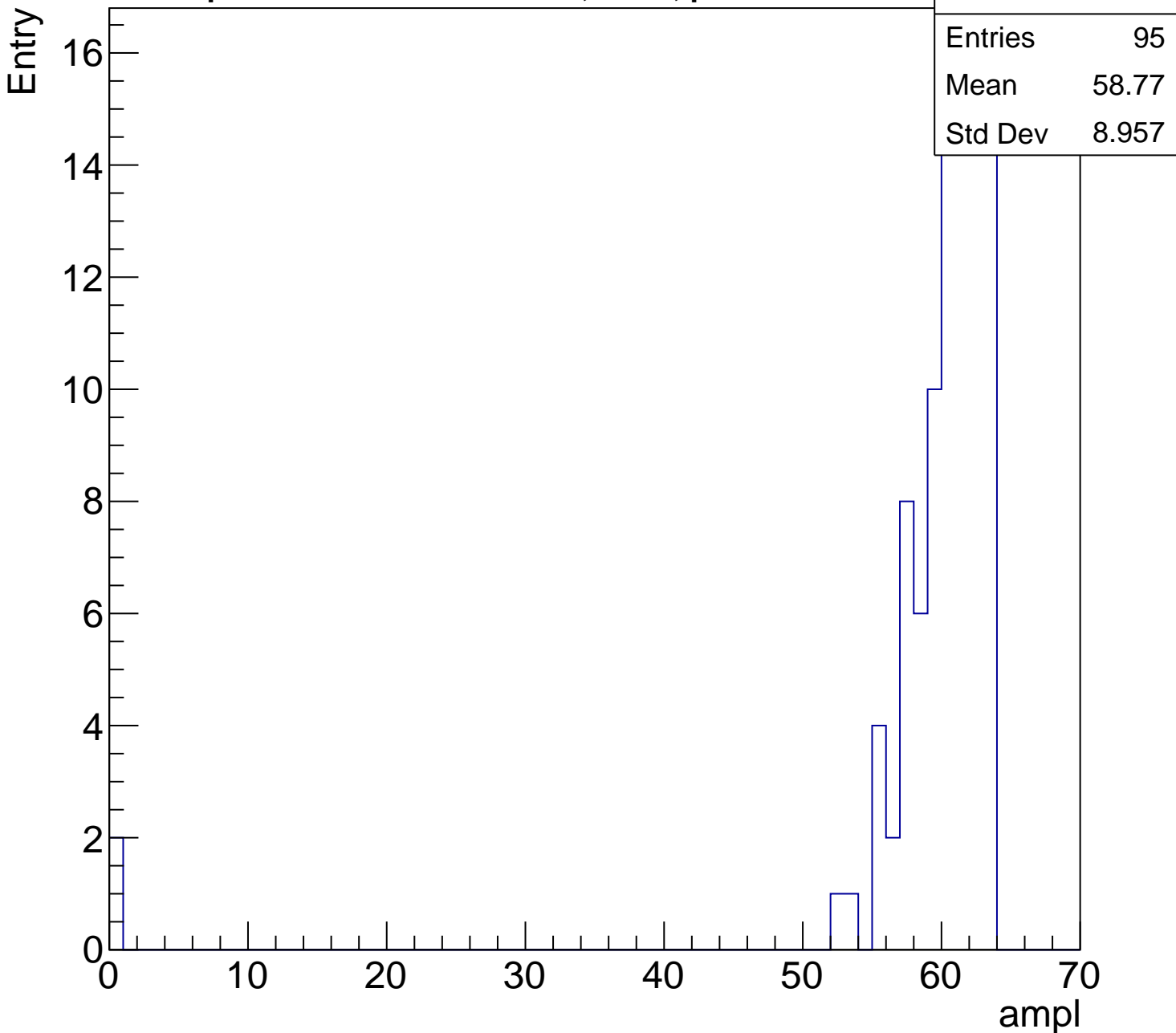
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	95
Mean	58.77
Std Dev	8.957

ampl

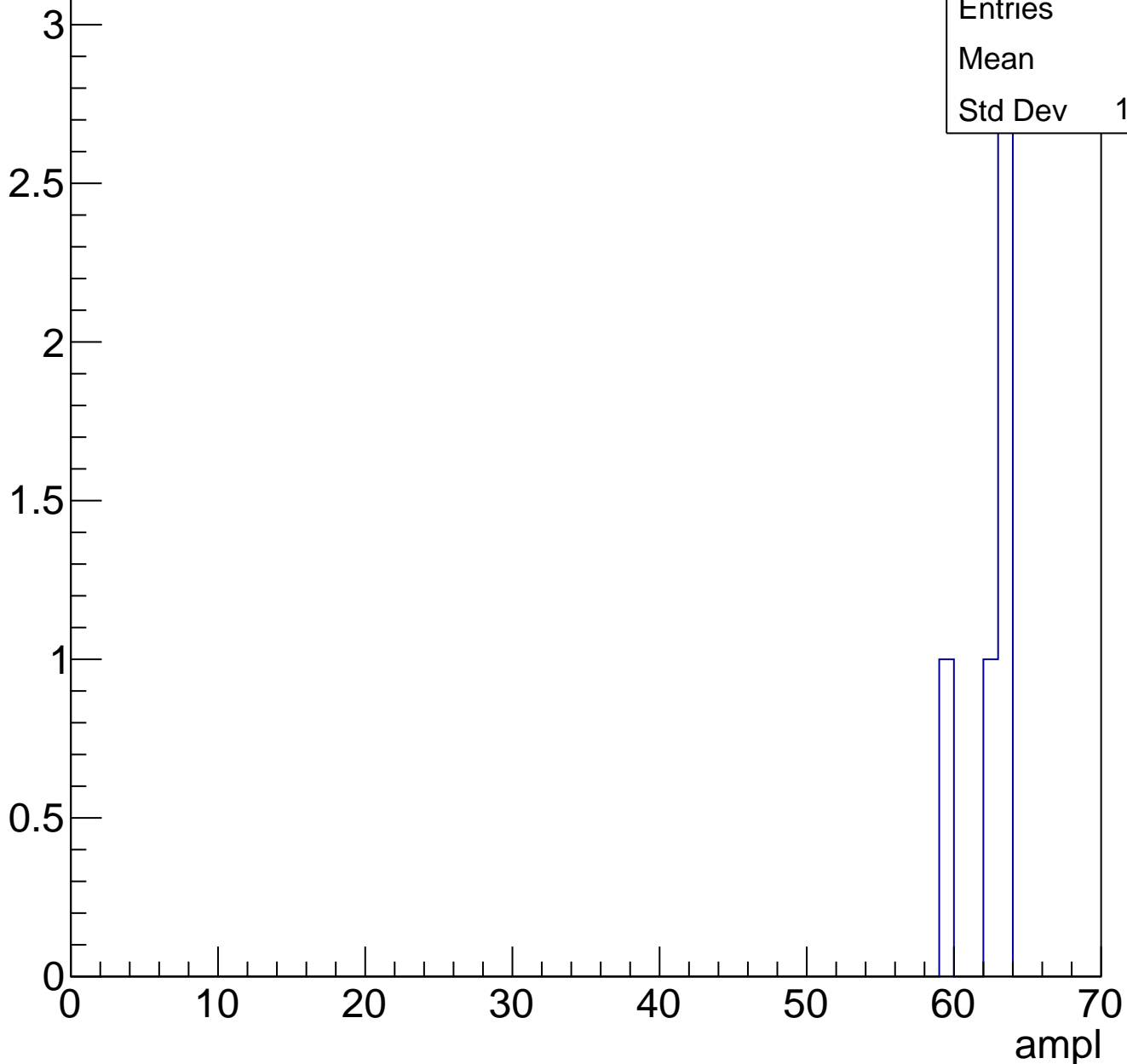
0 10 20 30 40 50 60 70



# B0L101S, U23-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L101S, U23-ch59, adc0

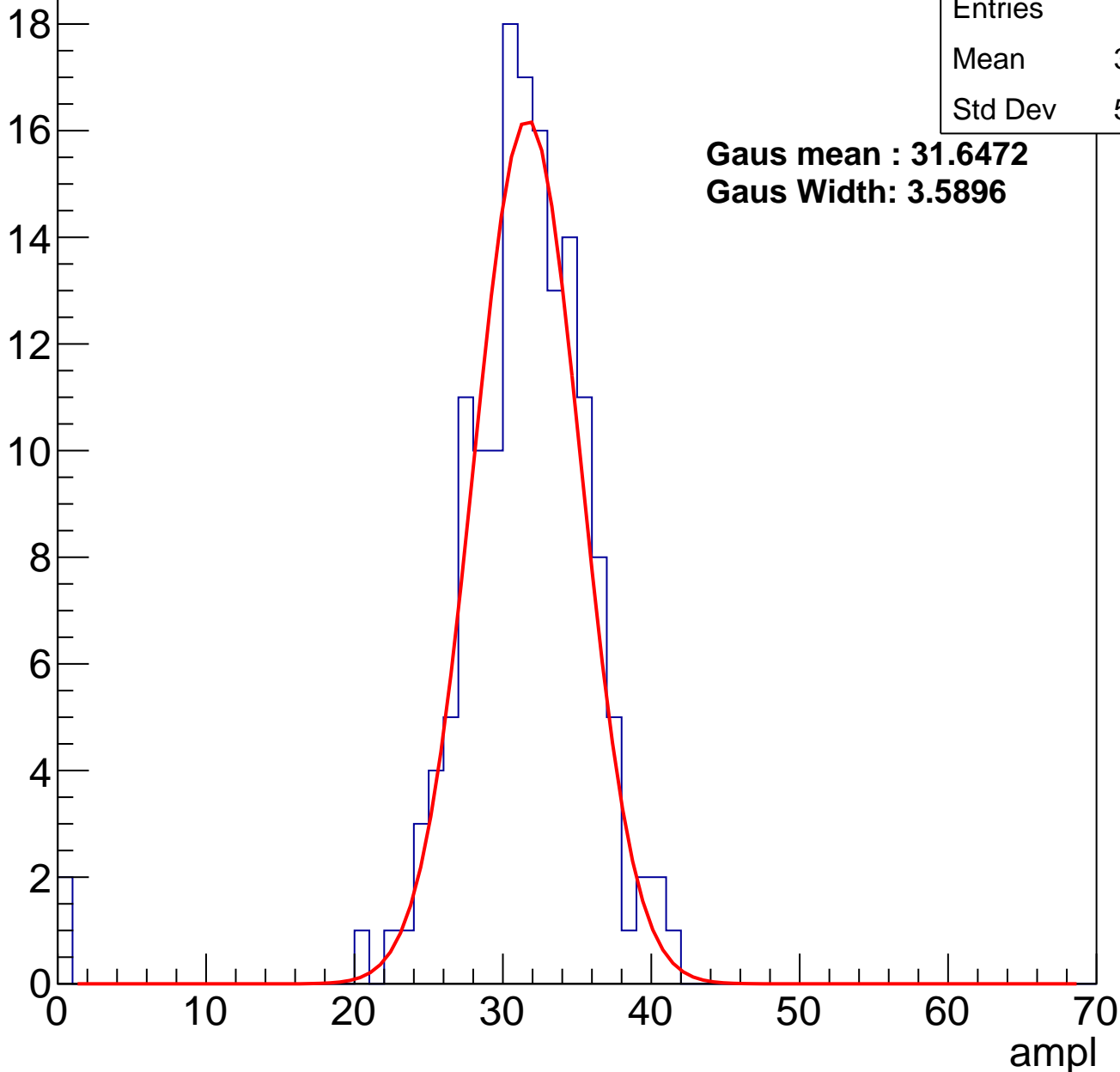
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	30.85
Std Dev	5.135

**Gaus mean : 31.6472**

**Gaus Width: 3.5896**

Entry



# B0L101S, U23-ch59, adc1

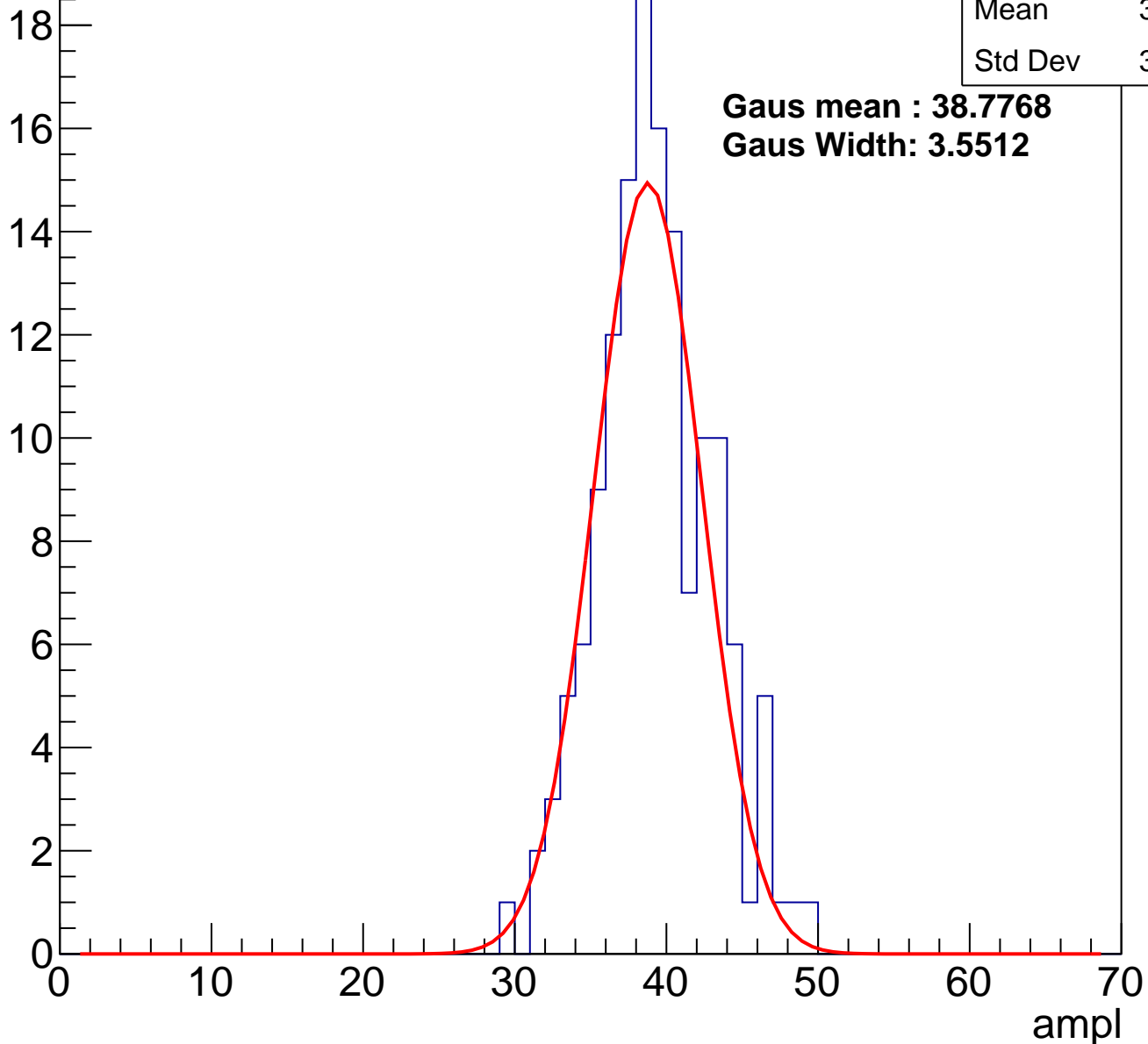
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	38.78
Std Dev	3.724

**Gaus mean : 38.7768**

**Gaus Width: 3.5512**

Entry



# B0L101S, U23-ch59, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	44.96
Std Dev	3.409

**Gaus mean : 45.3043**

**Gaus Width: 3.5161**

Entry

12

10

8

6

4

2

0

0

10

20

30

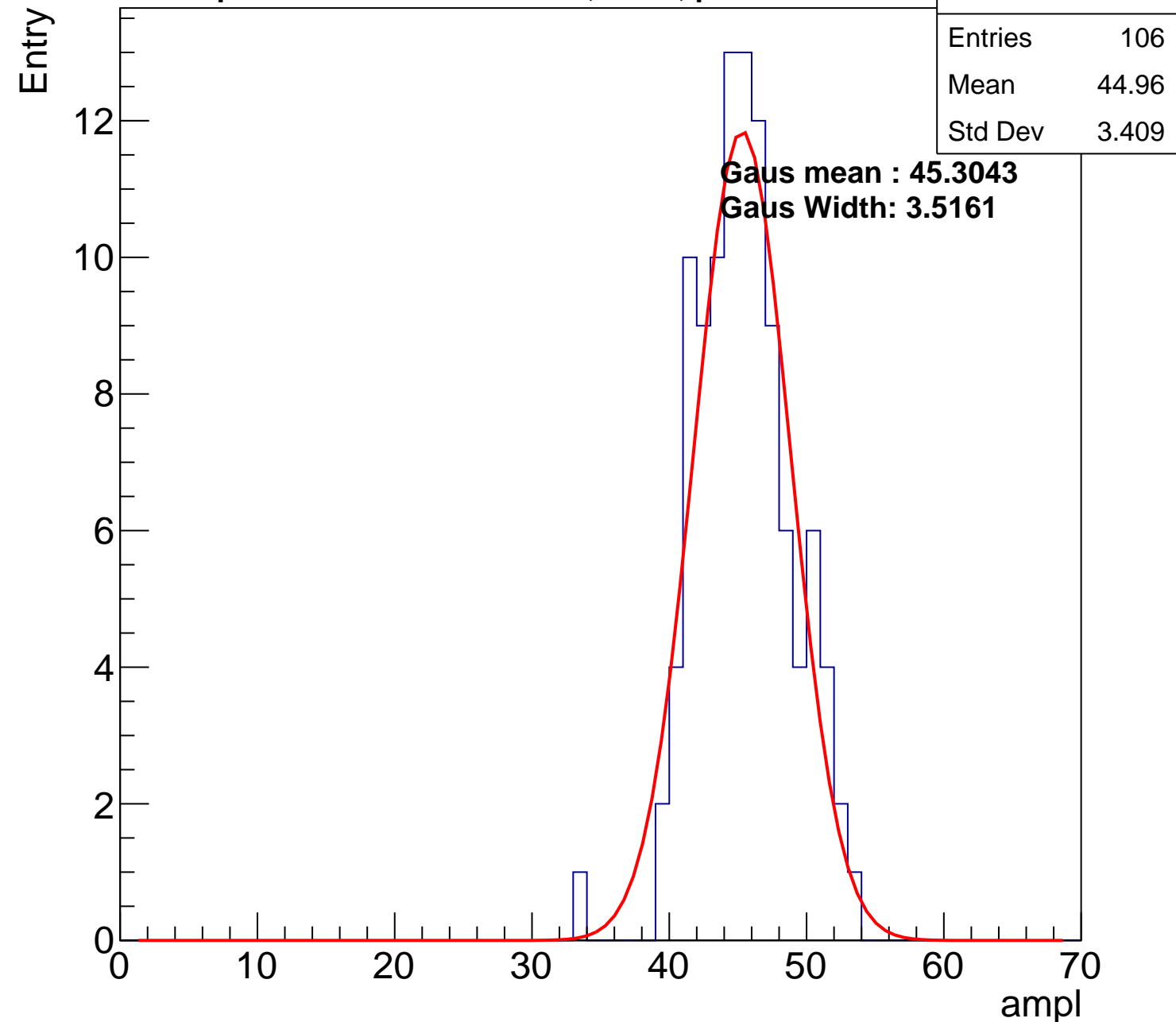
40

50

60

ampl

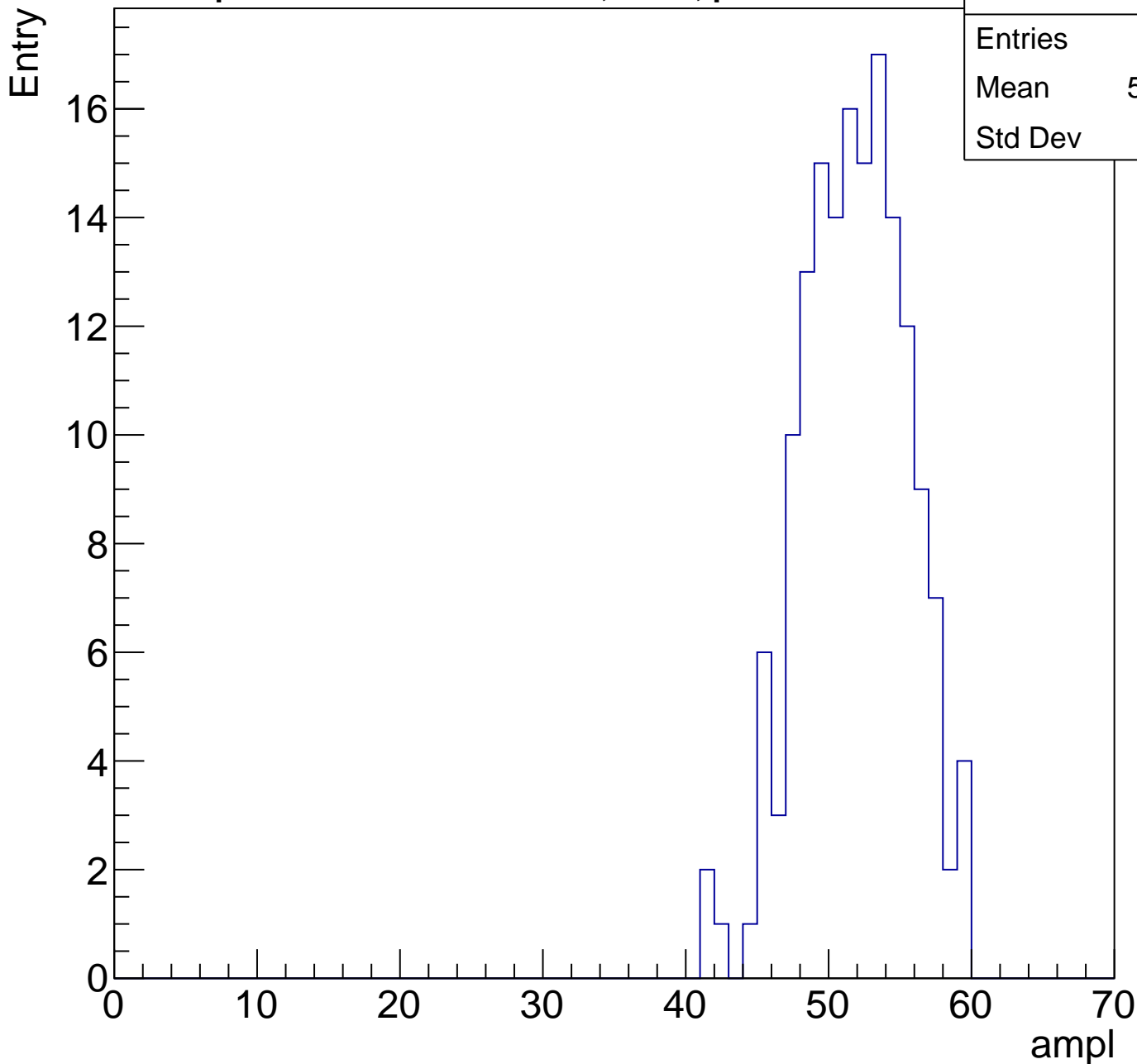
70



# B0L101S, U23-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	51.39
Std Dev	3.68



# B0L101S, U23-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	82
Mean	57.83
Std Dev	2.762

ampl

0 10 20 30 40 50 60 70

# B0L101S, U23-ch59, adc5

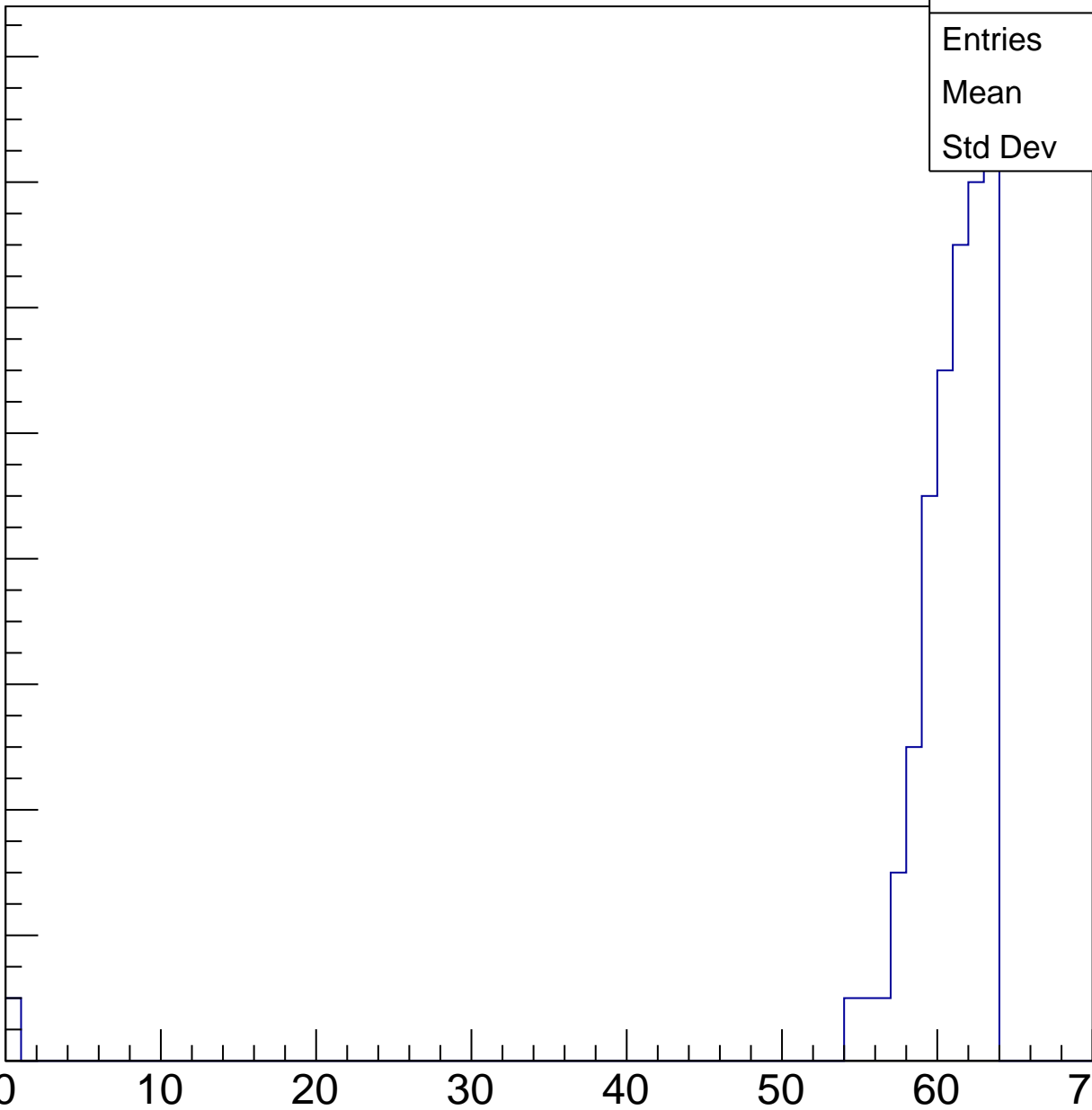
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	75
Mean	59.81
Std Dev	7.251

ampl



# B0L101S, U23-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

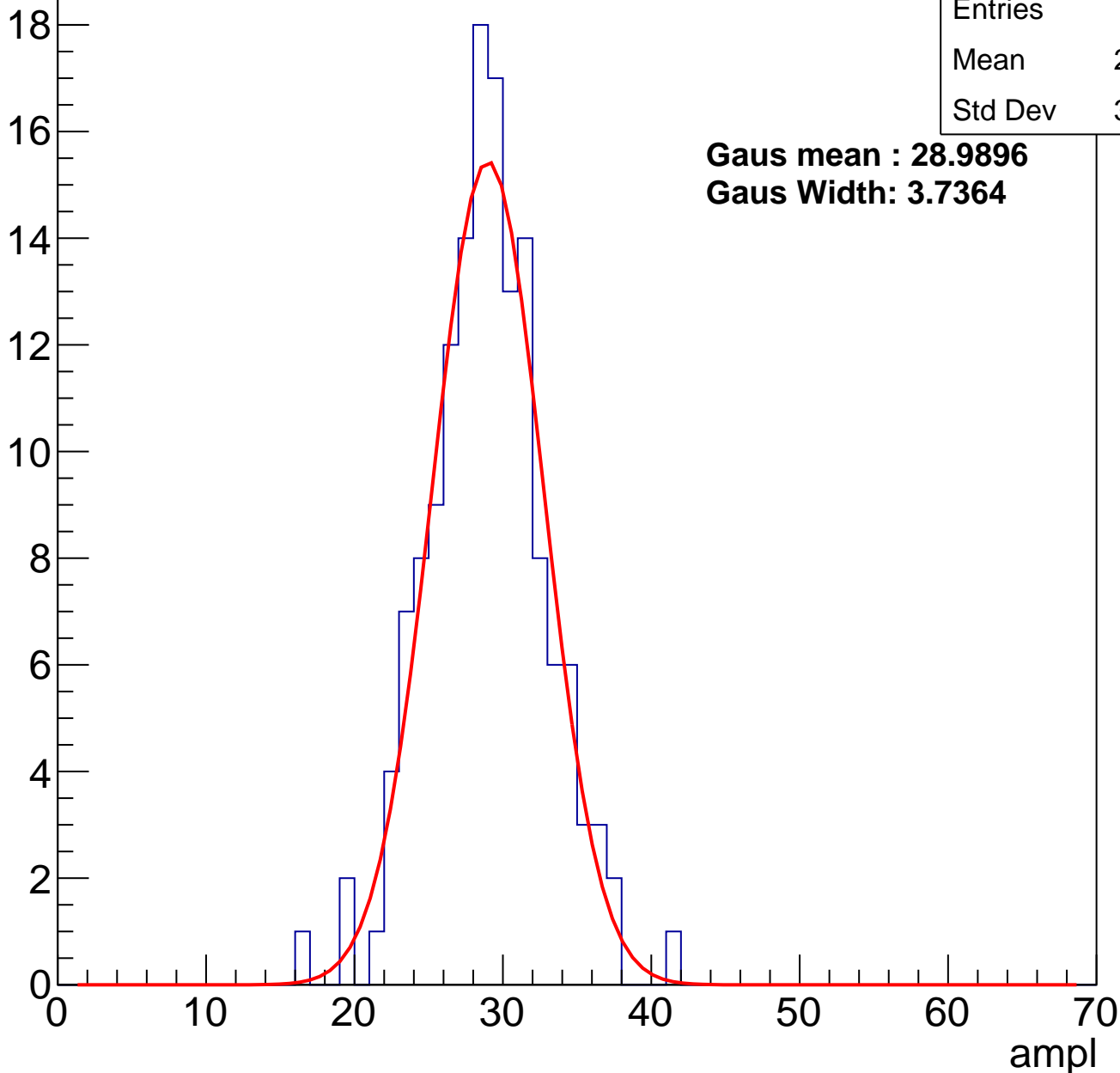


Entries	2
Mean	0
Std Dev	0

# B0L101S, U23-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

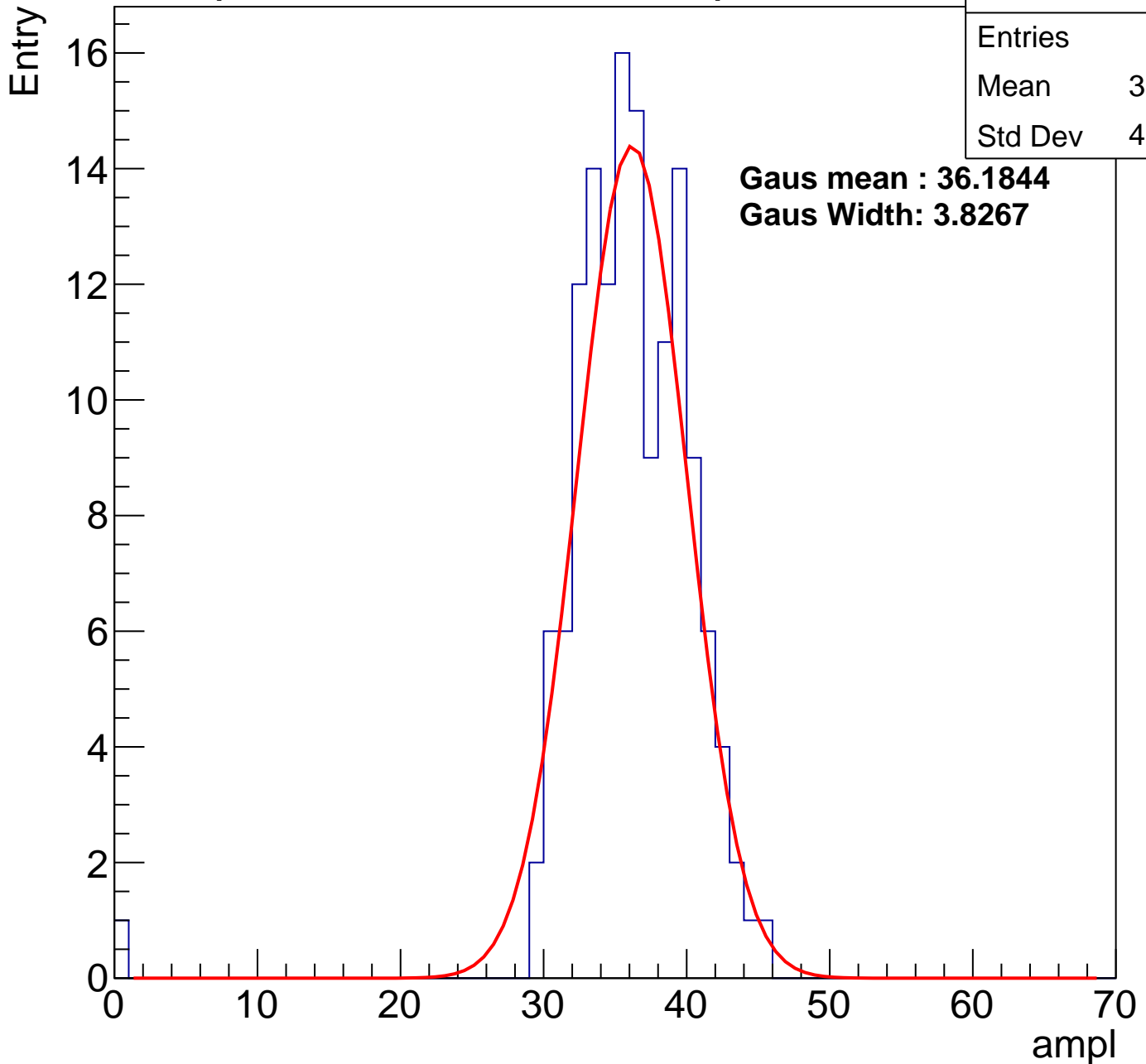


# B0L101S, U23-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	35.63
Std Dev	4.599

**Gaus mean : 36.1844**  
**Gaus Width: 3.8267**



# B0L101S, U23-ch60, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	42.49
Std Dev	3.636

**Gaus mean : 43.1487**

**Gaus Width: 3.6981**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

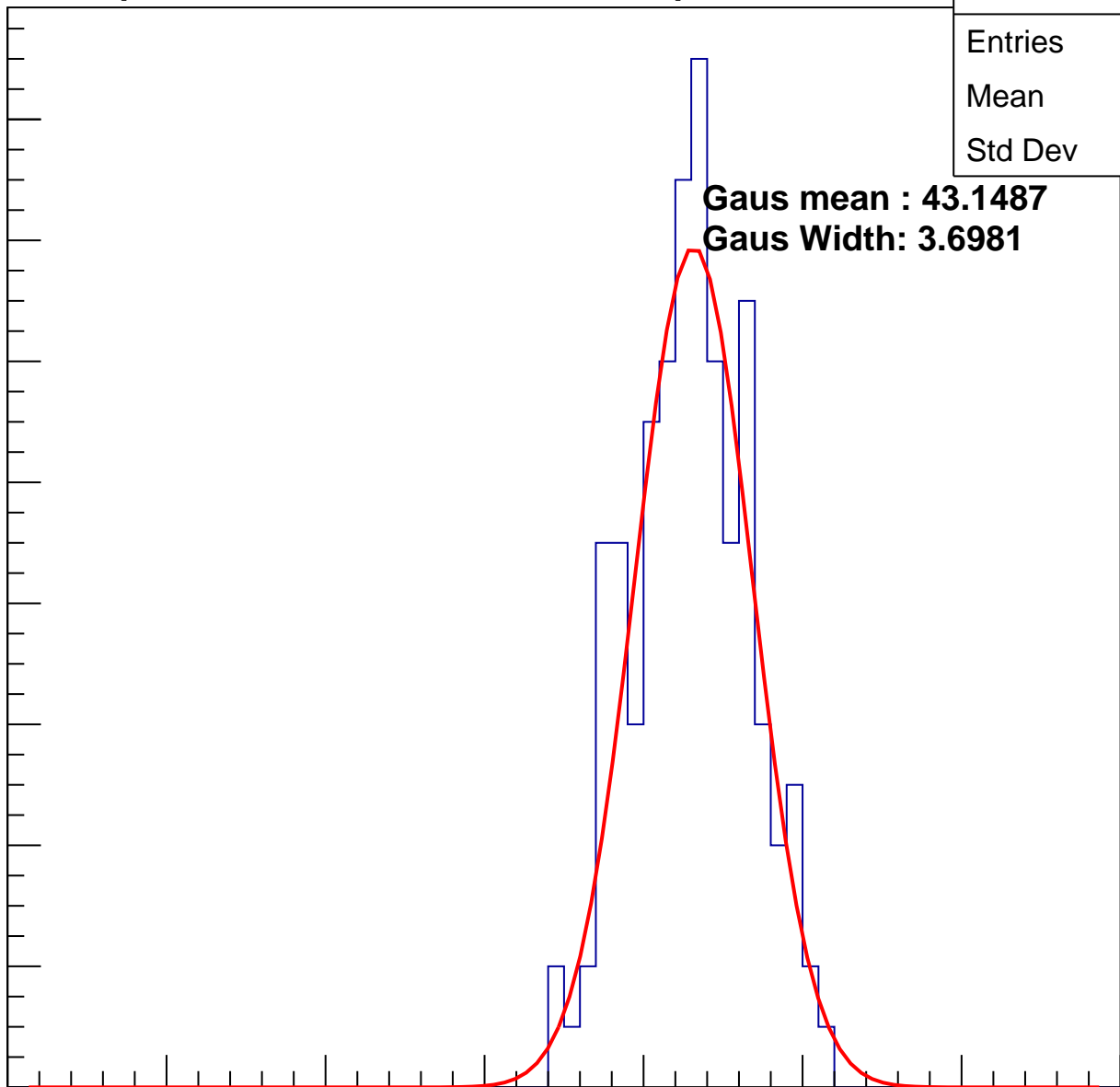
30

40

50

60

ampl



# B0L101S, U23-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

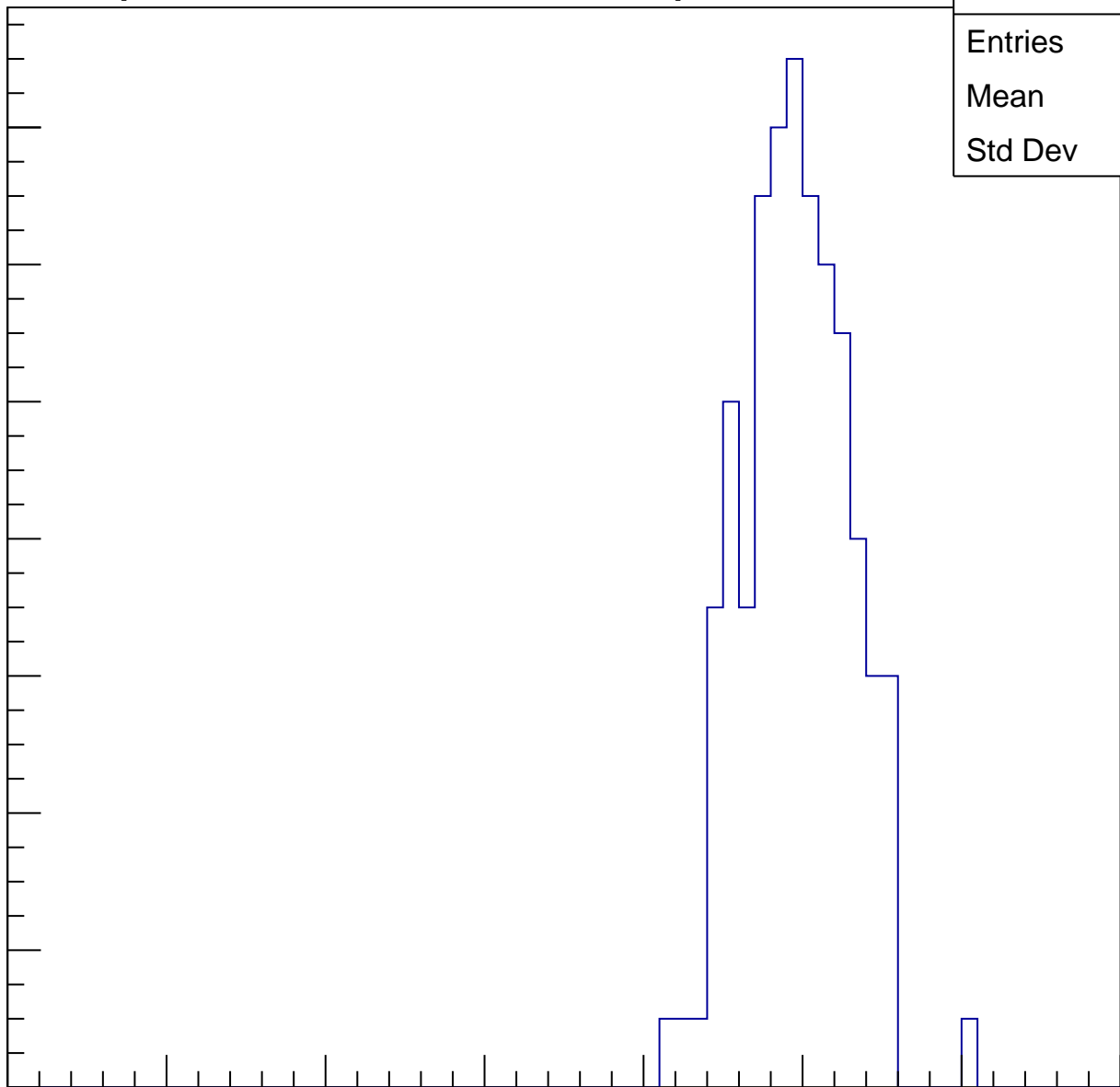
Entries	126
Mean	49.17
Std Dev	3.314

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

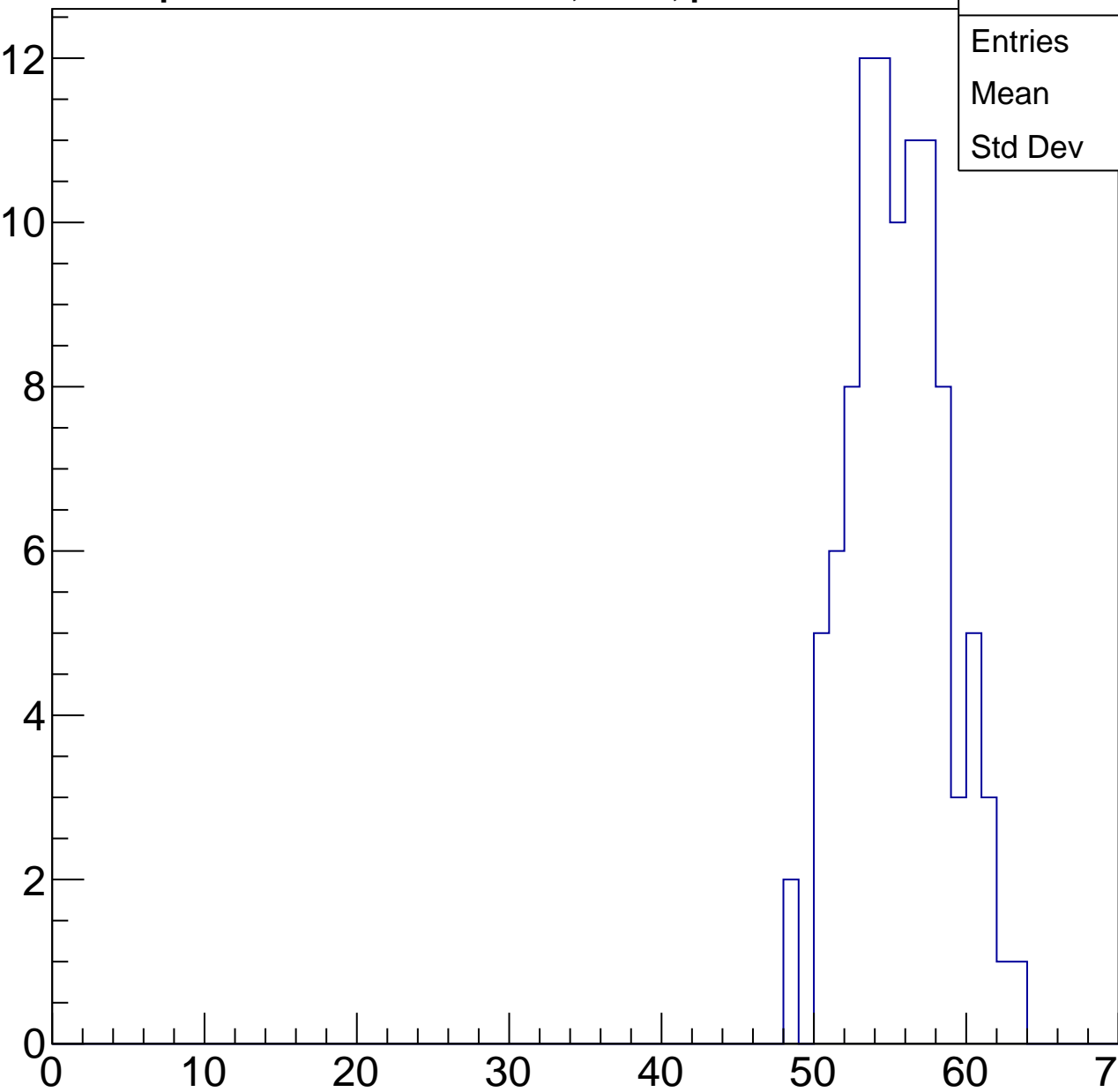
Entries	98
Mean	55.04
Std Dev	3.149

Entry

12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch60, adc5

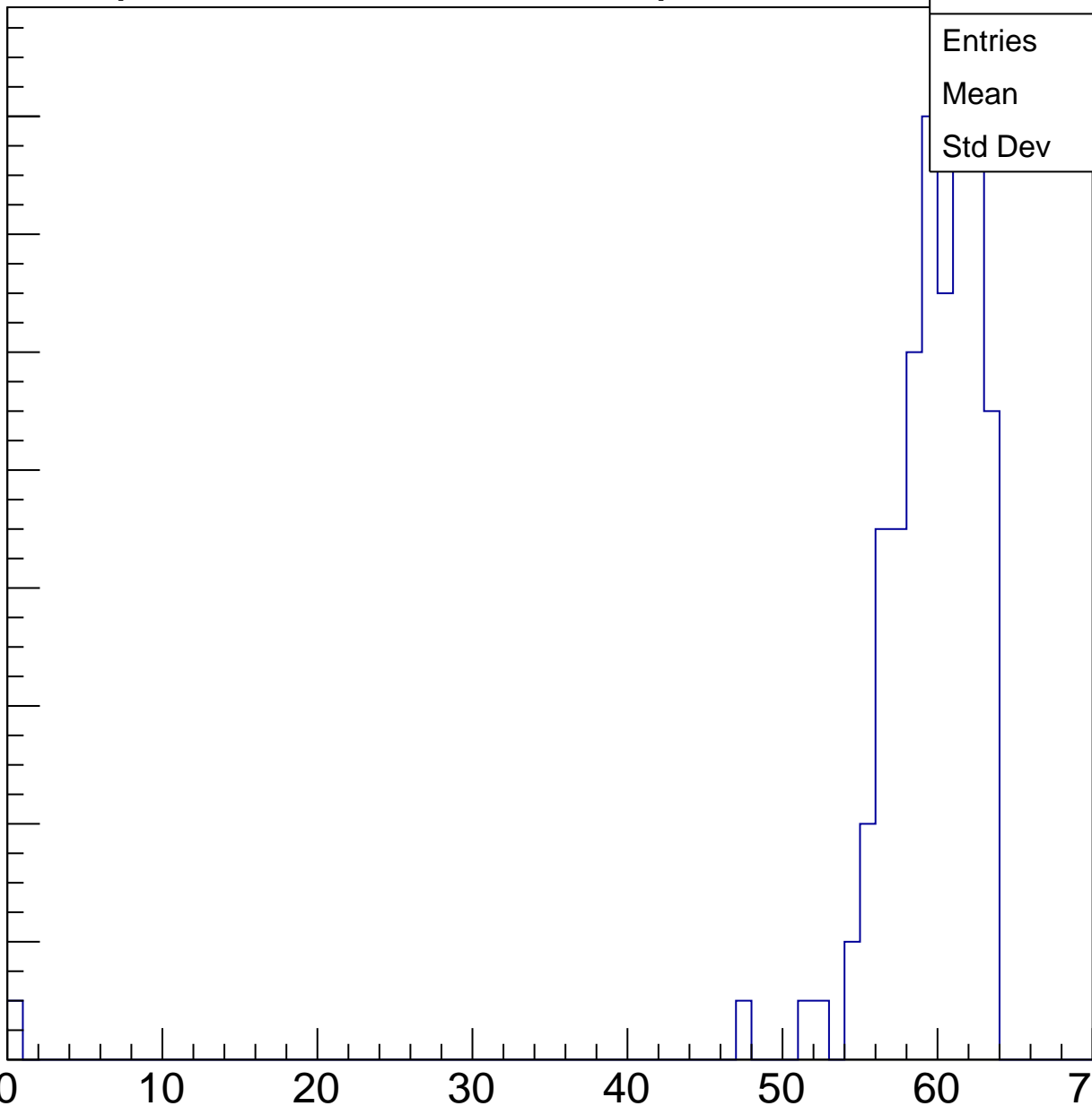
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	113
Mean	58.74
Std Dev	6.226

ampl

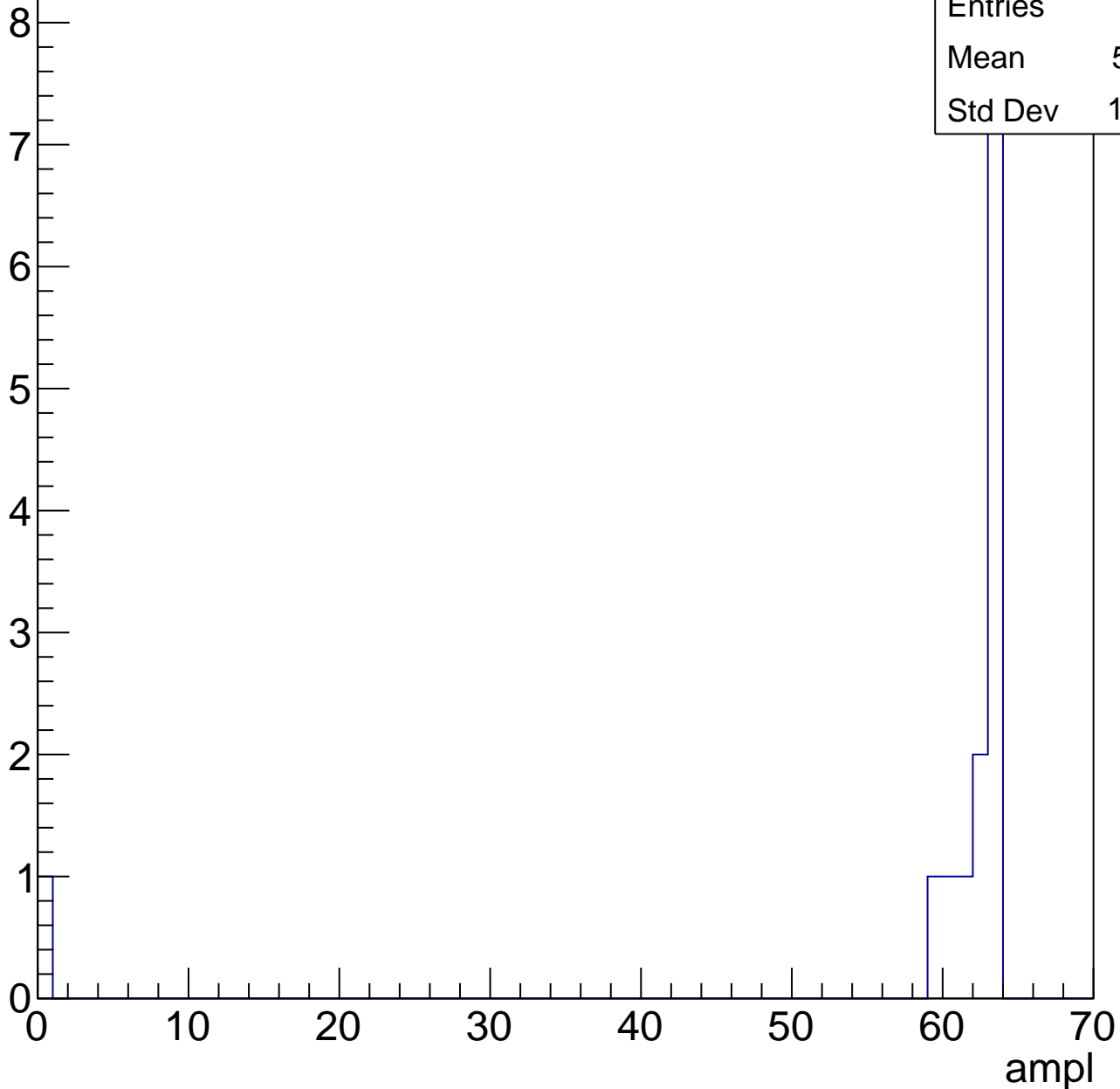


# B0L101S, U23-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.71
Std Dev	16.06





# B0L101S, U23-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch61, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	28.7
Std Dev	3.603

**Gaus mean : 29.2306**

**Gaus Width: 3.7769**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

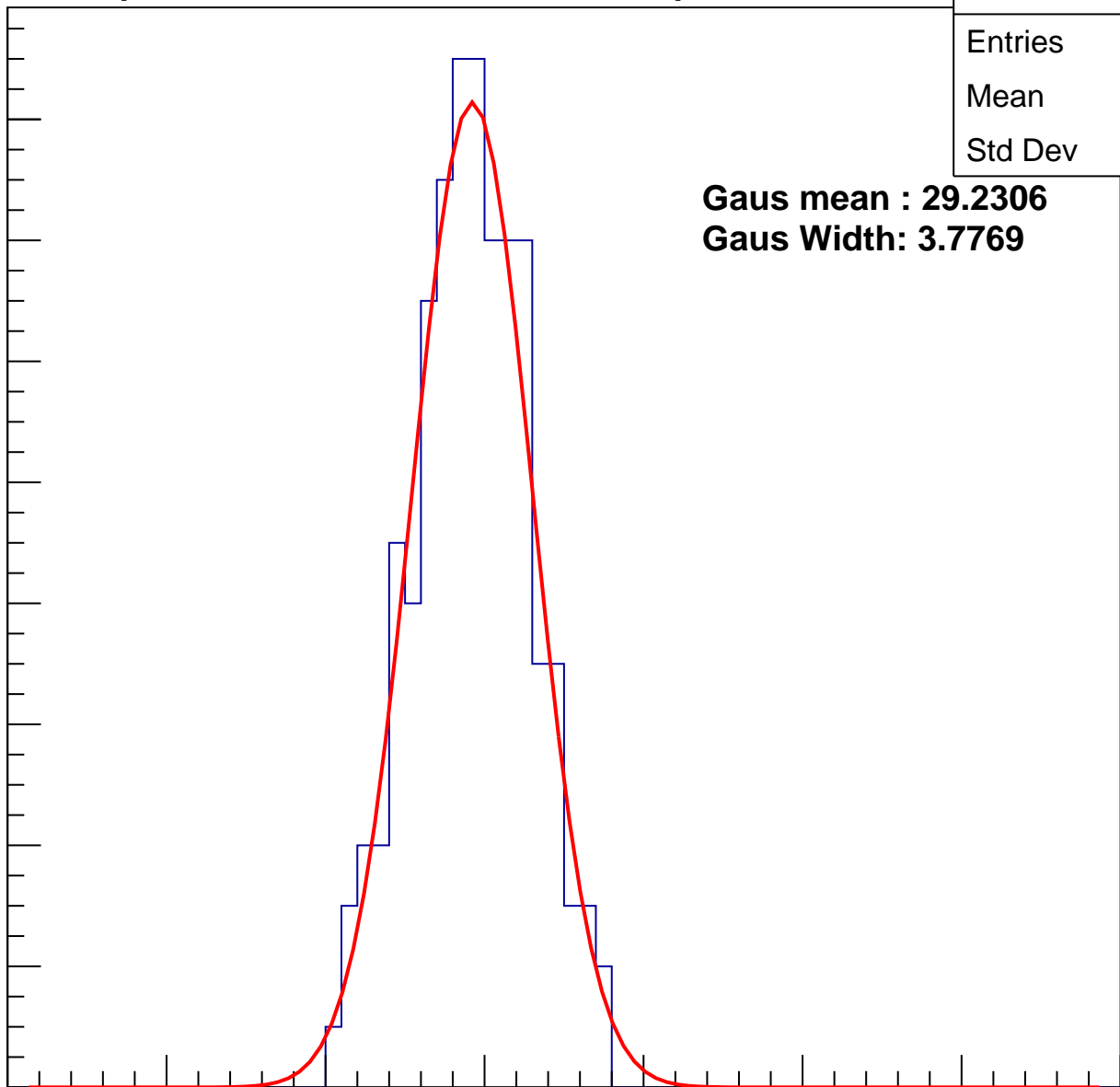
40

50

60

70

ampl



# B0L101S, U23-ch61, adc1

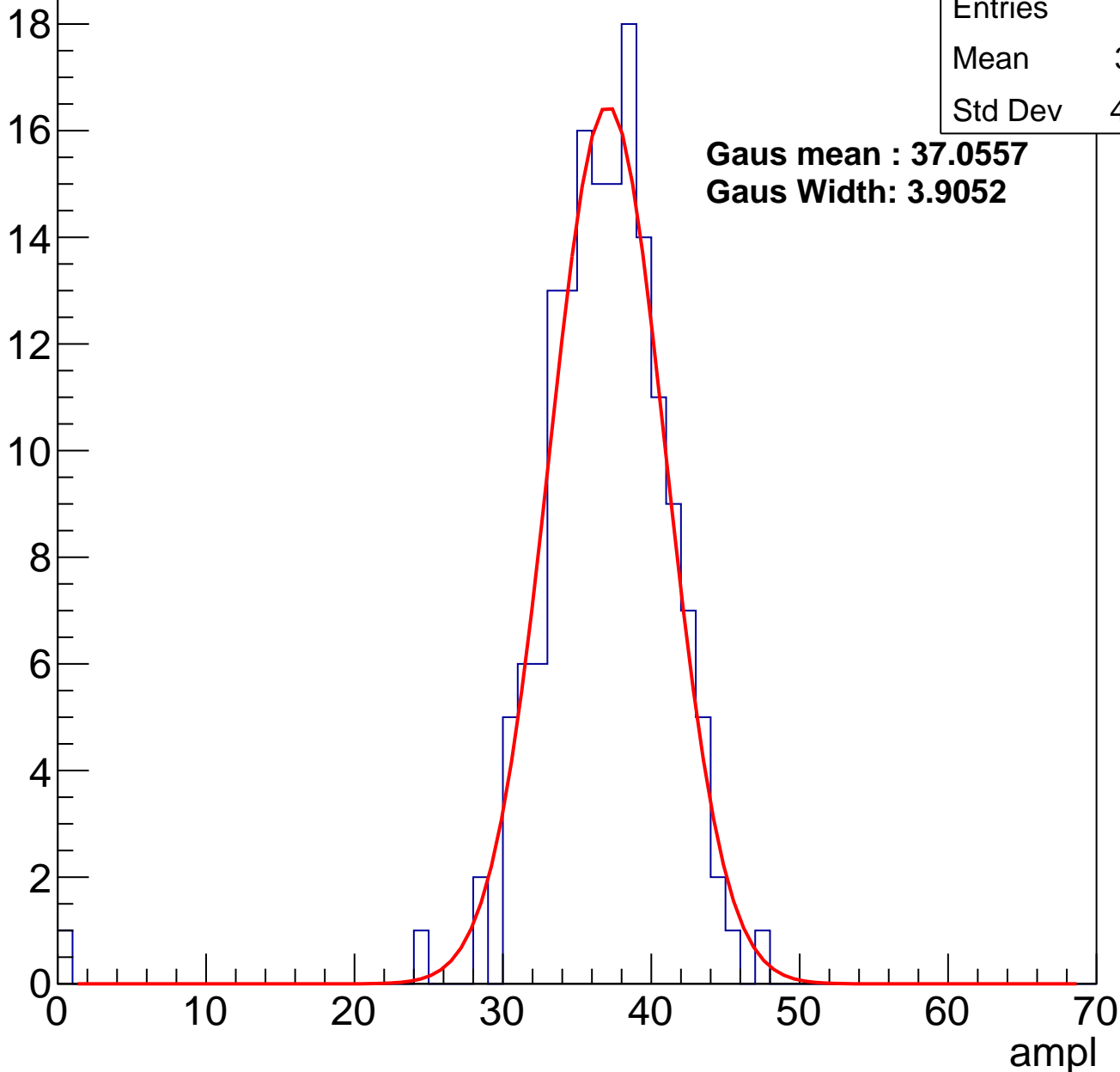
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	36.41
Std Dev	4.724

**Gaus mean : 37.0557**

**Gaus Width: 3.9052**

Entry



# B0L101S, U23-ch61, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

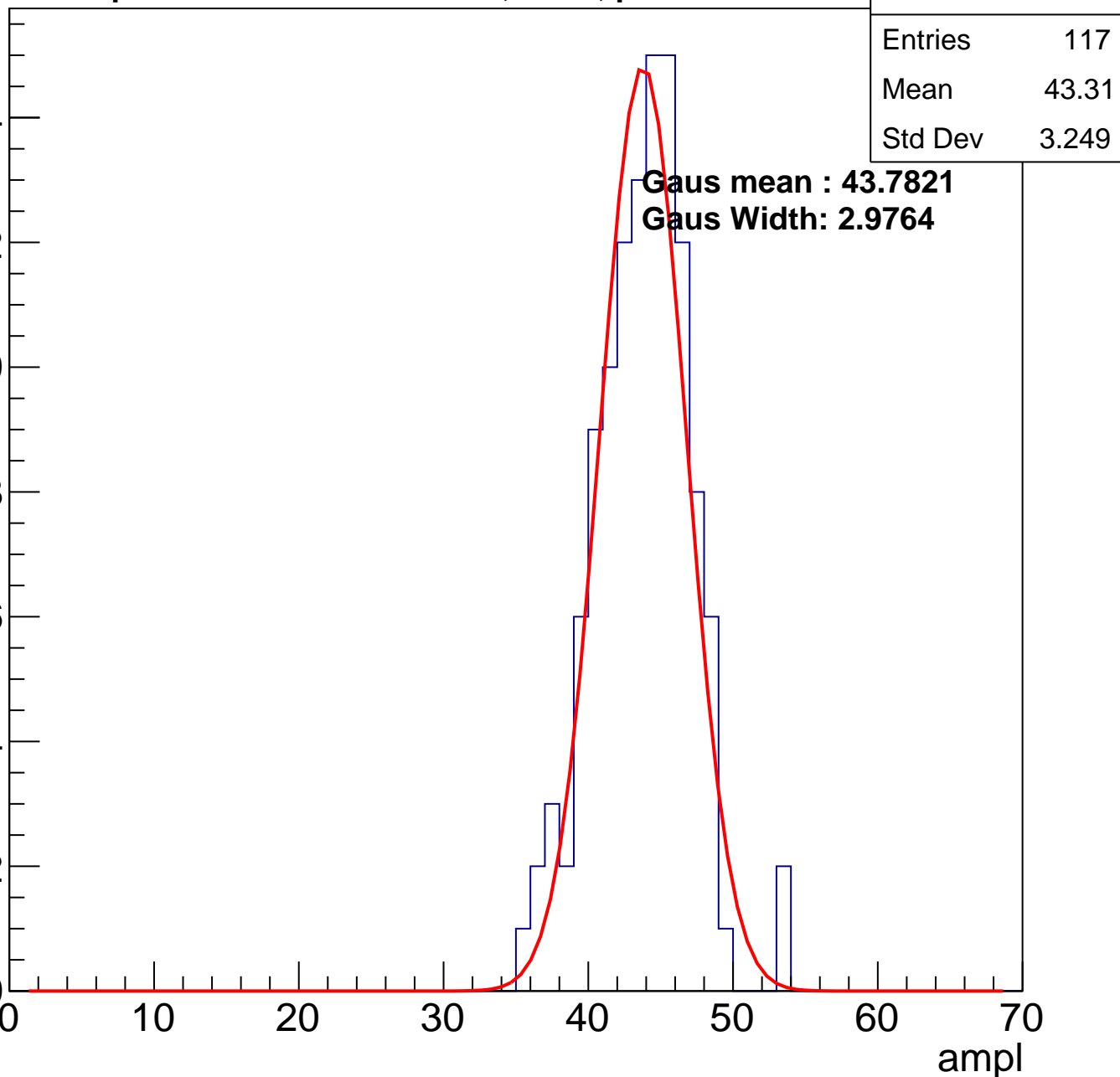
43.31

Std Dev

3.249

**Gaus mean : 43.7821**

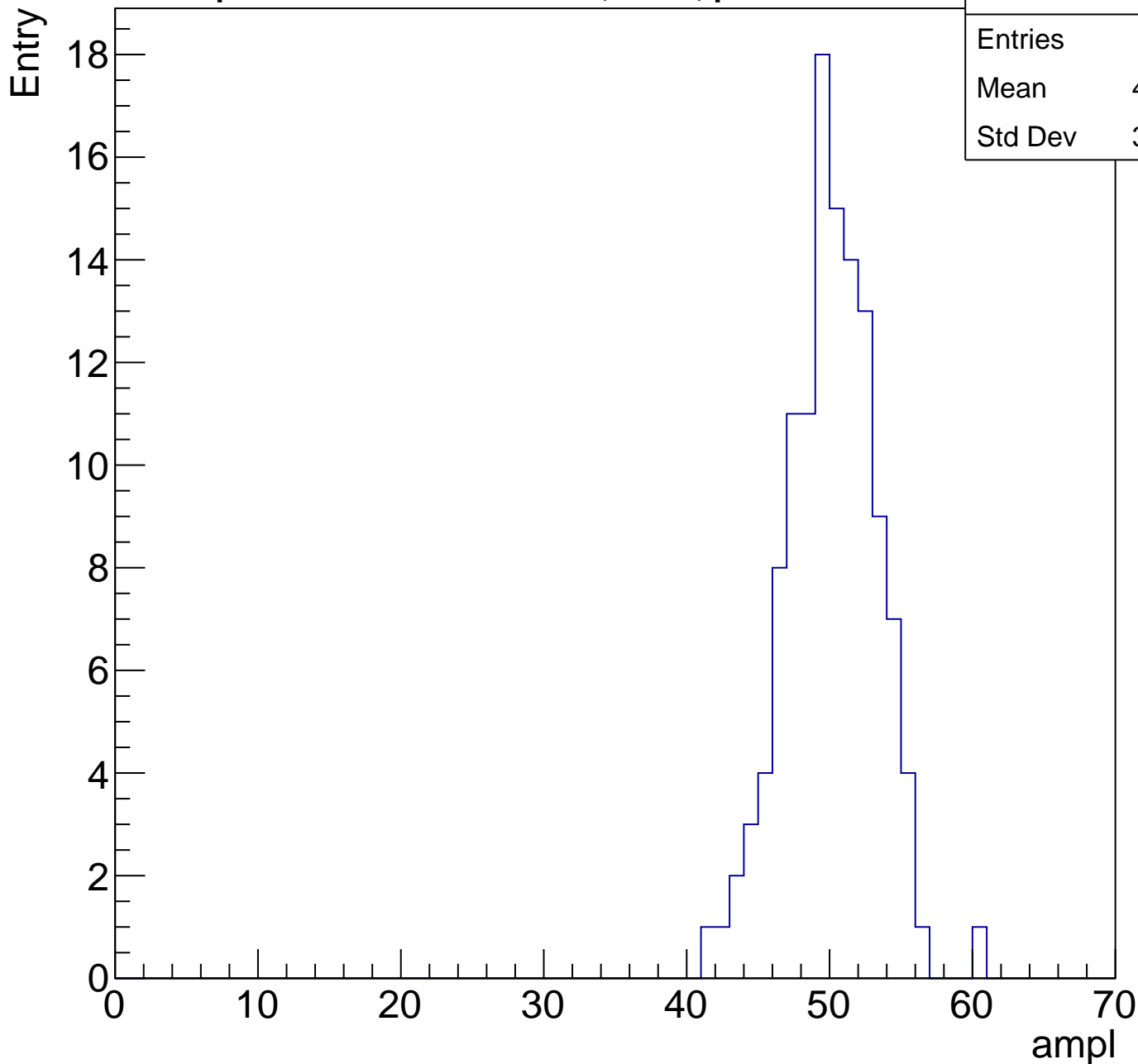
**Gaus Width: 2.9764**



# B0L101S, U23-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	49.65
Std Dev	3.149



# B0L101S, U23-ch61, adc4

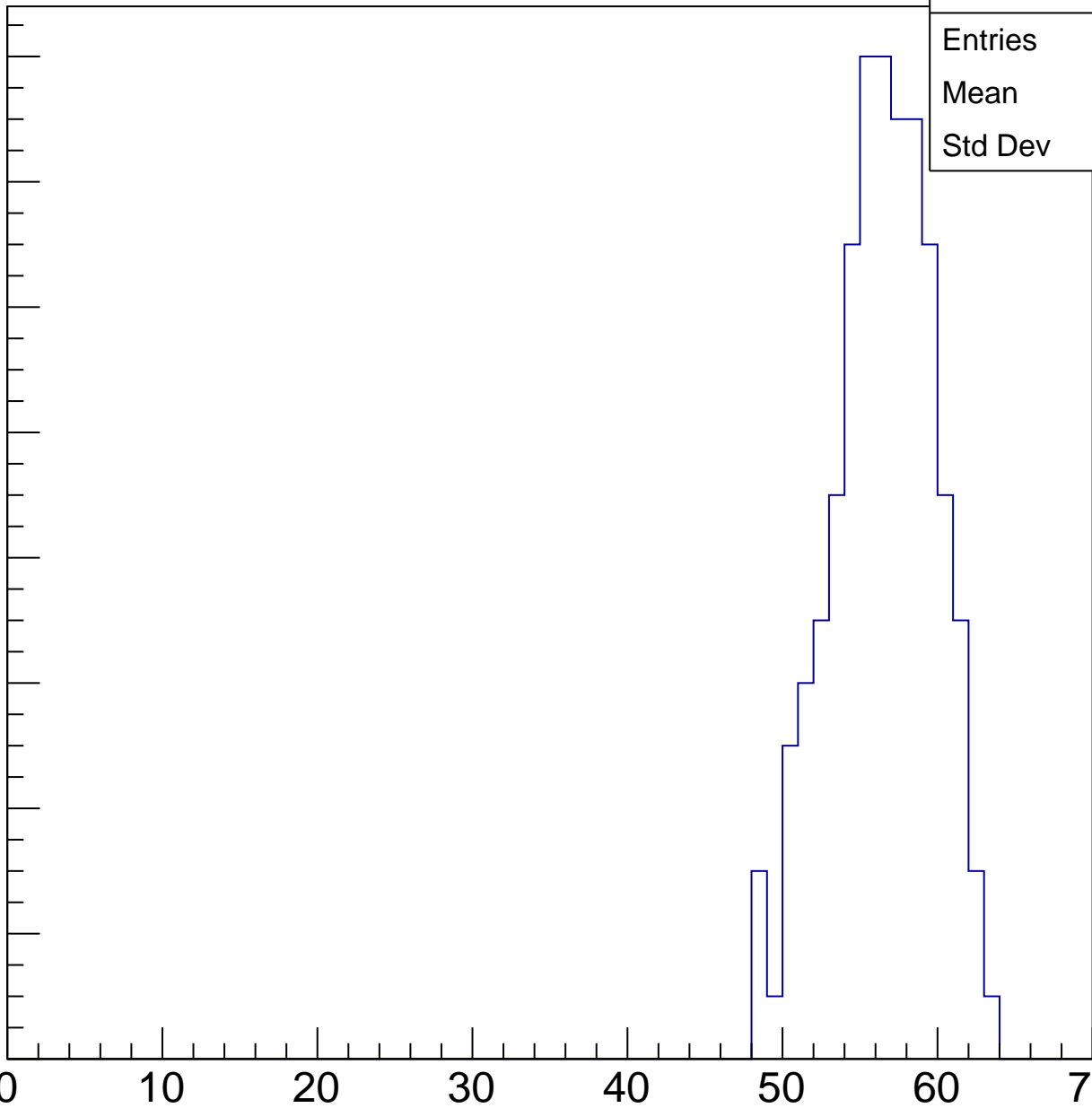
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	139
Mean	55.94
Std Dev	3.285

ampl



# B0L101S, U23-ch61, adc5

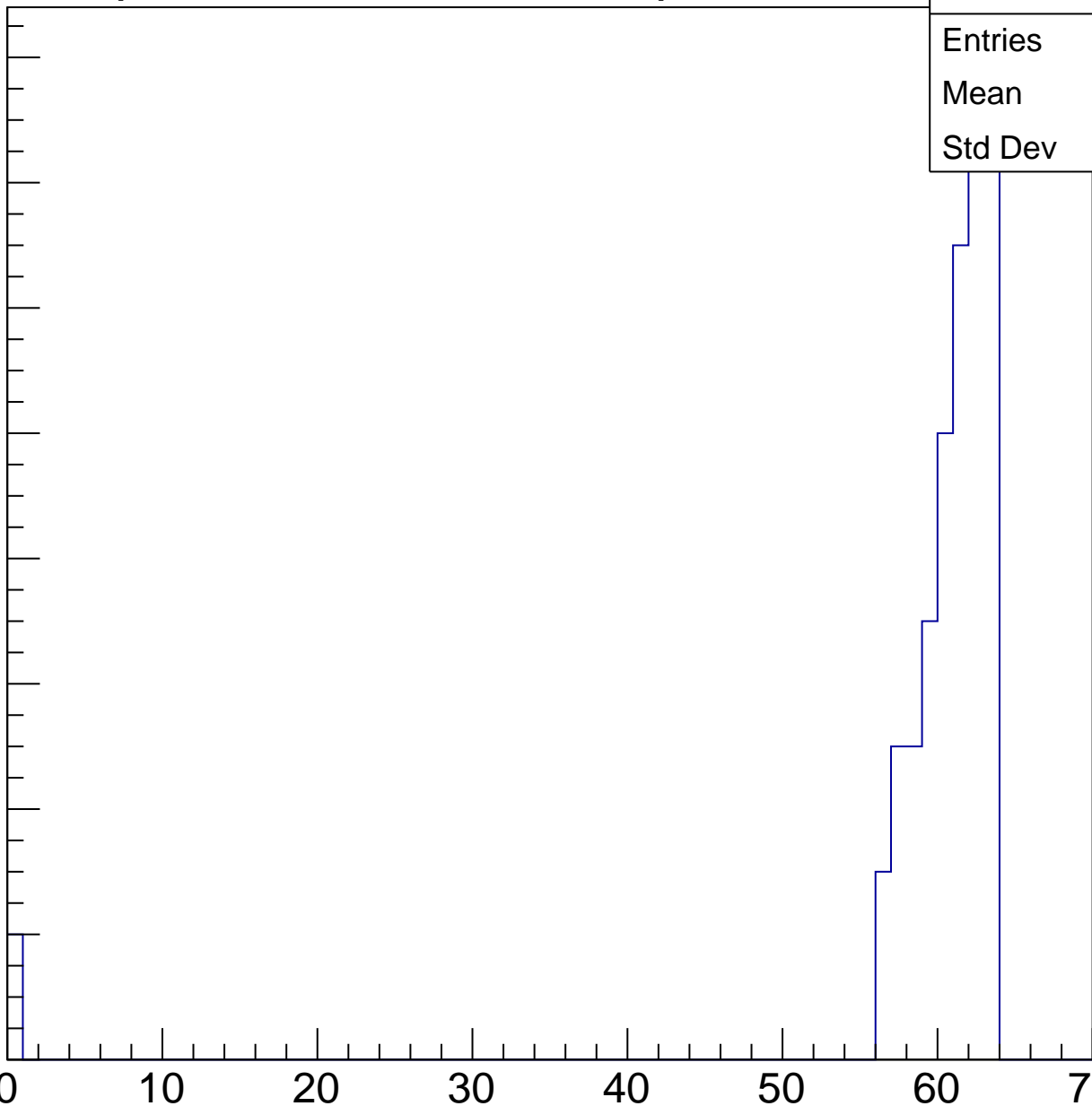
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	76
Mean	59.03
Std Dev	9.907

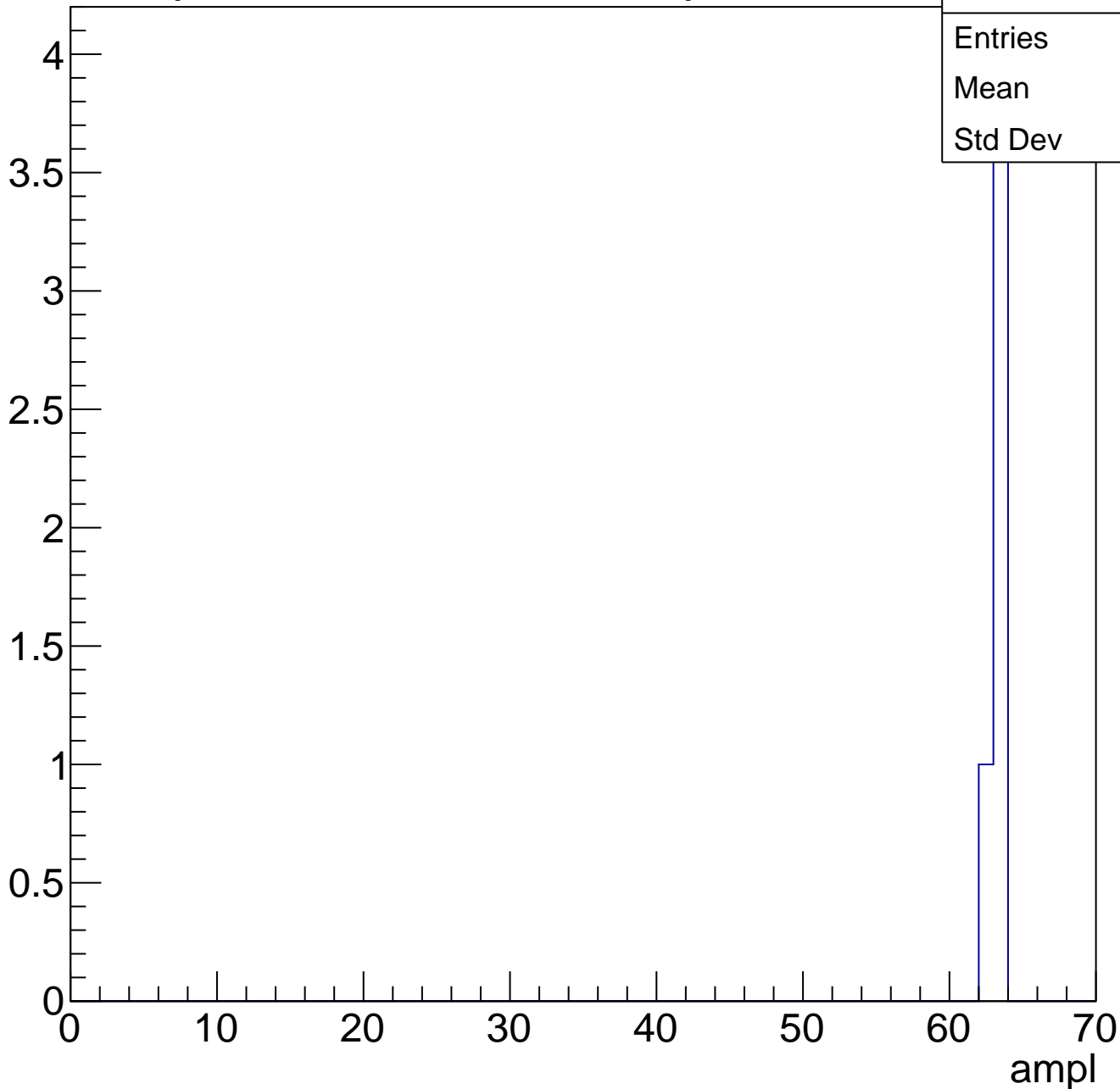
ampl



# B0L101S, U23-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

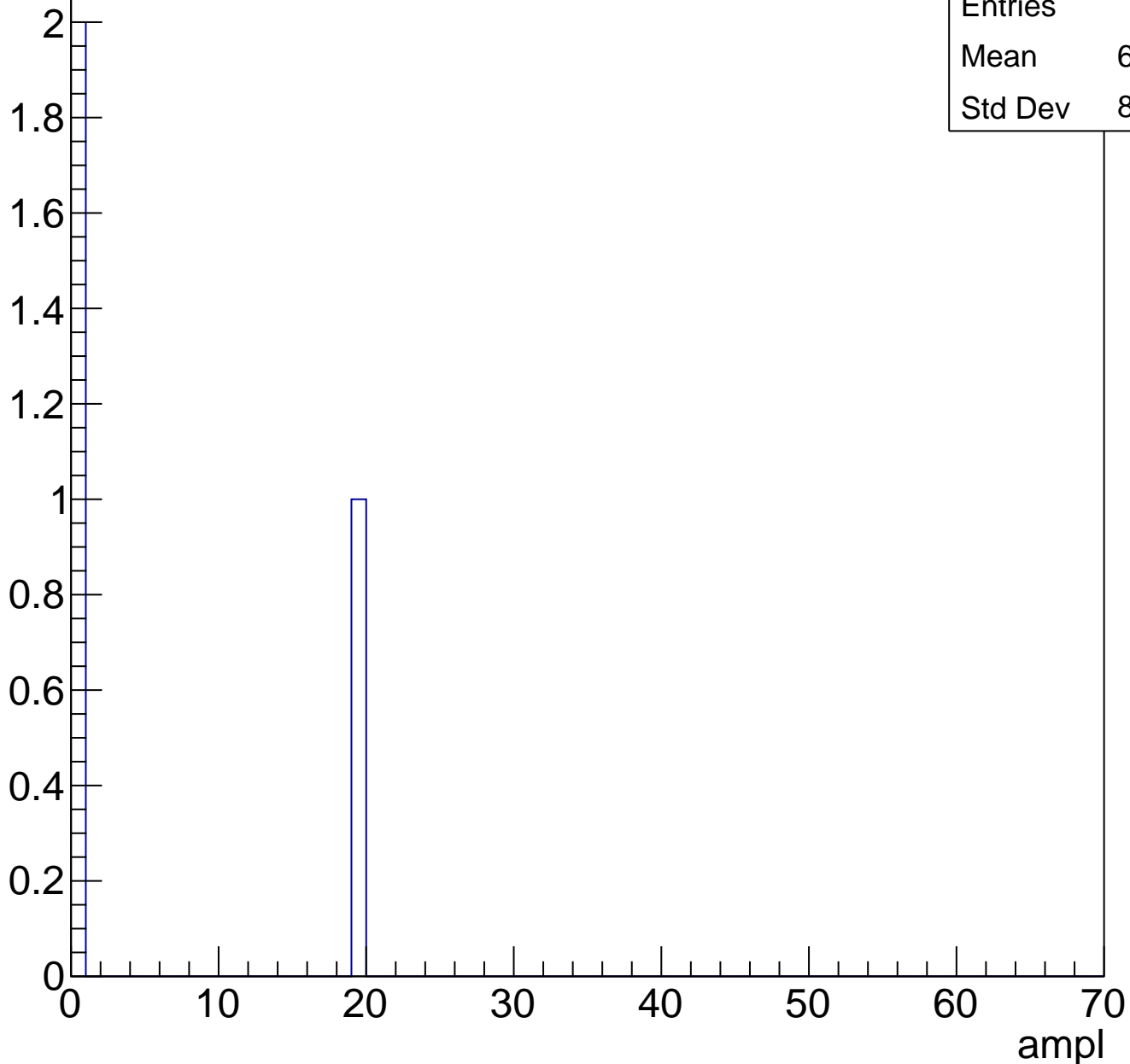




# B0L101S, U23-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

# B0L101S, U23-ch62, adc0

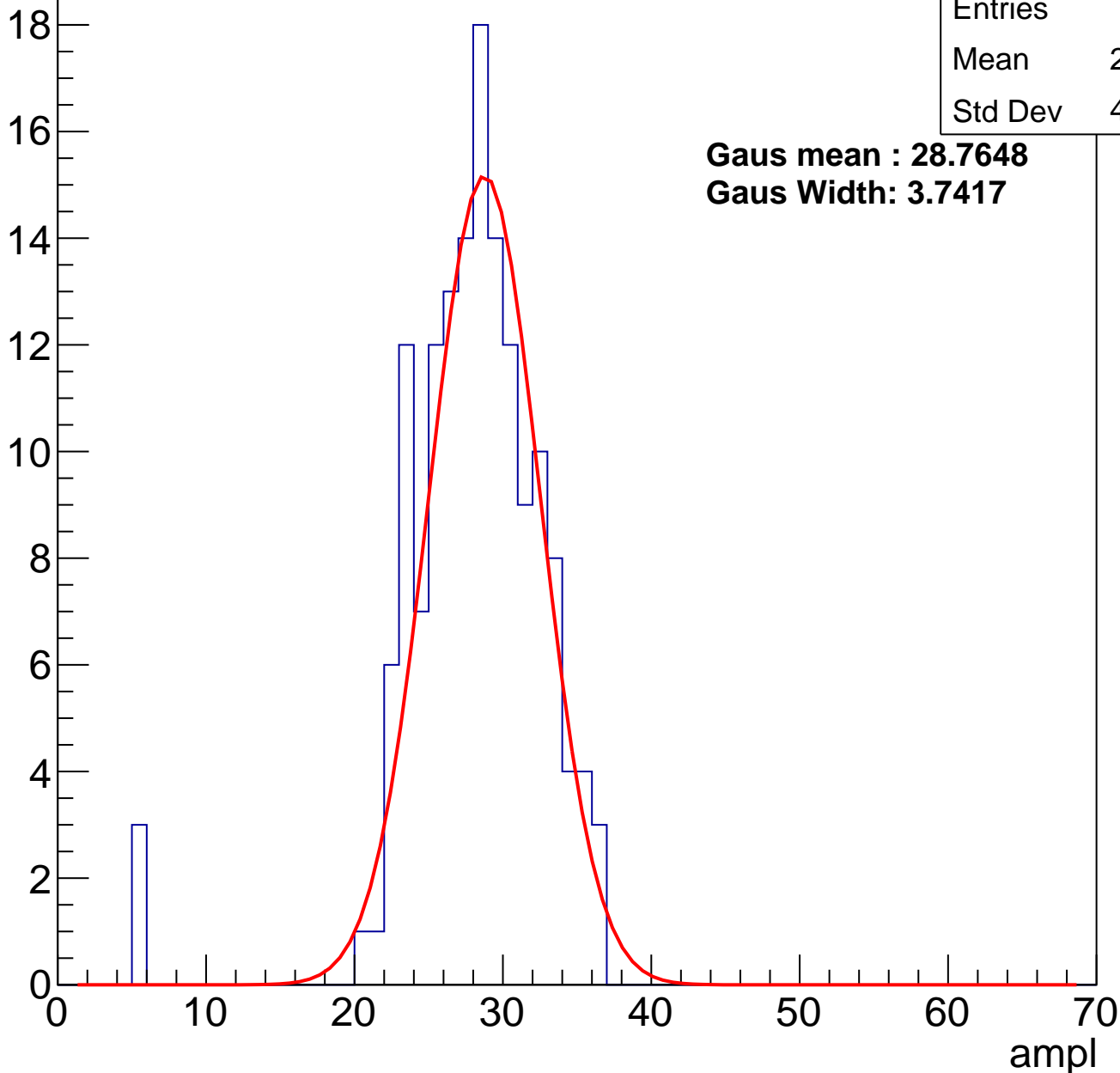
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	27.58
Std Dev	4.822

**Gaus mean : 28.7648**

**Gaus Width: 3.7417**

Entry



# B0L101S, U23-ch62, adc1

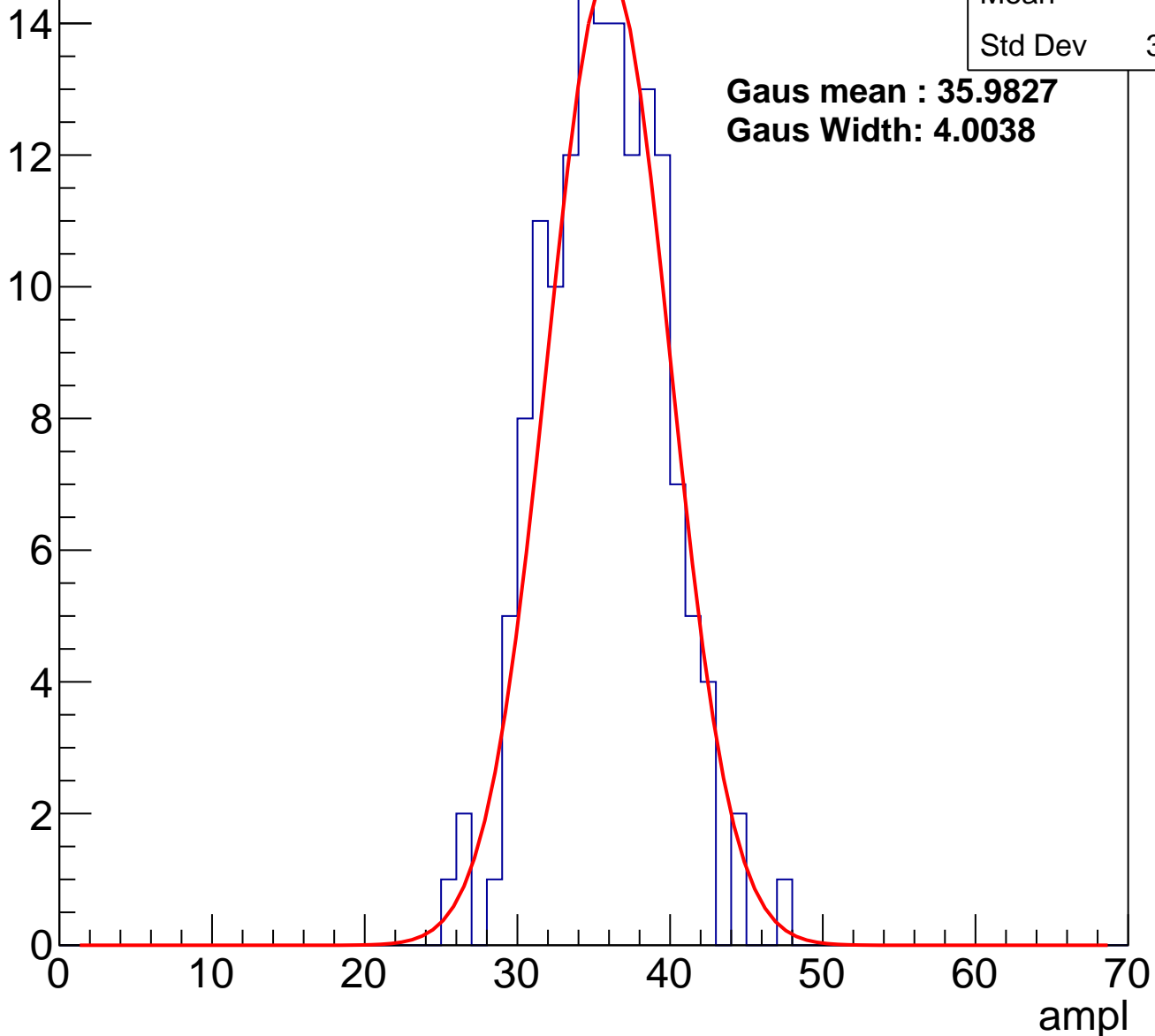
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	35.2
Std Dev	3.879

**Gaus mean : 35.9827**

**Gaus Width: 4.0038**

Entry



# B0L101S, U23-ch62, adc2

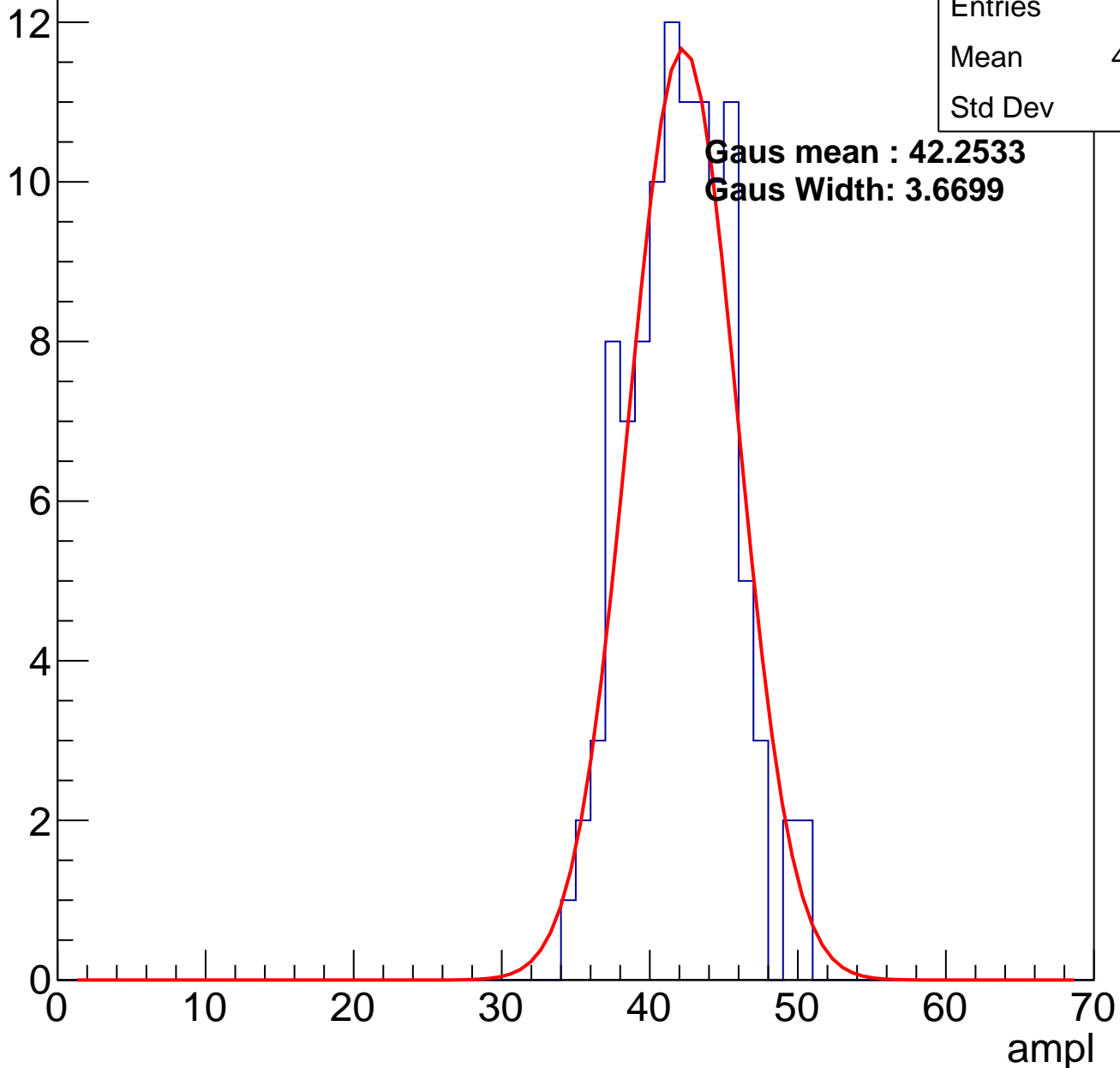
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	41.67
Std Dev	3.4

**Gaus mean : 42.2533**

**Gaus Width: 3.6699**

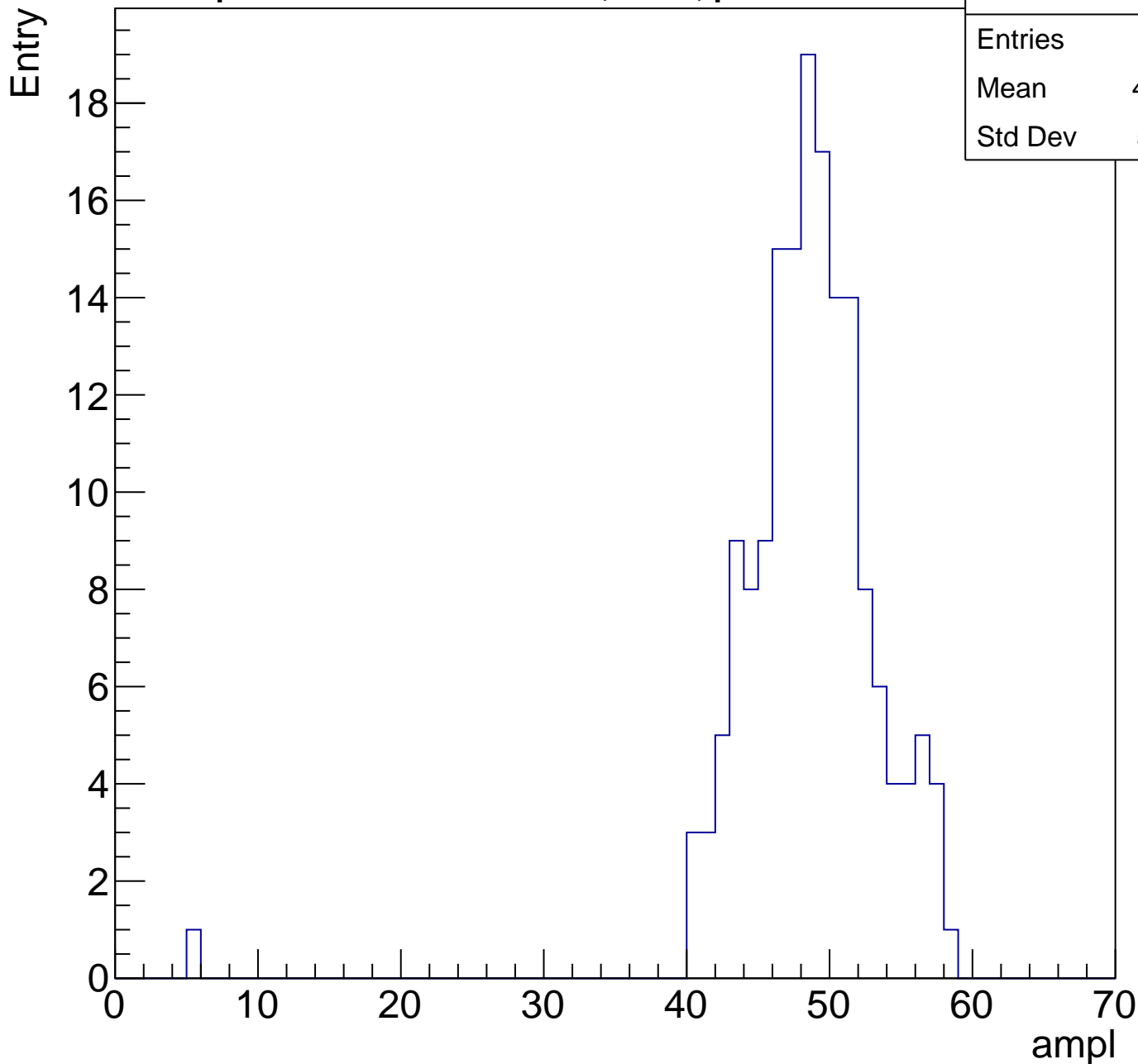
Entry



# B0L101S, U23-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	48.12
Std Dev	5.201



# B0L101S, U23-ch62, adc4

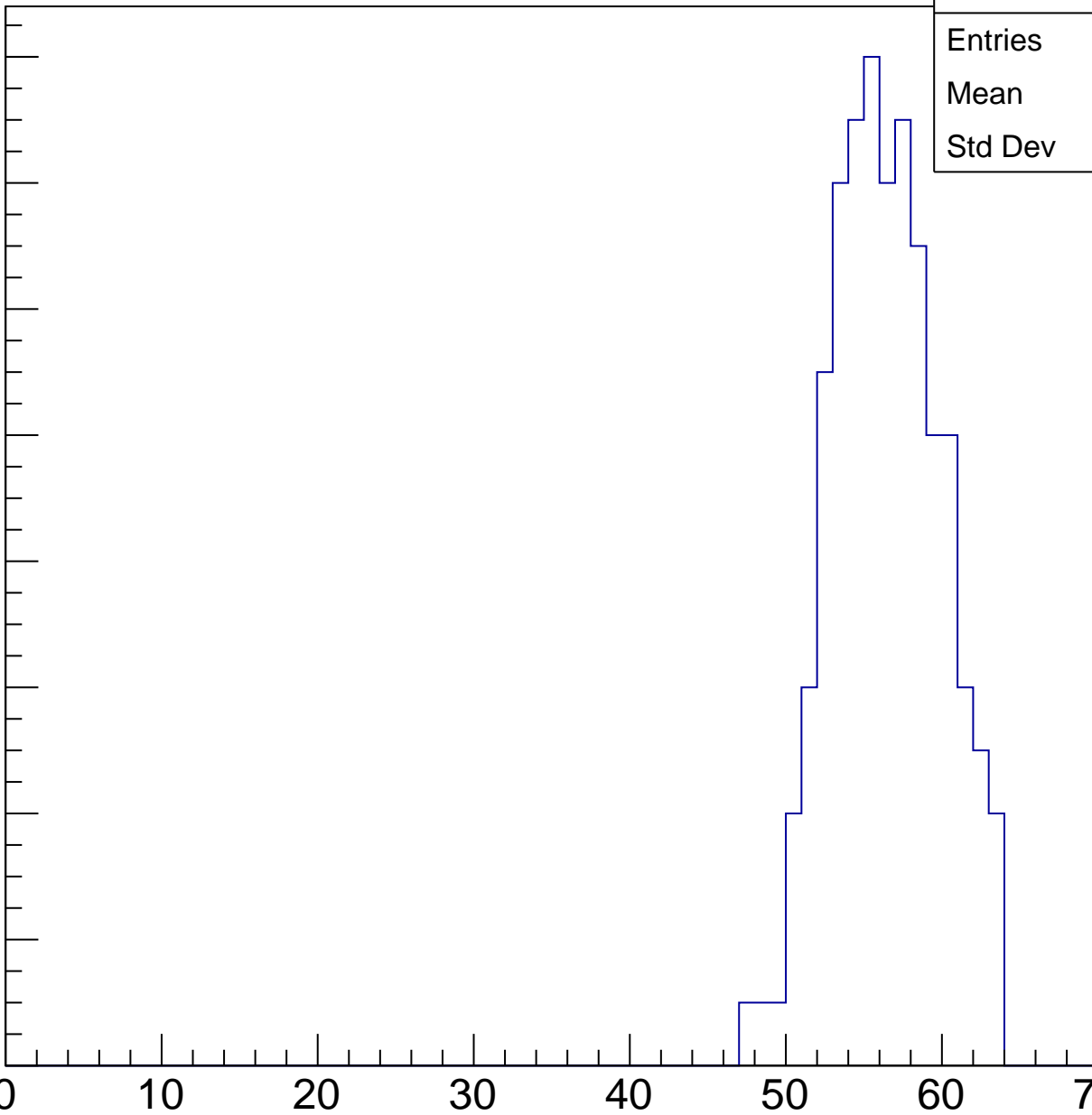
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	146
Mean	55.92
Std Dev	3.42

ampl



# B0L101S, U23-ch62, adc5

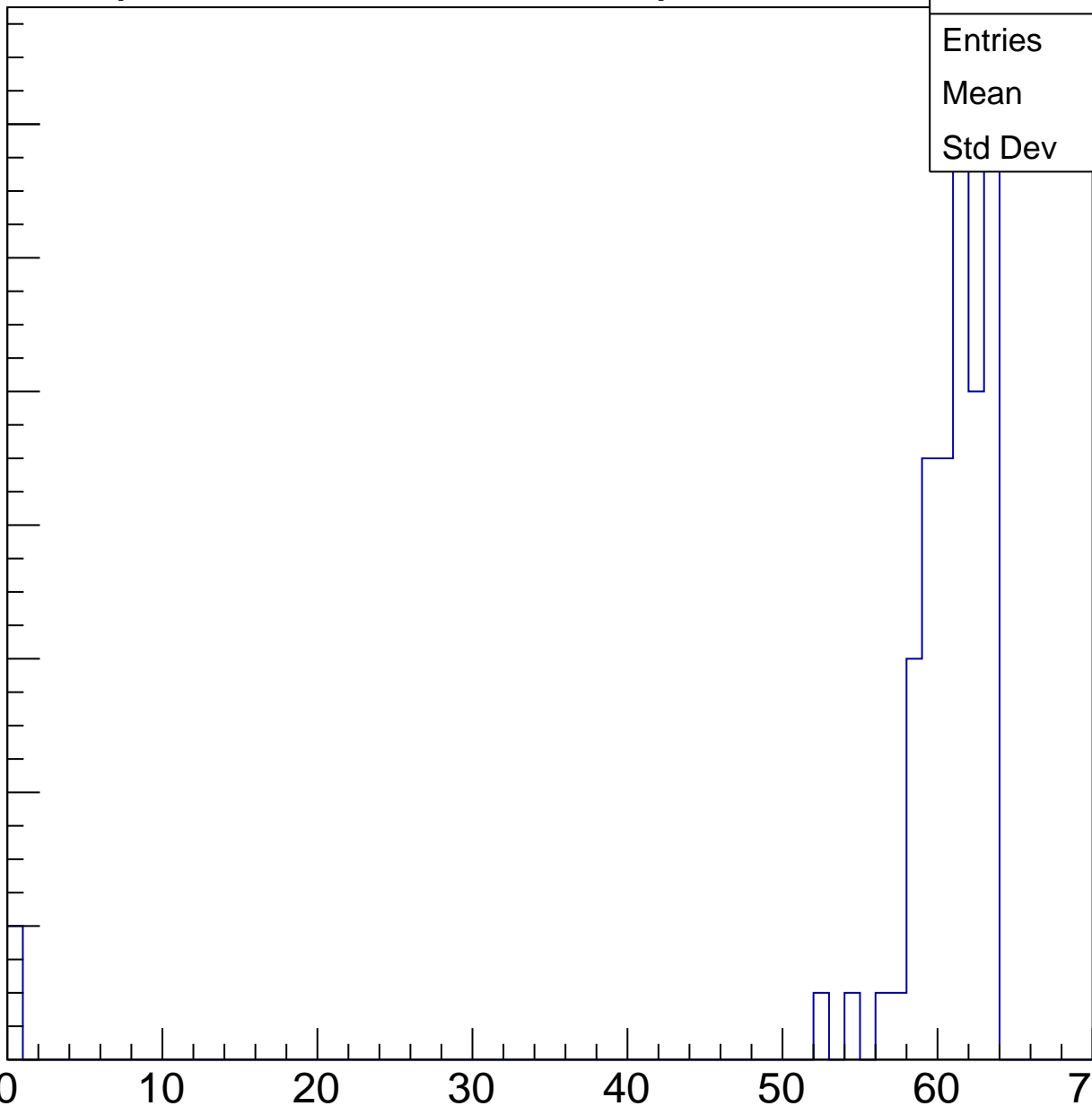
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	58.8
Std Dev	10.39

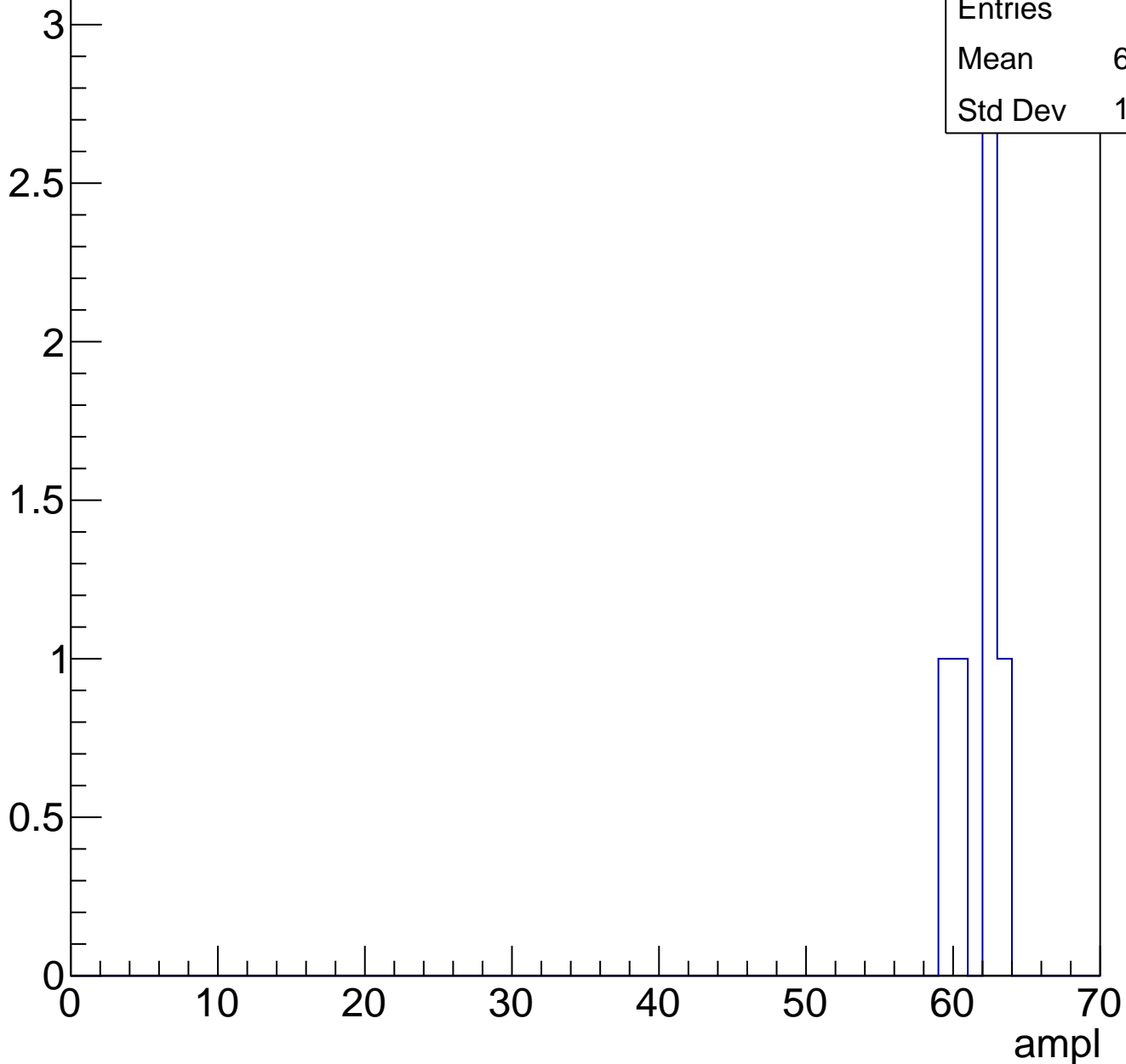
ampl



# B0L101S, U23-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

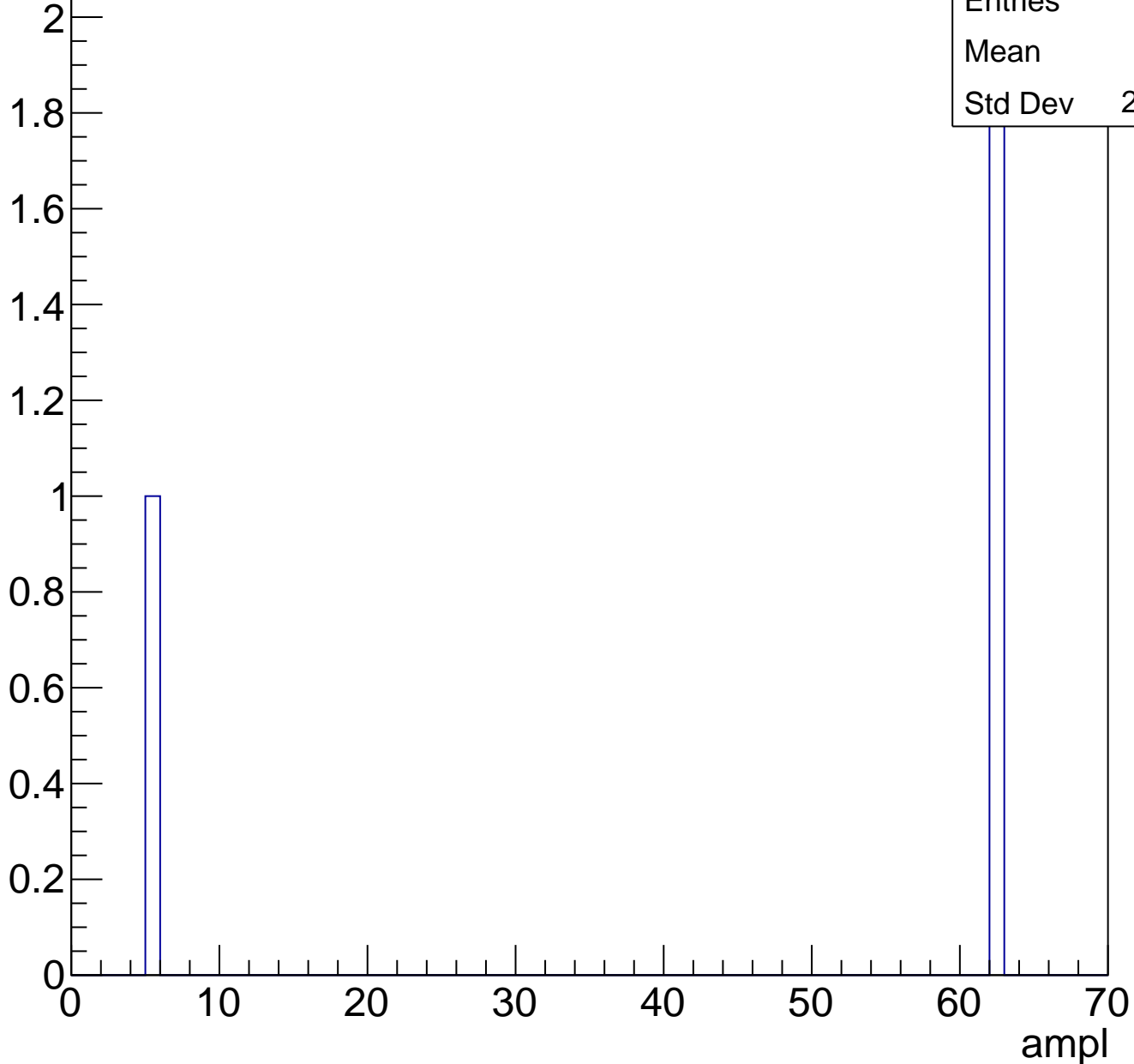




# B0L101S, U23-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



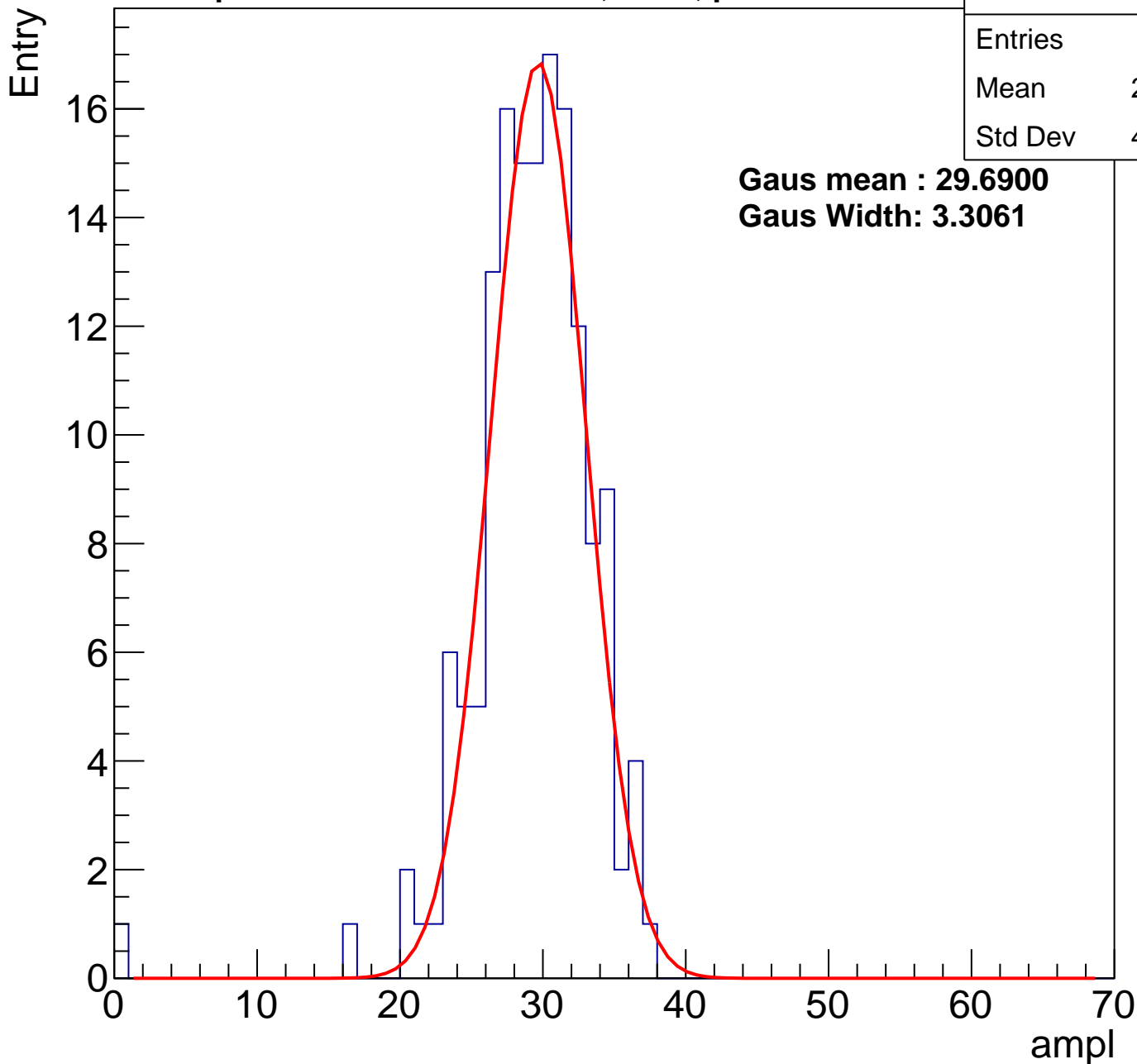
# B0L101S, U23-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	28.79
Std Dev	4.297

**Gaus mean : 29.6900**

**Gaus Width: 3.3061**



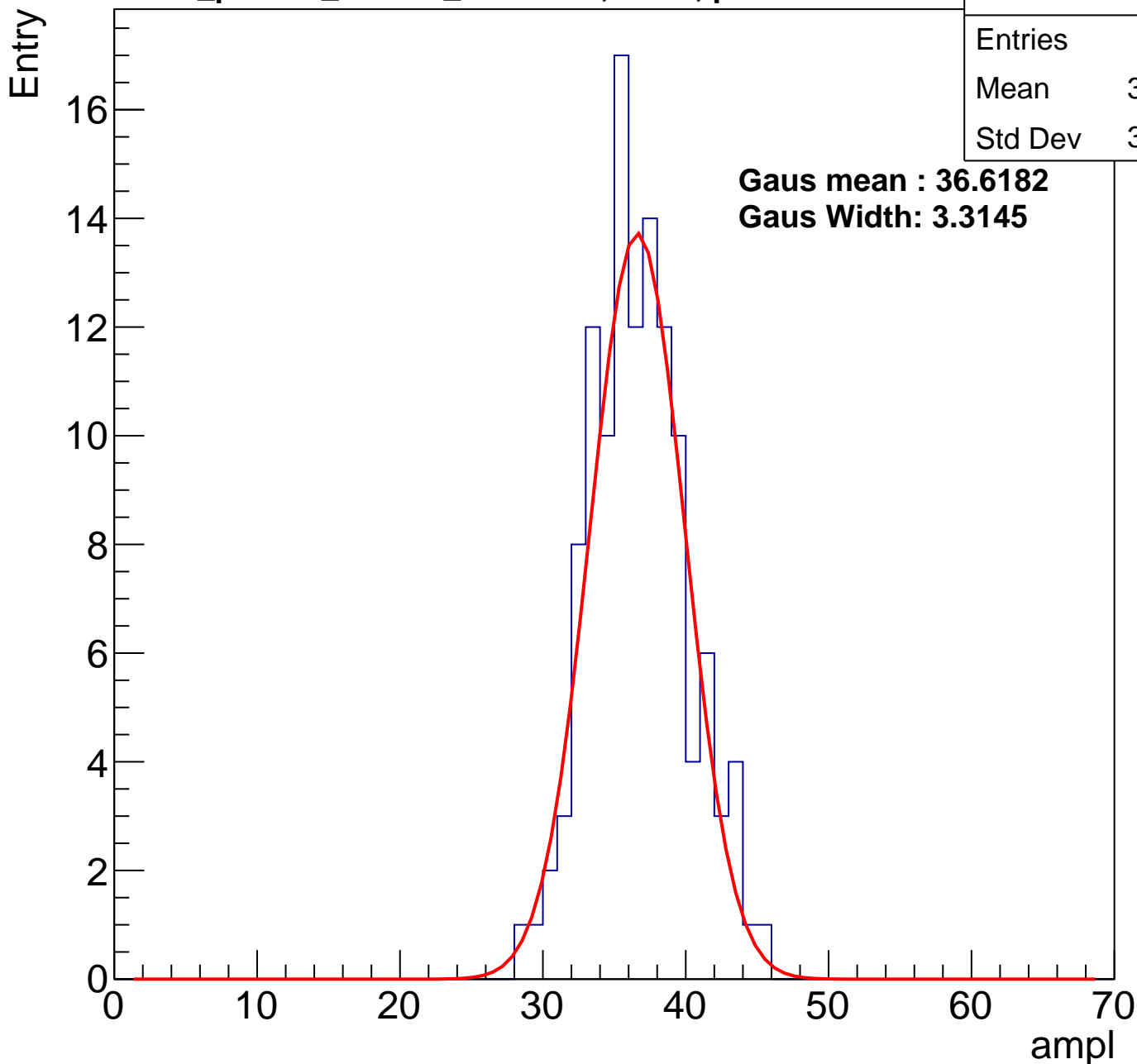
# B0L101S, U23-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	36.25
Std Dev	3.353

**Gaus mean : 36.6182**

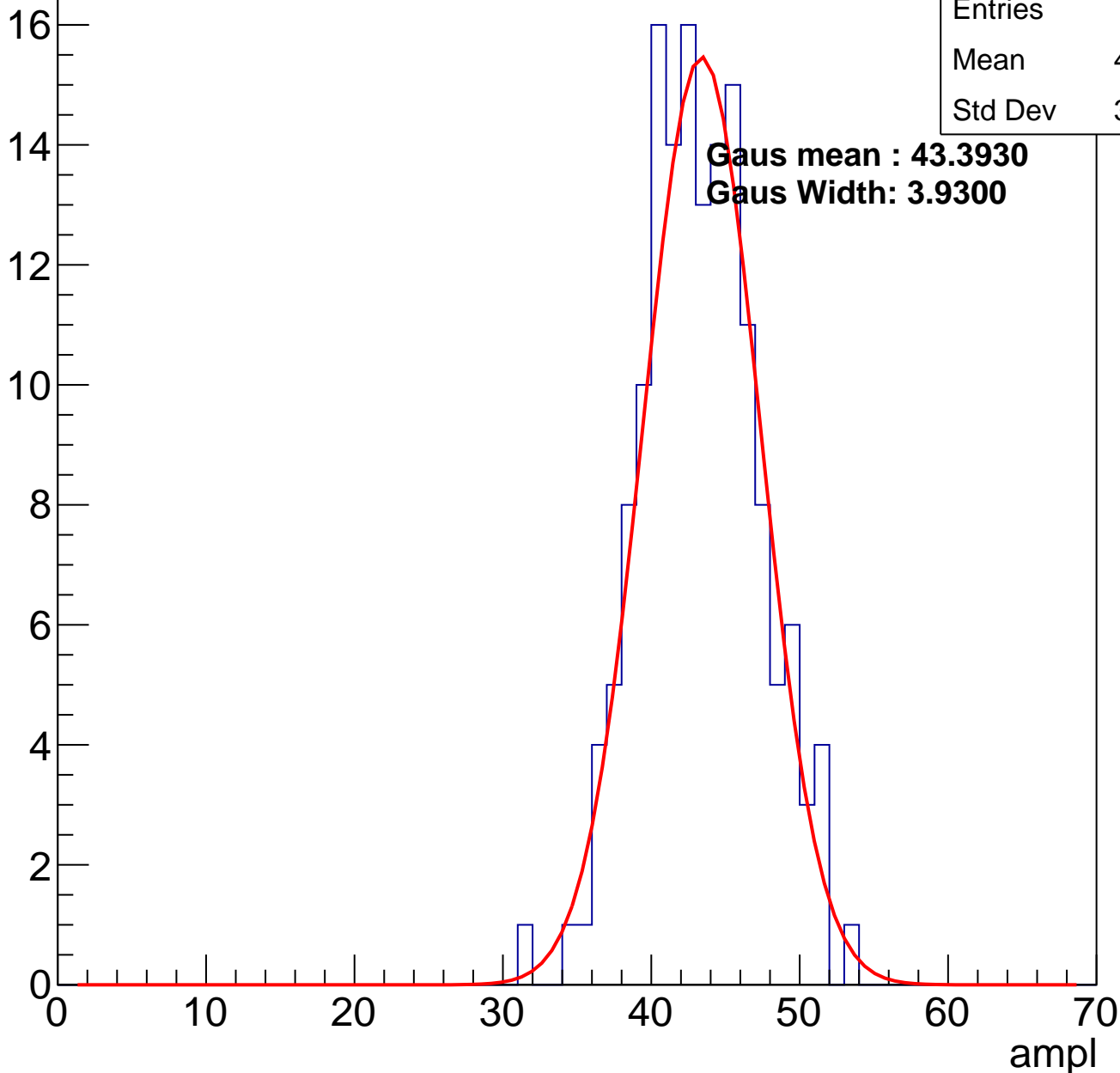
**Gaus Width: 3.3145**



# B0L101S, U23-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

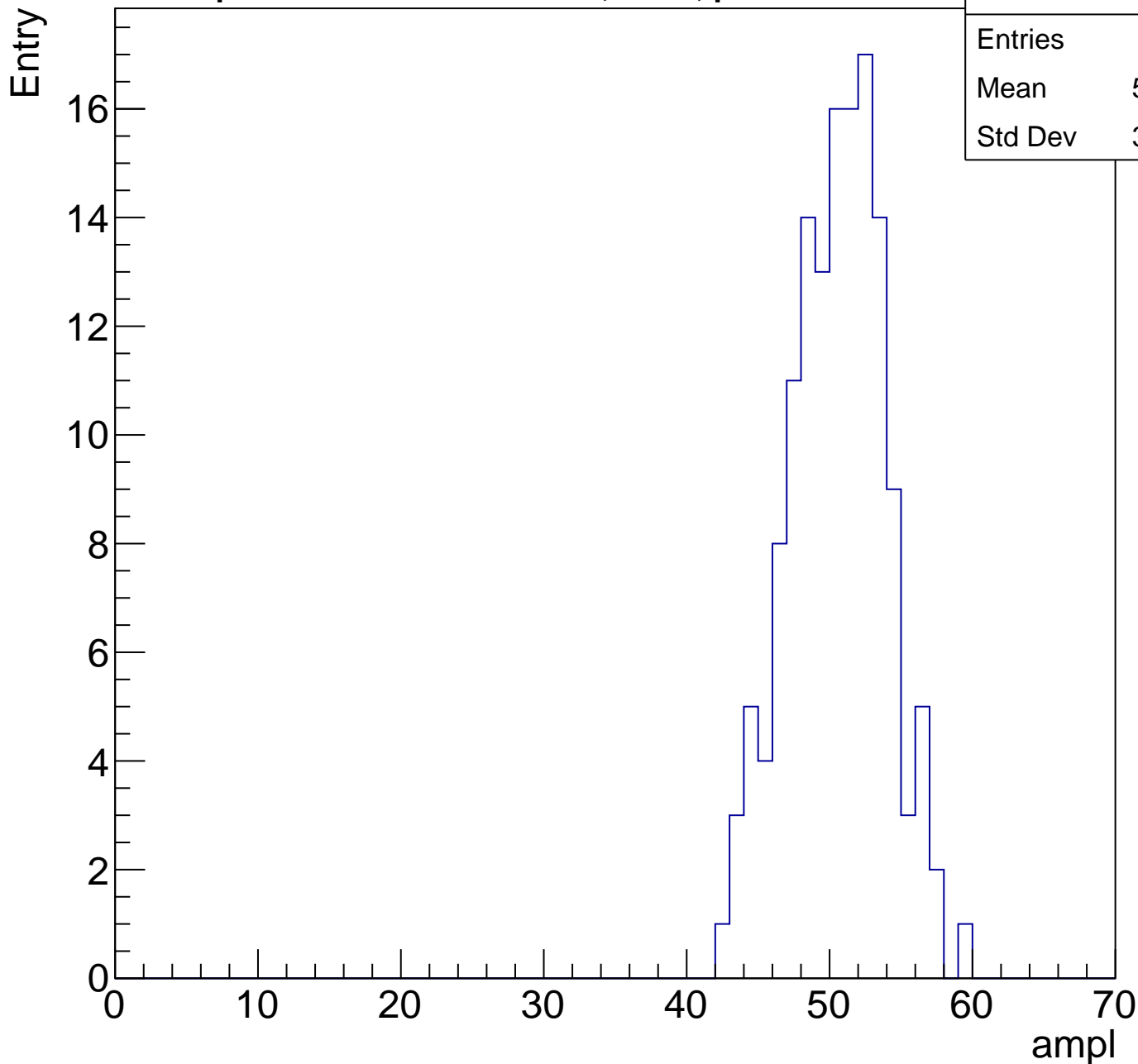
Entry



# B0L101S, U23-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

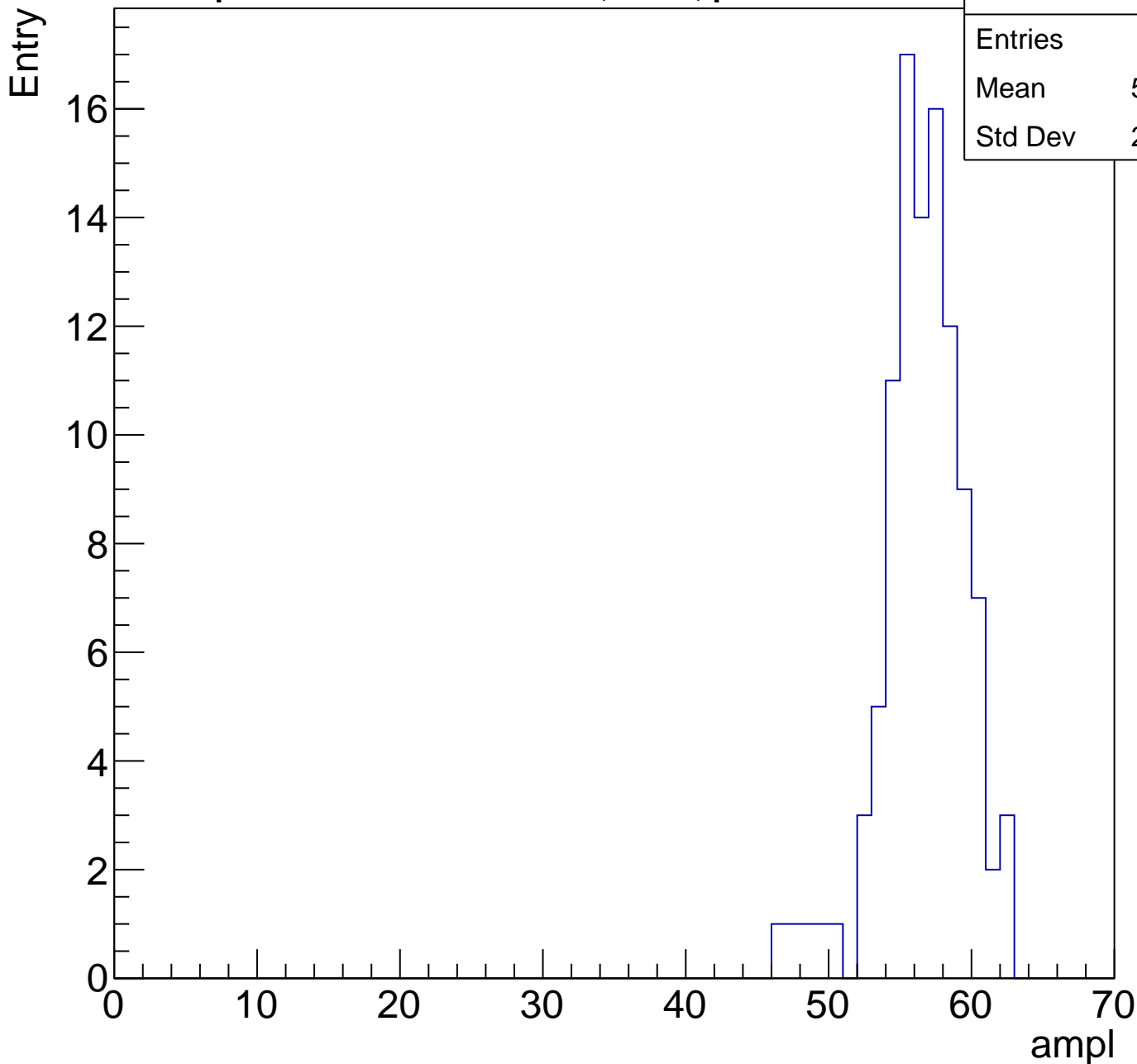
Entries	142
Mean	50.08
Std Dev	3.329



# B0L101S, U23-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	104
Mean	56.16
Std Dev	2.936



# B0L101S, U23-ch63, adc5

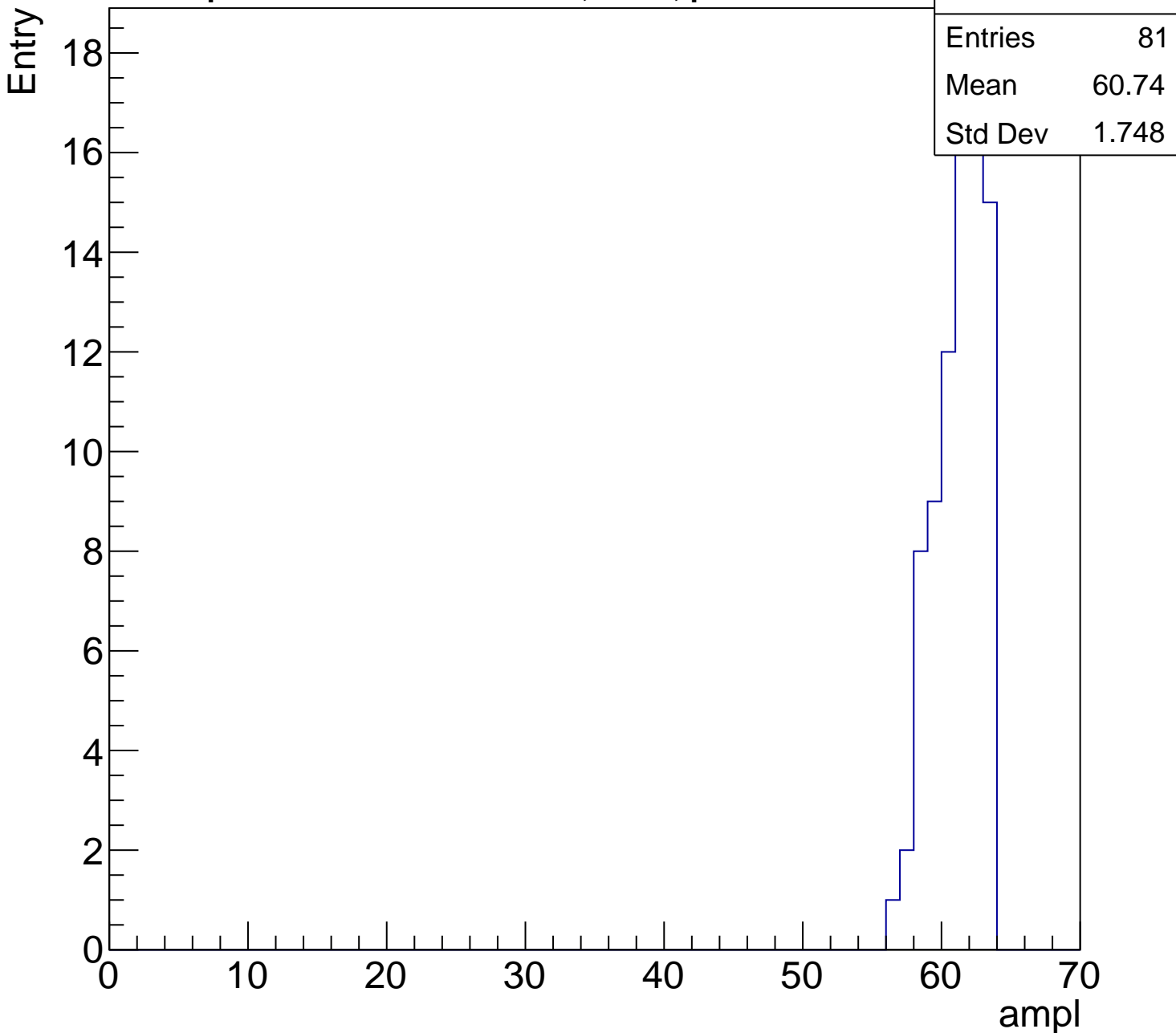
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	60.74
Std Dev	1.748

ampl

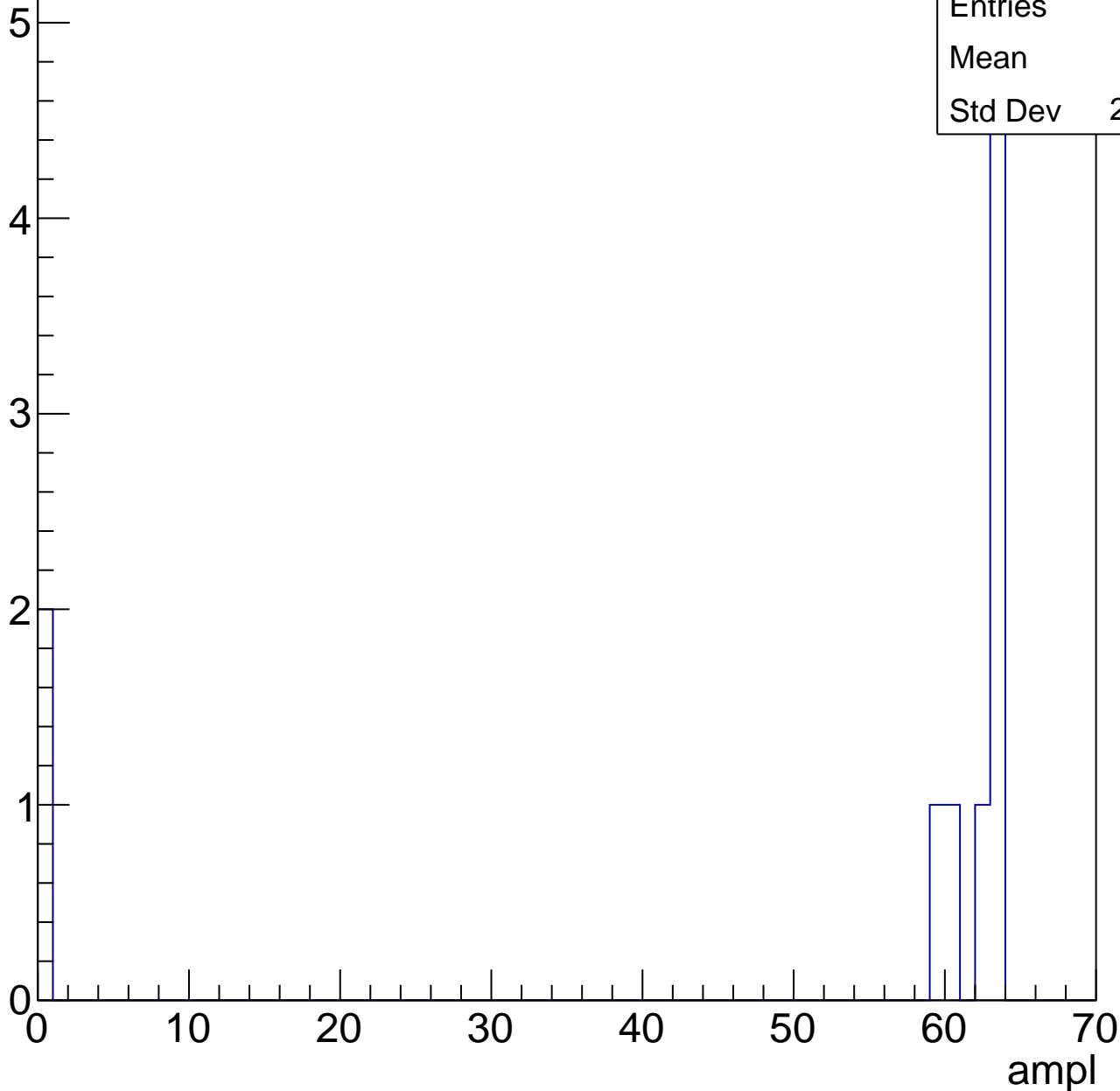


# B0L101S, U23-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	10
Mean	49.6
Std Dev	24.84





# B0L101S, U23-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch64, adc0

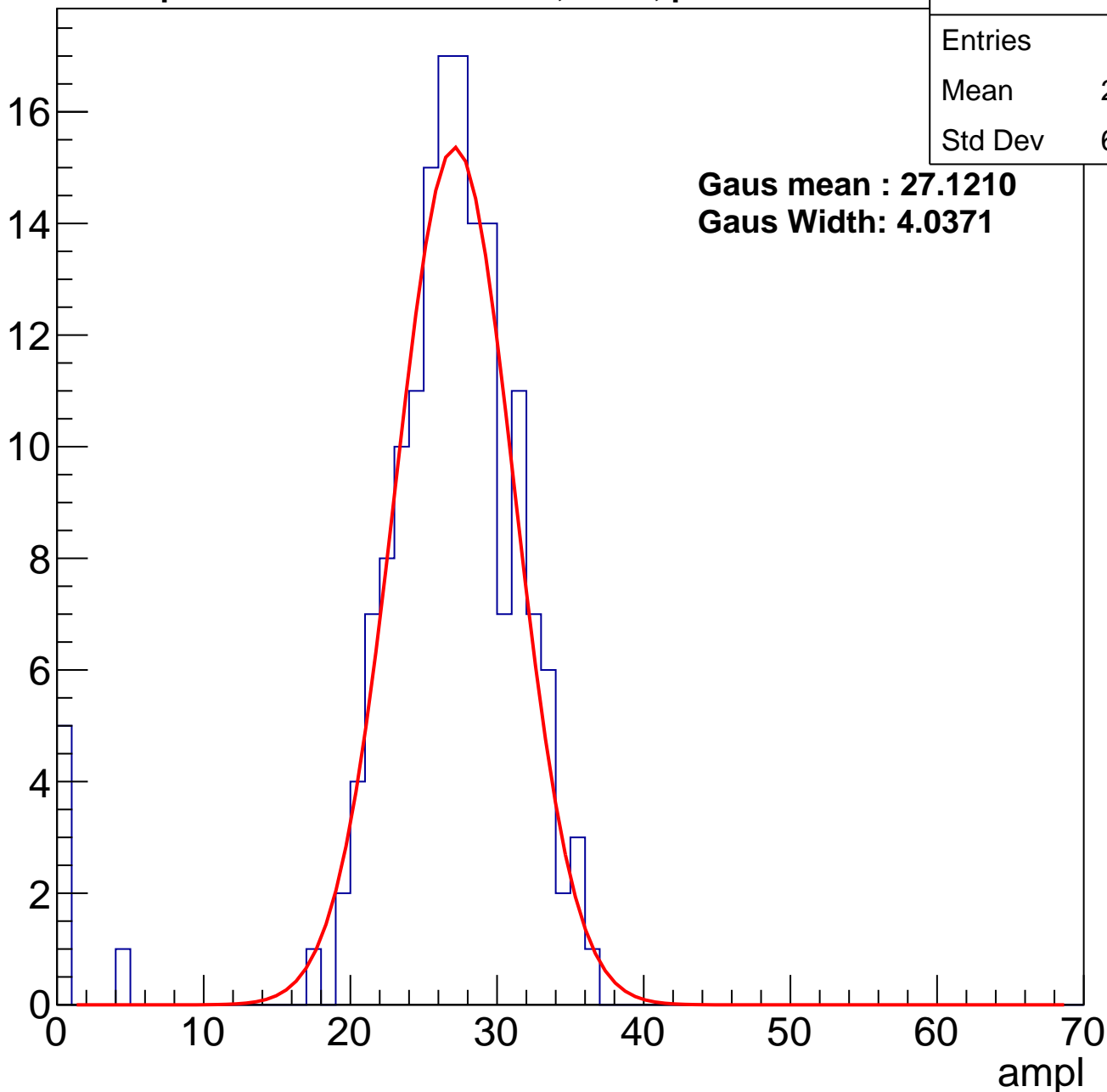
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	25.84
Std Dev	6.186

**Gaus mean : 27.1210**

**Gaus Width: 4.0371**

Entry

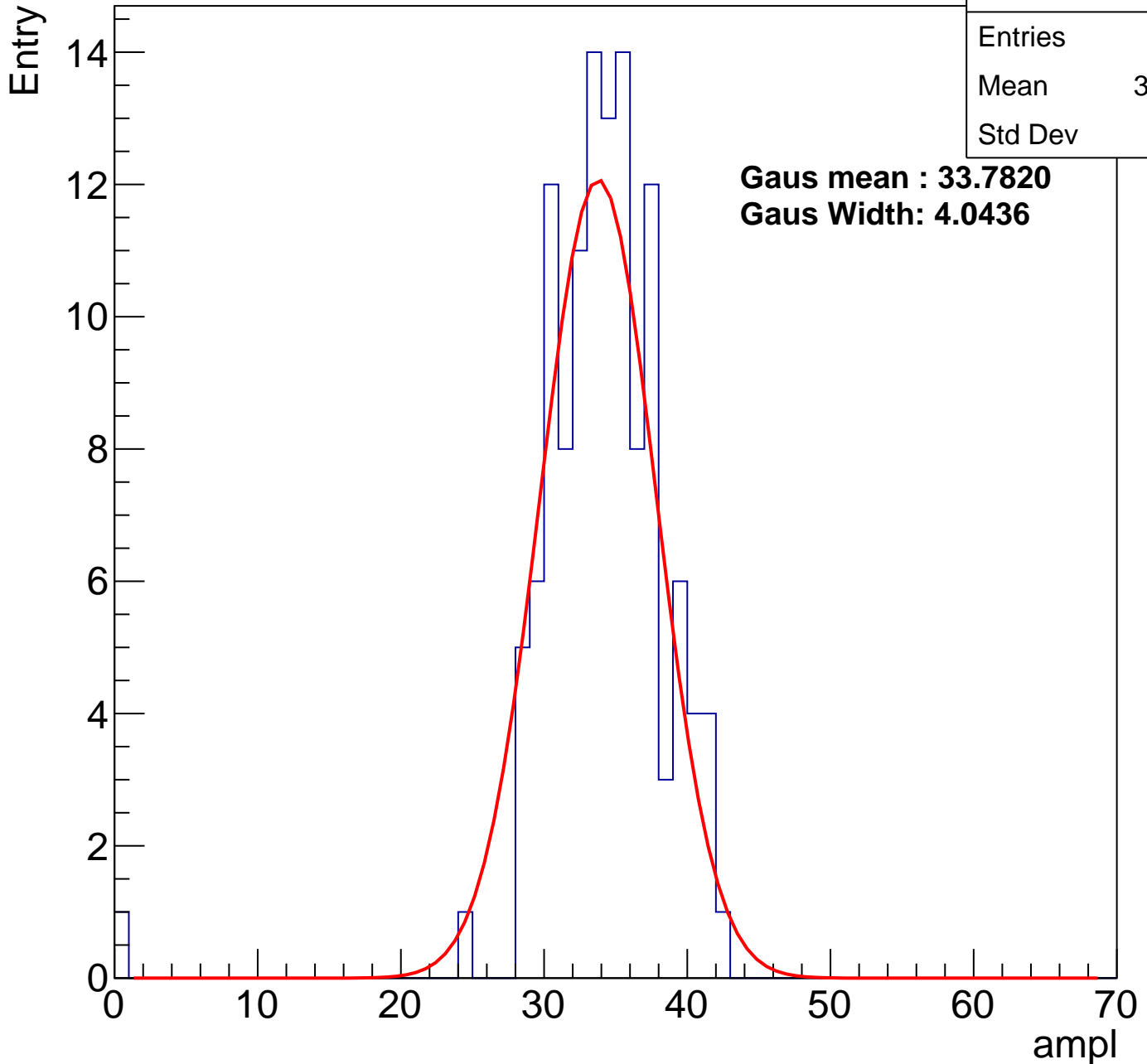


# B0L101S, U23-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	33.64
Std Dev	4.64

**Gaus mean : 33.7820**  
**Gaus Width: 4.0436**



# B0L101S, U23-ch64, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	39.72
Std Dev	3.481

**Gaus mean : 40.3277**

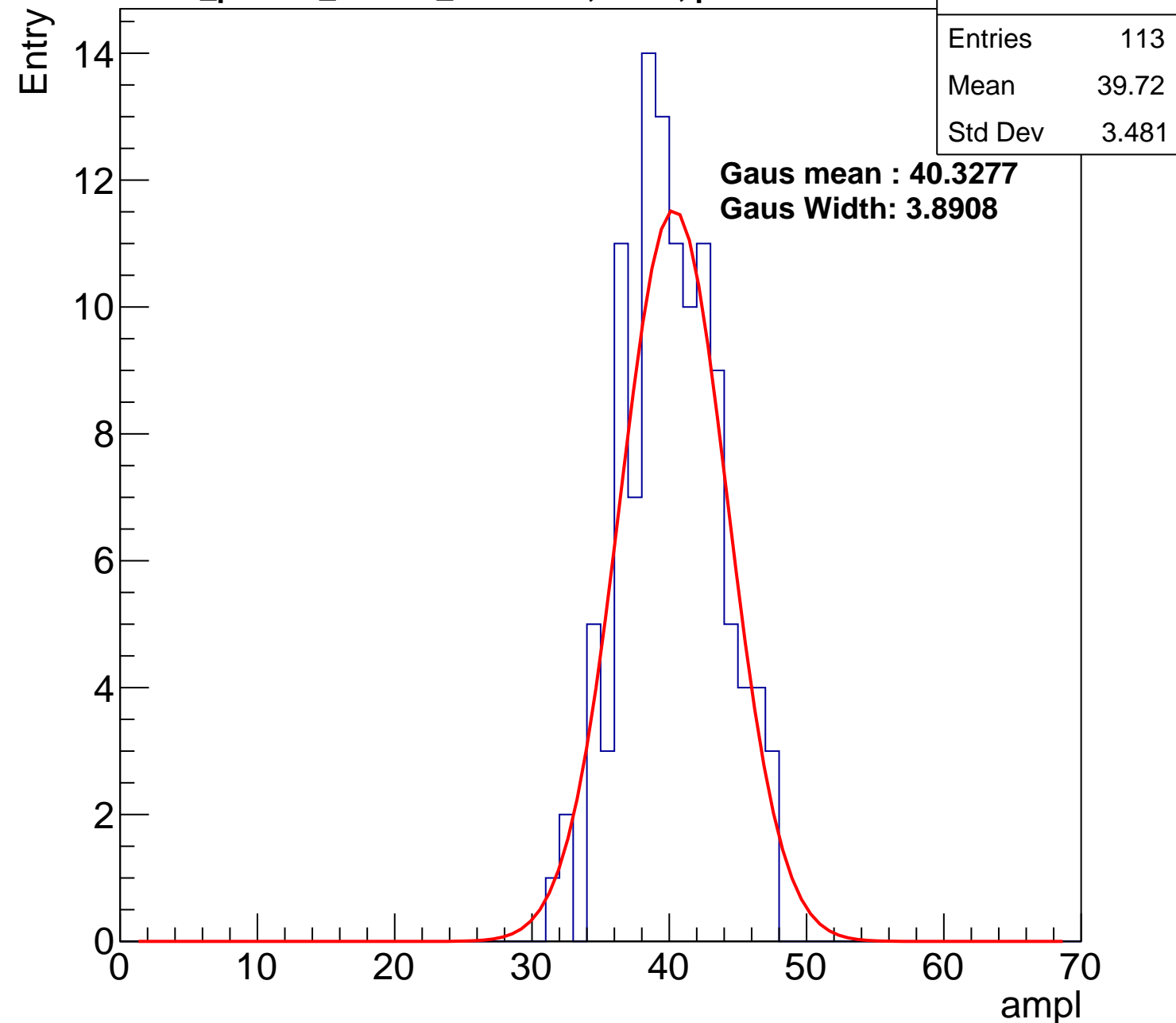
**Gaus Width: 3.8908**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch64, adc3

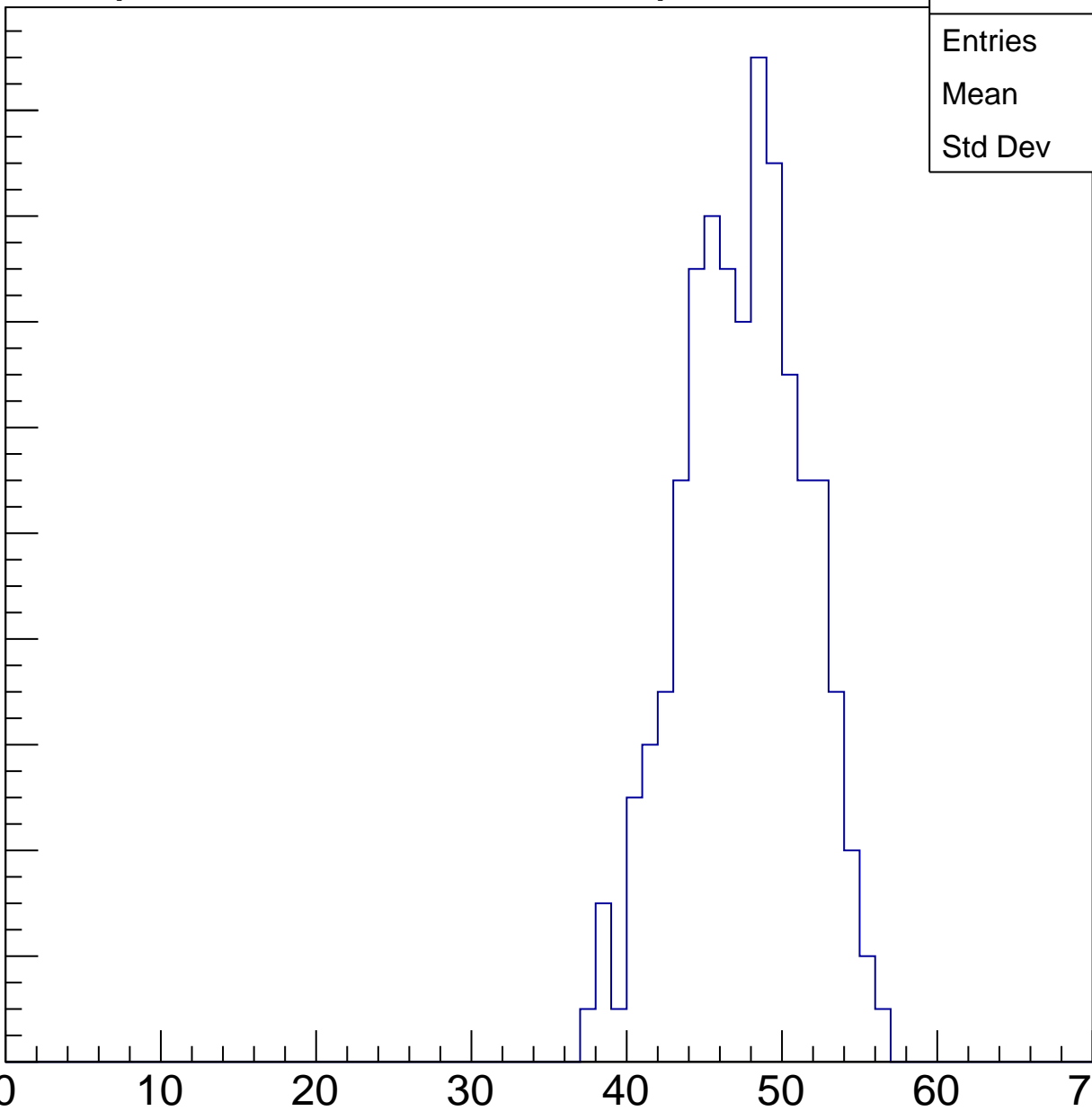
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	179
Mean	46.99
Std Dev	3.916

ampl



# B0L101S, U23-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

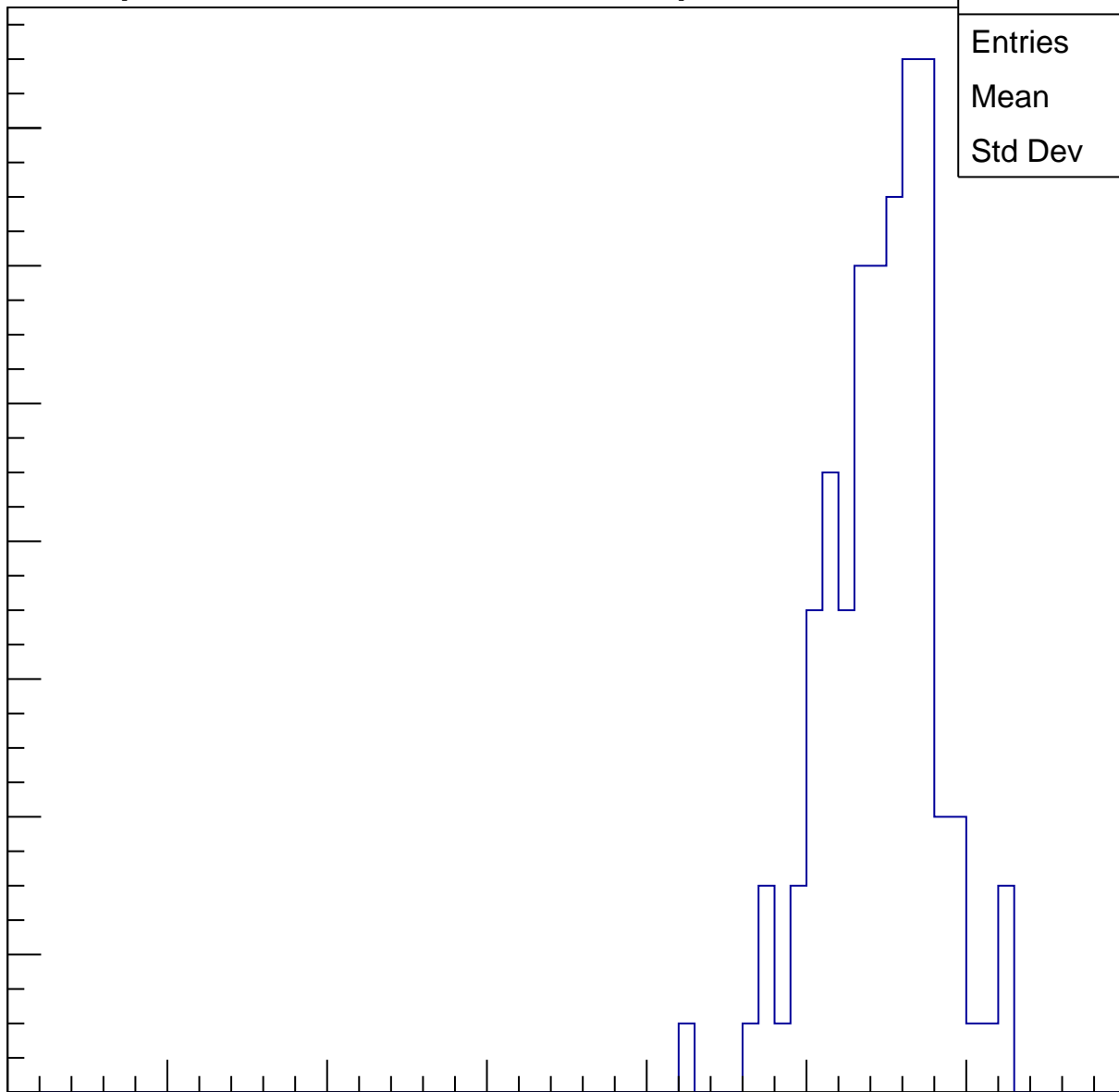
Entries	112
Mean	54.16
Std Dev	3.466

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

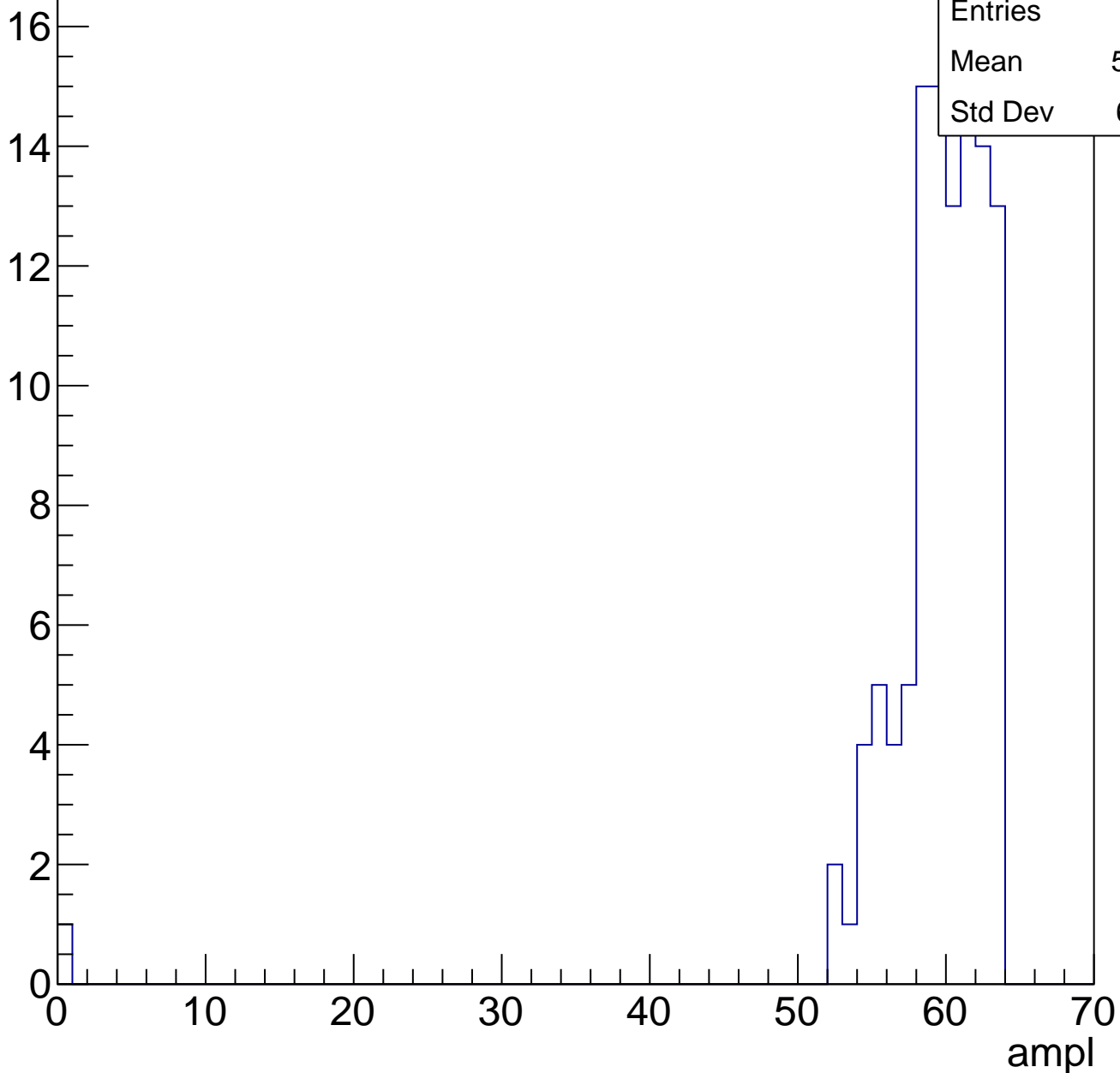
ampl



# B0L101S, U23-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

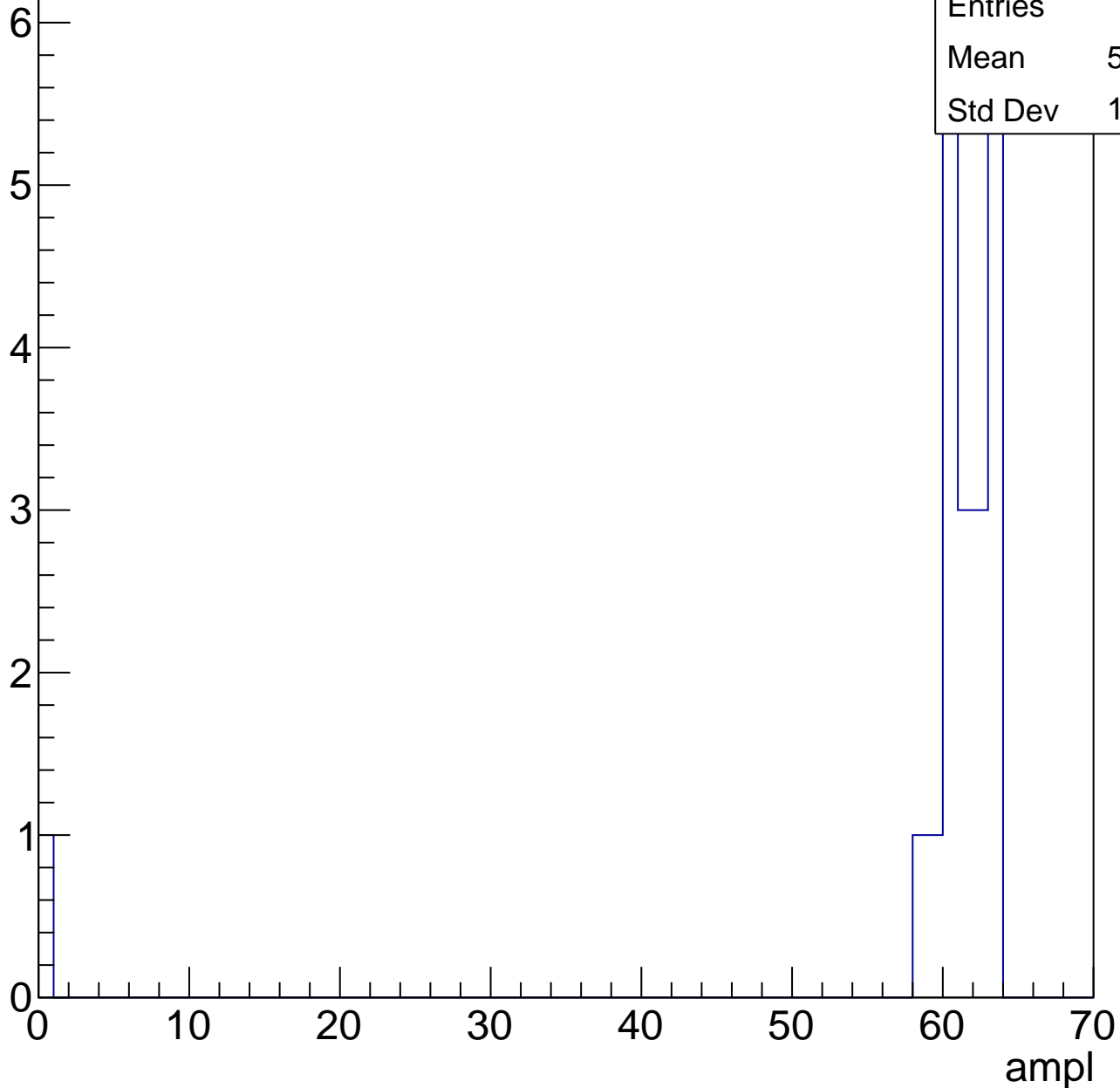
Entry



# B0L101S, U23-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch65, adc0

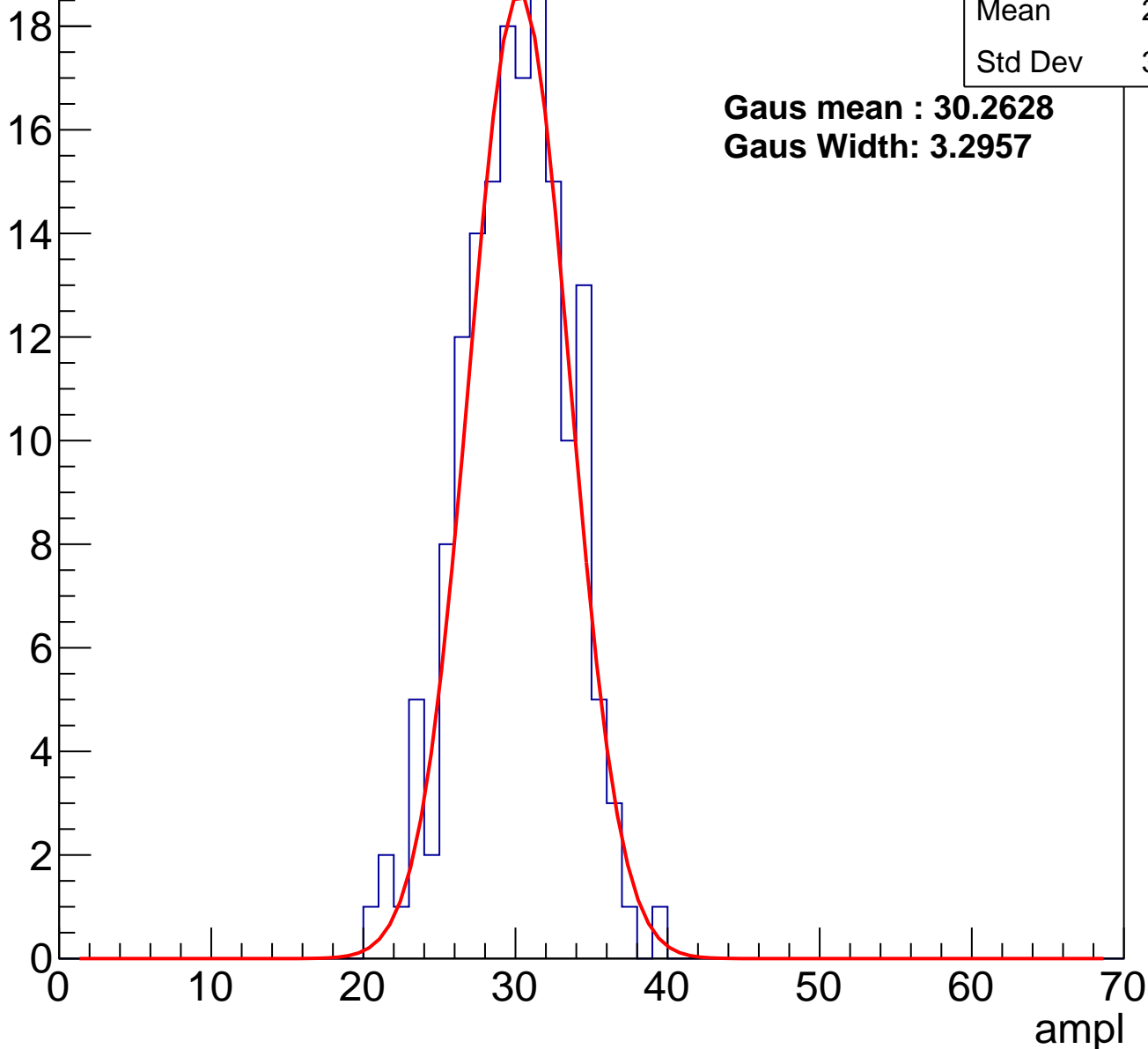
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	29.56
Std Dev	3.468

**Gaus mean : 30.2628**

**Gaus Width: 3.2957**

Entry



# B0L101S, U23-ch65, adc1

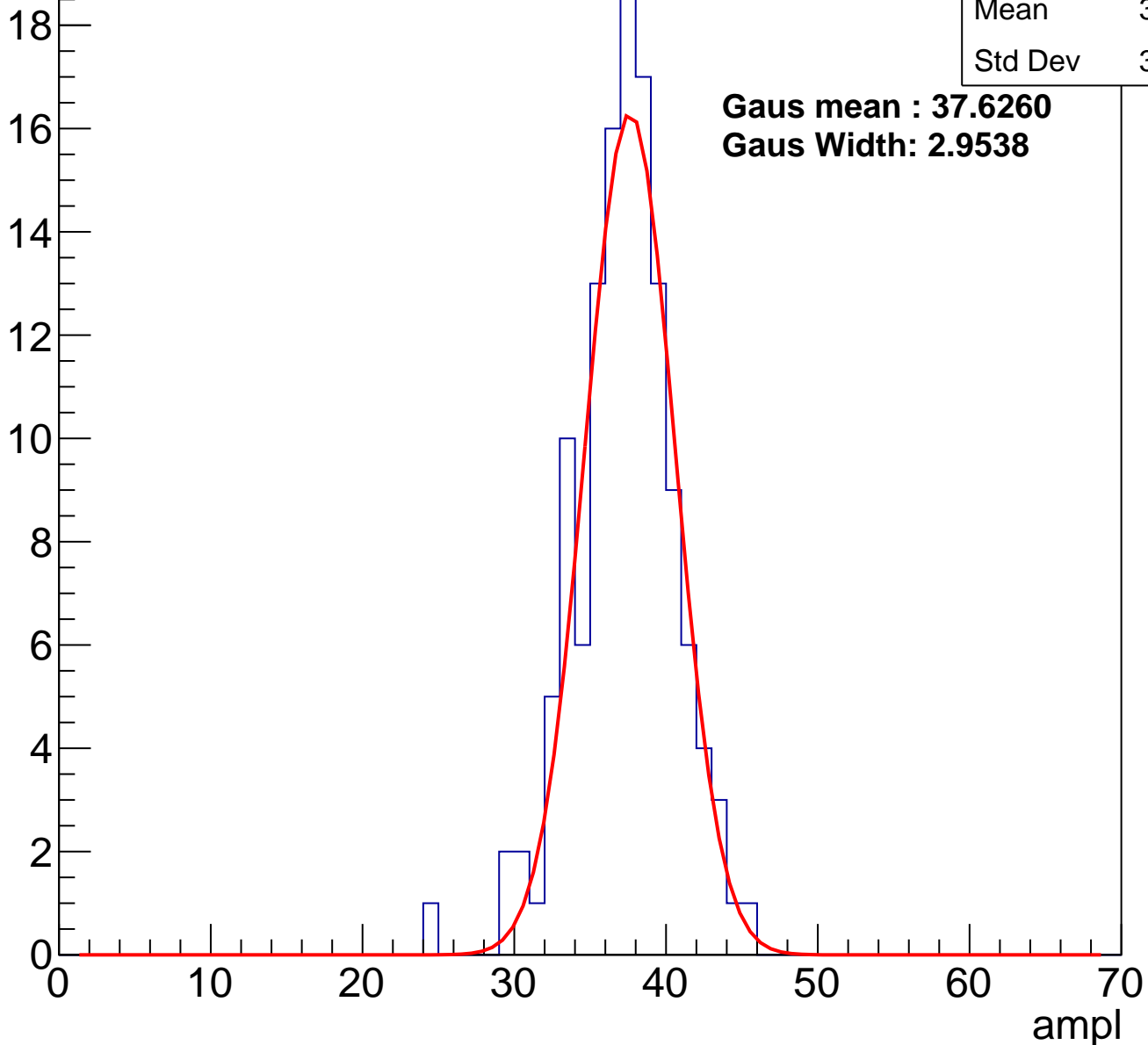
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	36.79
Std Dev	3.294

**Gaus mean : 37.6260**

**Gaus Width: 2.9538**

Entry

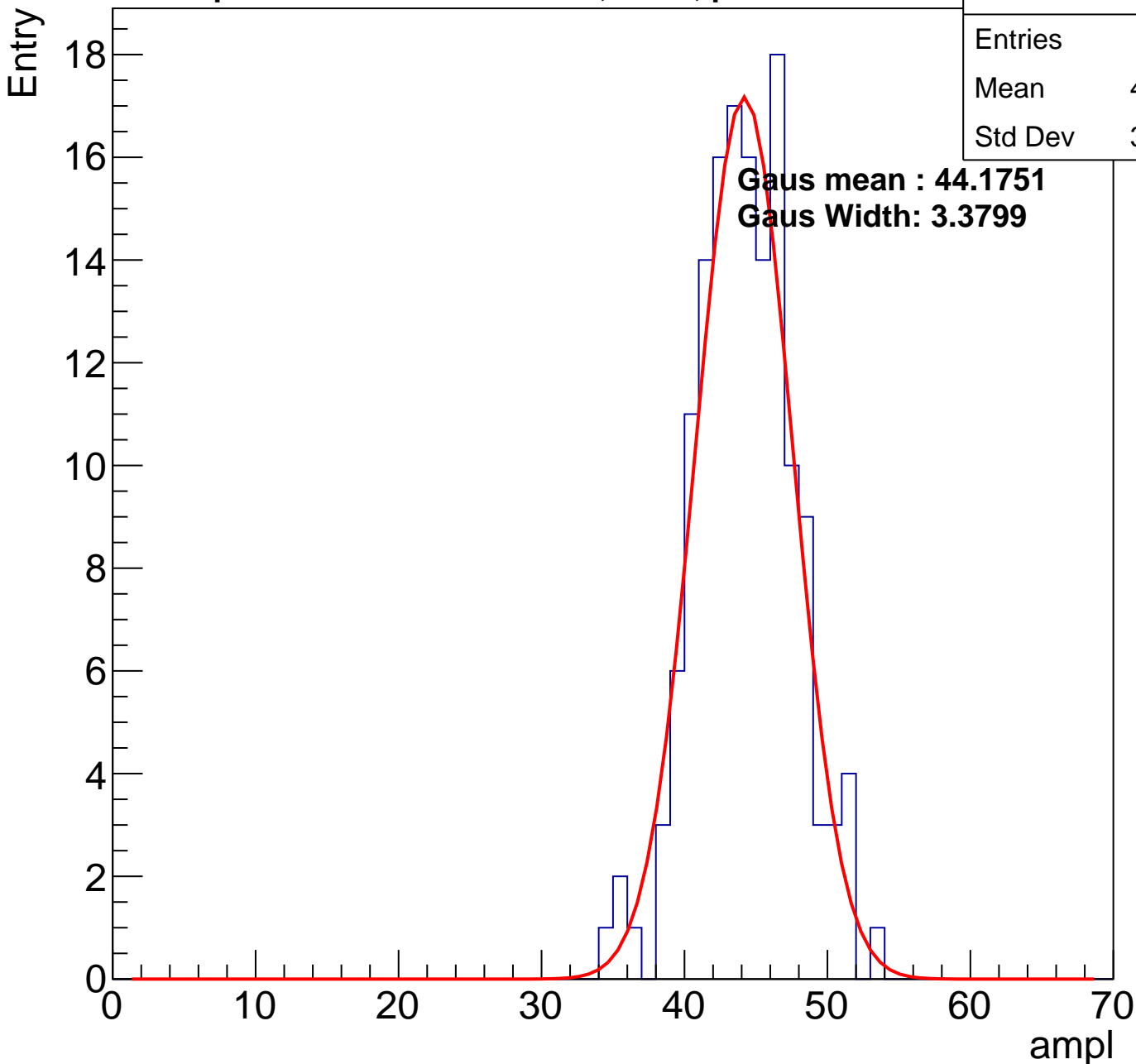


# B0L101S, U23-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	43.78
Std Dev	3.417

**Gaus mean : 44.1751**  
**Gaus Width: 3.3799**



# B0L101S, U23-ch65, adc3

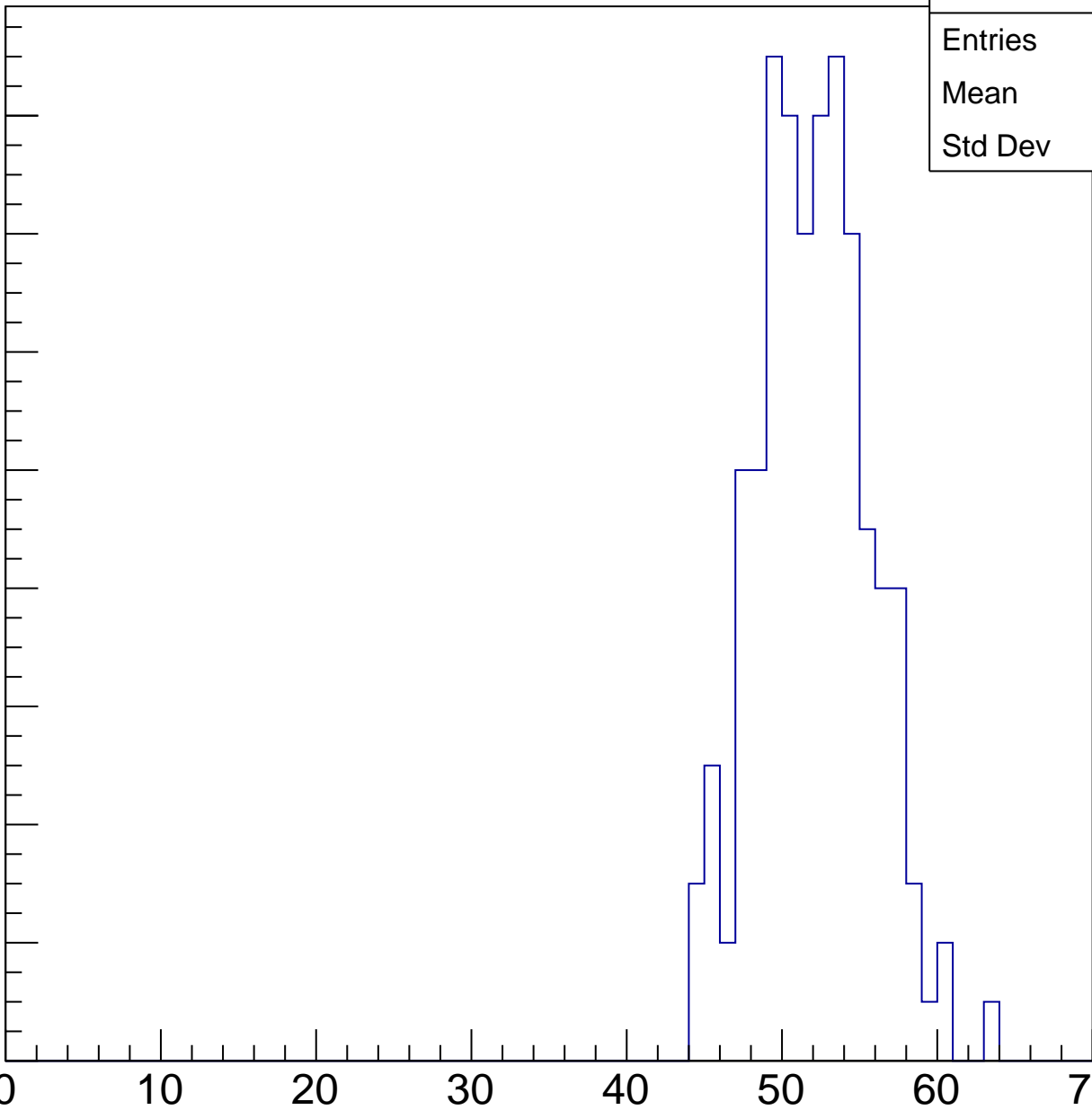
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	156
Mean	51.6
Std Dev	3.593

ampl

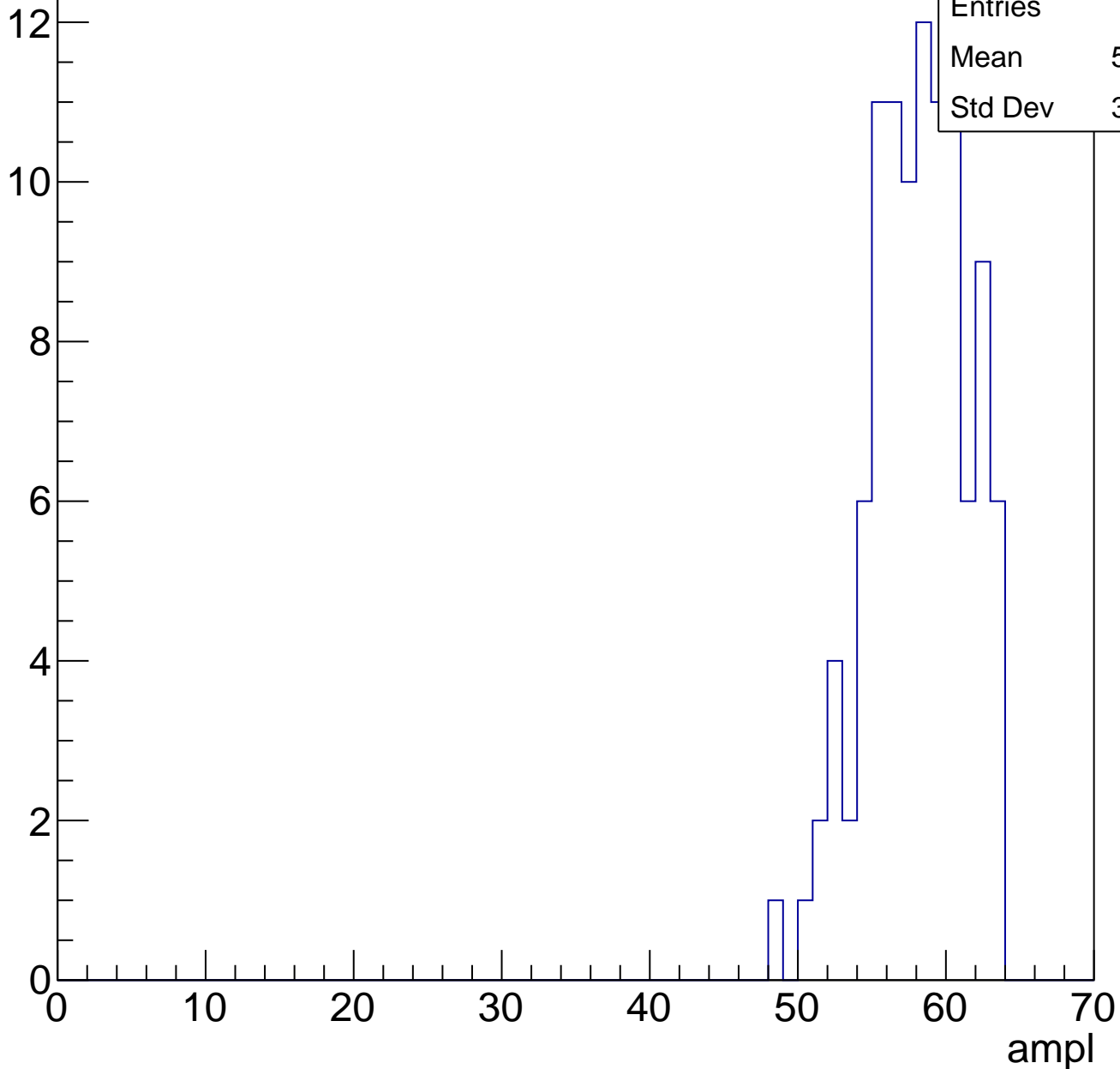


# B0L101S, U23-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	104
Mean	57.65
Std Dev	3.252

Entry



# B0L101S, U23-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

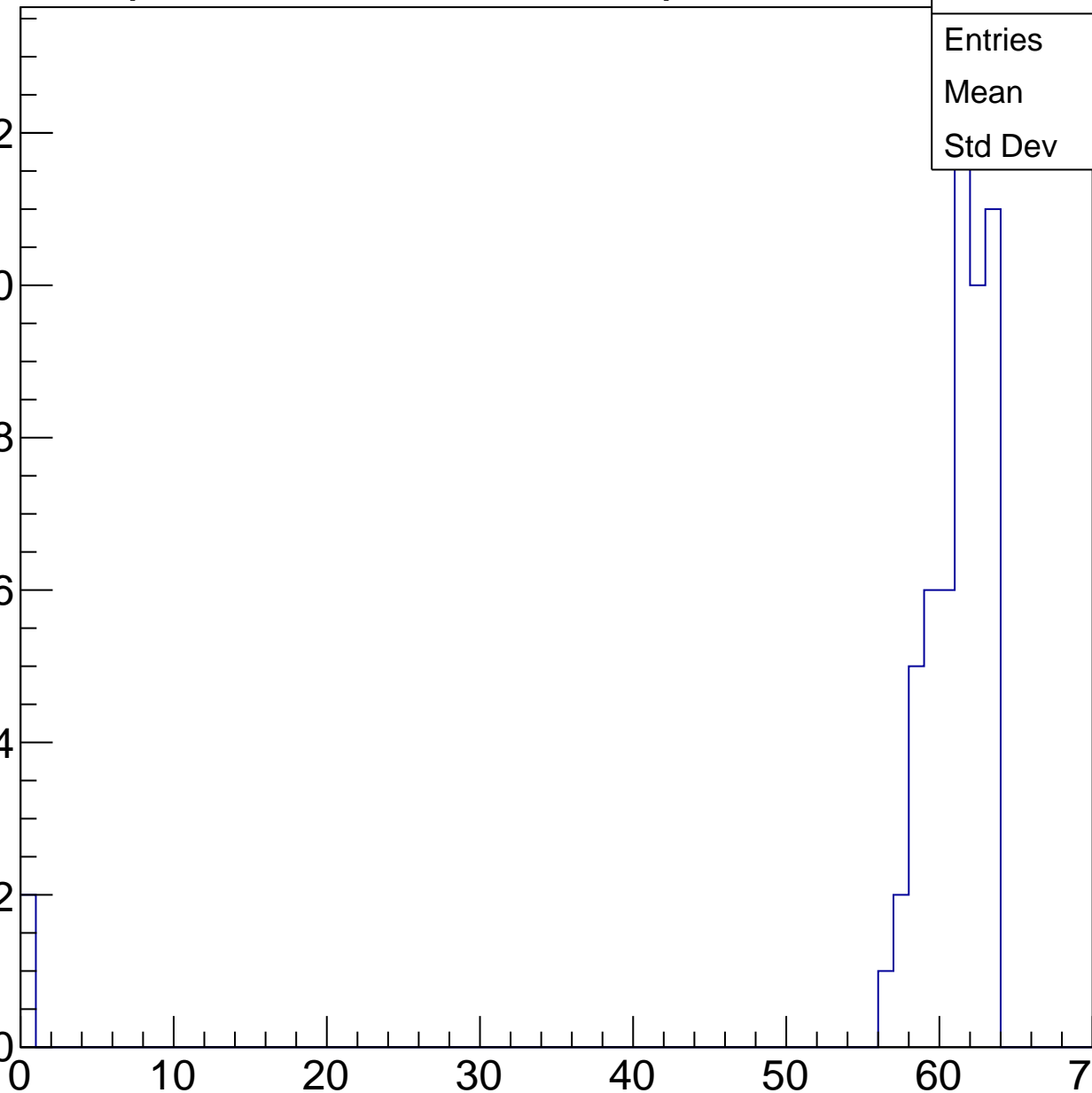
4

2

0

Entries	56
Mean	58.57
Std Dev	11.42

ampl



# B0L101S, U23-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.17
Std Dev	1.675

ampl



# B0L101S, U23-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

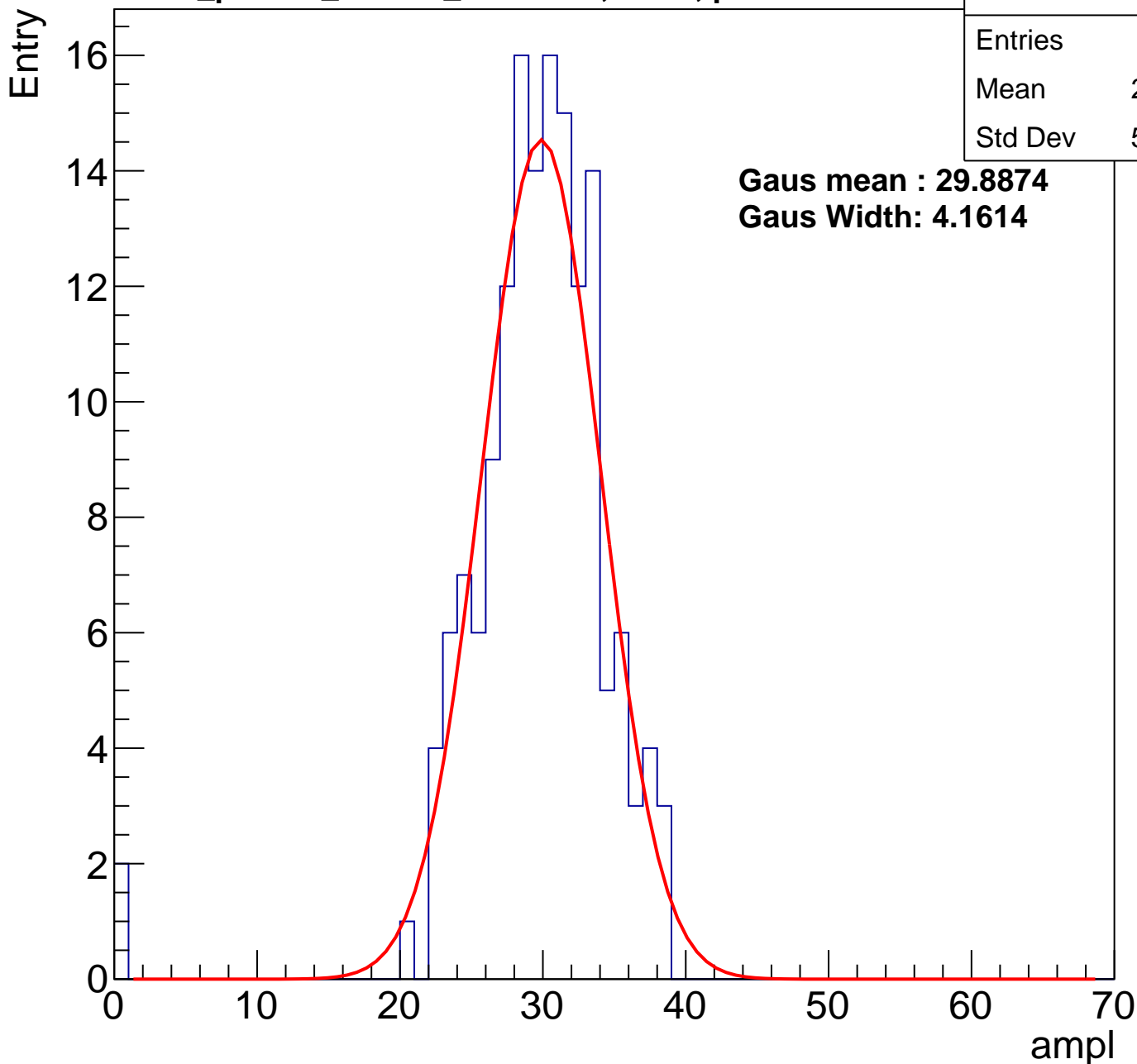
# B0L101S, U23-ch66, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	29.14
Std Dev	5.069

**Gaus mean : 29.8874**

**Gaus Width: 4.1614**



# B0L101S, U23-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	36.94
Std Dev	3.863

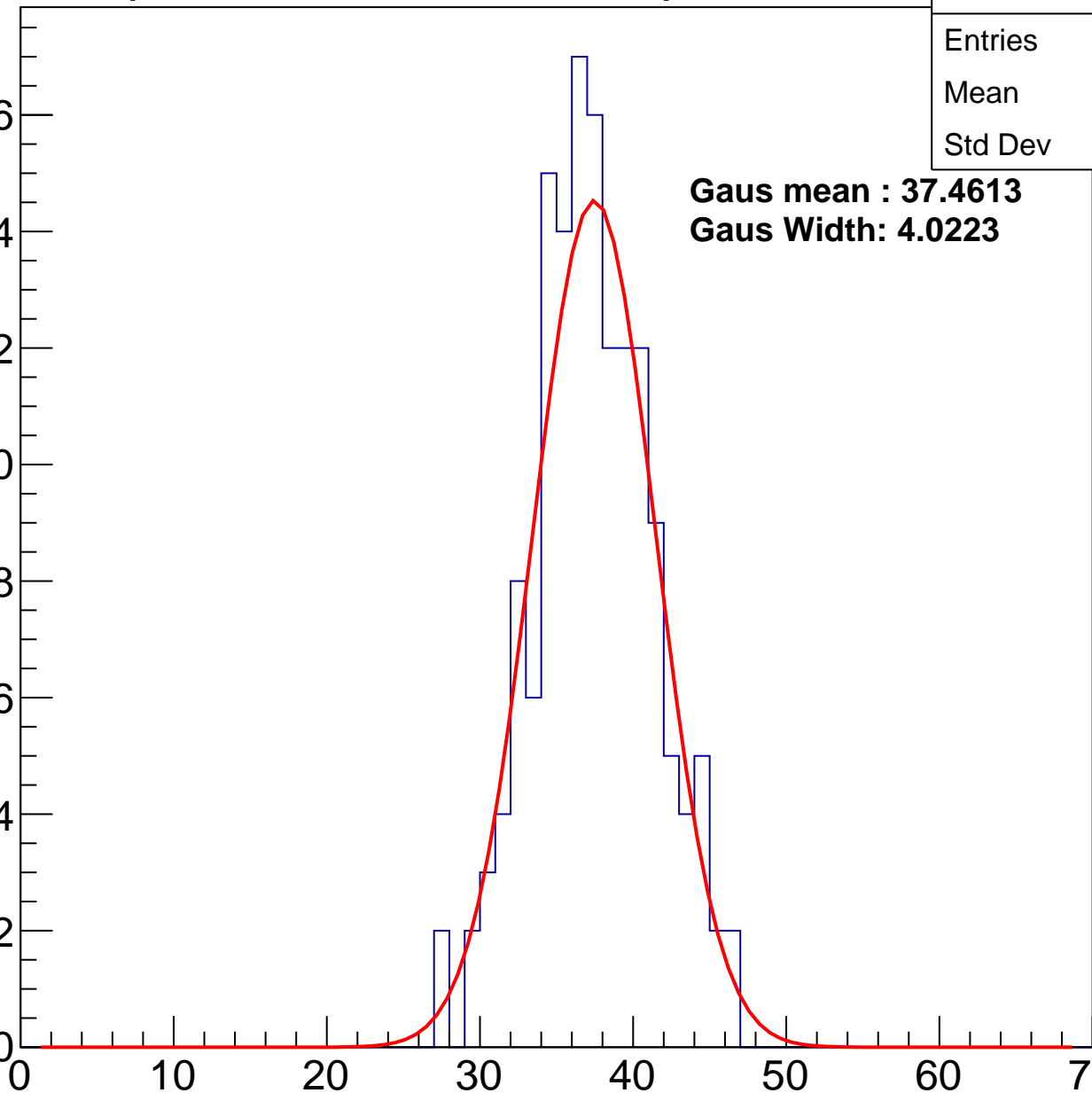
**Gaus mean : 37.4613**

**Gaus Width: 4.0223**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

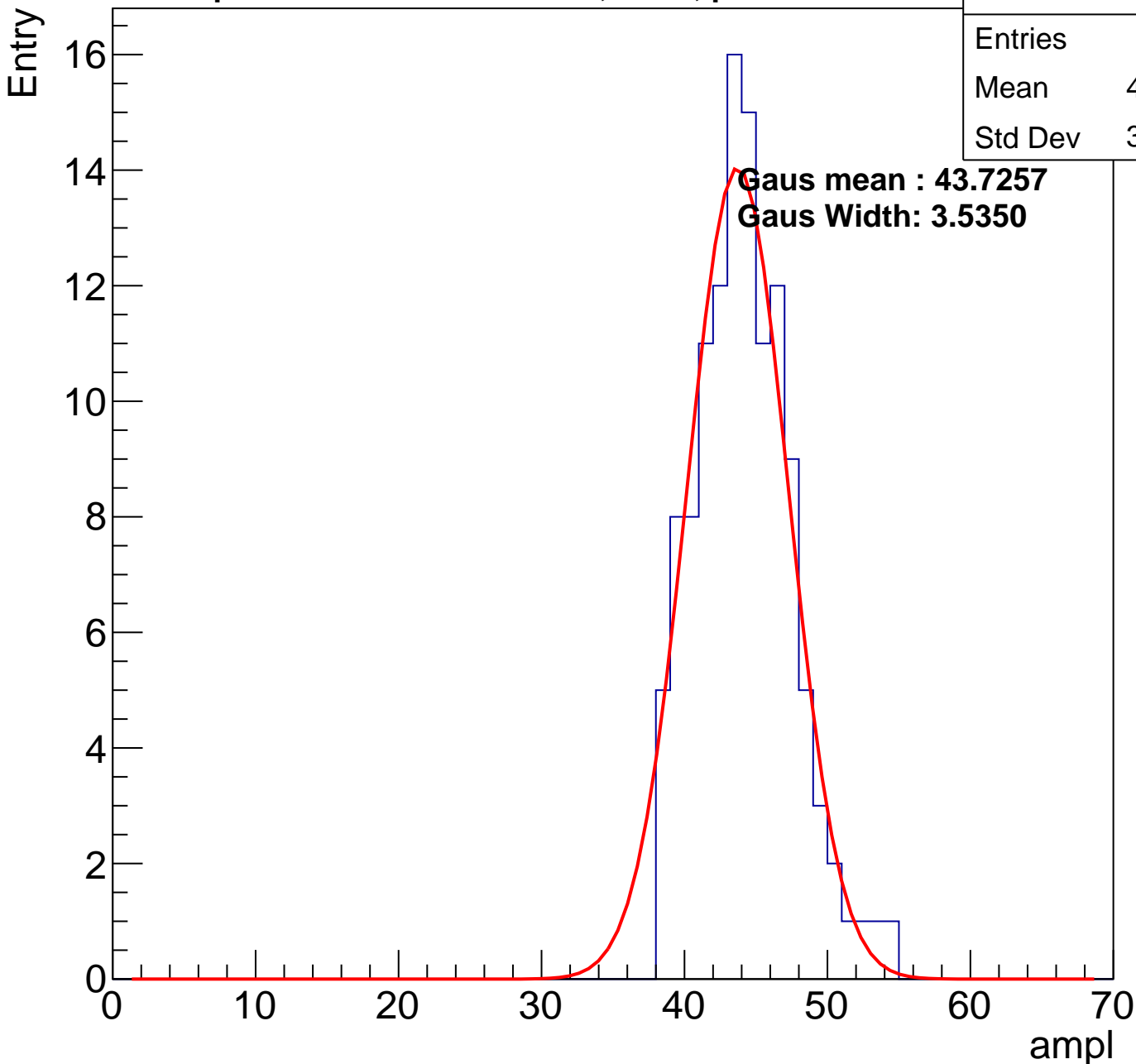


# B0L101S, U23-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	43.74
Std Dev	3.307

**Gaus mean : 43.7257**  
**Gaus Width: 3.5350**



# B0L101S, U23-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

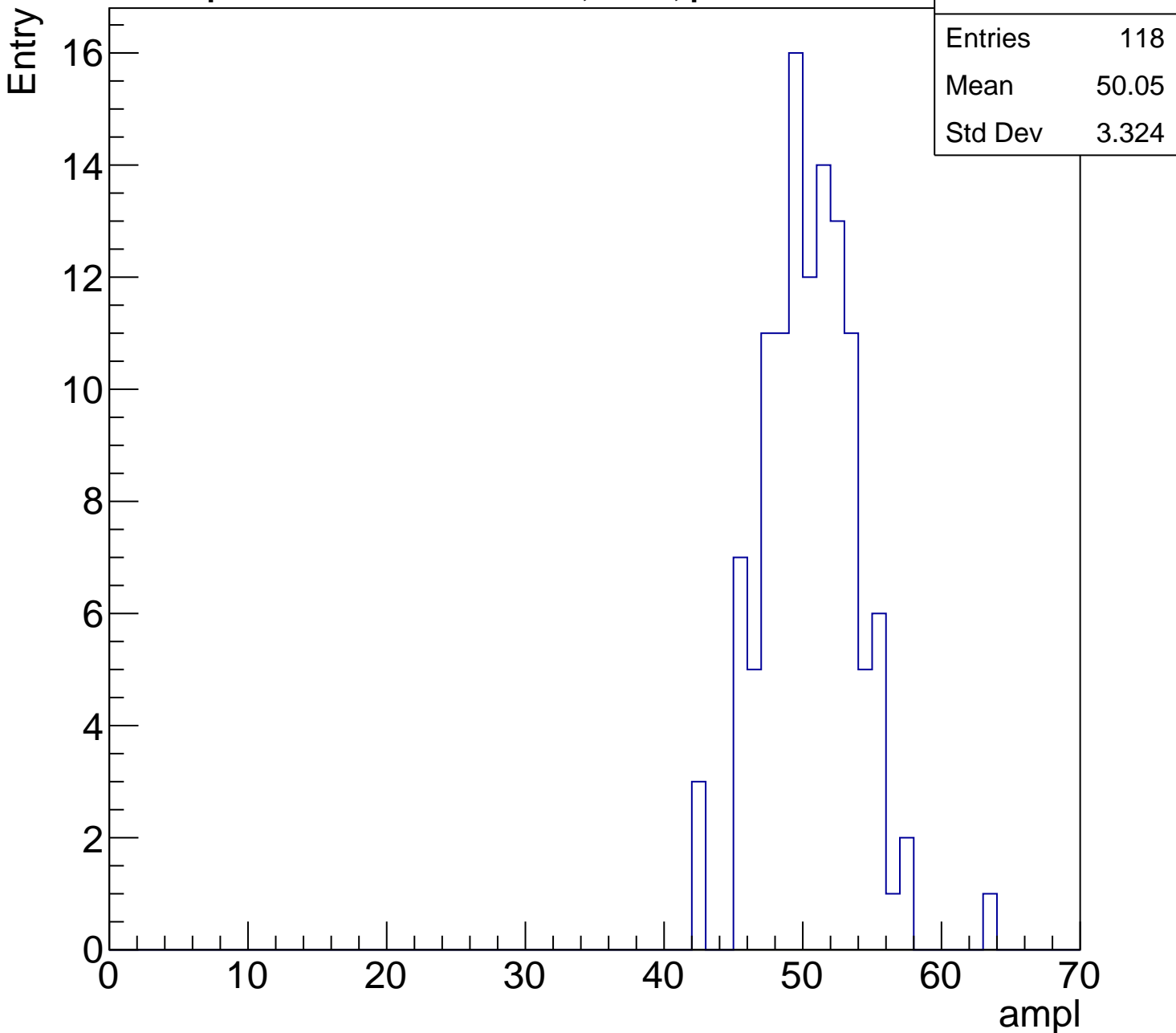
Entries	118
Mean	50.05
Std Dev	3.324

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

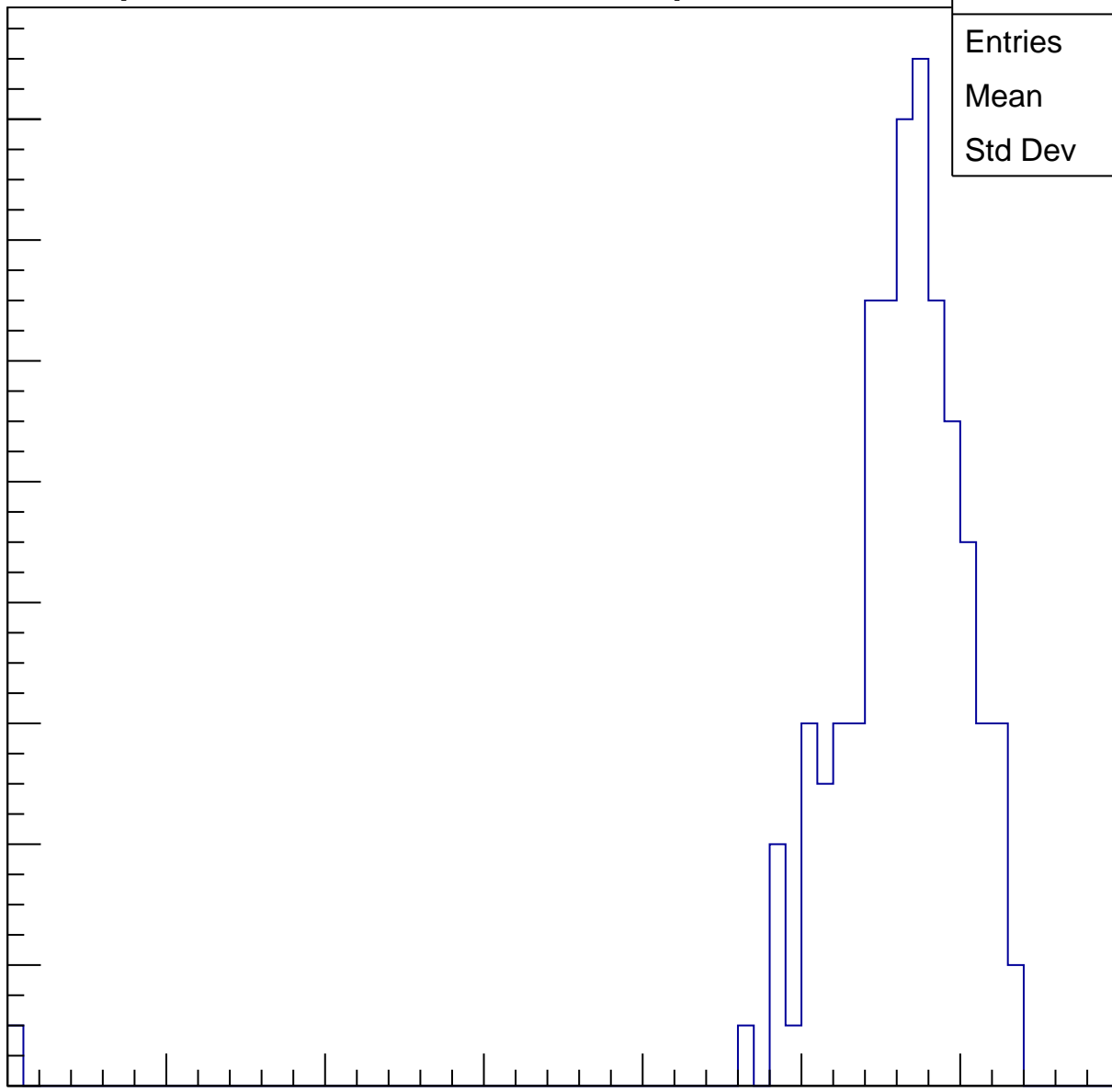
Entries	136
Mean	55.6
Std Dev	5.972

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch66, adc5

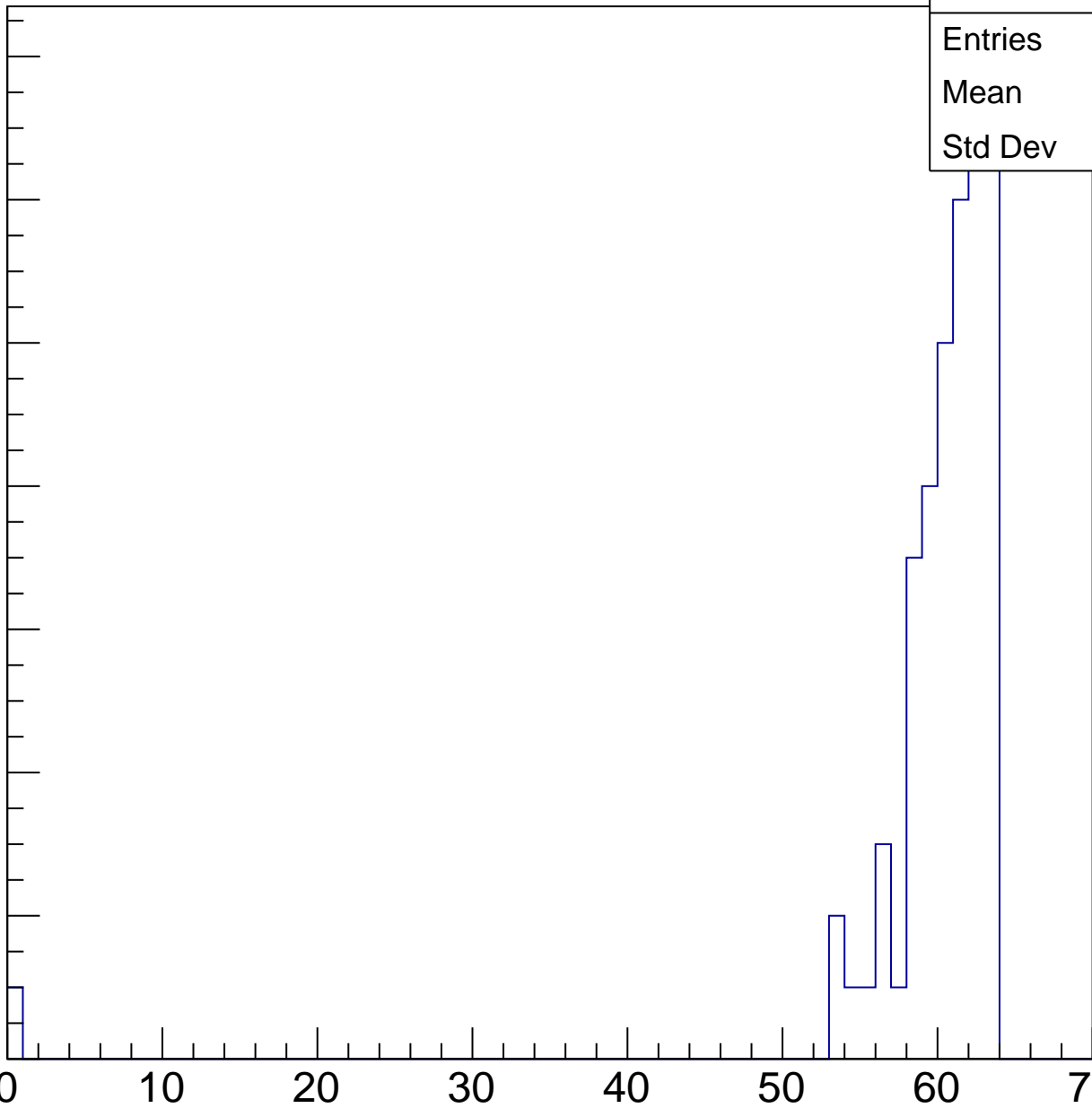
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	59.41
Std Dev	7.413

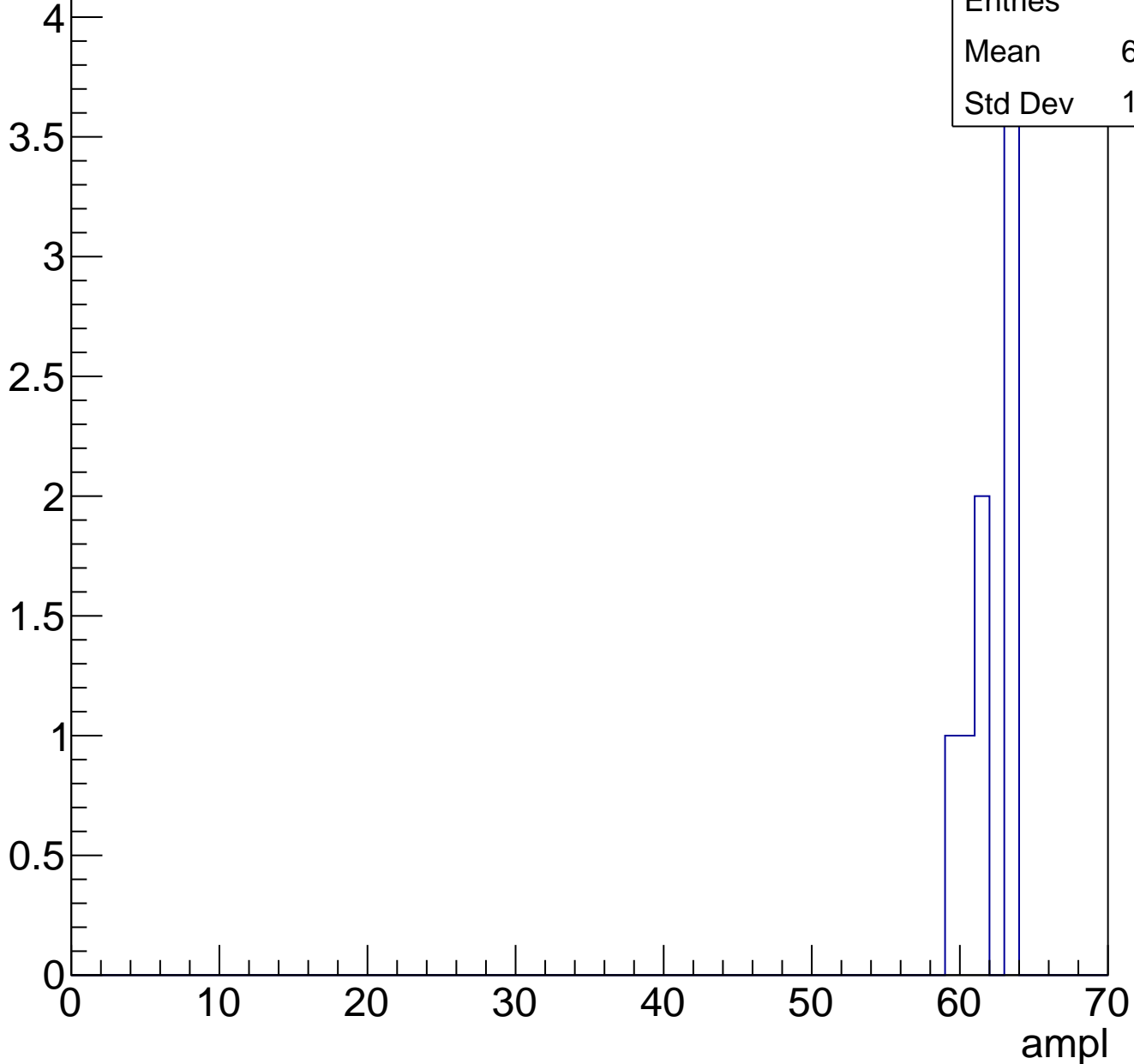
ampl



# B0L101S, U23-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L101S, U23-ch67, adc0

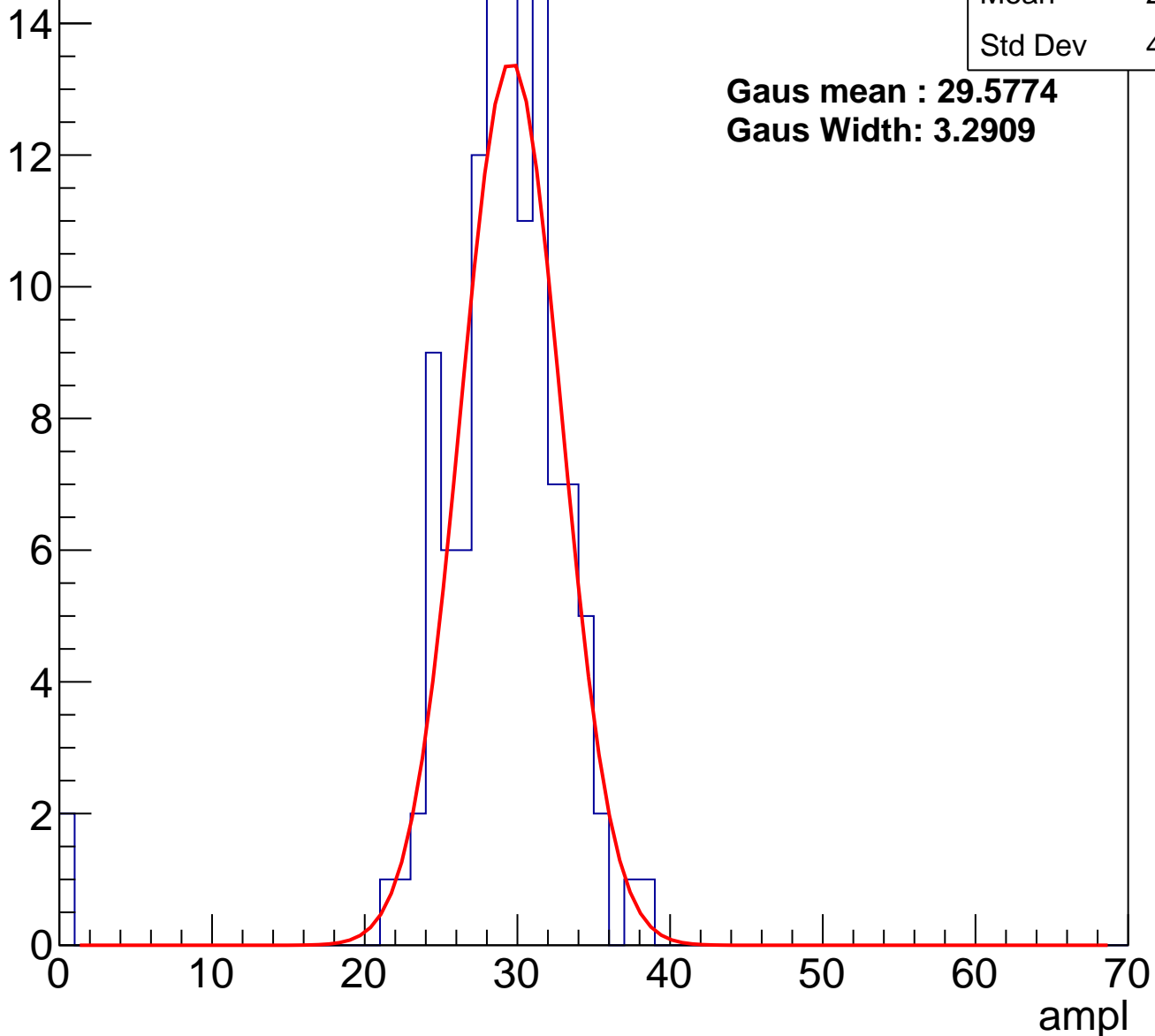
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	28.43
Std Dev	4.919

**Gaus mean : 29.5774**

**Gaus Width: 3.2909**

Entry



# B0L101S, U23-ch67, adc1

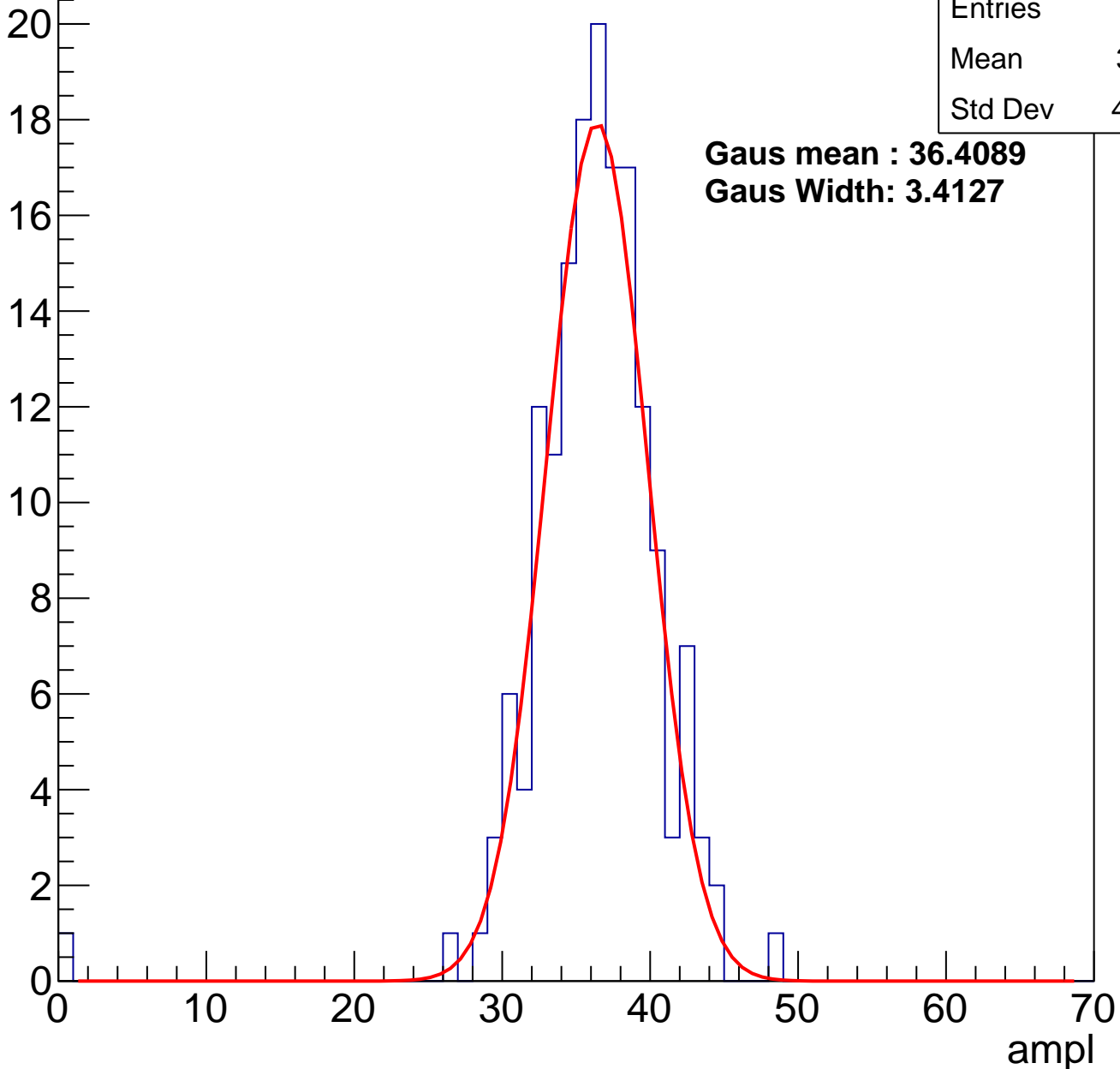
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	35.81
Std Dev	4.546

**Gaus mean : 36.4089**

**Gaus Width: 3.4127**

Entry



# B0L101S, U23-ch67, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

43.5

Std Dev

2.991

**Gaus mean : 43.9447**

**Gaus Width: 3.1506**

0

10

20

30

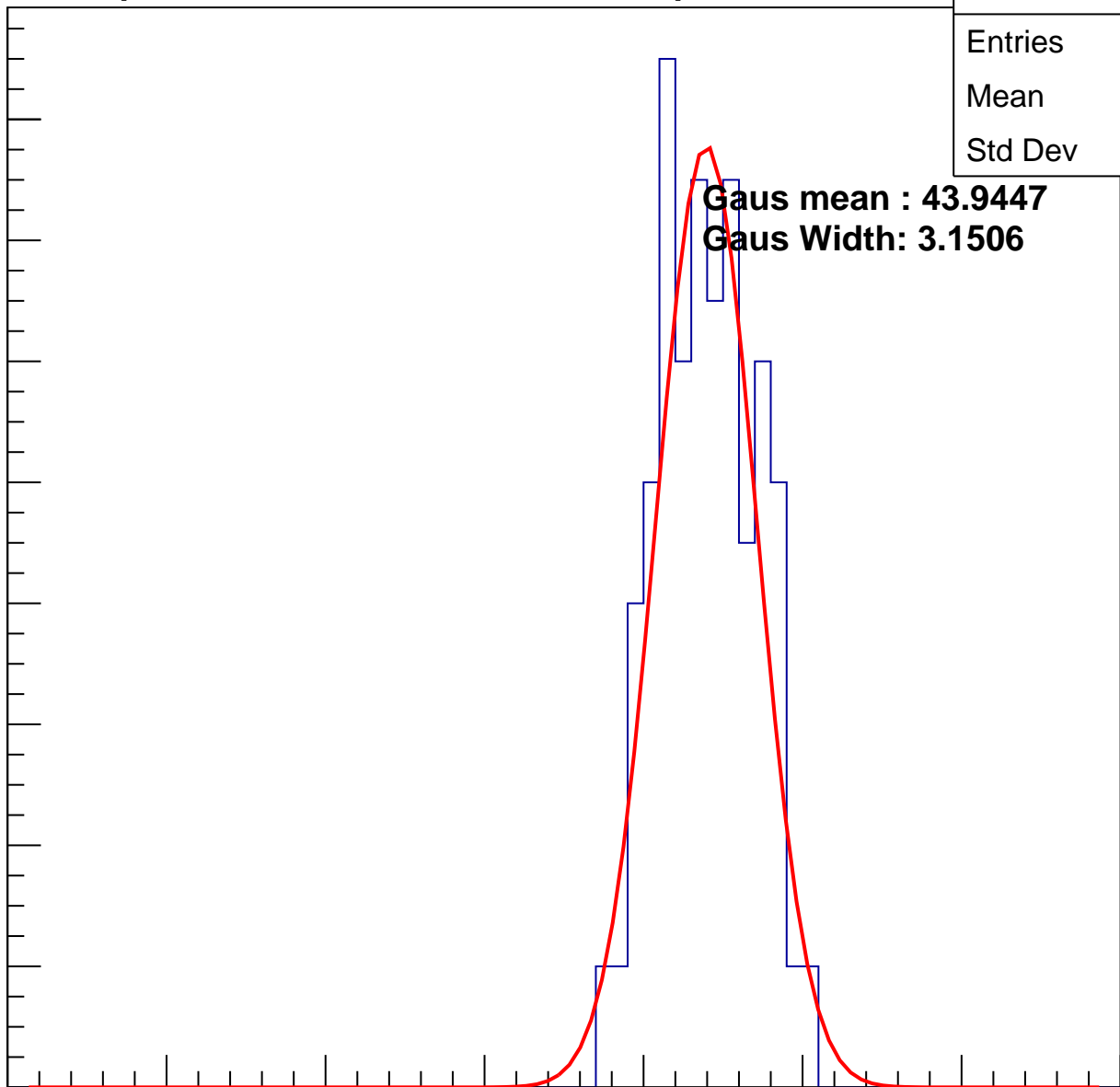
40

50

60

70

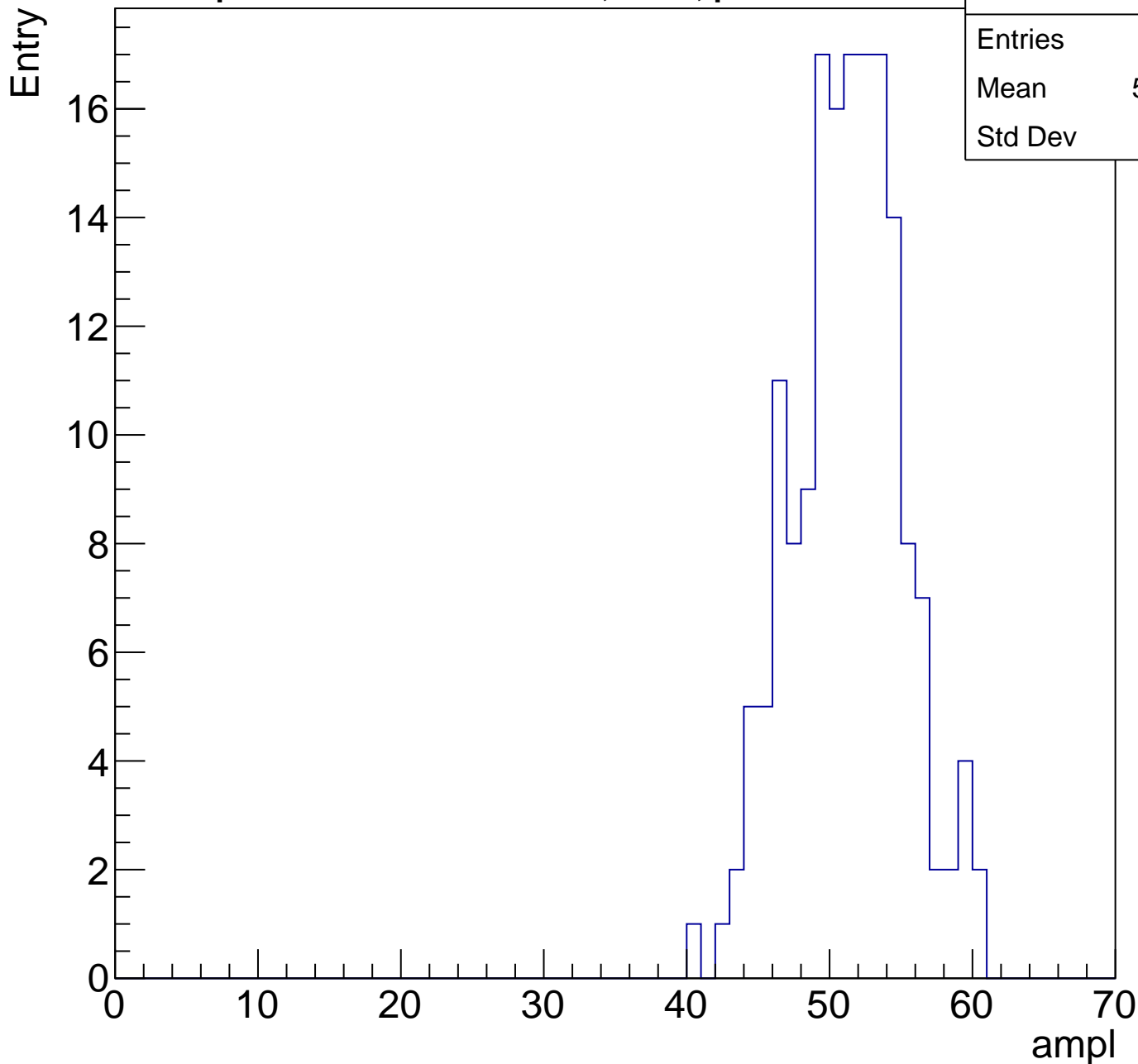
ampl



# B0L101S, U23-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	50.82
Std Dev	3.83



# B0L101S, U23-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

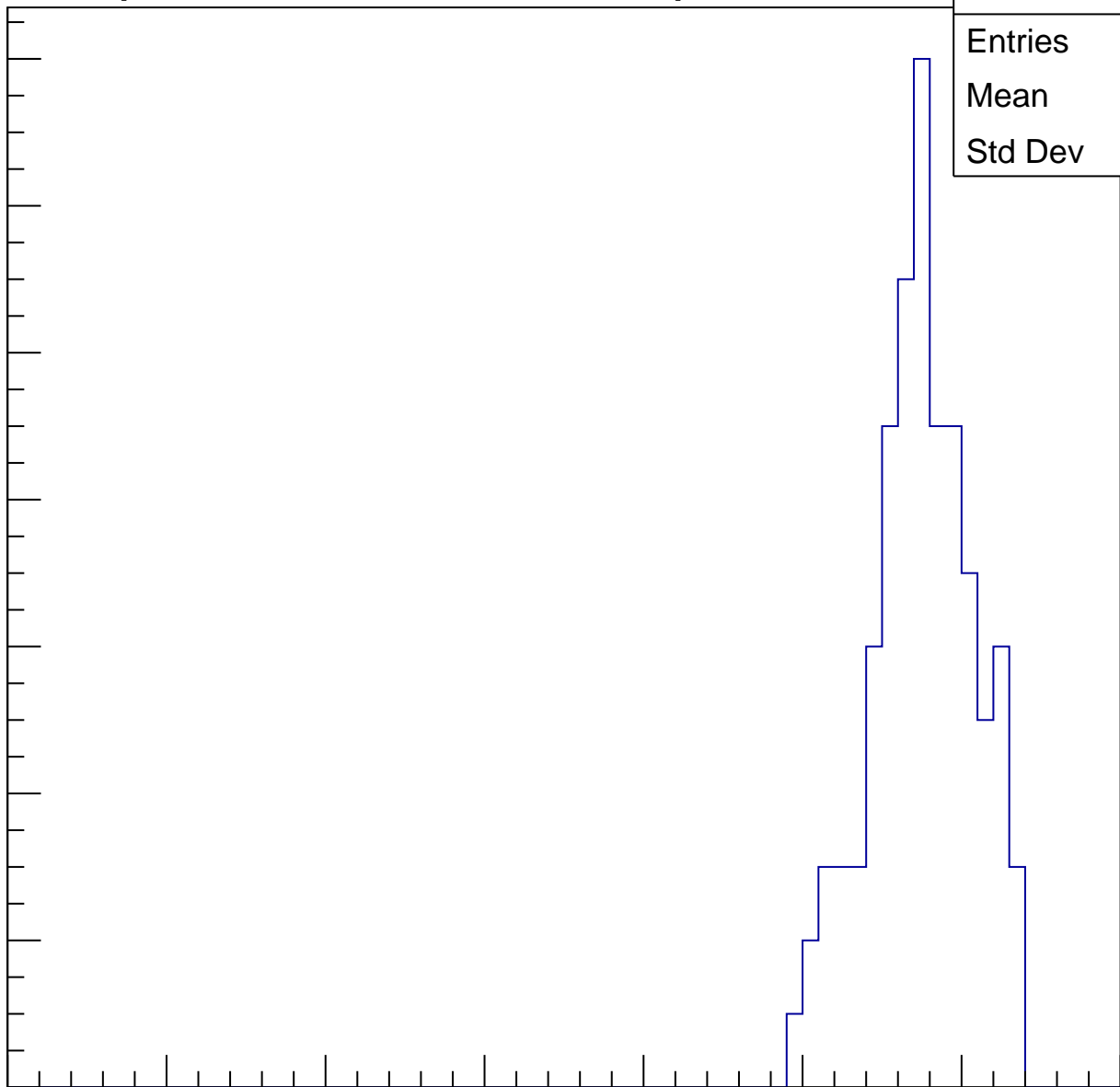
Entries	91
Mean	57.02
Std Dev	3.214

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch67, adc5

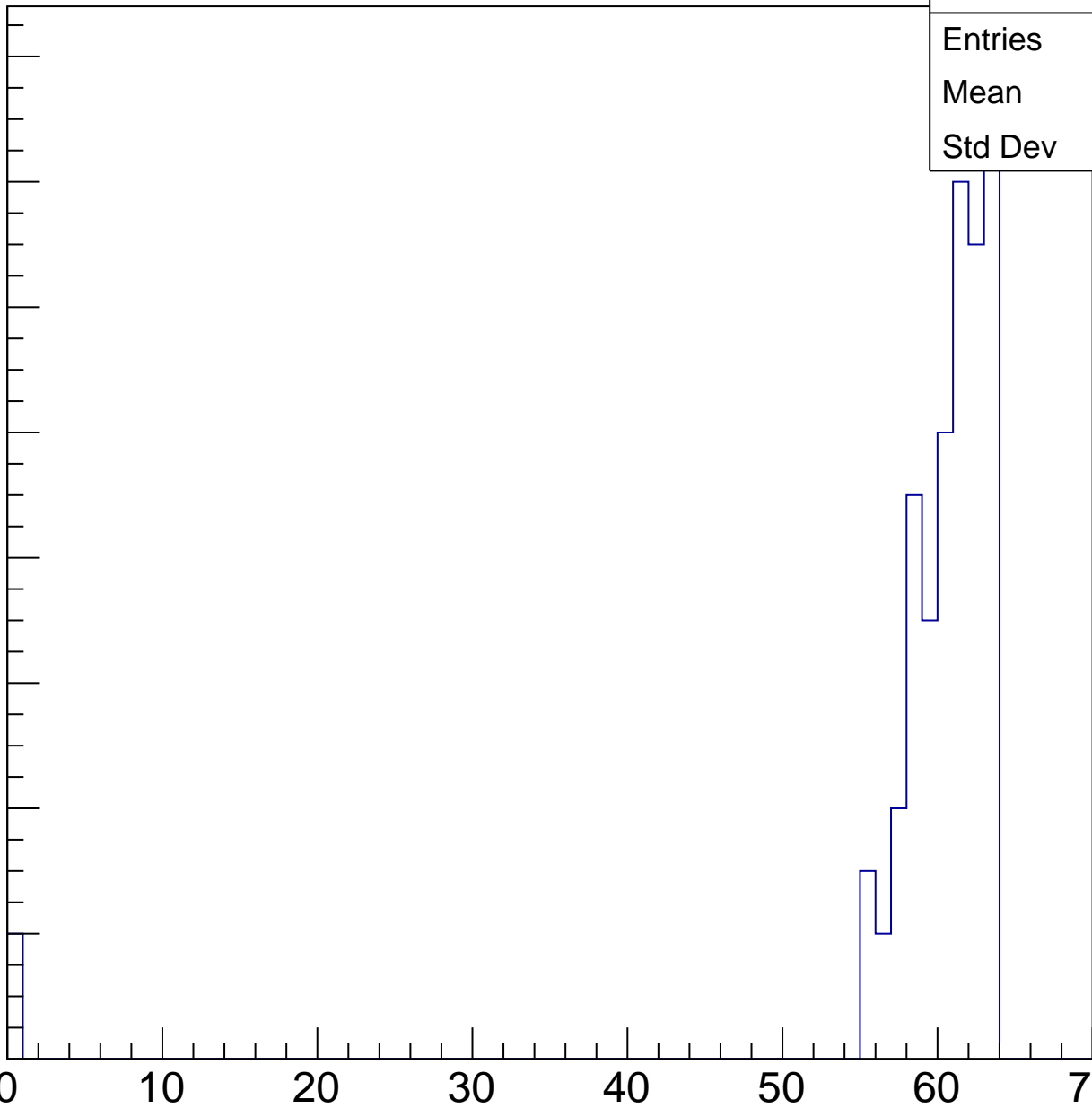
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	80
Mean	58.85
Std Dev	9.676

ampl



# B0L101S, U23-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	27.81
Std Dev	3.852

**Gaus mean : 28.3284**

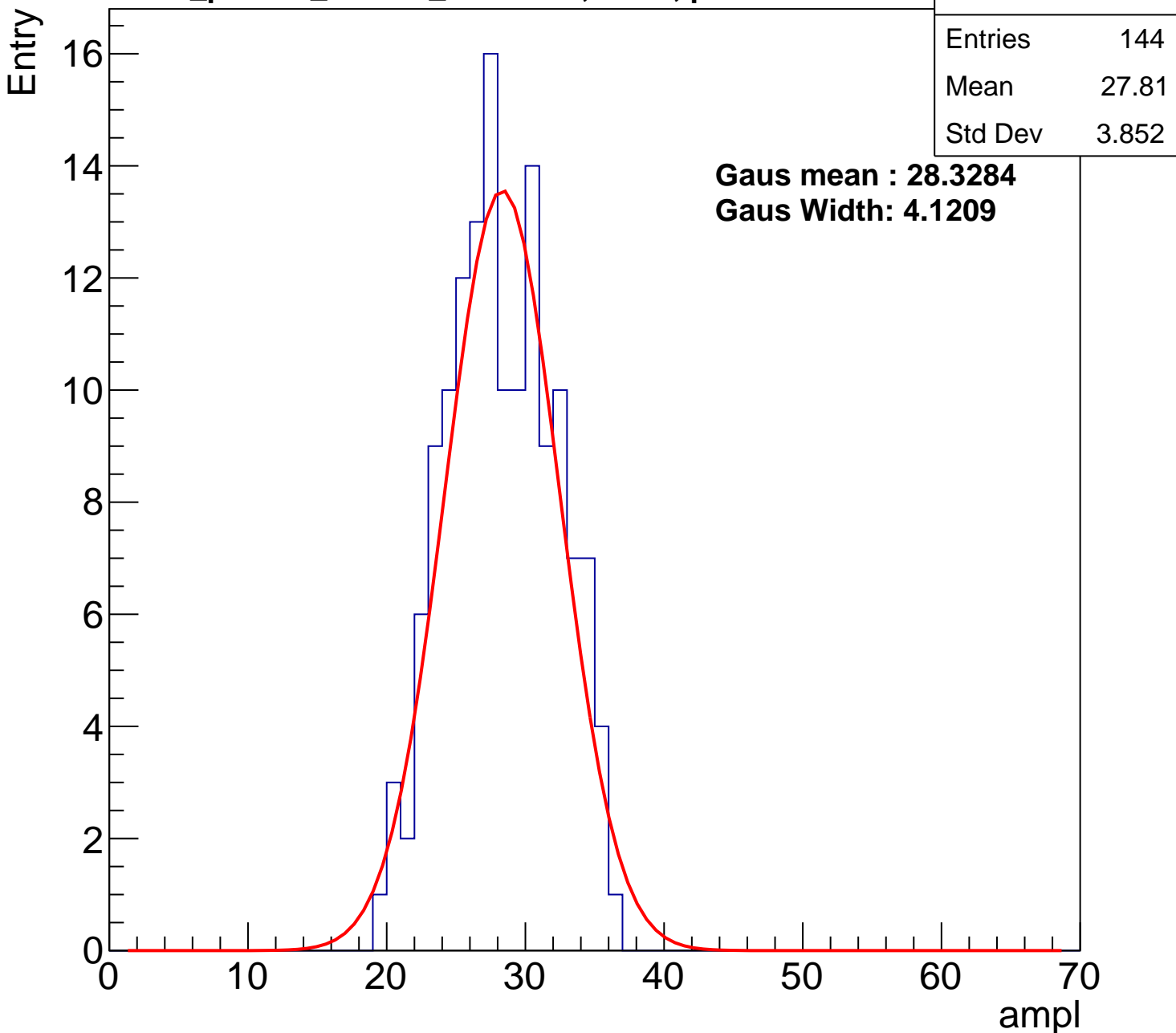
**Gaus Width: 4.1209**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch68, adc1

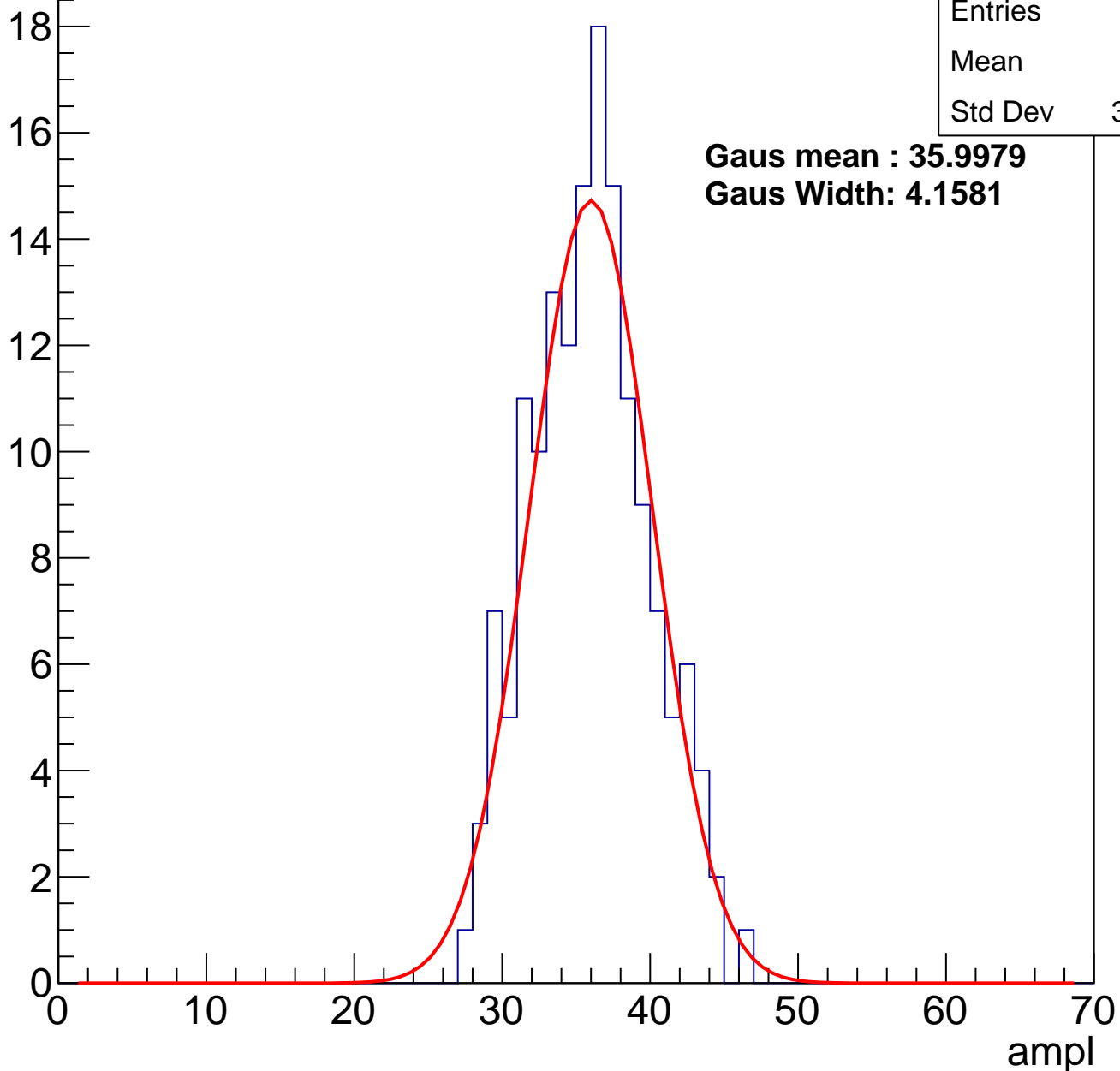
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	35.5
Std Dev	3.926

**Gaus mean : 35.9979**

**Gaus Width: 4.1581**

Entry



# B0L101S, U23-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

159

Mean

43.38

Std Dev

3.766

**Gaus mean : 43.8972**

**Gaus Width: 3.7603**

0

10

20

30

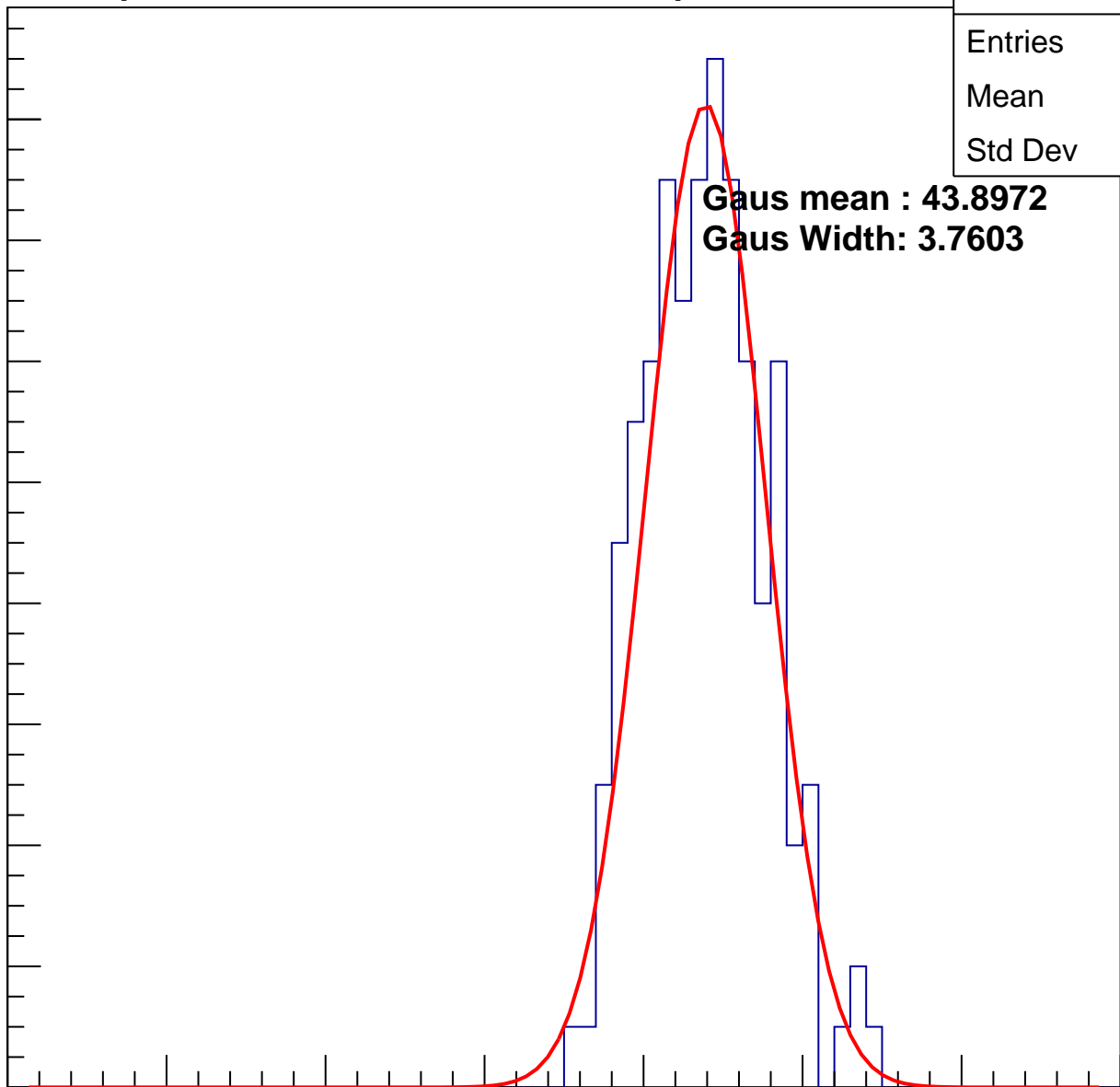
40

50

60

70

ampl

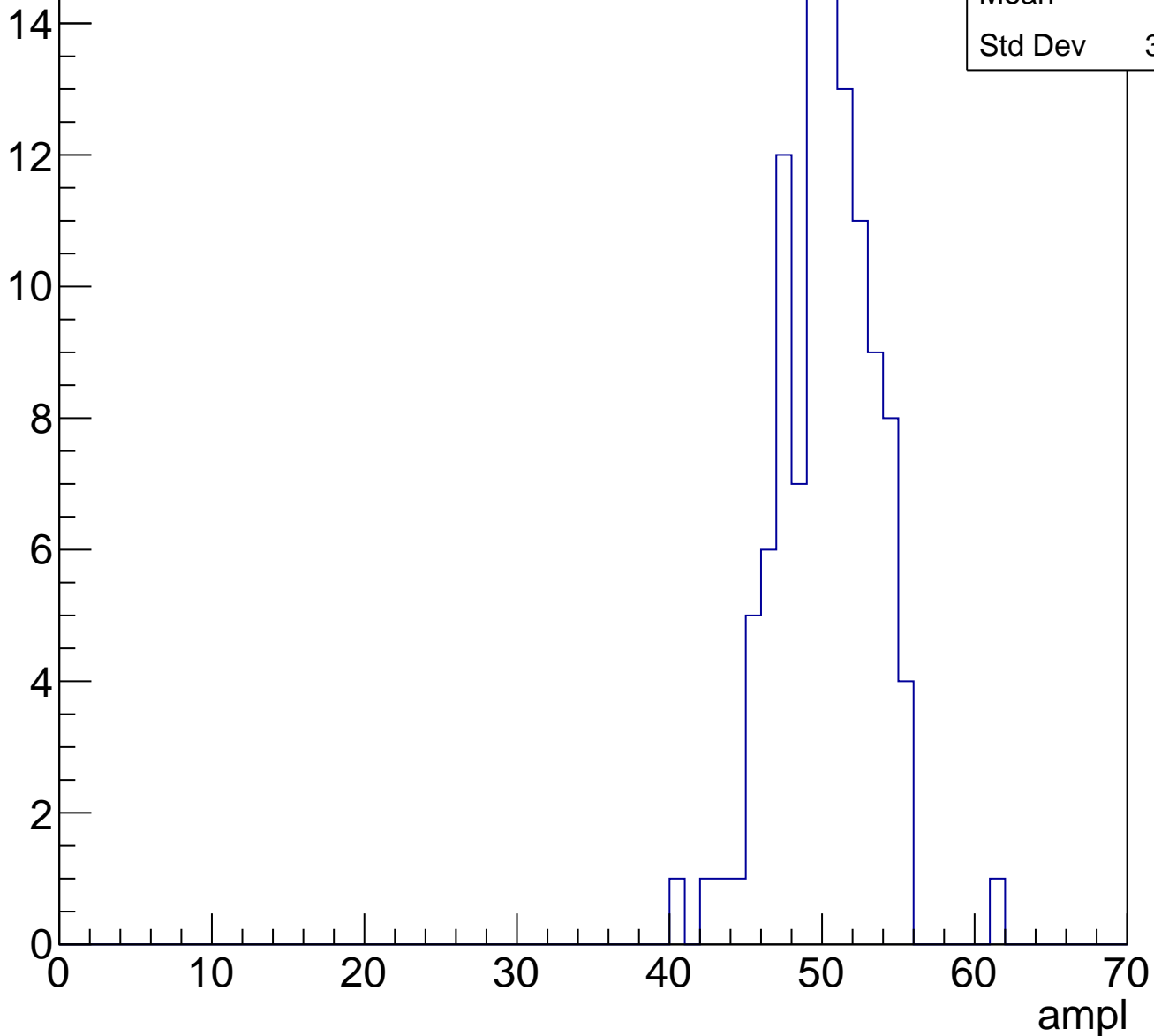


# B0L101S, U23-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	49.82
Std Dev	3.169

Entry

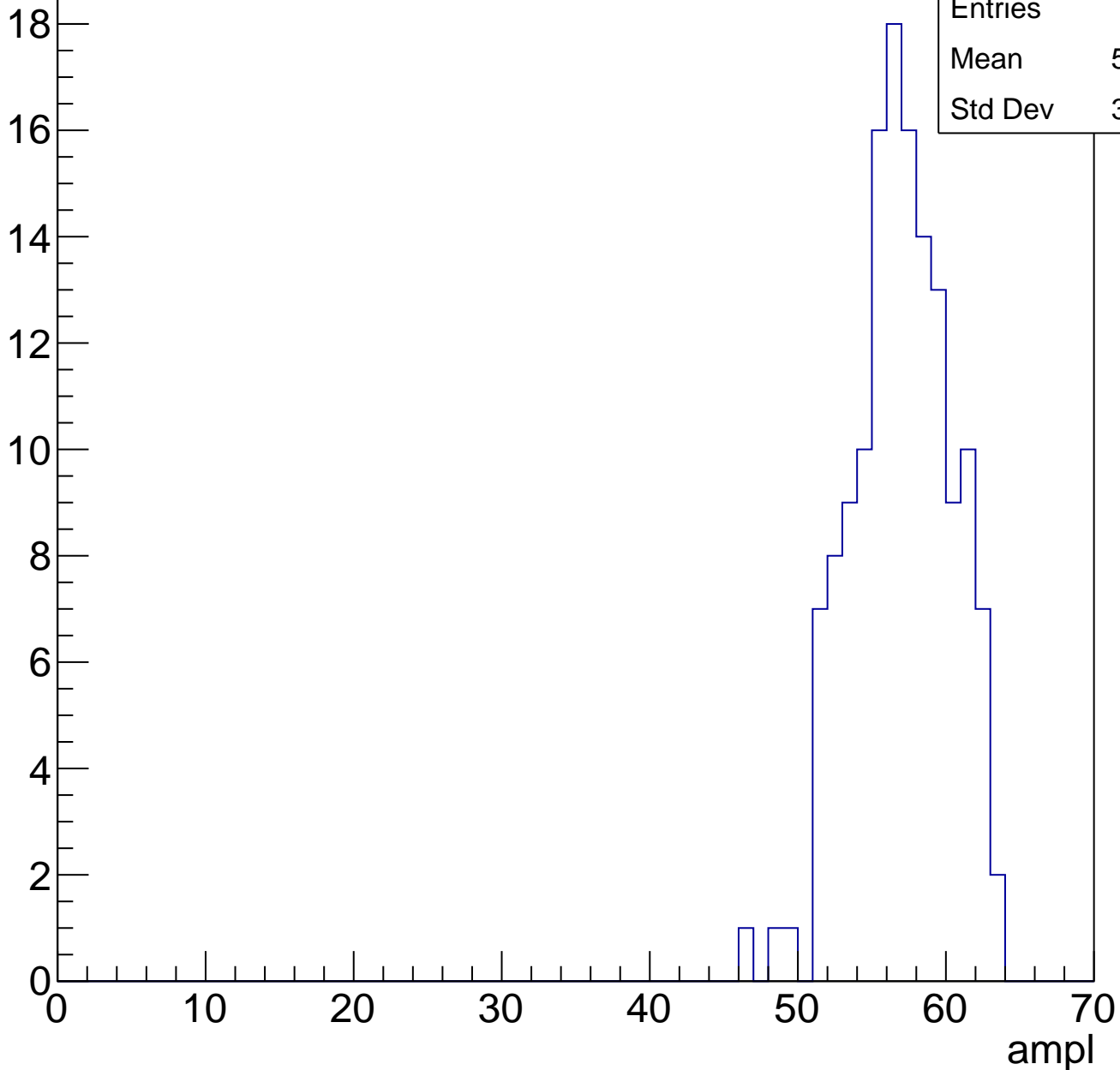


# B0L101S, U23-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	56.49
Std Dev	3.305

Entry



# B0L101S, U23-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	58
Mean	58.26
Std Dev	11.23

Entry

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

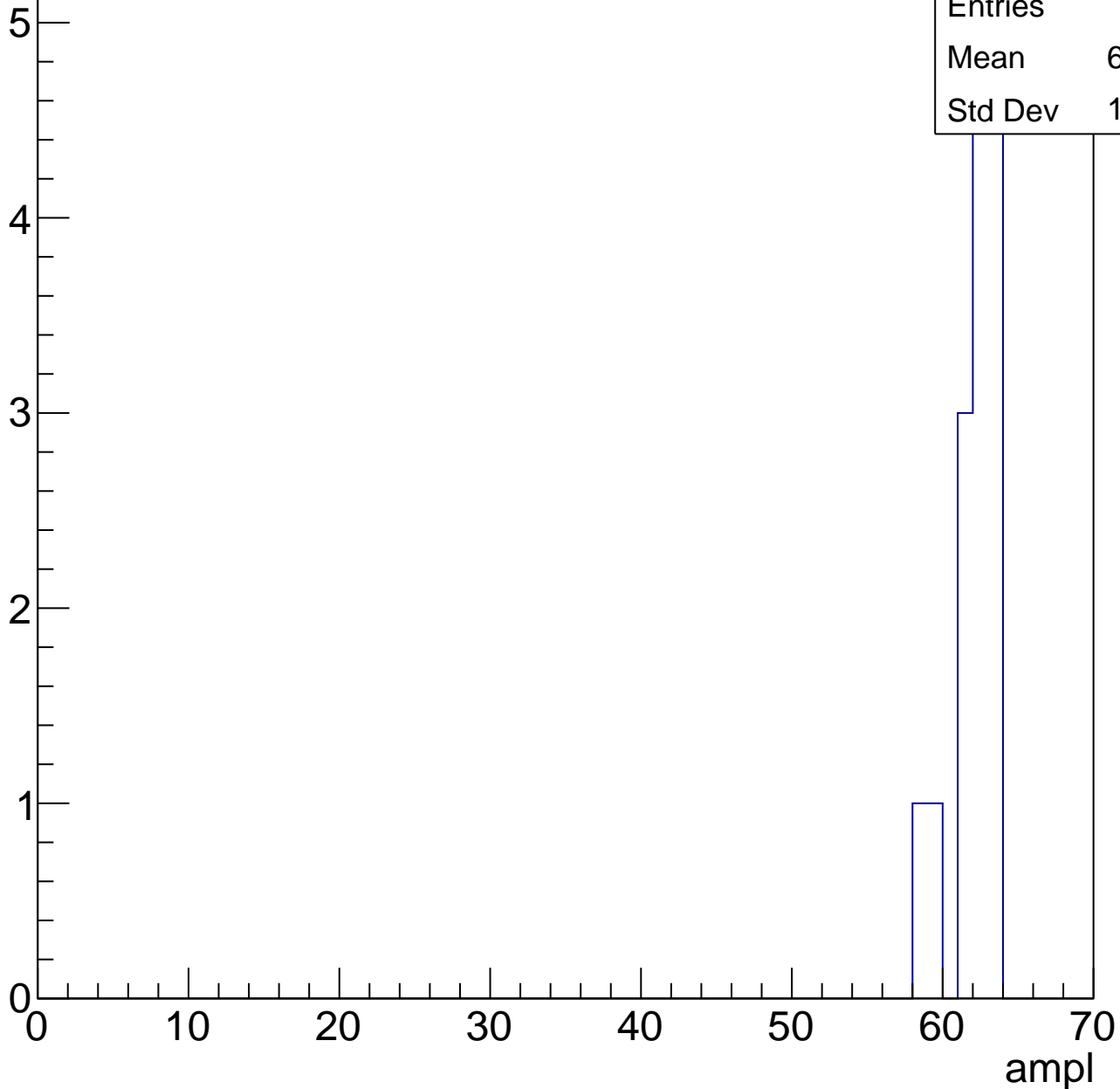
70

# B0L101S, U23-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.67
Std Dev	1.445

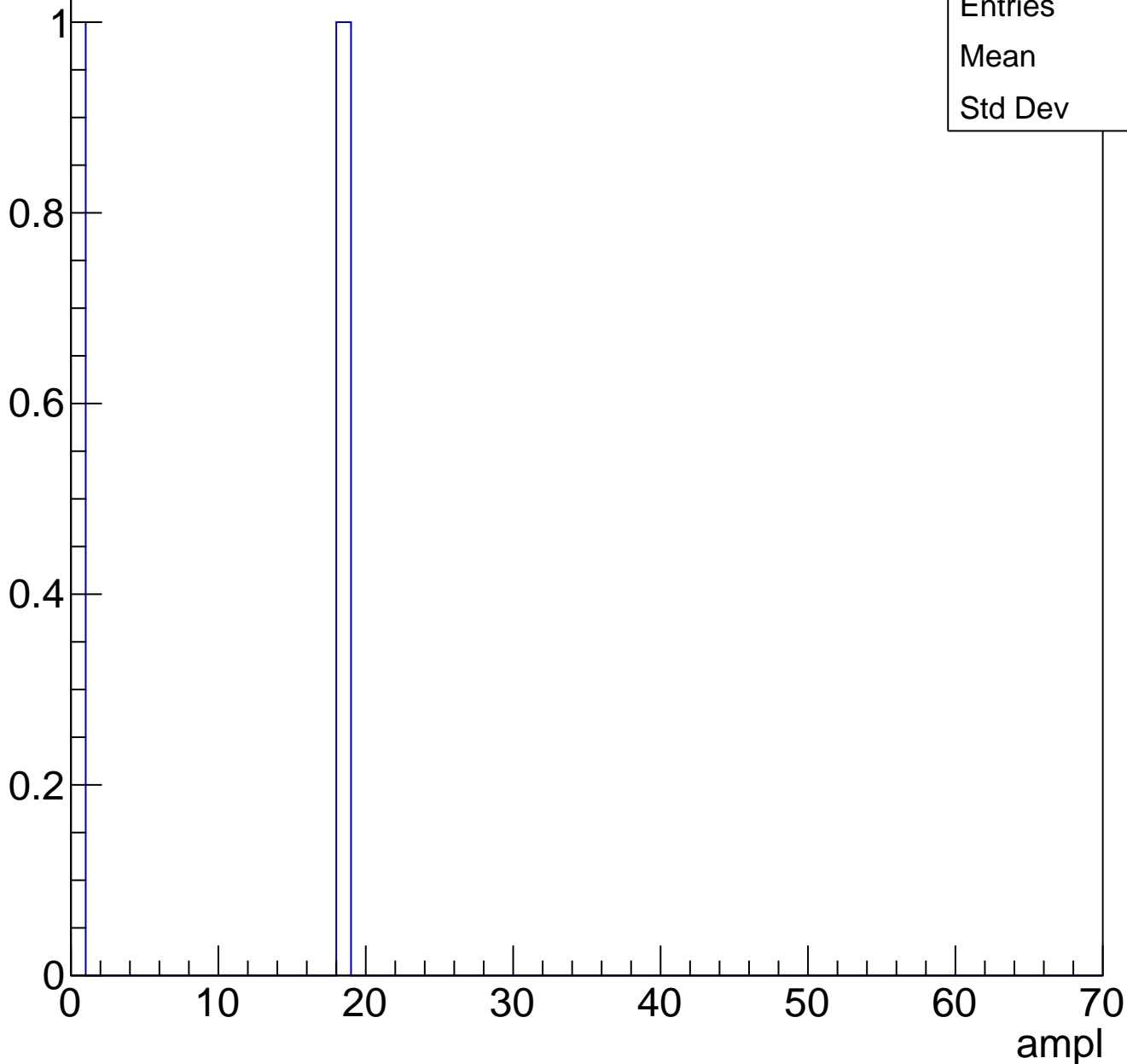




# B0L101S, U23-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	29.47
Std Dev	3.458

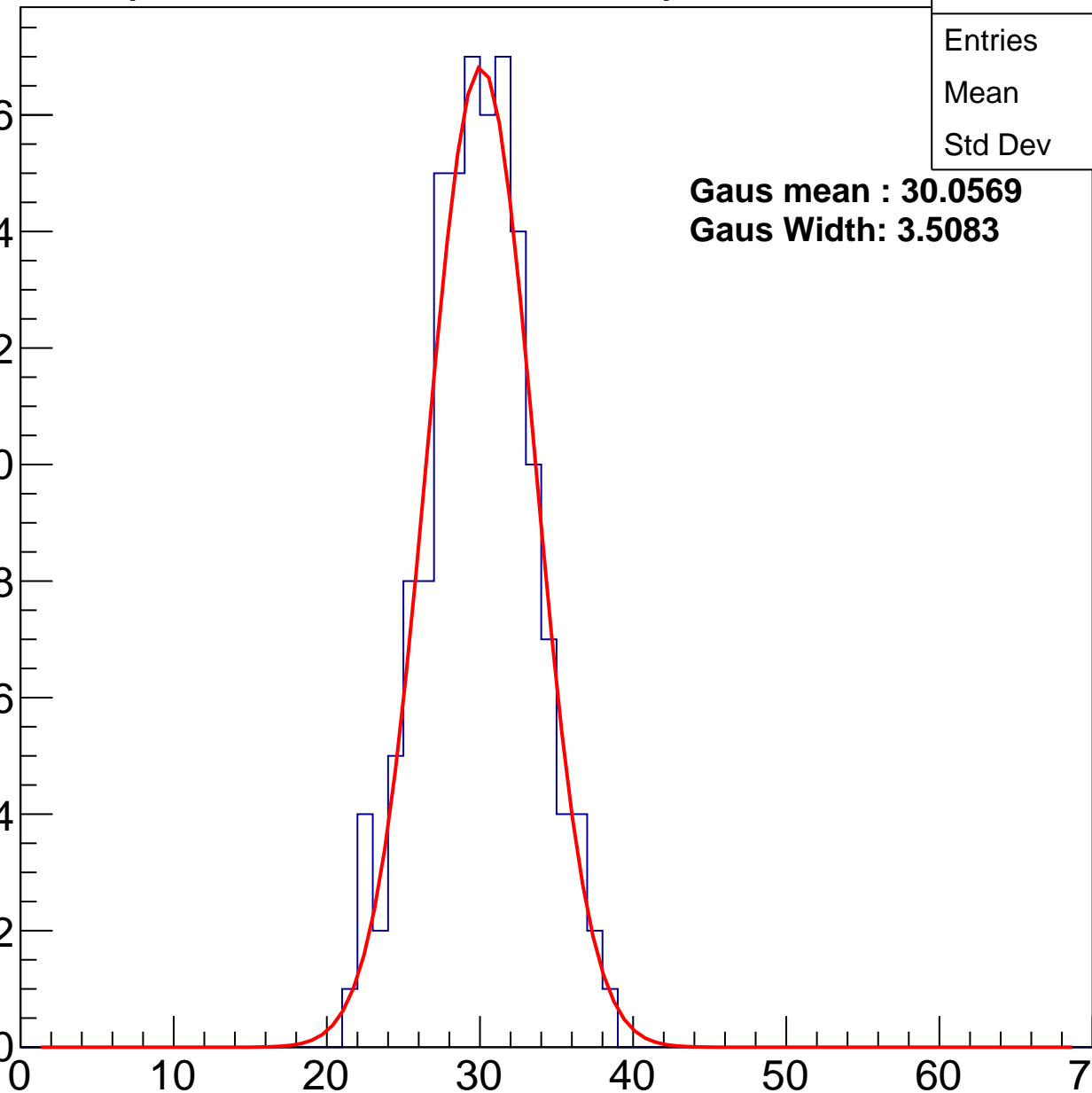
**Gaus mean : 30.0569**

**Gaus Width: 3.5083**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

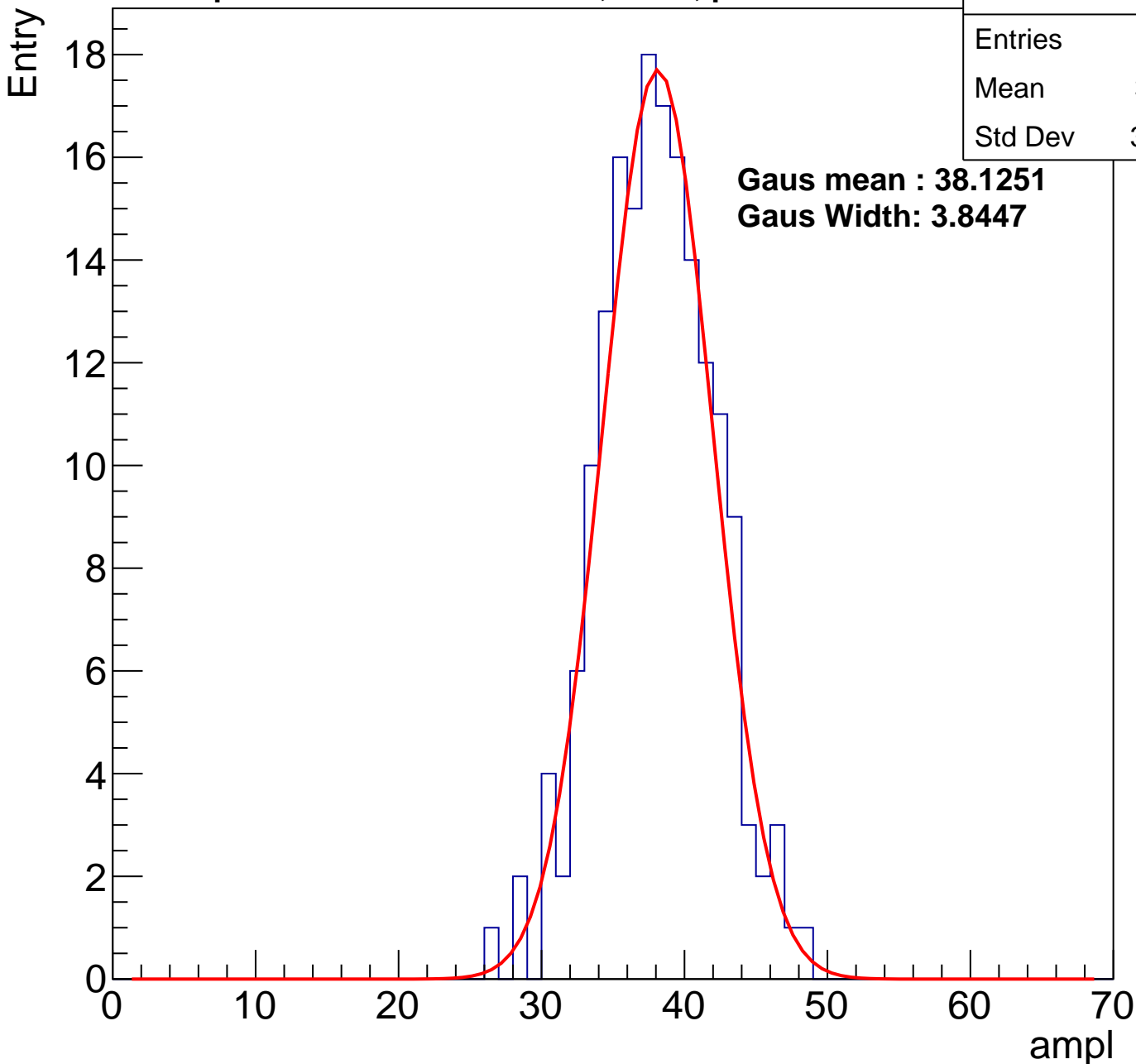


# B0L101S, U23-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	176
Mean	37.61
Std Dev	3.937

**Gaus mean : 38.1251**  
**Gaus Width: 3.8447**



# B0L101S, U23-ch69, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	97
Mean	44.22
Std Dev	3.153

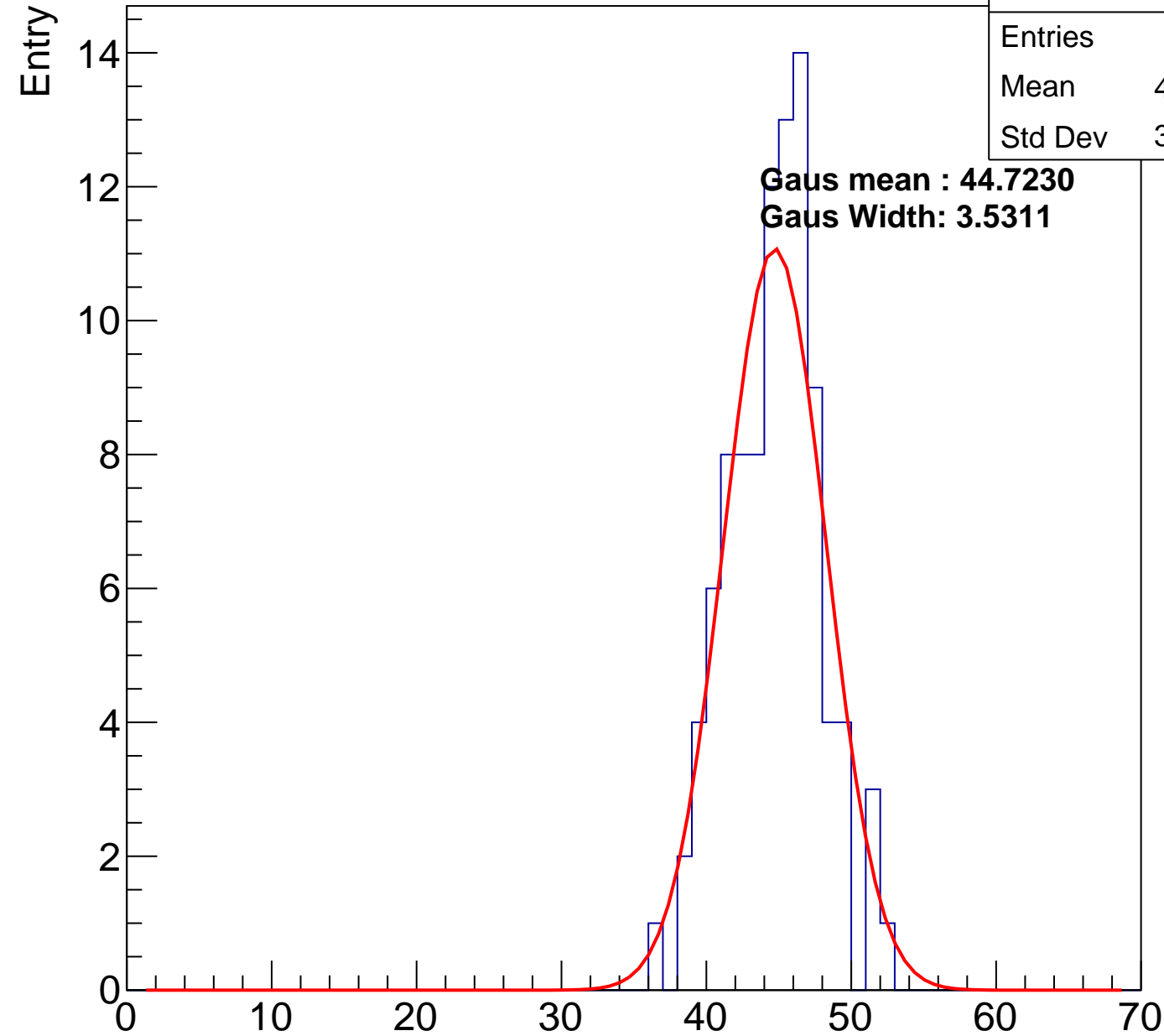
**Gaus mean : 44.7230**

**Gaus Width: 3.5311**

Entry

14  
12  
10  
8  
6  
4  
2  
0

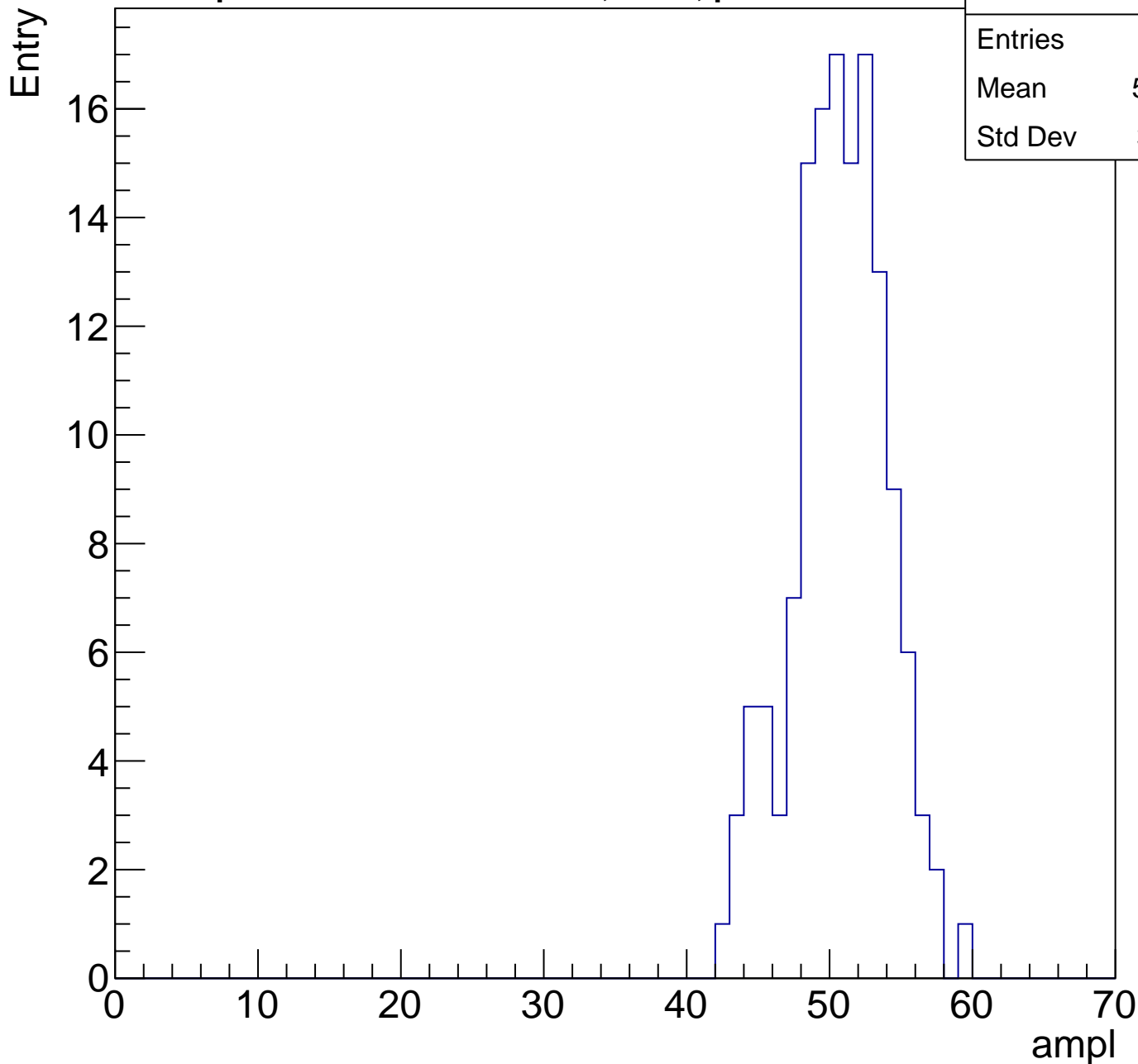
ampl



# B0L101S, U23-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	50.23
Std Dev	3.271



# B0L101S, U23-ch69, adc4

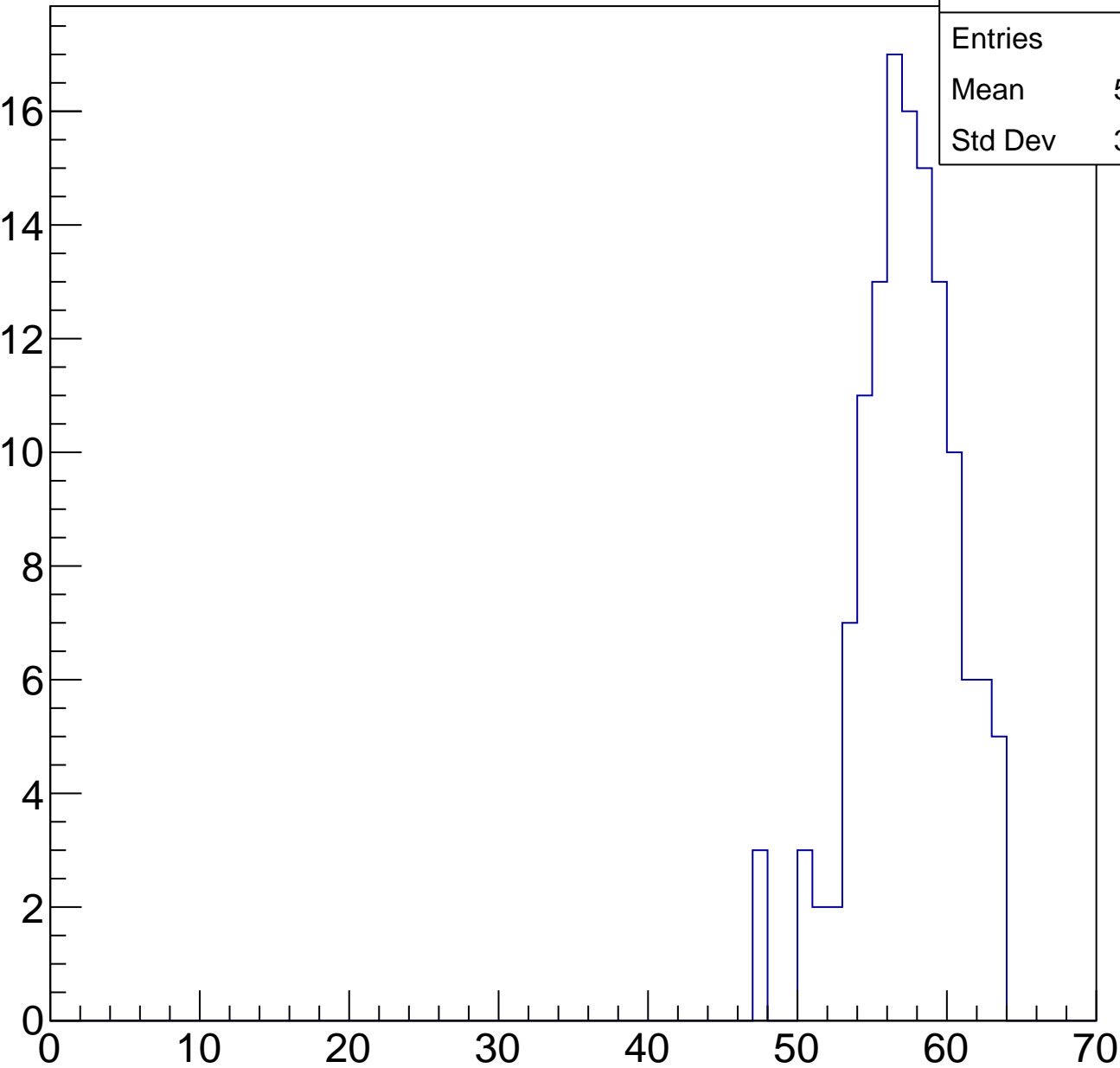
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	56.83
Std Dev	3.327

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	60
Mean	58.7
Std Dev	11.05

ampl

0

10

20

30

40

50

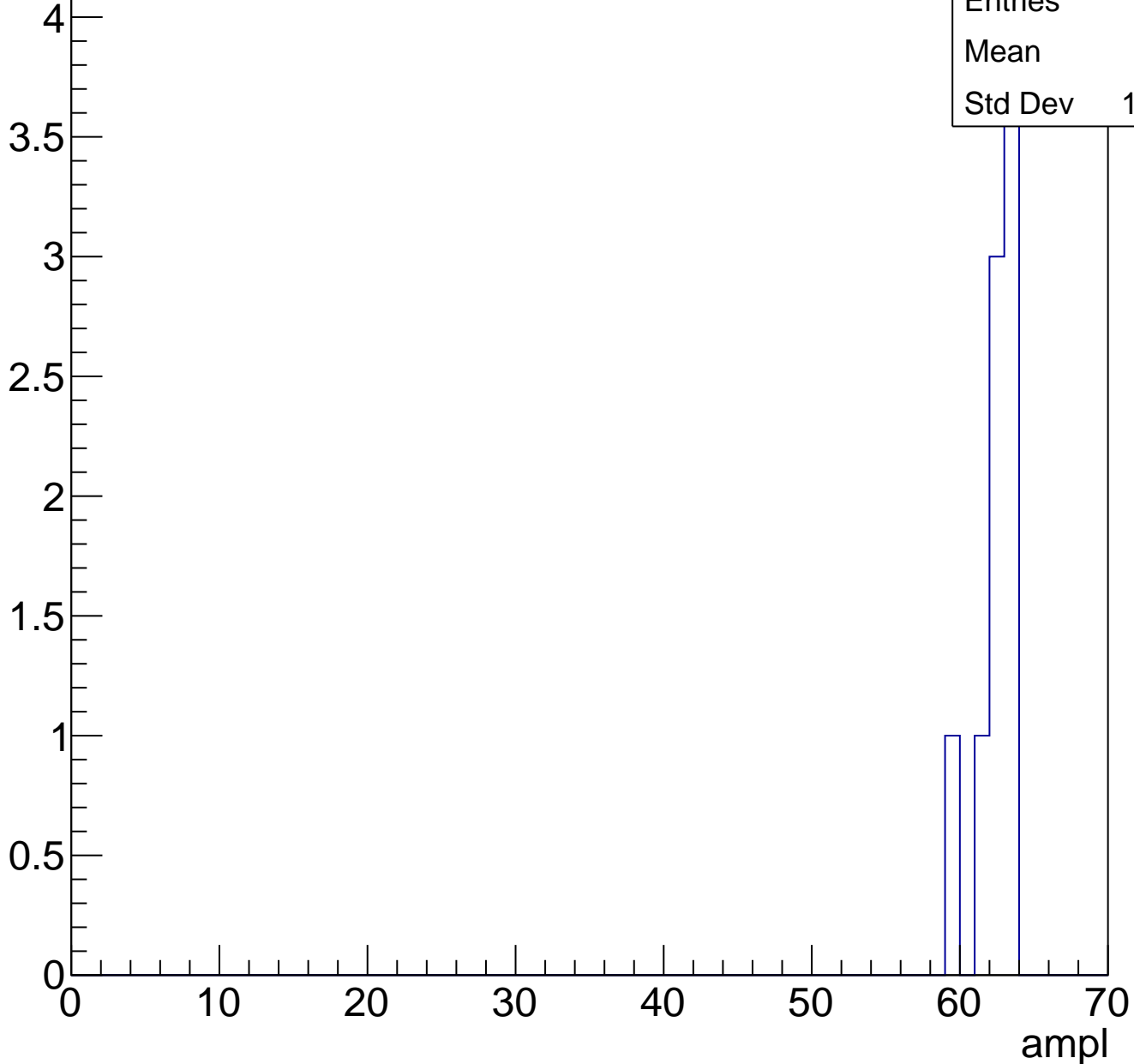
60

70

# B0L101S, U23-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	9
Mean	62
Std Dev	1.247



# B0L101S, U23-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	27.98
Std Dev	3.405

**Gaus mean : 28.4555**

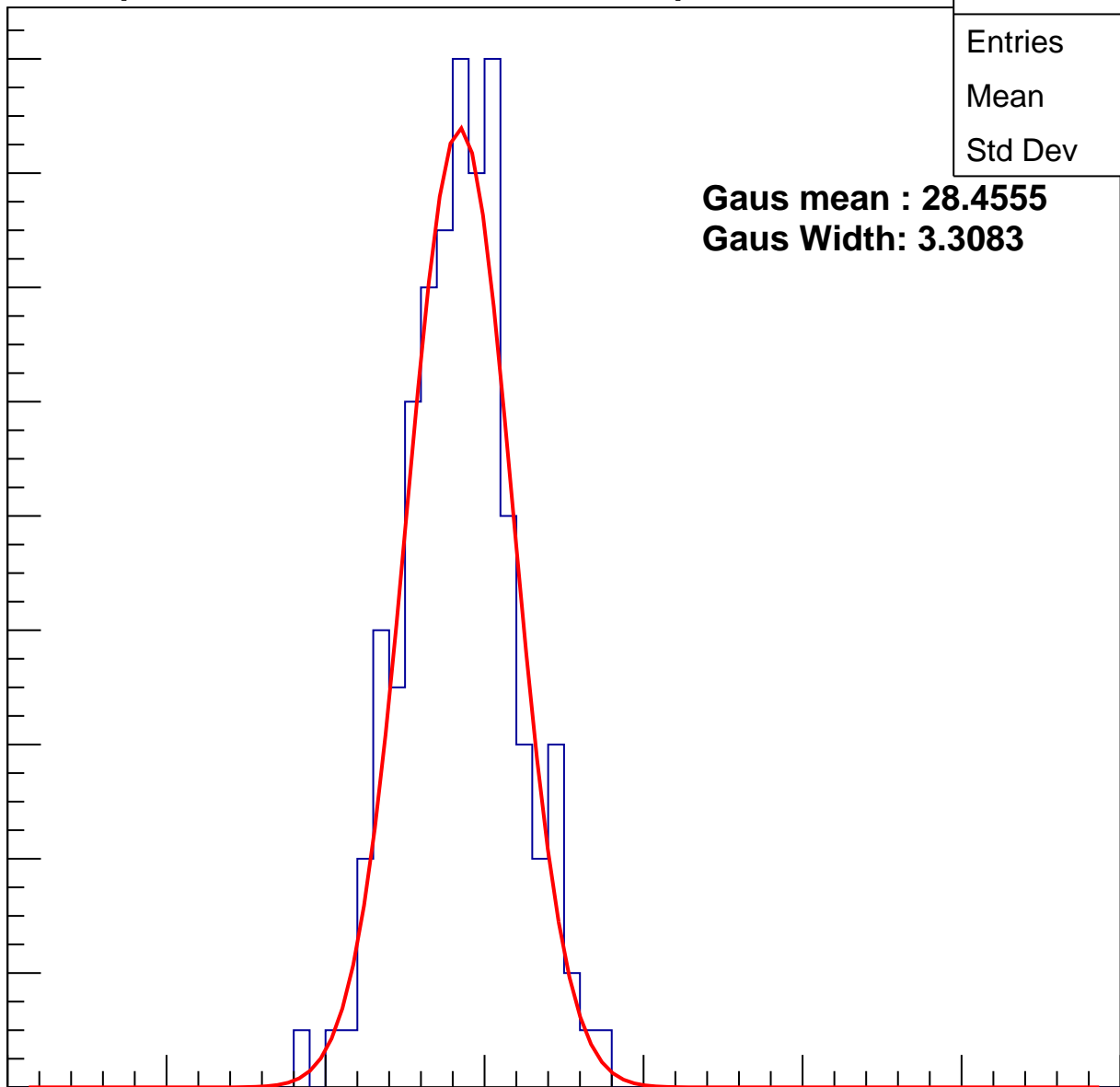
**Gaus Width: 3.3083**

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch70, adc1

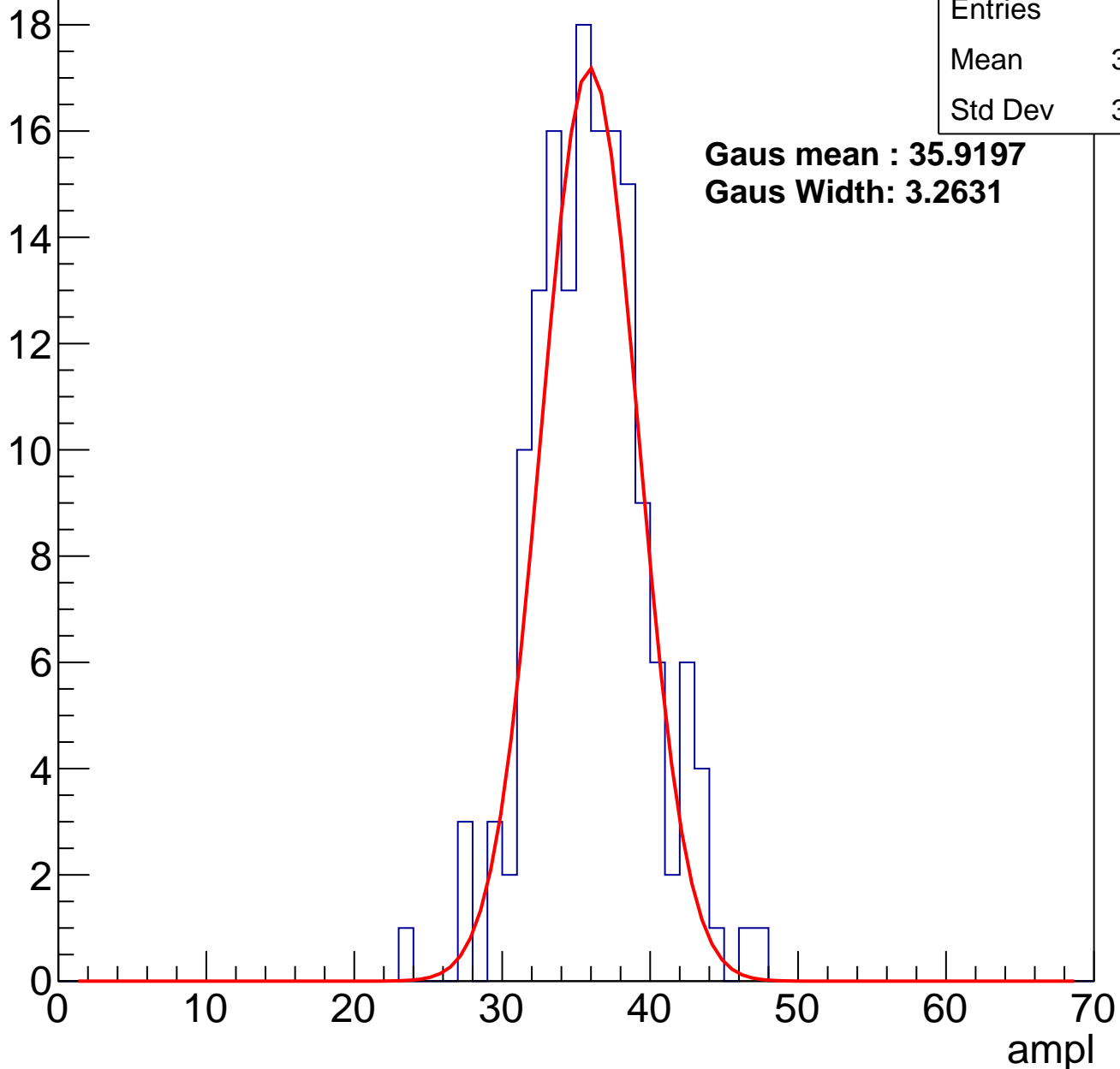
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	35.57
Std Dev	3.818

**Gaus mean : 35.9197**

**Gaus Width: 3.2631**

Entry



# B0L101S, U23-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

122

Mean

42.37

Std Dev

3.079

**Gaus mean : 42.8310**

**Gaus Width: 3.2676**

0

10

20

30

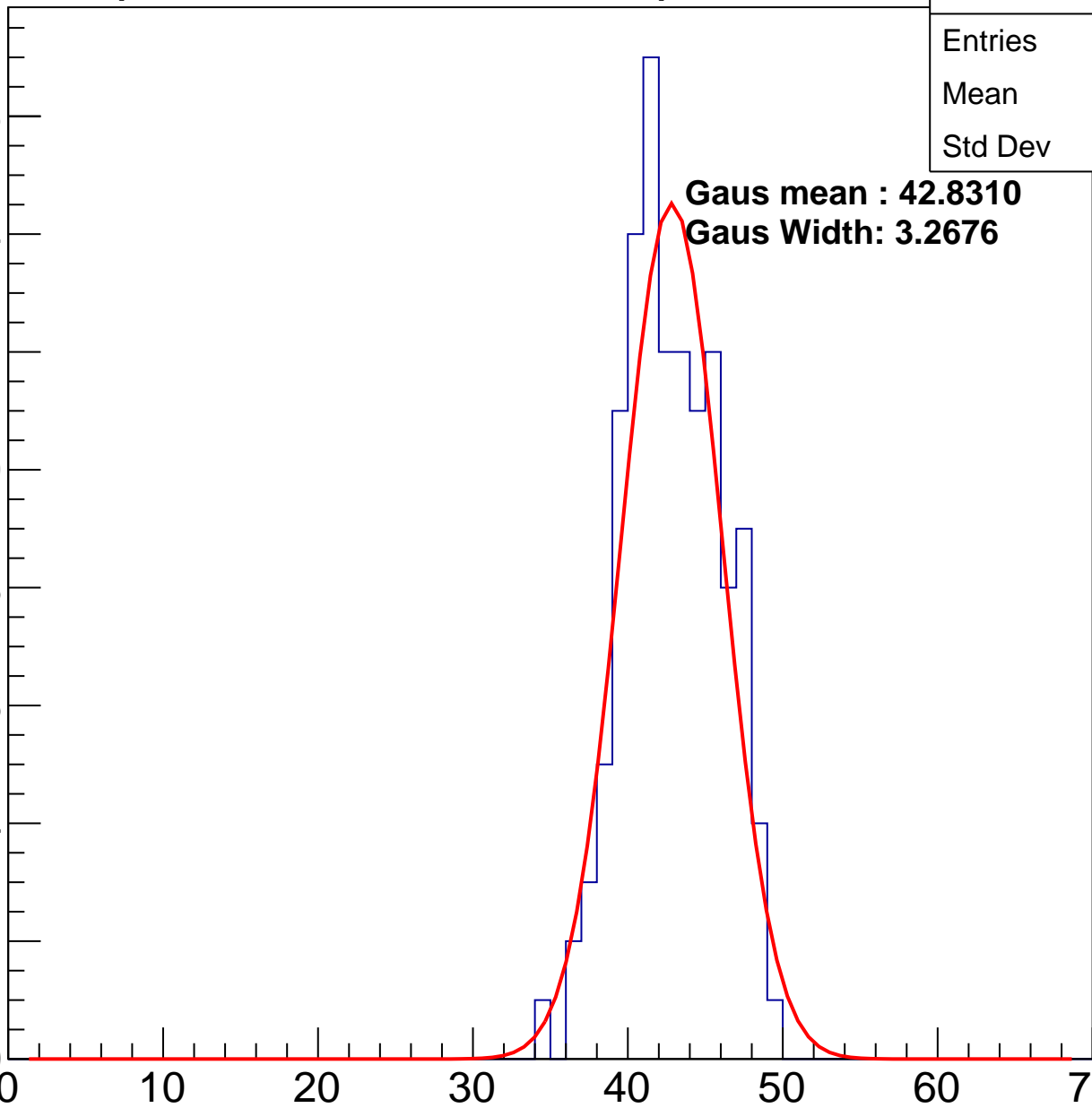
40

50

60

70

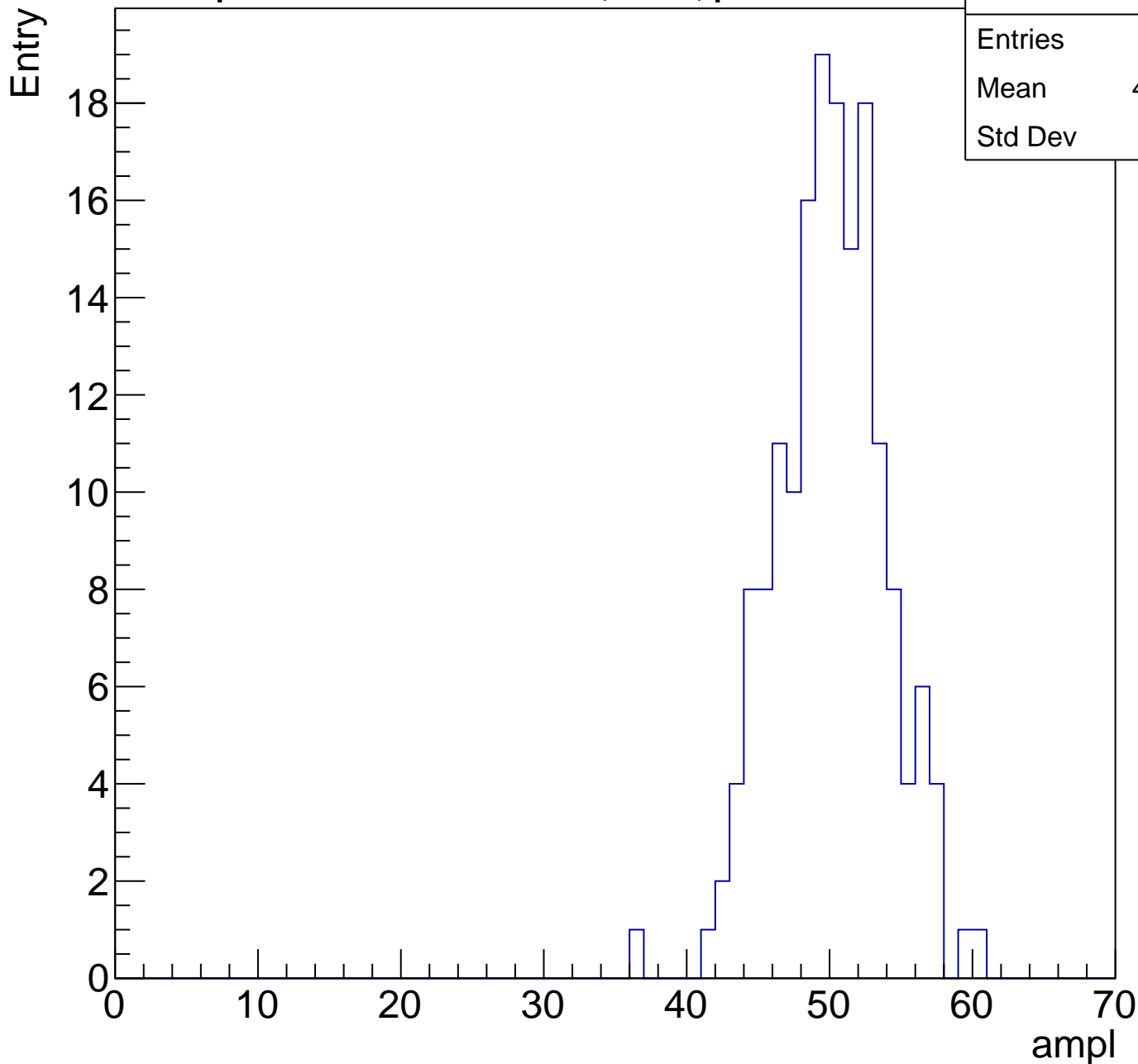
ampl



# B0L101S, U23-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	49.63
Std Dev	3.82



# B0L101S, U23-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

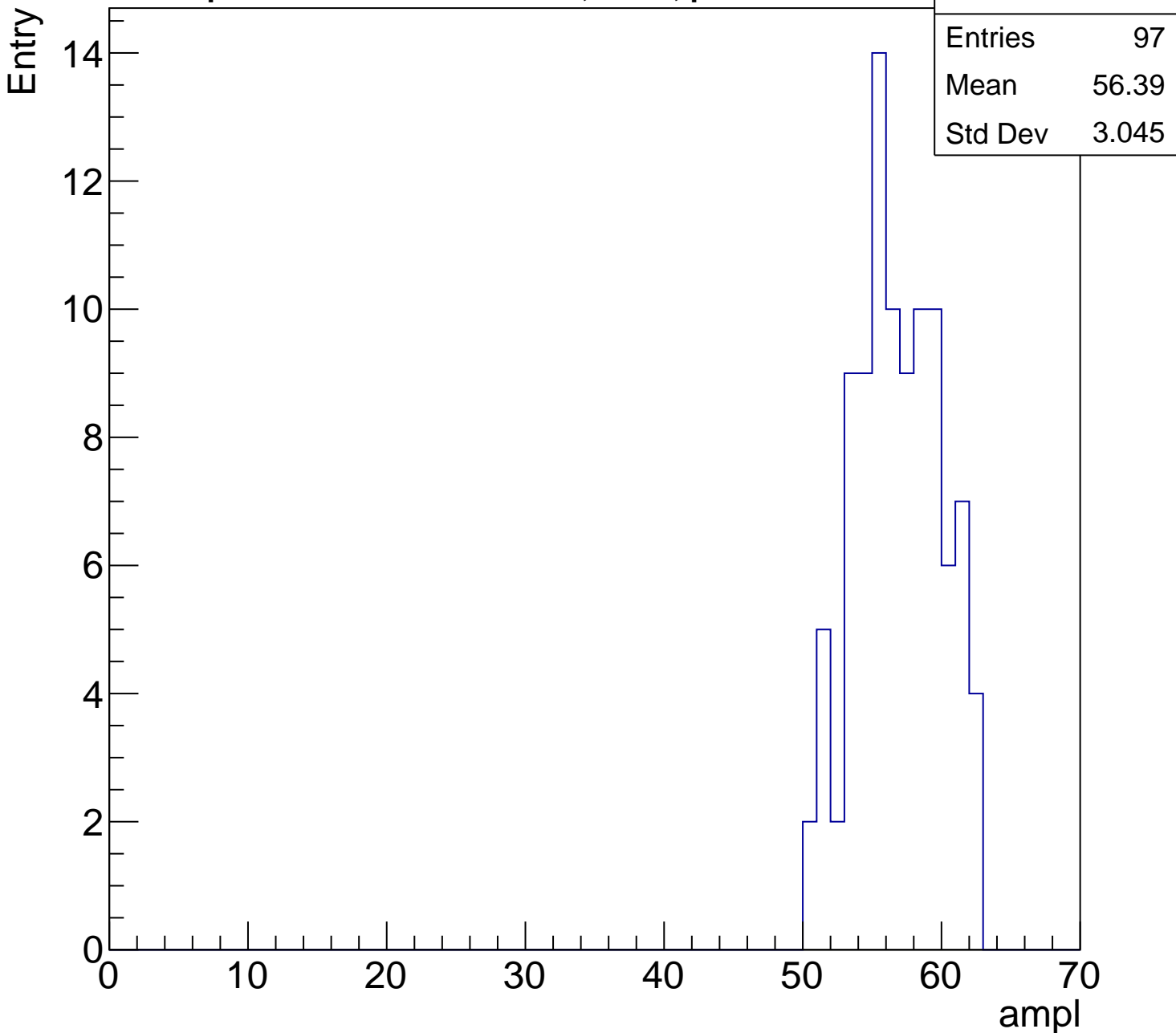
Entries	97
Mean	56.39
Std Dev	3.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch70, adc5

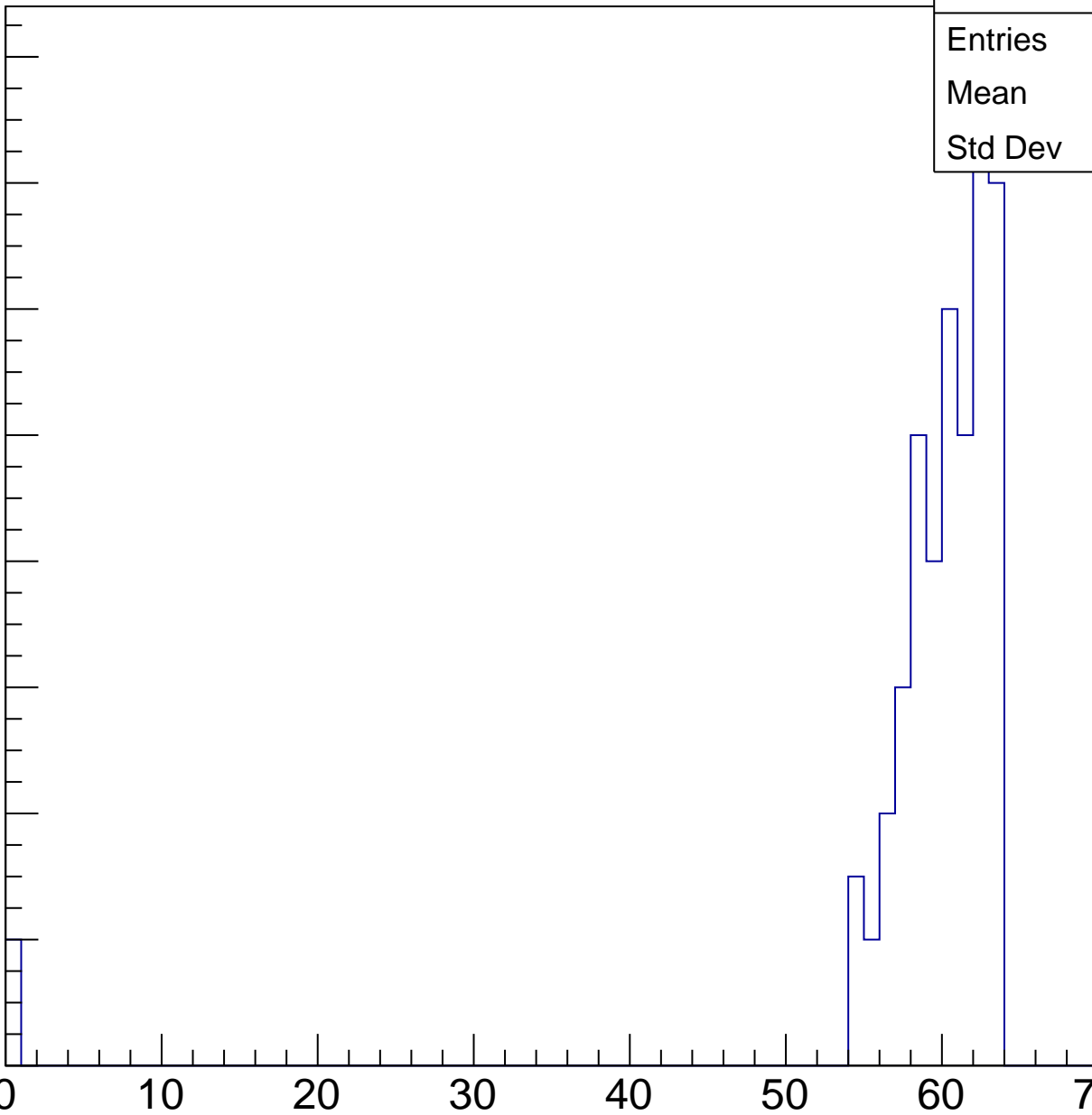
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	87
Mean	58.55
Std Dev	9.31

ampl

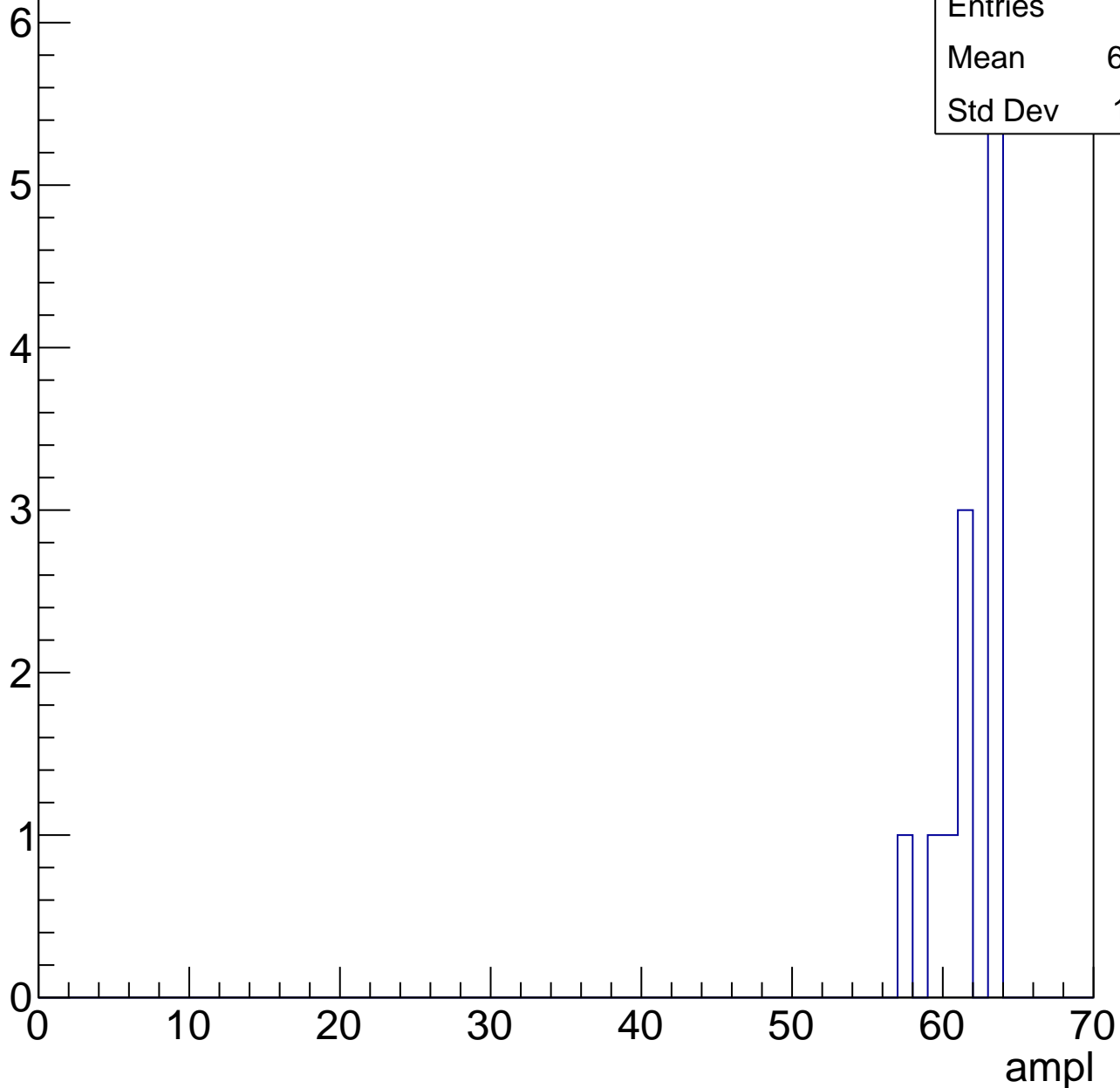


# B0L101S, U23-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.42
Std Dev	1.891





# B0L101S, U23-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch71, adc0

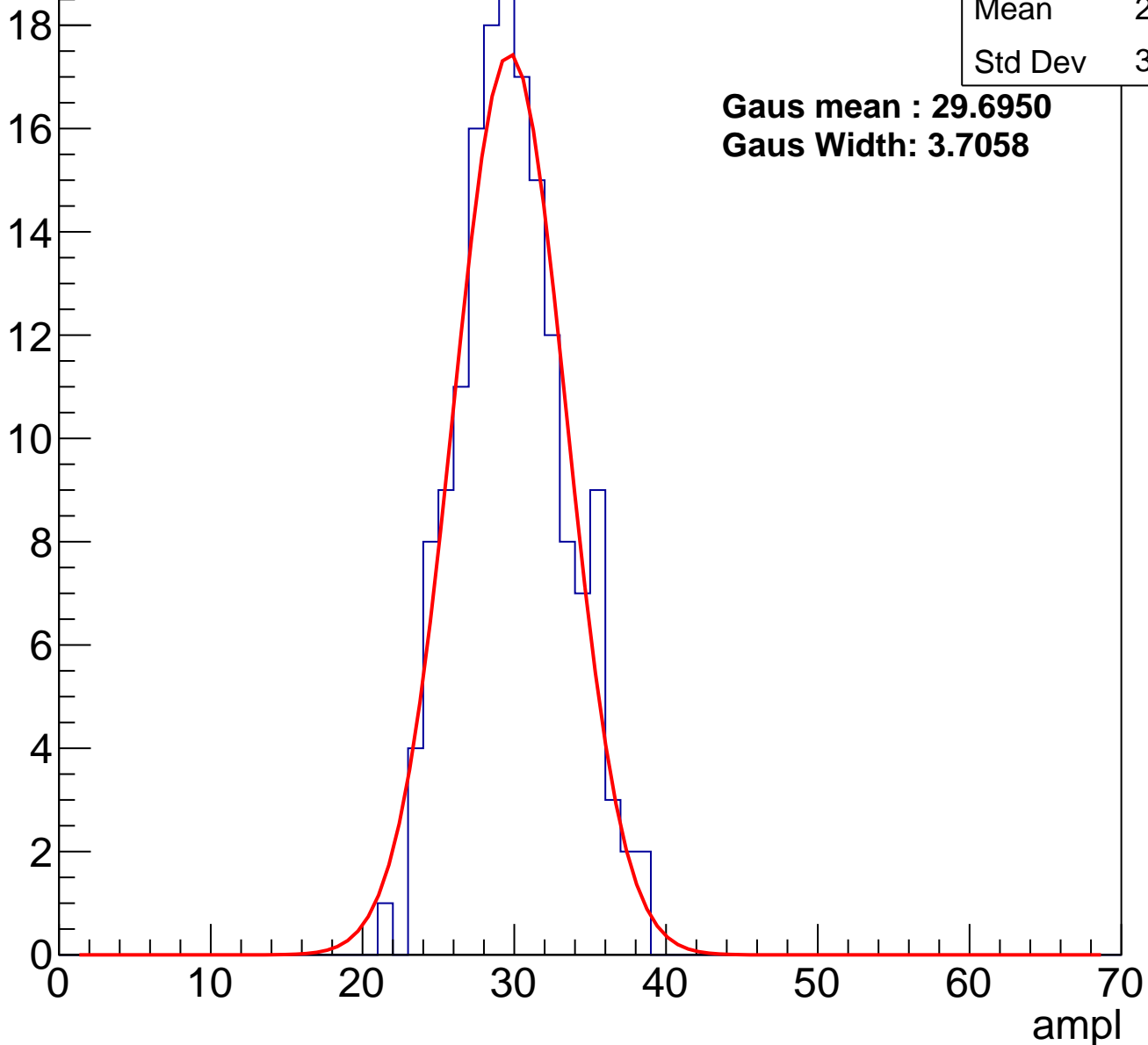
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	29.42
Std Dev	3.483

**Gaus mean : 29.6950**

**Gaus Width: 3.7058**

Entry



# B0L101S, U23-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	36.74
Std Dev	3.556

**Gaus mean : 36.9861**

**Gaus Width: 3.4205**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

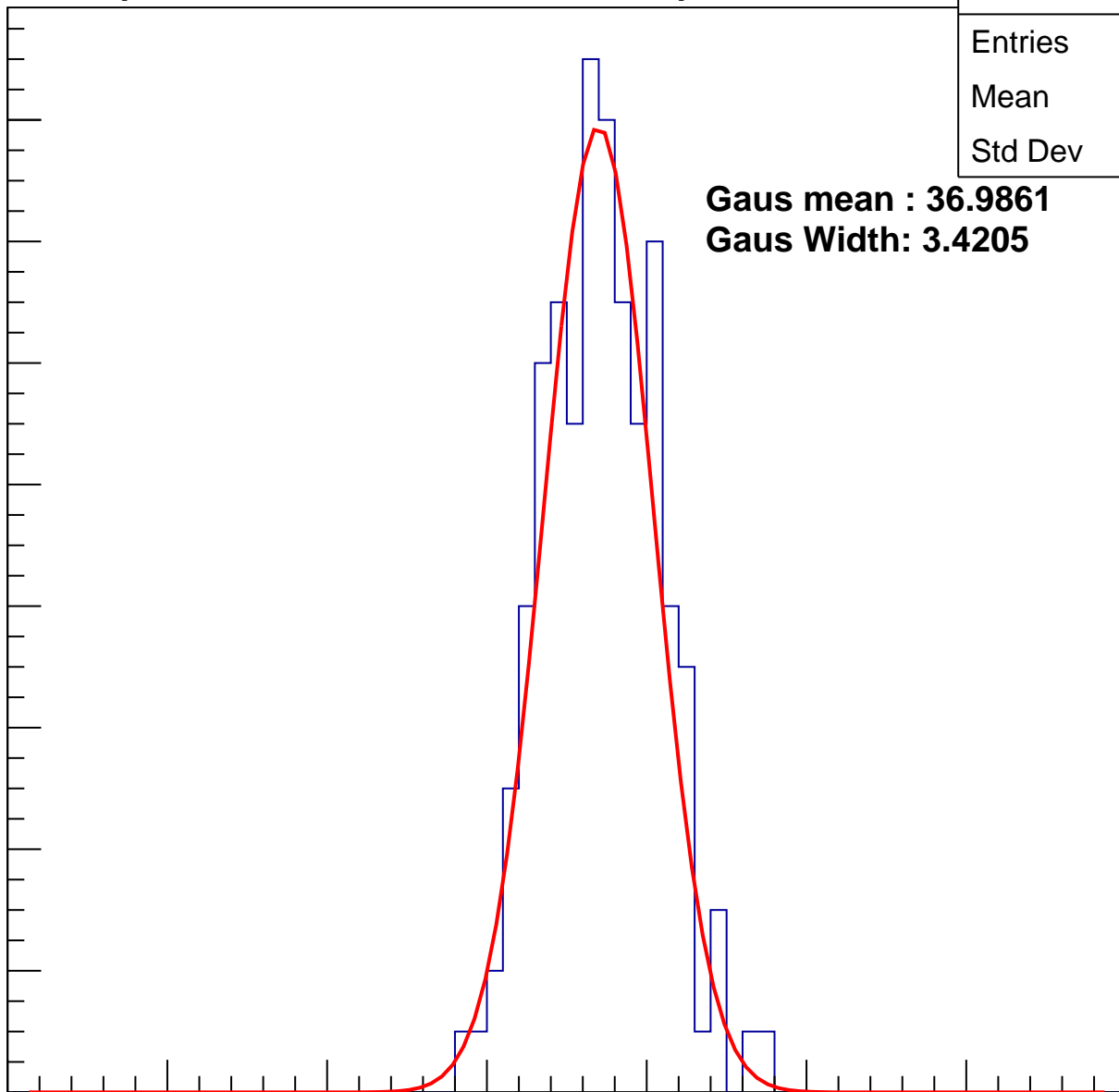
40

50

60

70

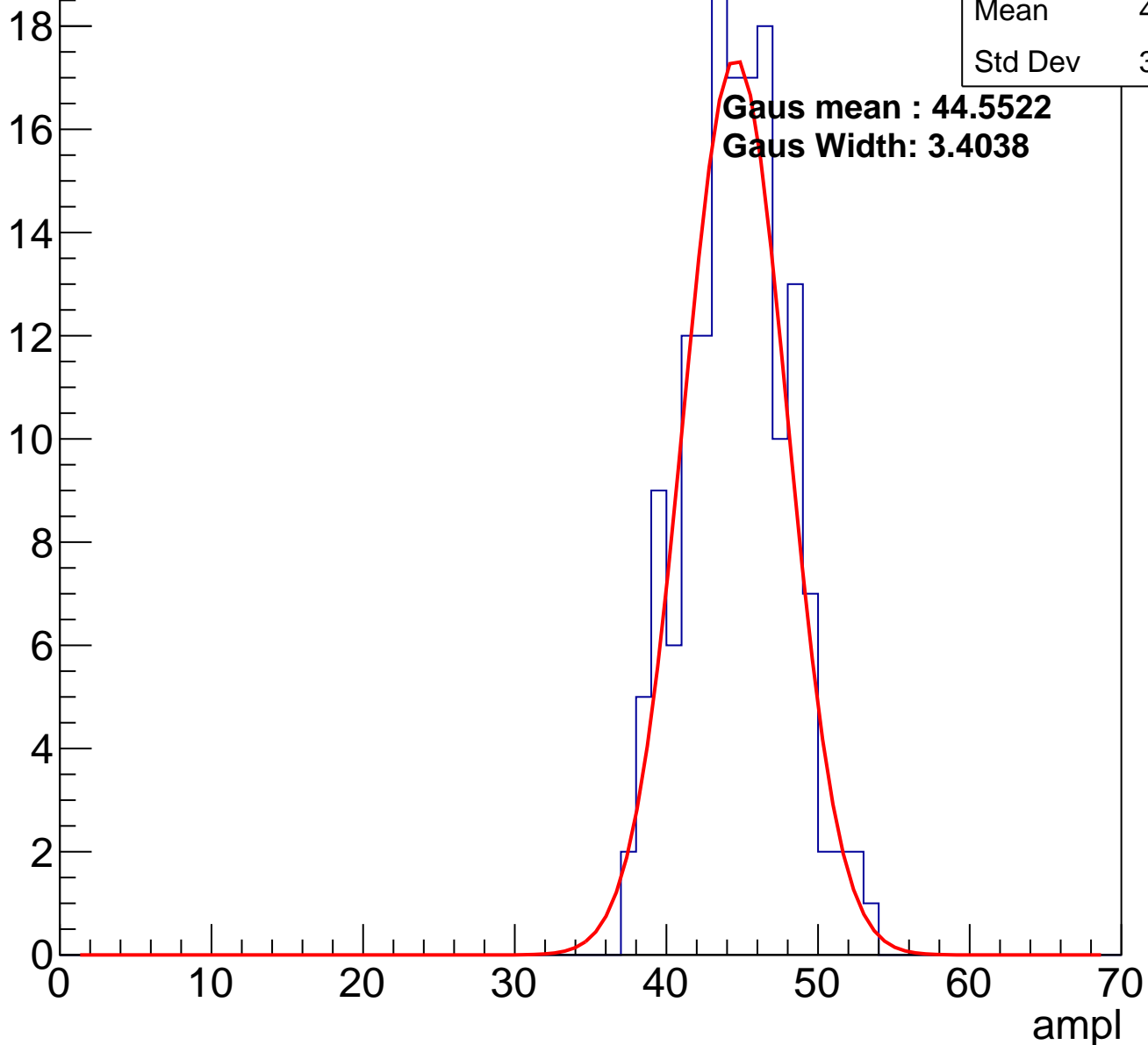
ampl



# B0L101S, U23-ch71, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch71, adc3

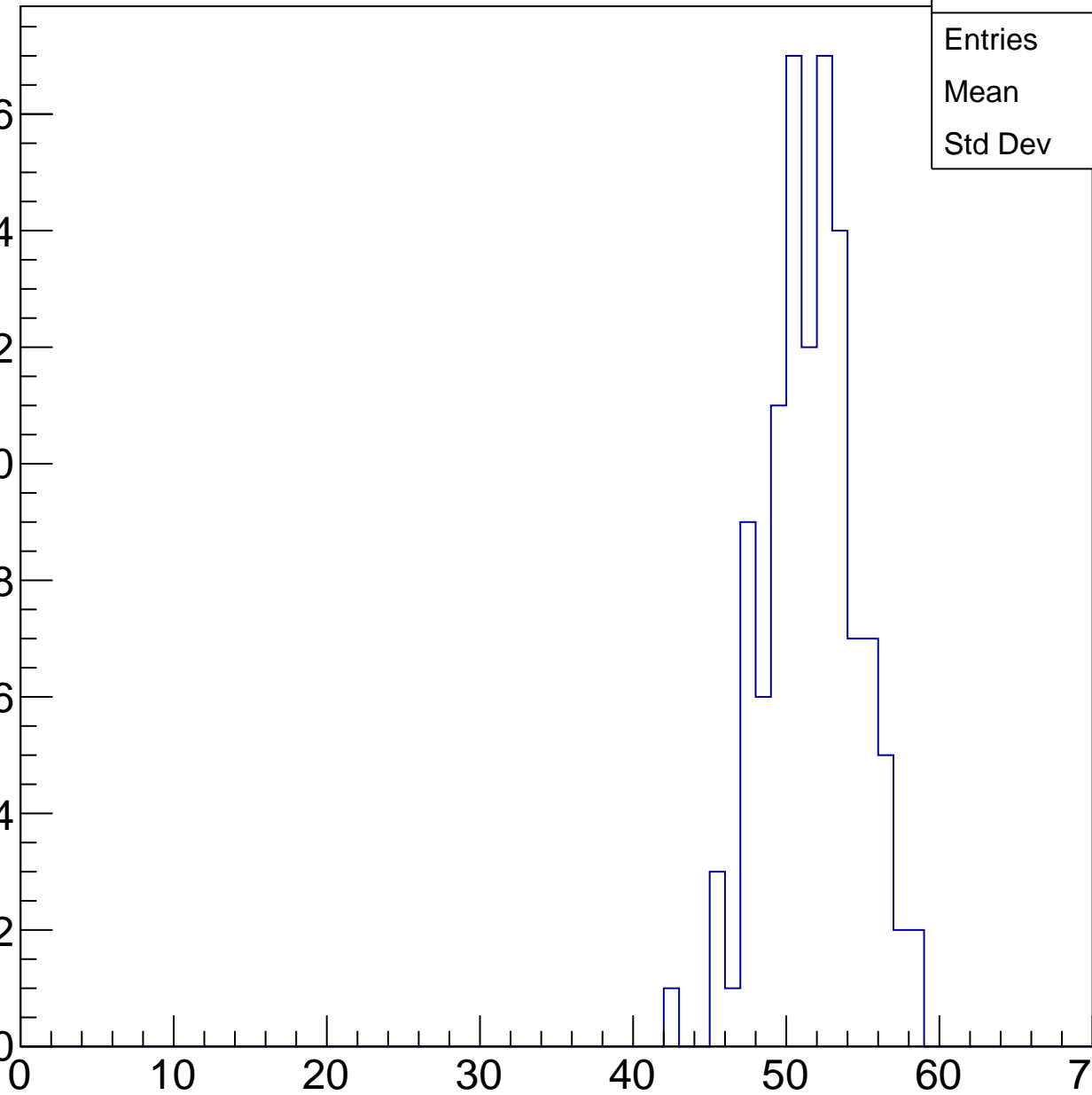
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	51.18
Std Dev	2.98

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

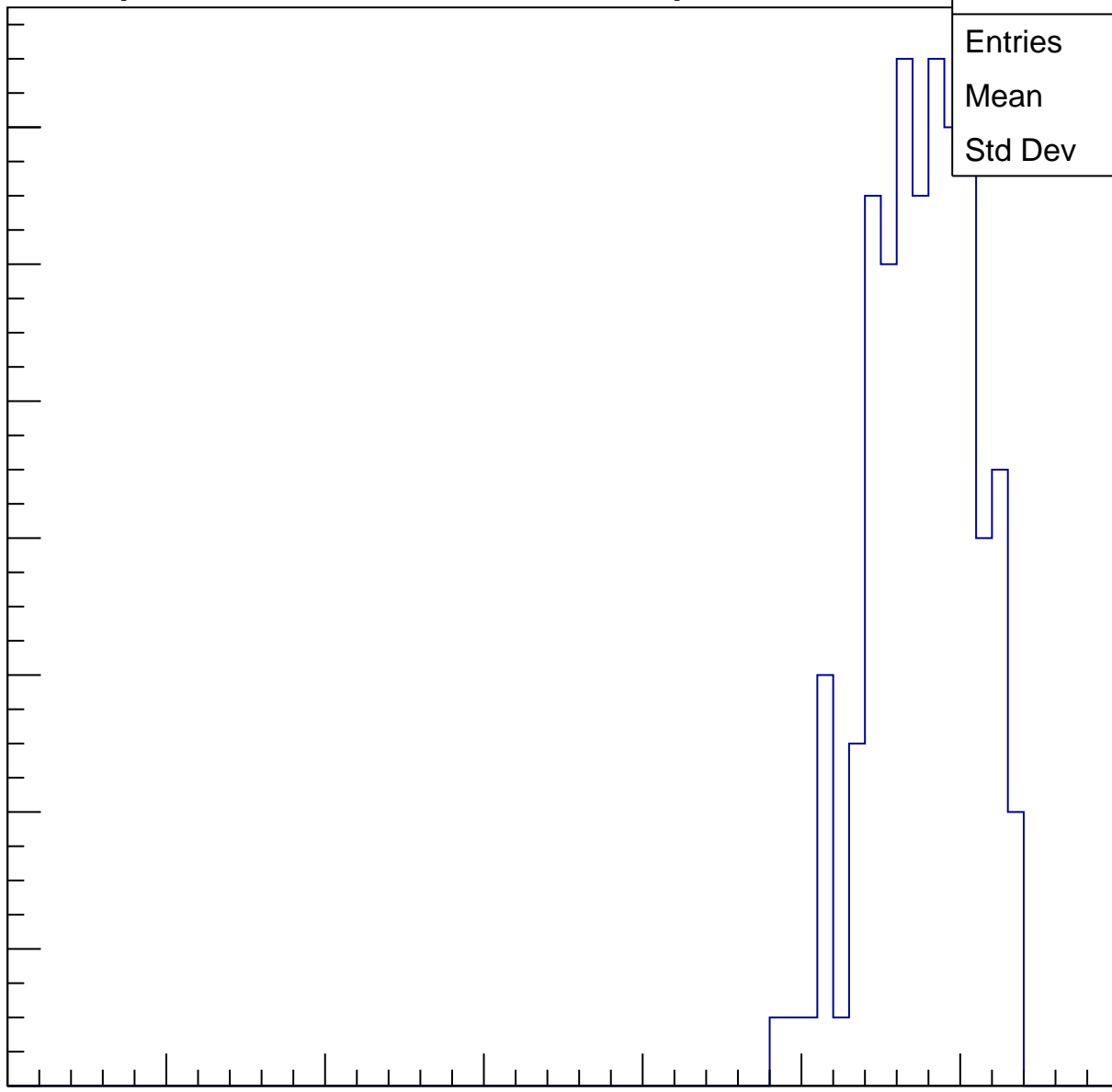
Entries	132
Mean	57.17
Std Dev	3.239

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	56
Mean	58.2
Std Dev	11.47

ampl

0

10

20

30

40

50

60

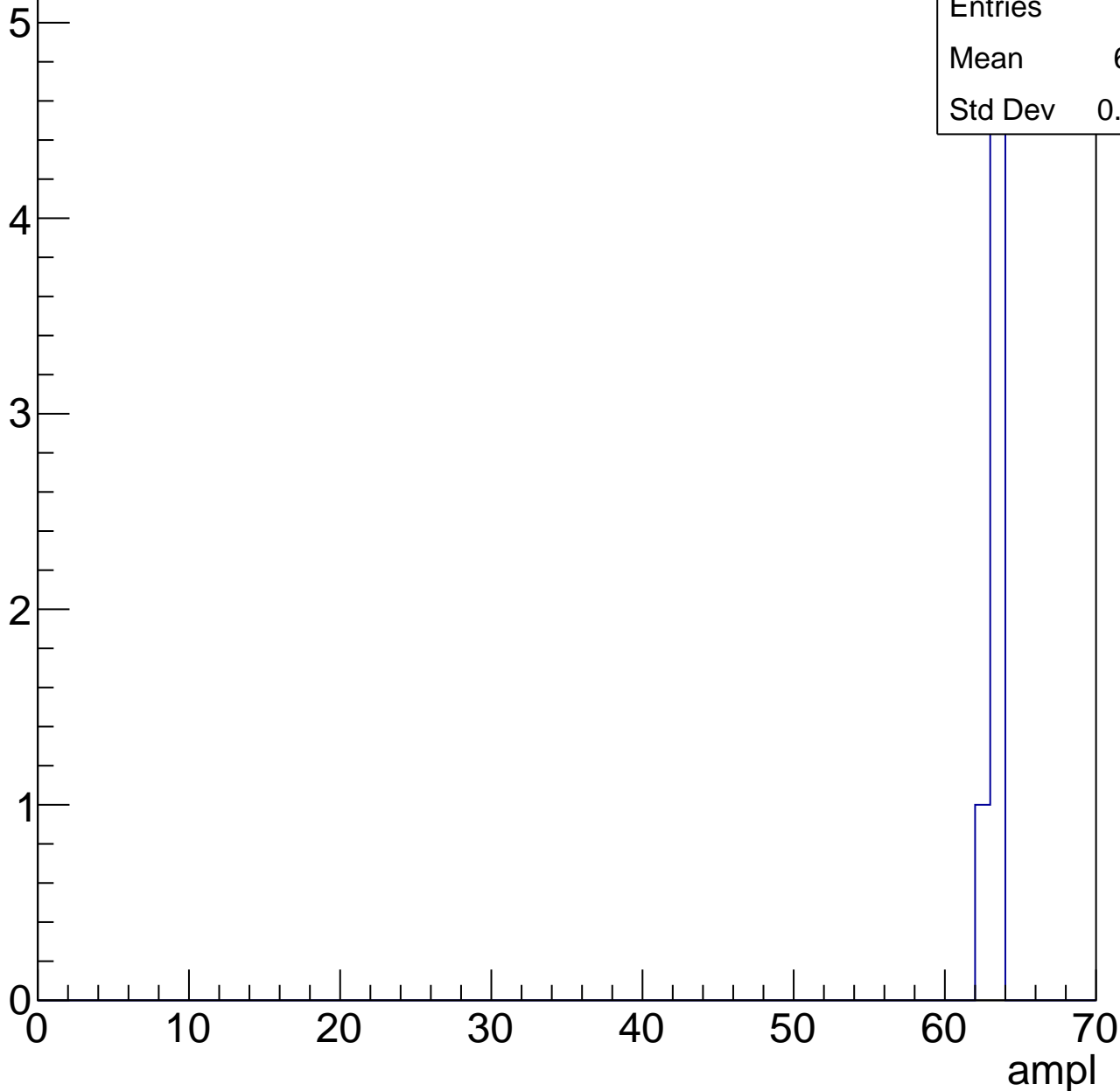
70

# B0L101S, U23-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727

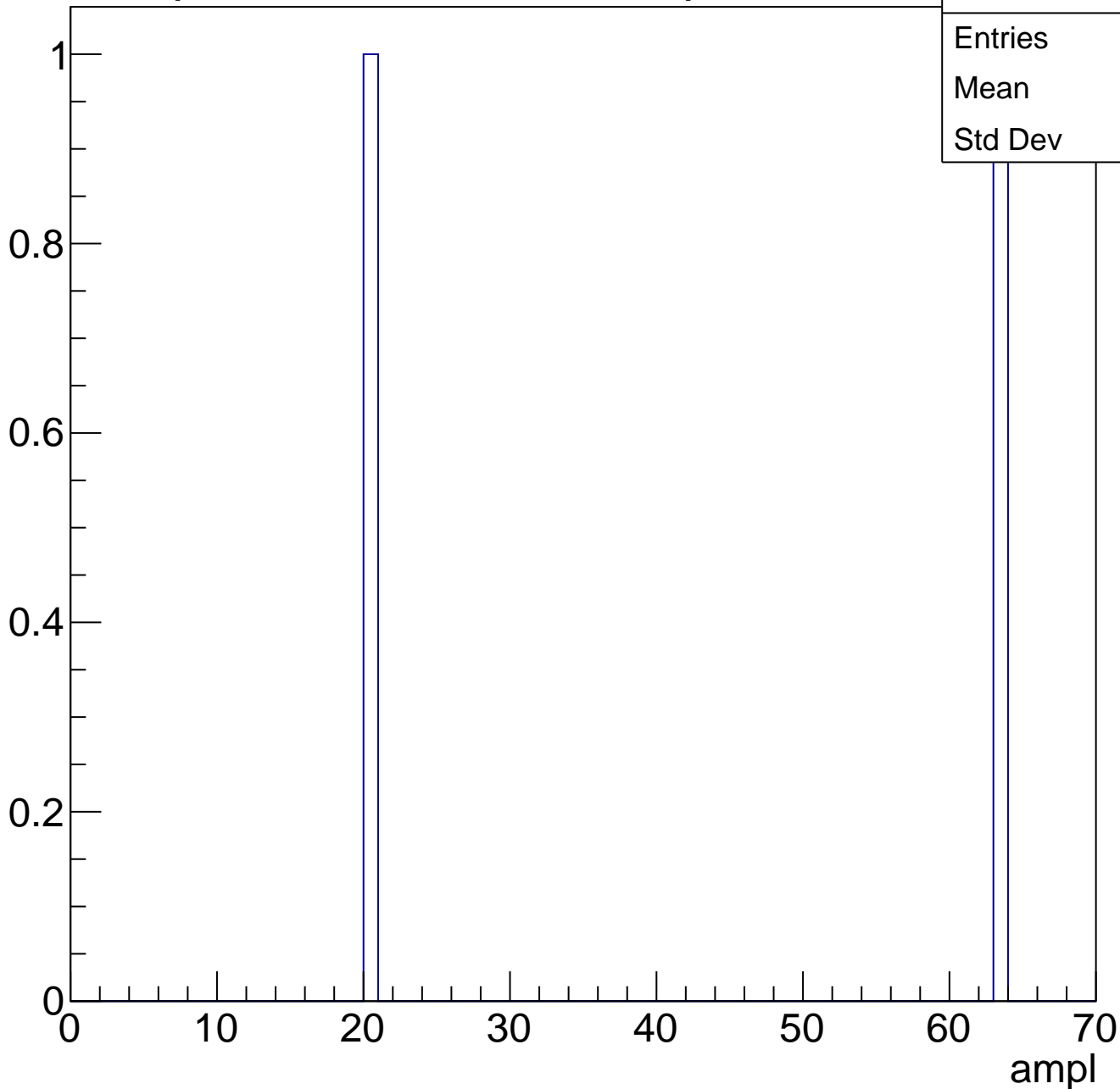




# B0L101S, U23-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch72, adc0

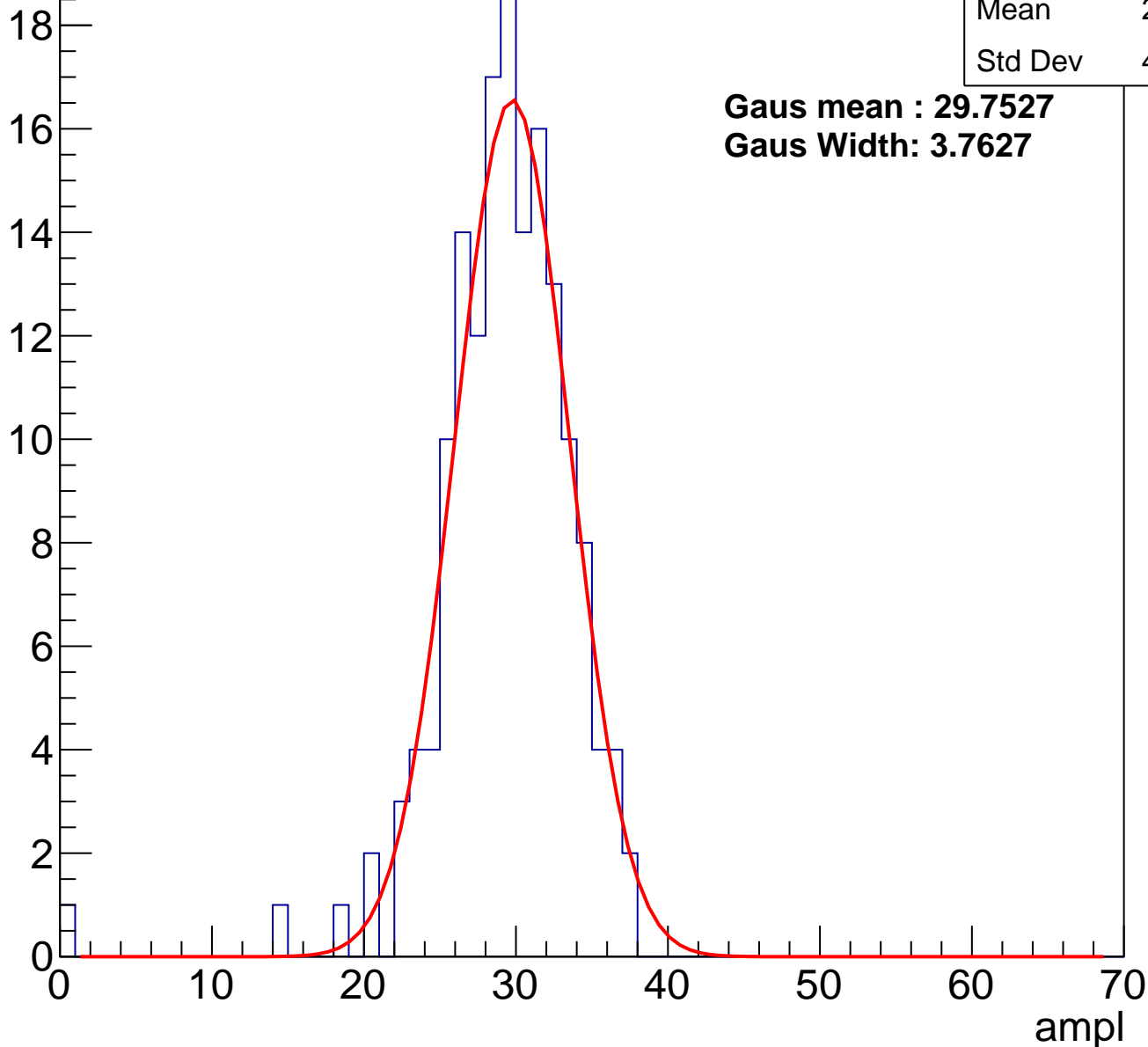
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	28.82
Std Dev	4.422

**Gaus mean : 29.7527**

**Gaus Width: 3.7627**

Entry



# B0L101S, U23-ch72, adc1

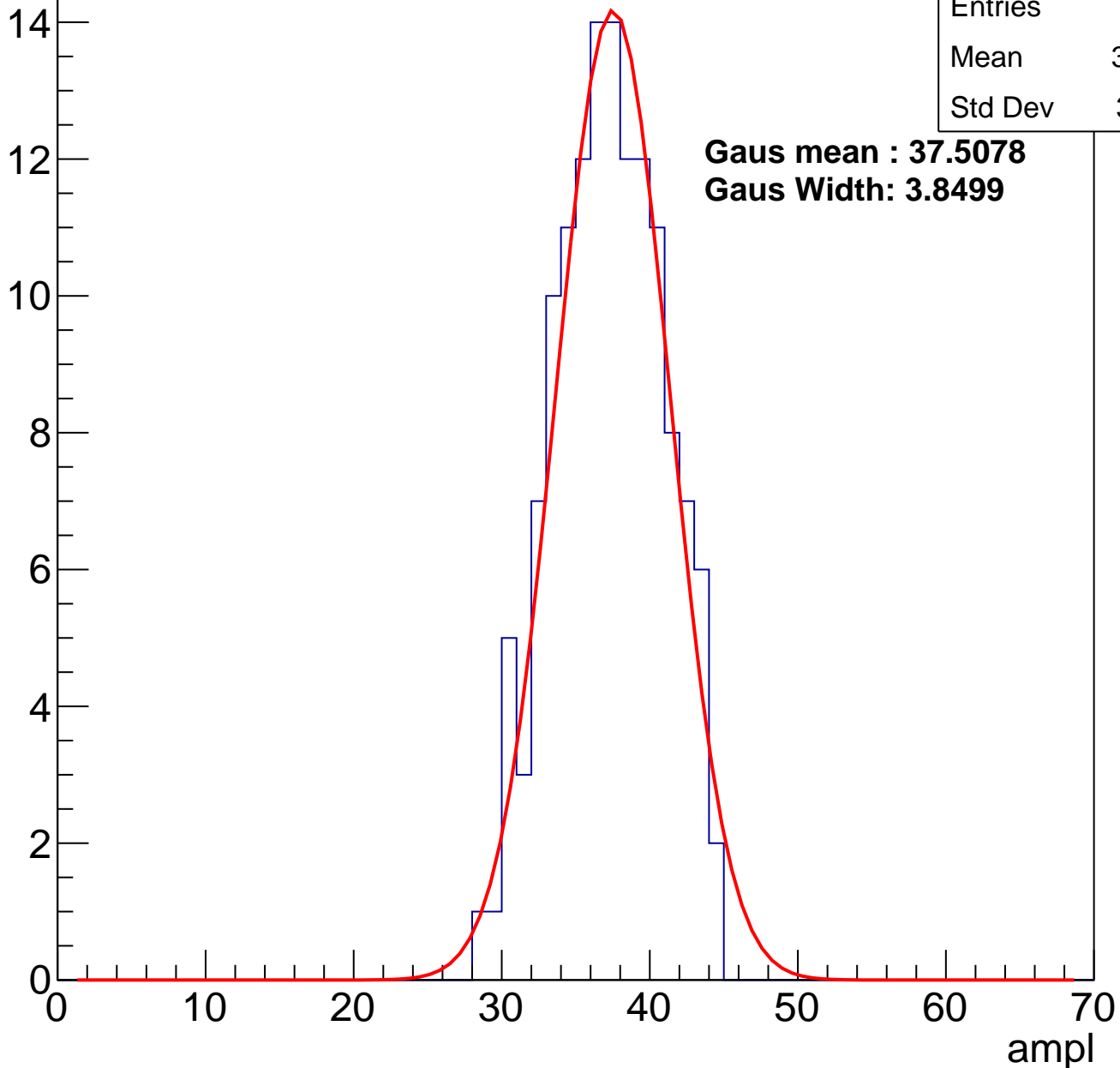
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	36.78
Std Dev	3.601

**Gaus mean : 37.5078**

**Gaus Width: 3.8499**

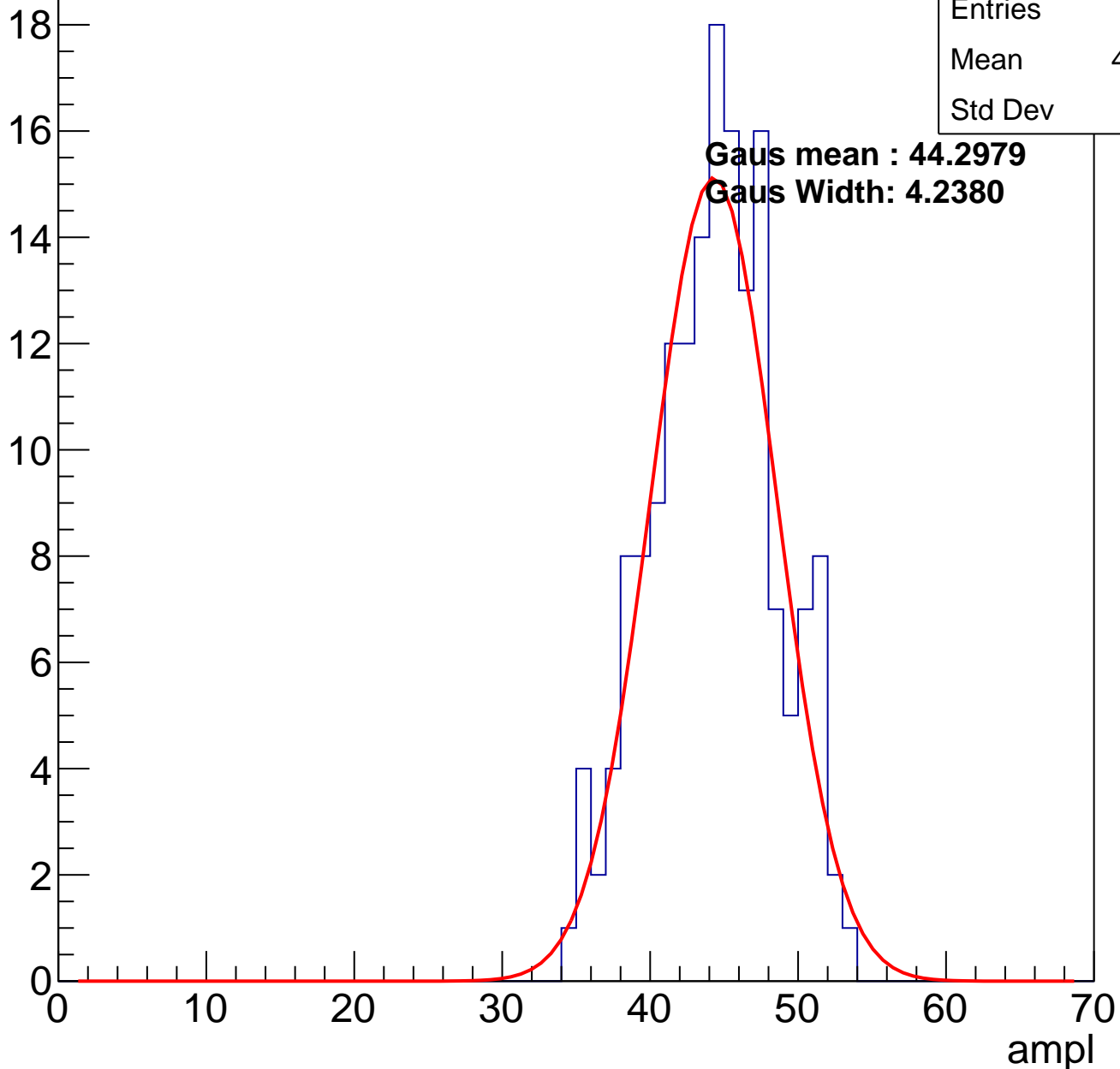
Entry



# B0L101S, U23-ch72, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

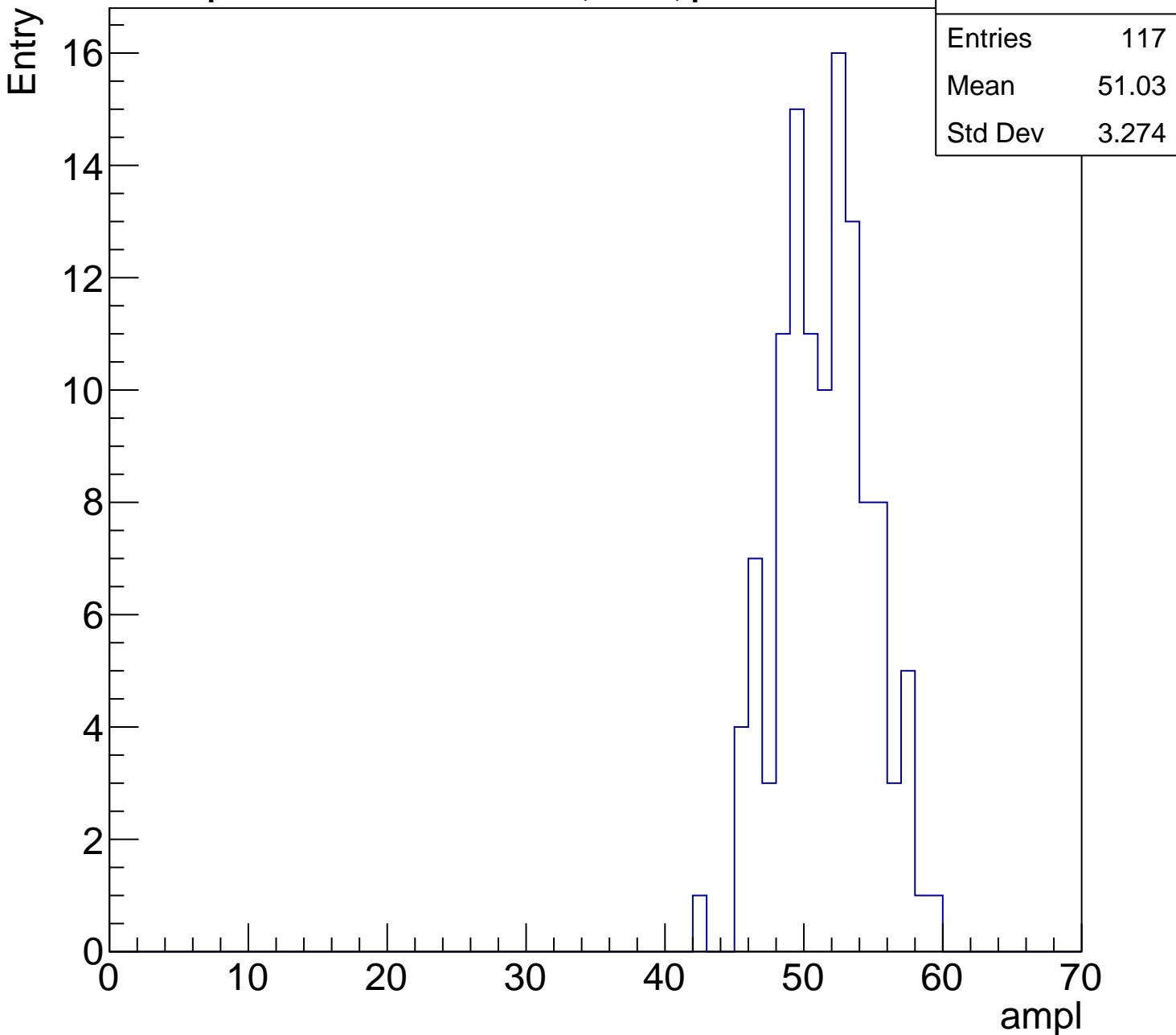
Entries	117
Mean	51.03
Std Dev	3.274

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

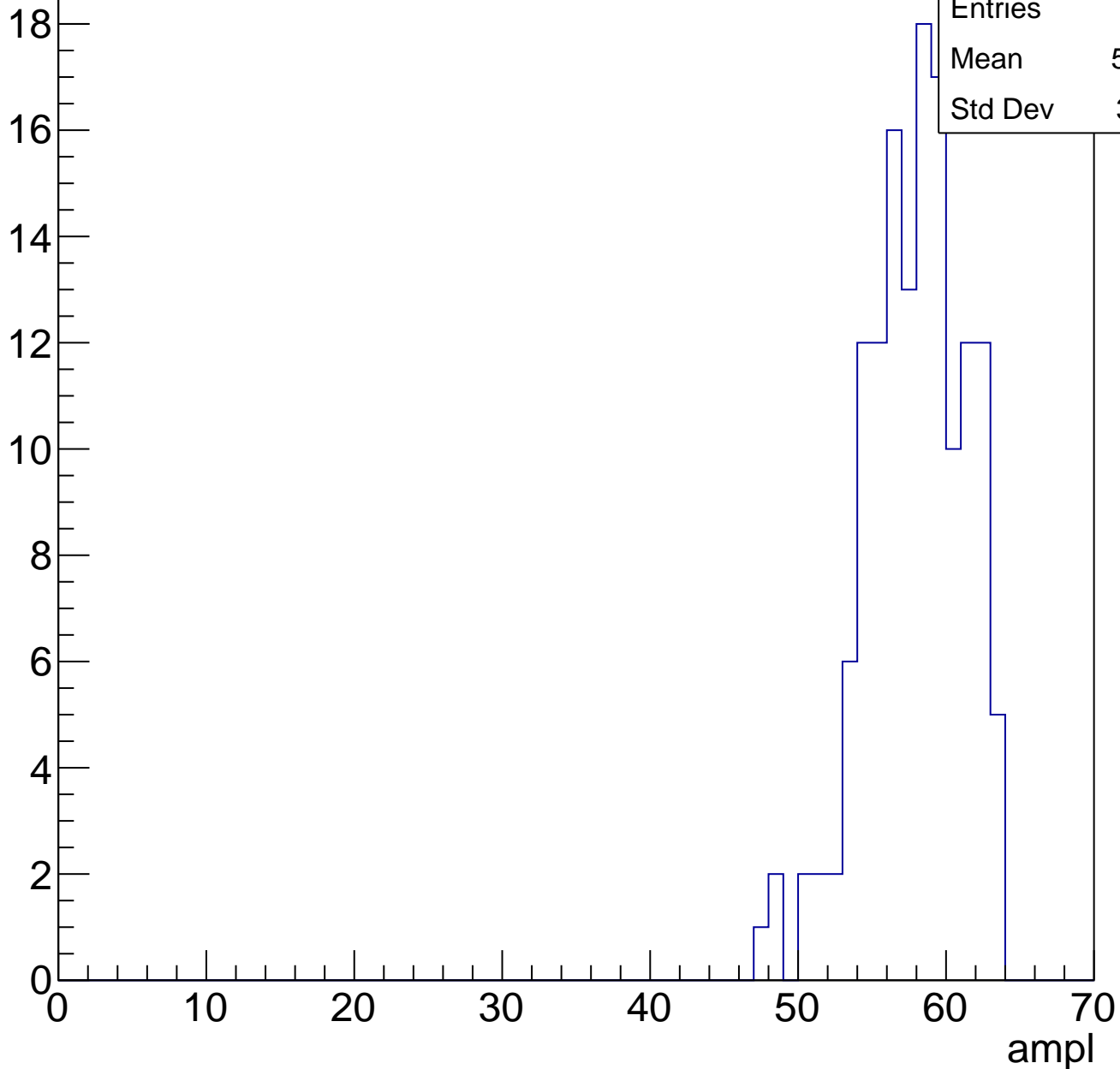
ampl



# B0L101S, U23-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	58.14
Std Dev	12.96

0

2

4

6

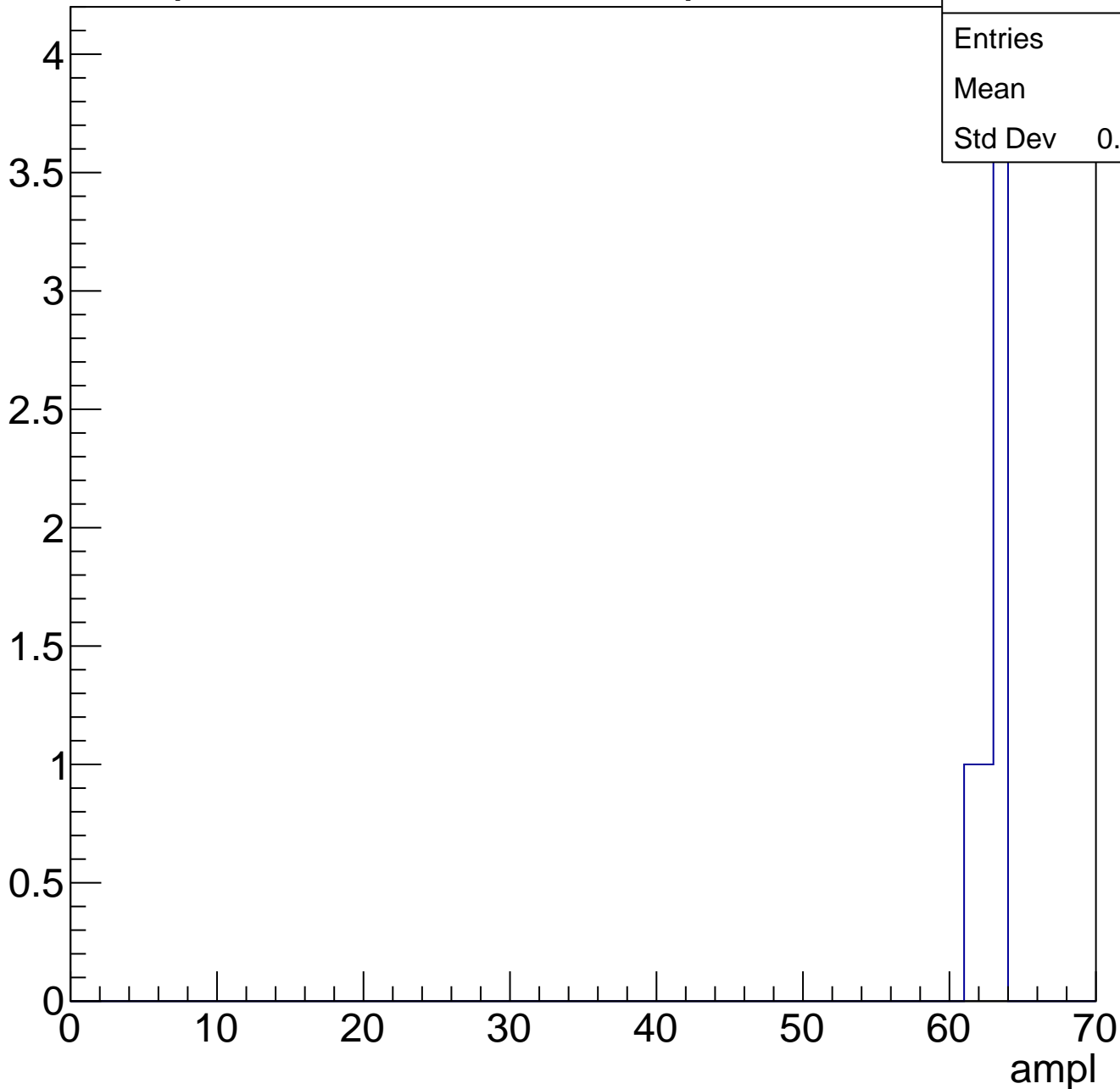
8

10

# B0L101S, U23-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

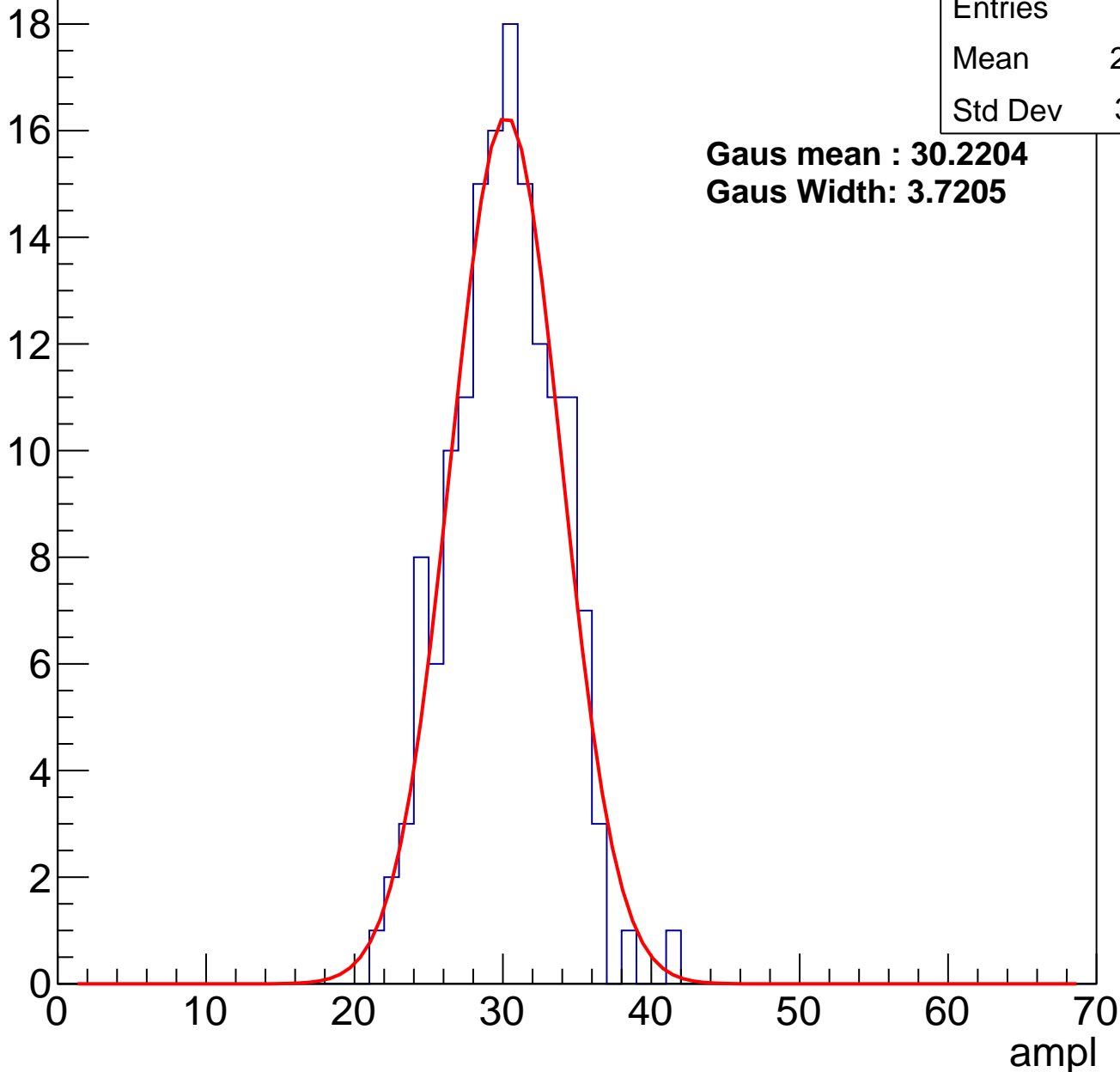


Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch73, adc1

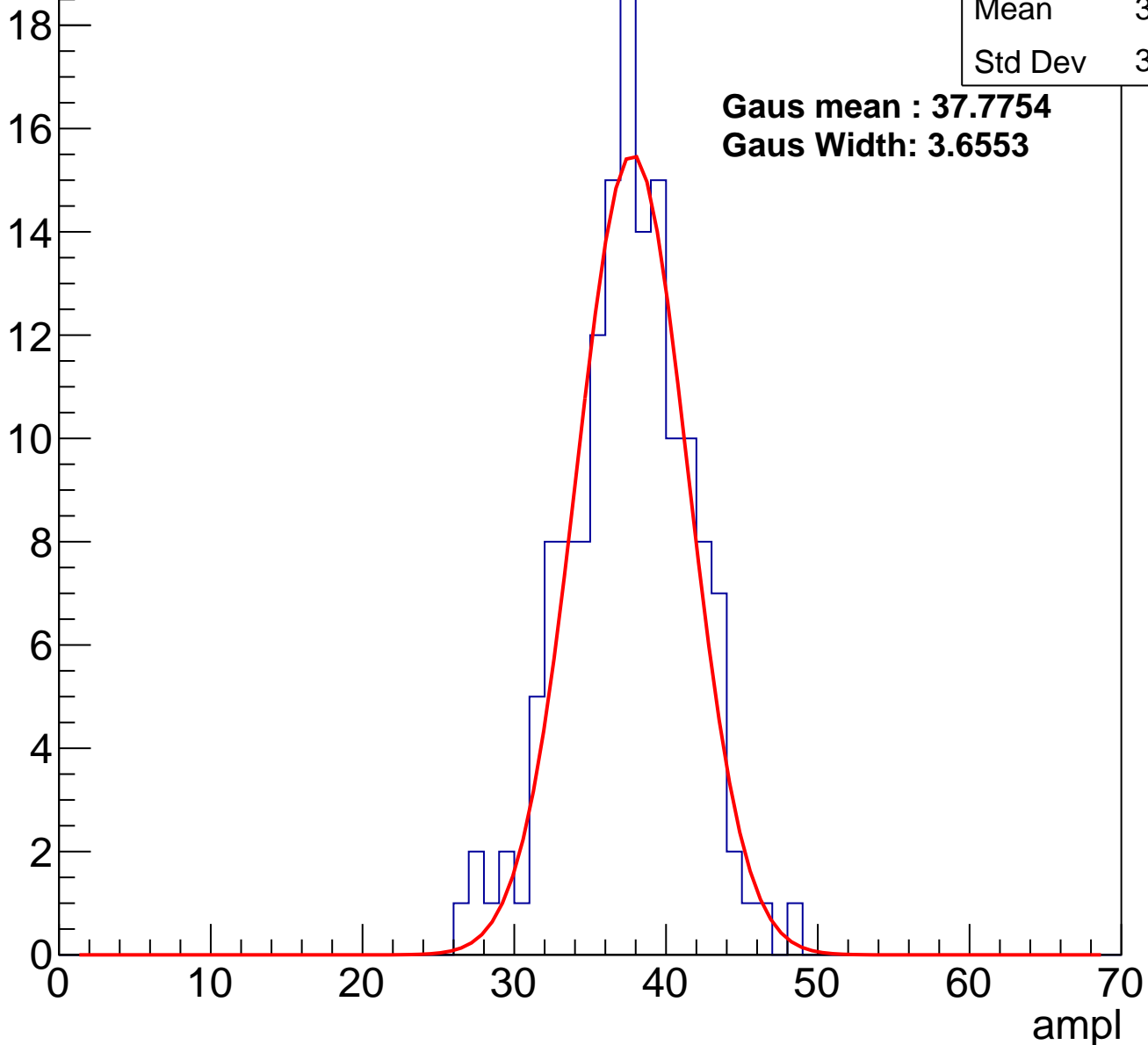
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	37.07
Std Dev	3.954

**Gaus mean : 37.7754**

**Gaus Width: 3.6553**

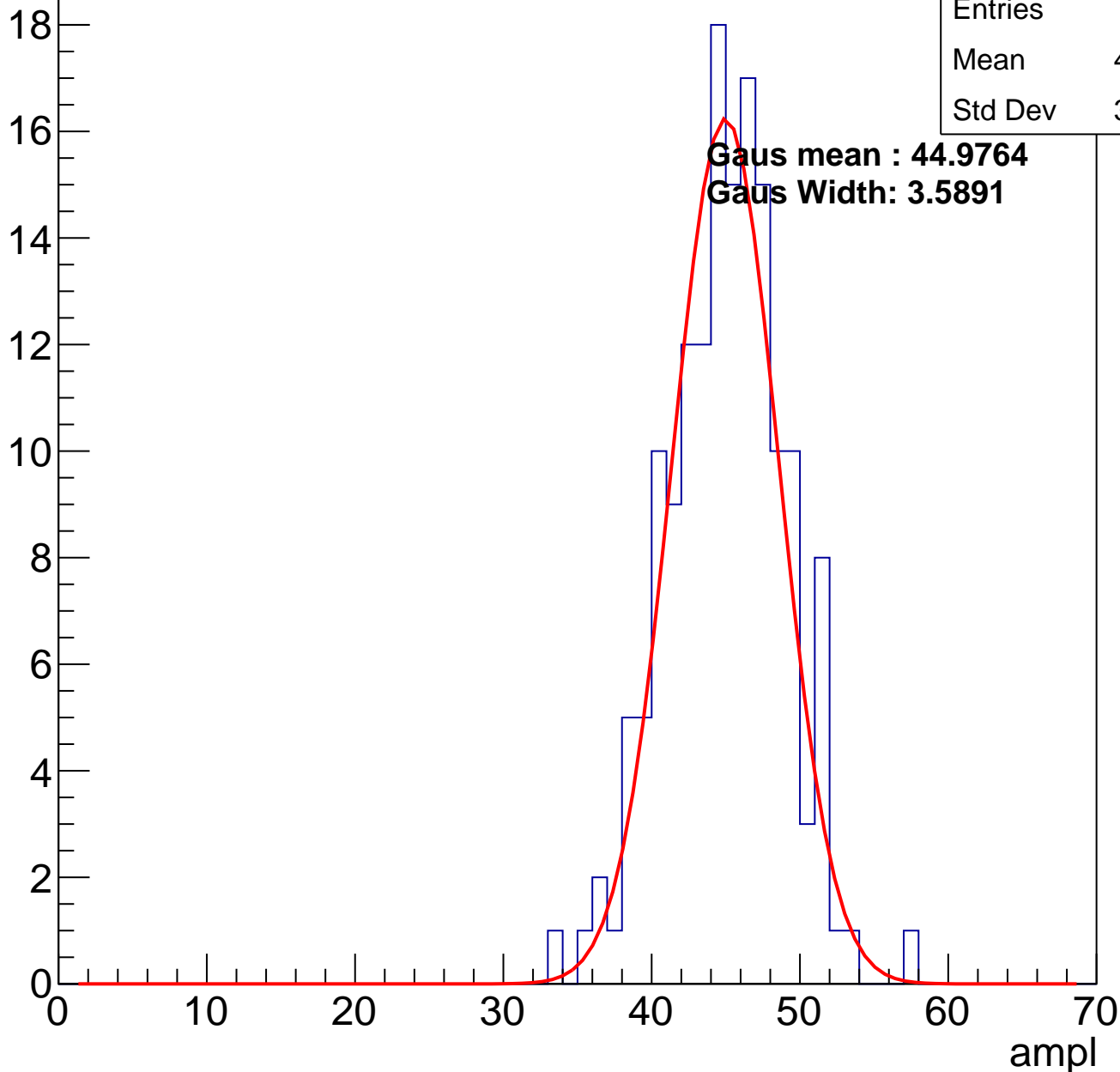
Entry



# B0L101S, U23-ch73, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

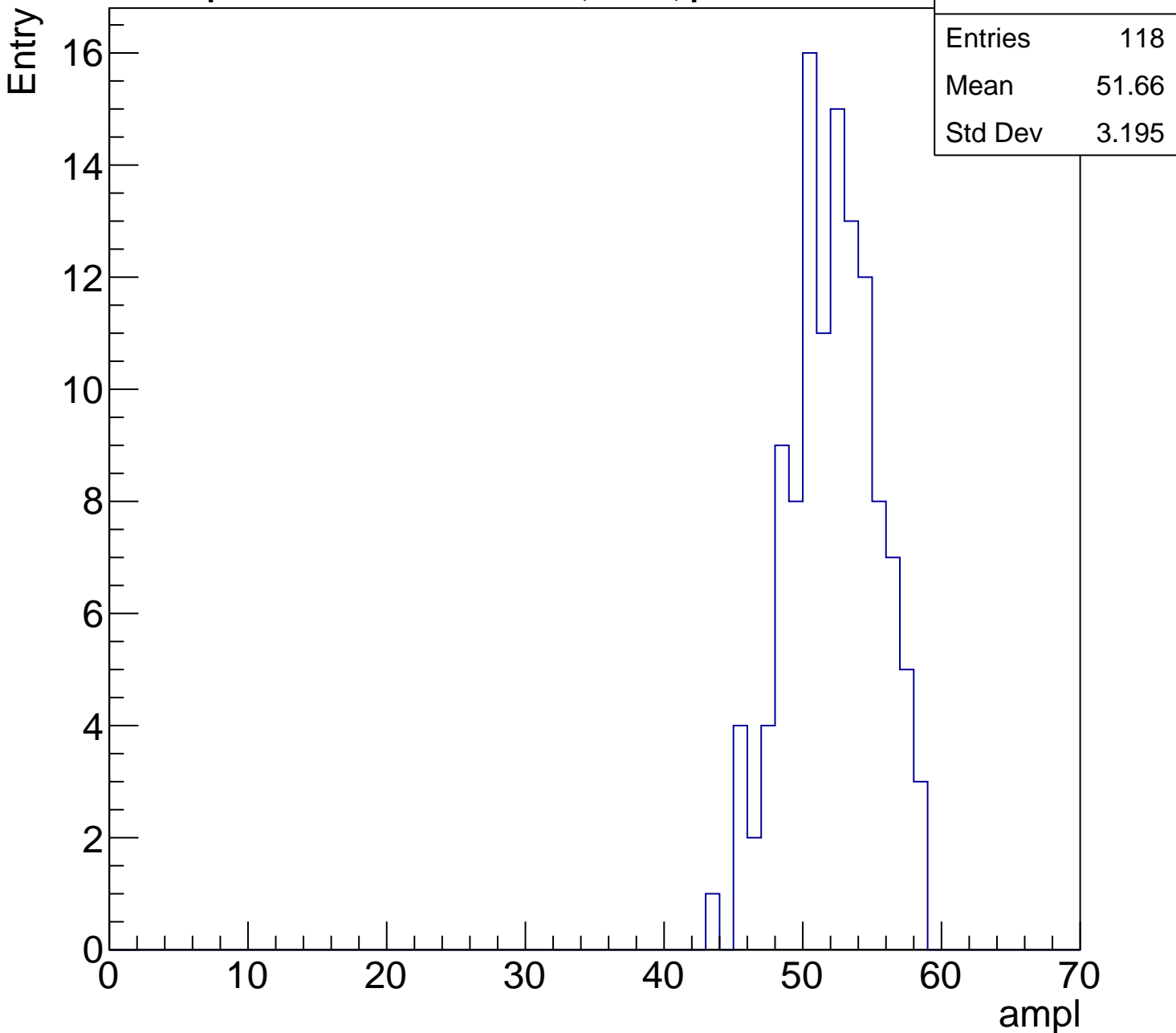
Entries	118
Mean	51.66
Std Dev	3.195

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch73, adc4

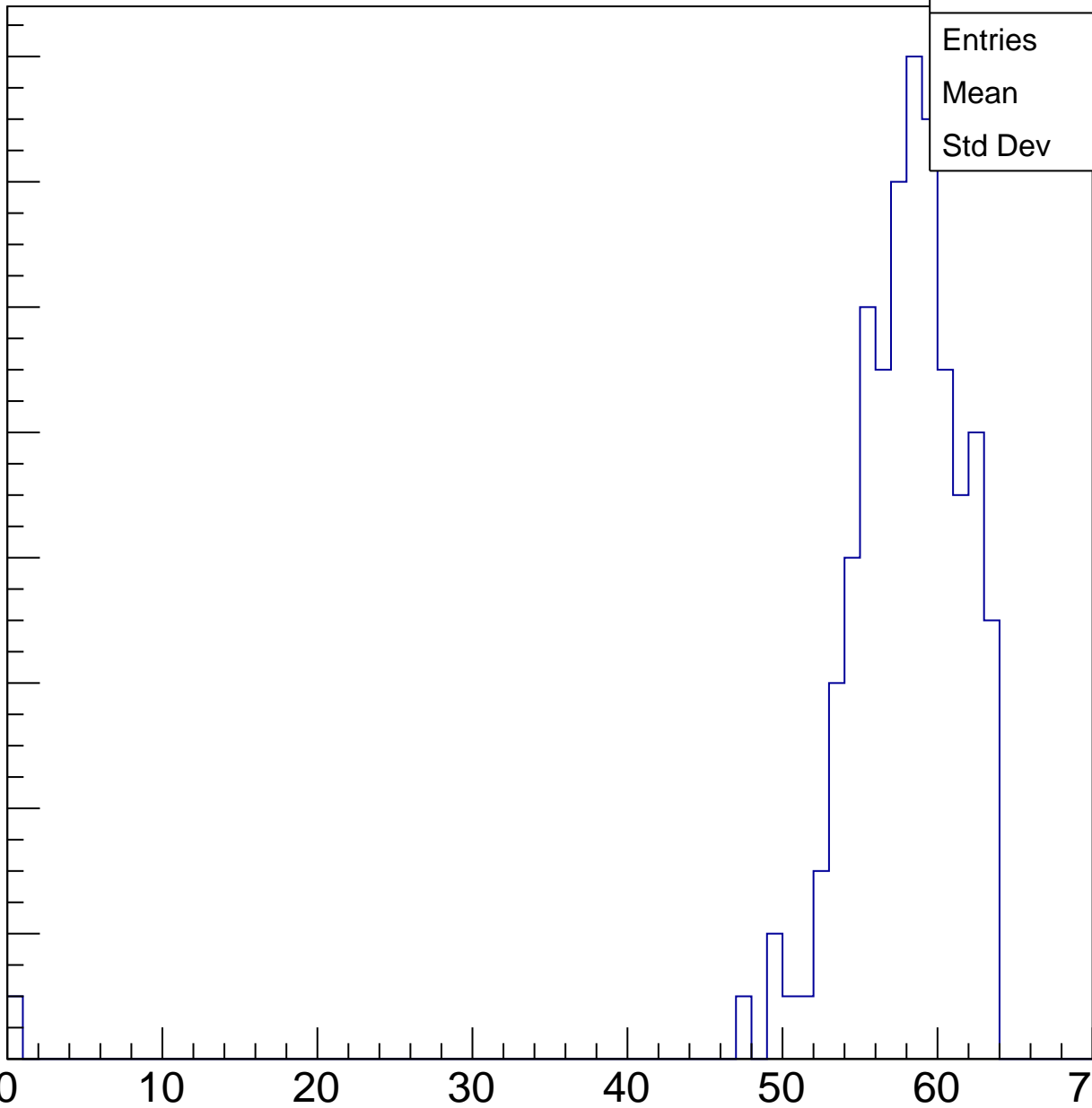
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	128
Mean	57.1
Std Dev	6.049

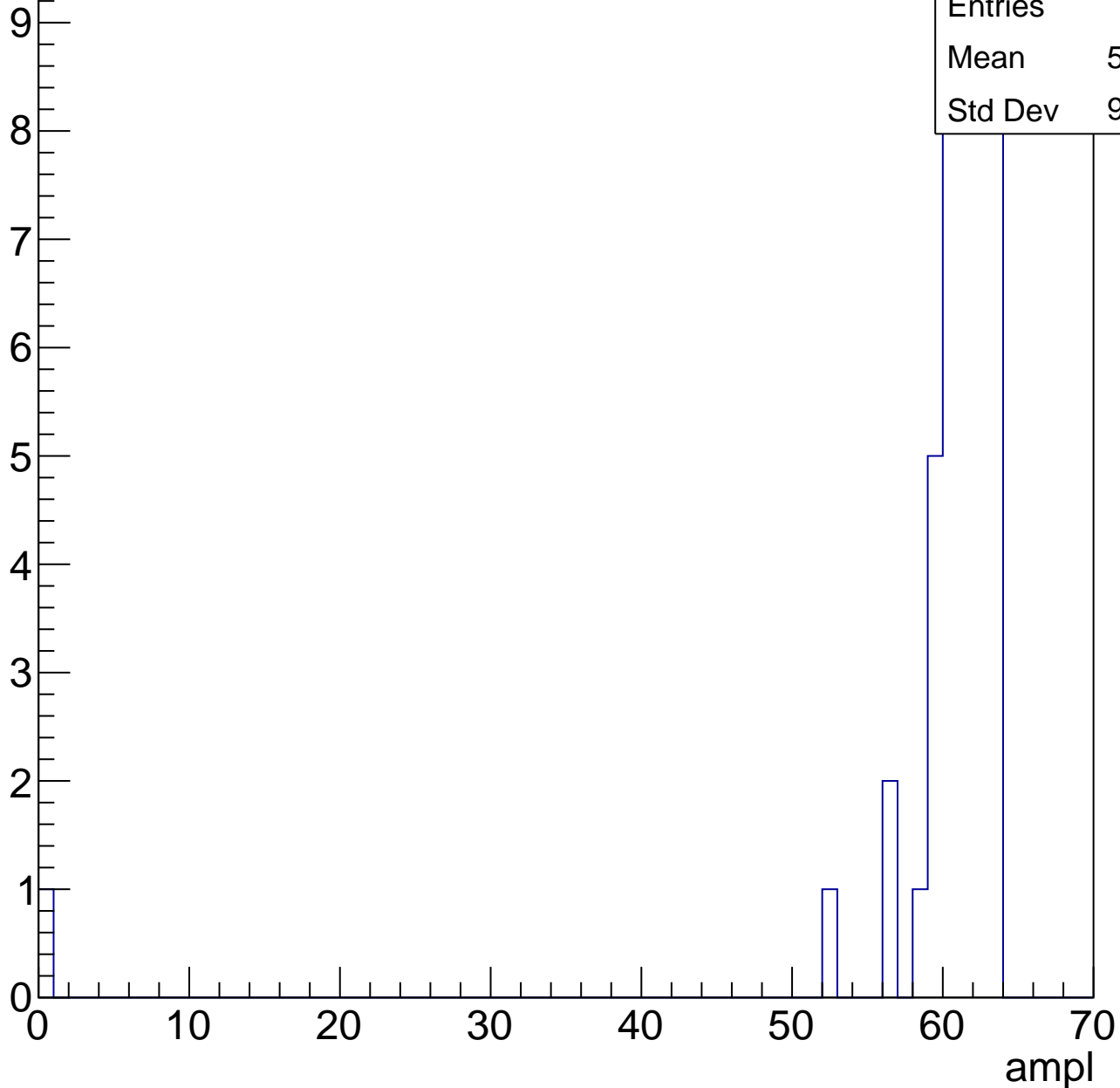
ampl



# B0L101S, U23-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

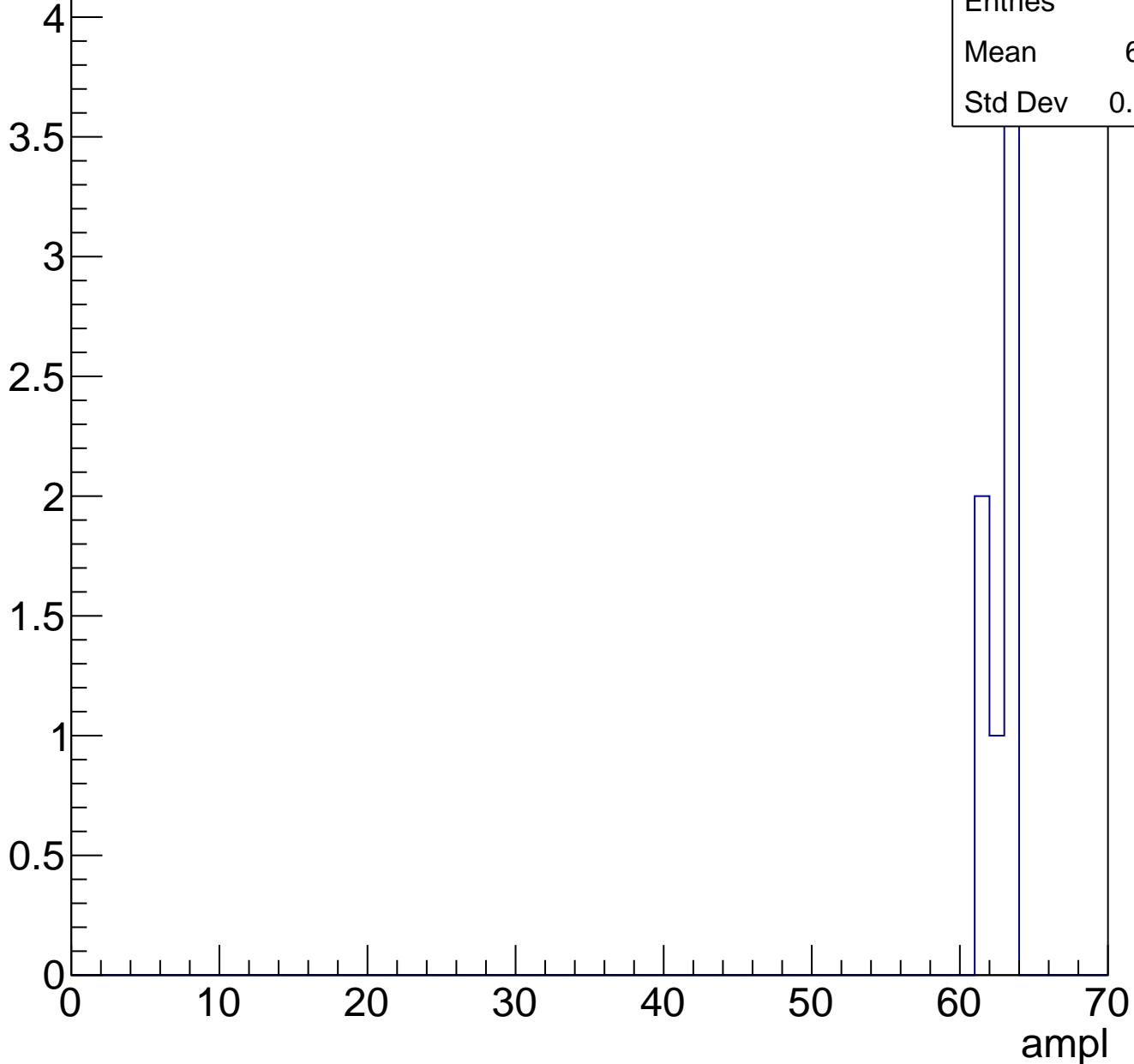
Entry



# B0L101S, U23-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch74, adc0

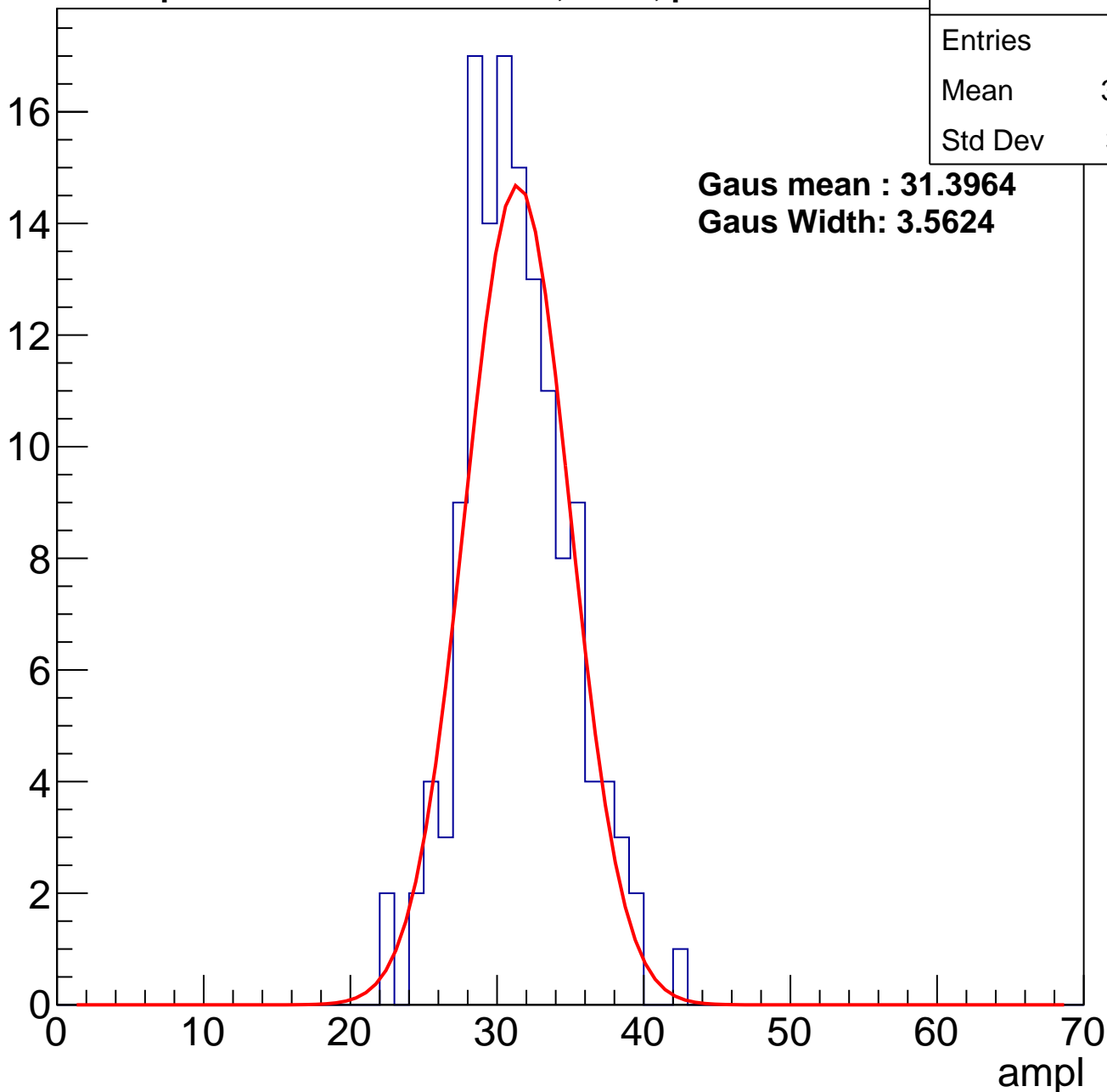
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	30.88
Std Dev	3.571

**Gaus mean : 31.3964**

**Gaus Width: 3.5624**

Entry



# B0L101S, U23-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

148

Mean

37.64

Std Dev

3.585

**Gaus mean : 38.3282**

**Gaus Width: 3.6404**

0

10

20

30

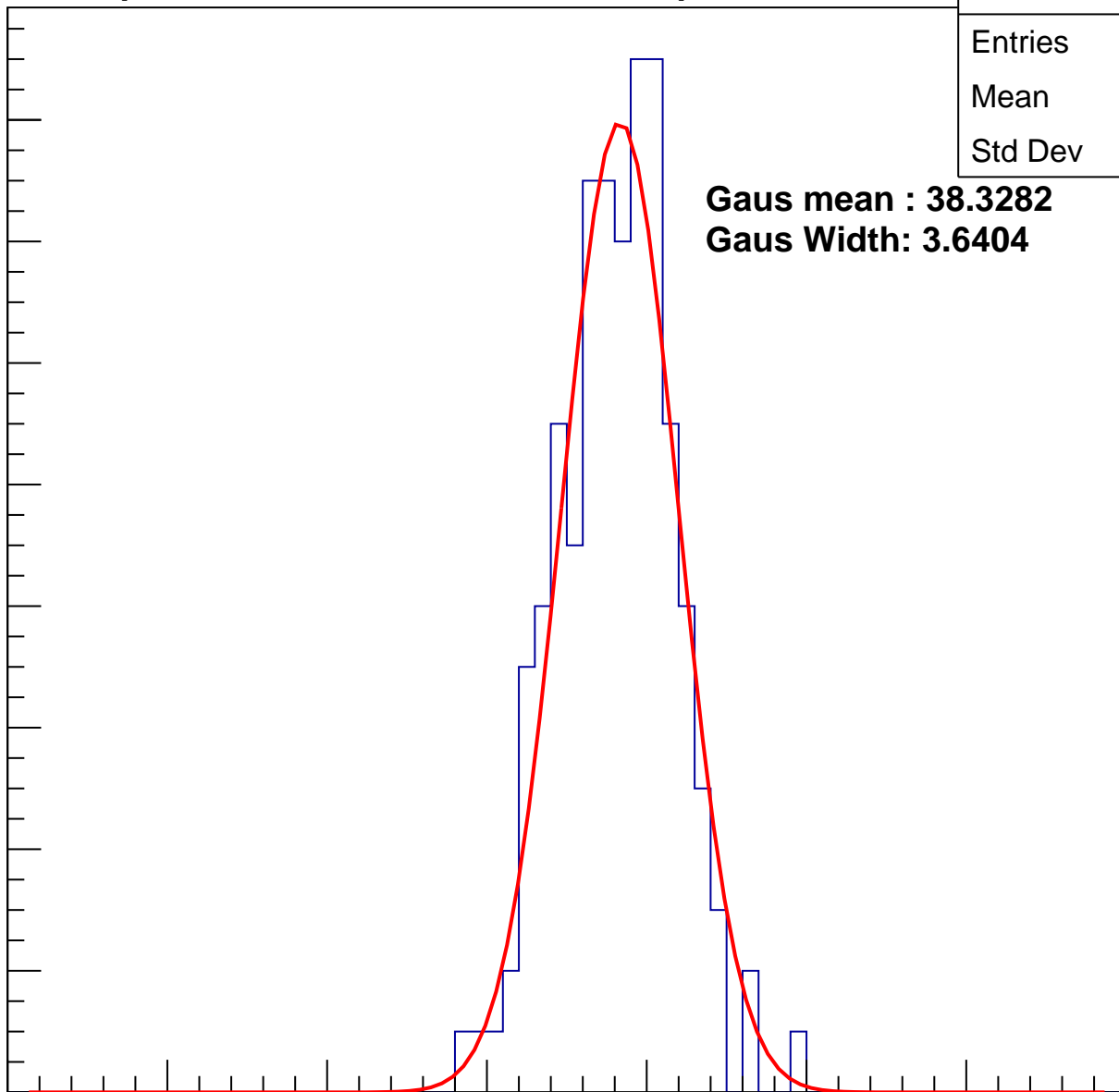
40

50

60

70

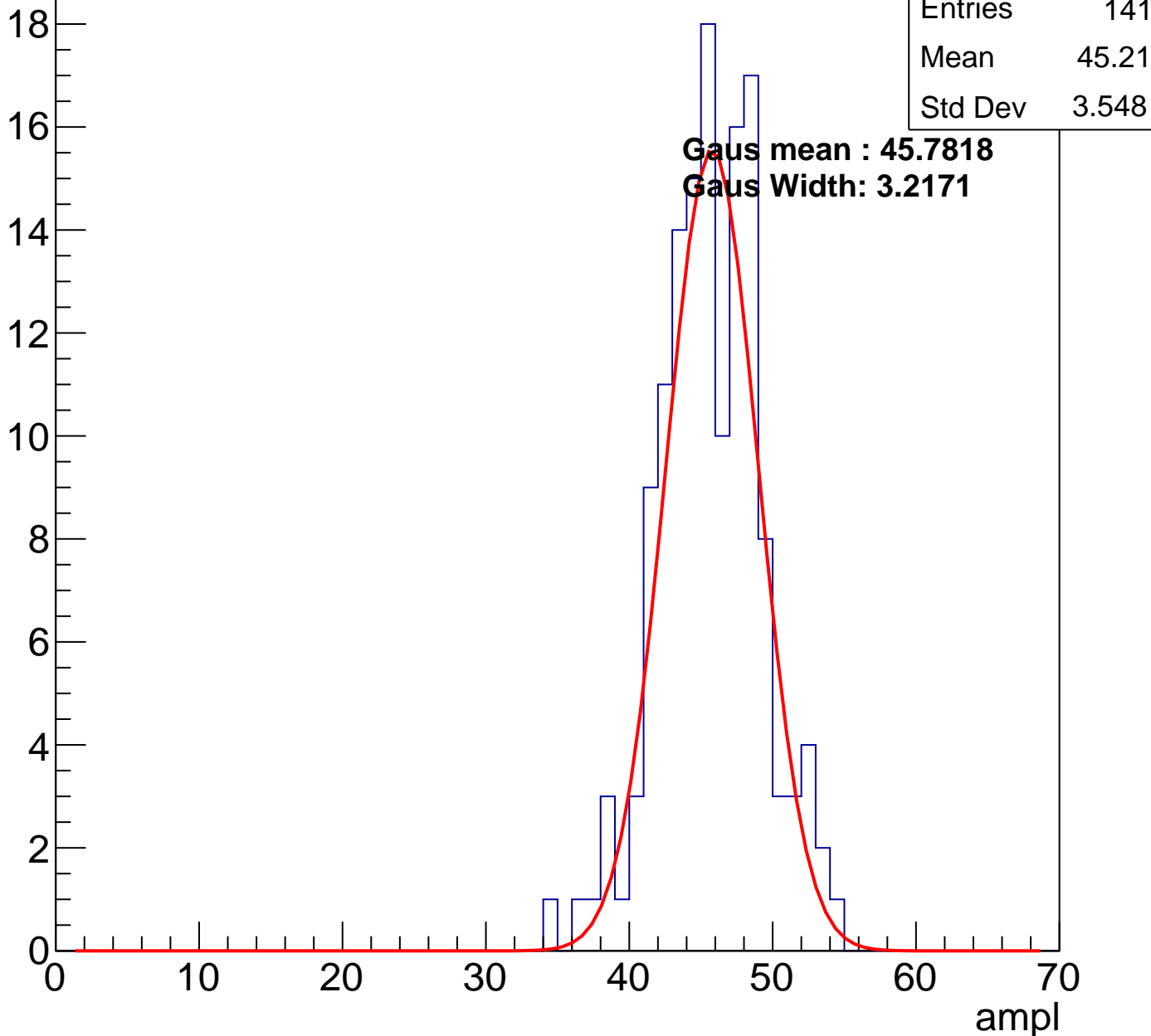
ampl



# B0L101S, U23-ch74, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	51.71
Std Dev	3.438

Entry

16

14

12

10

8

6

4

2

0

0

10

20

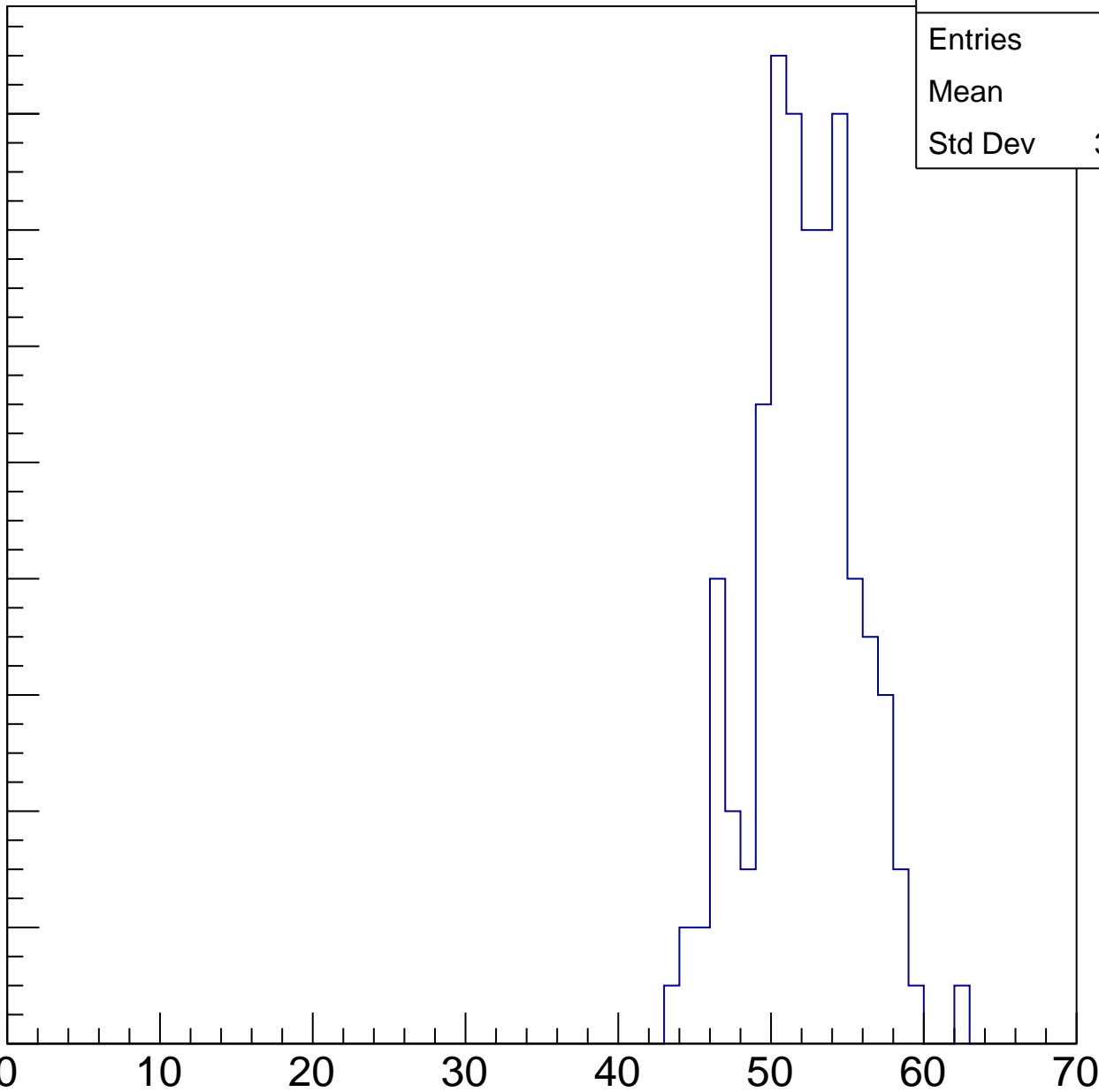
30

40

50

60

ampl



# B0L101S, U23-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

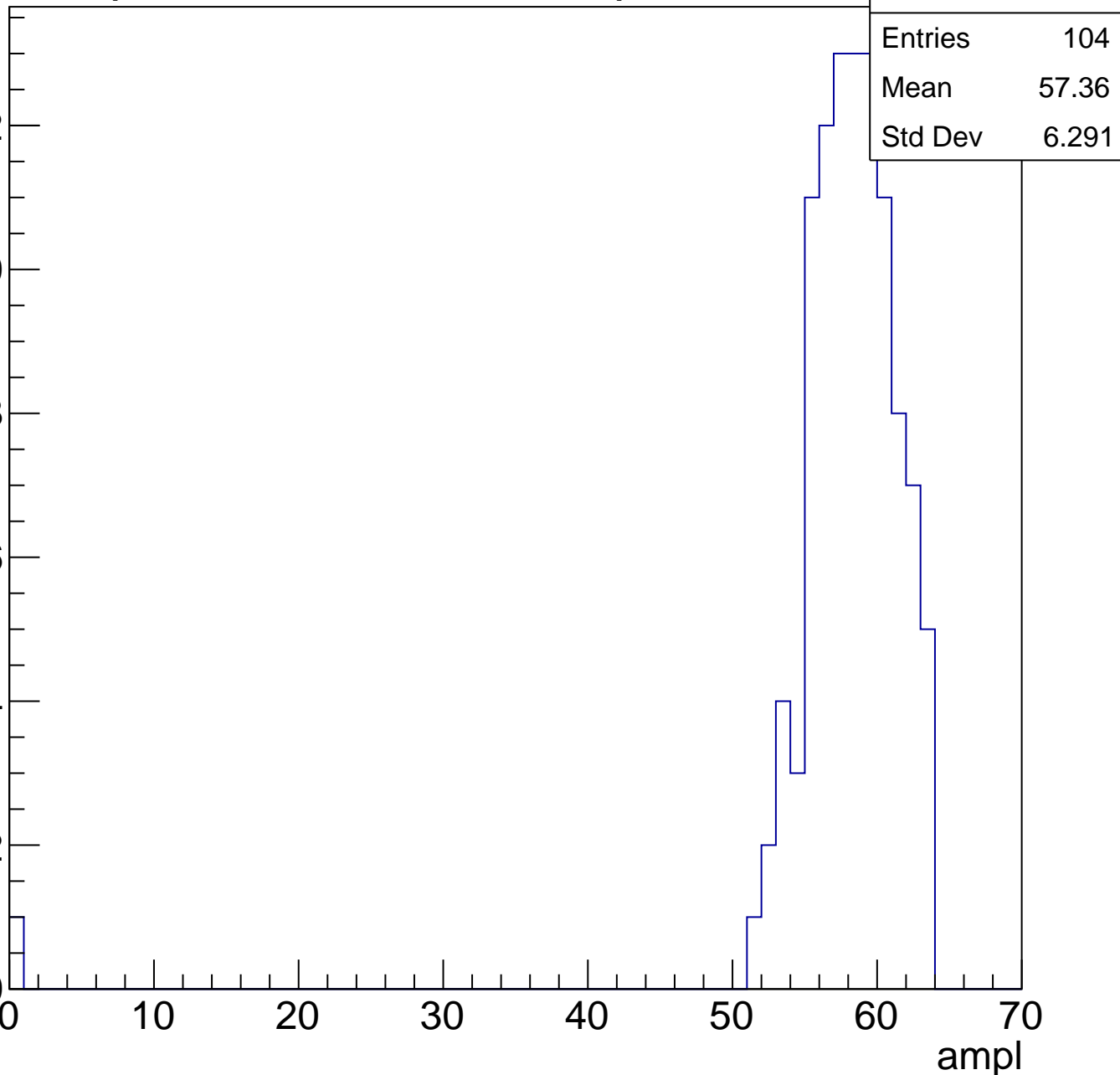
50

60

70

ampl

Entries	104
Mean	57.36
Std Dev	6.291



# B0L101S, U23-ch74, adc5

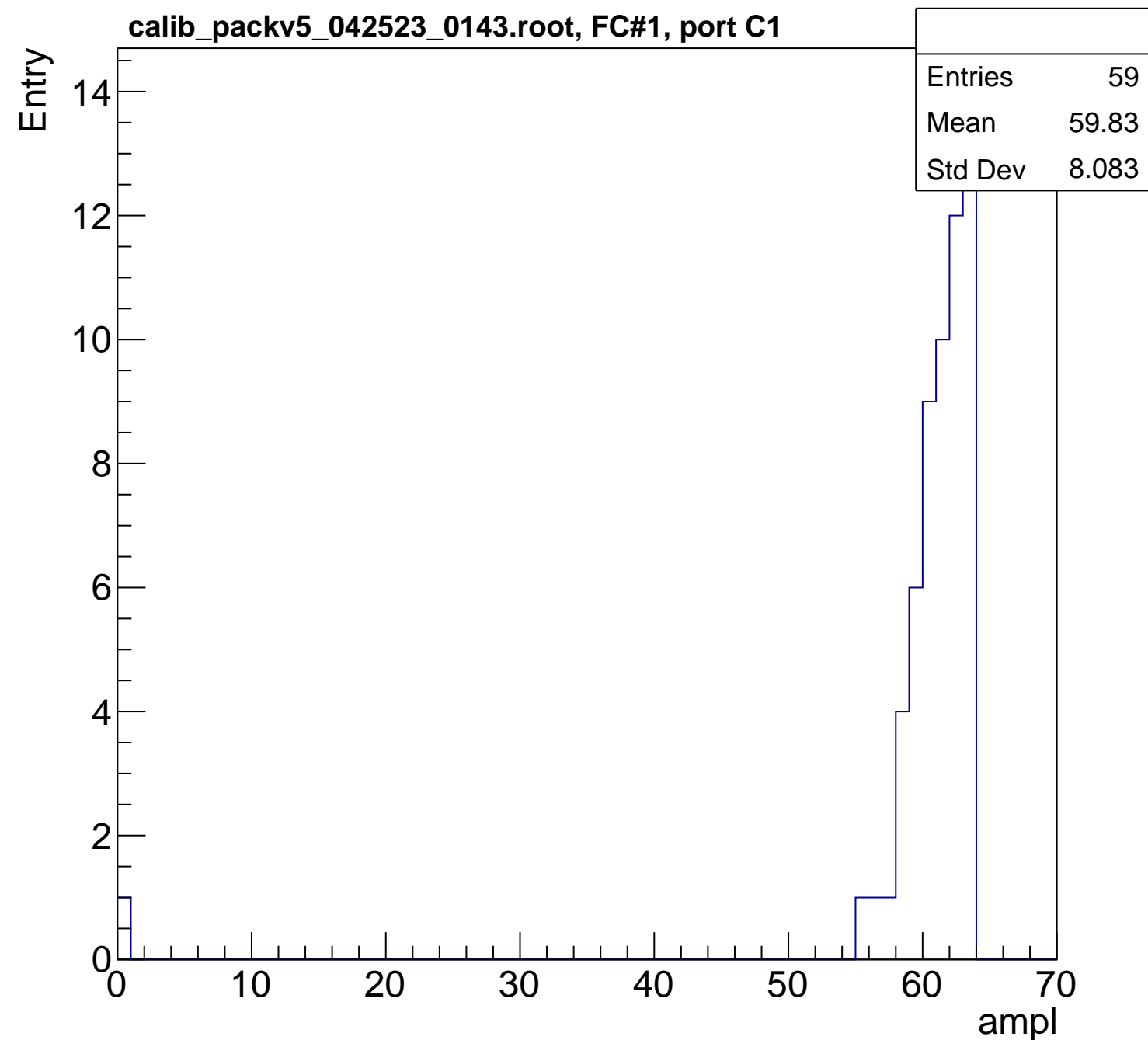
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	59
Mean	59.83
Std Dev	8.083

ampl



# B0L101S, U23-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L101S, U23-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	28.54
Std Dev	4.498

**Gaus mean : 29.0406**

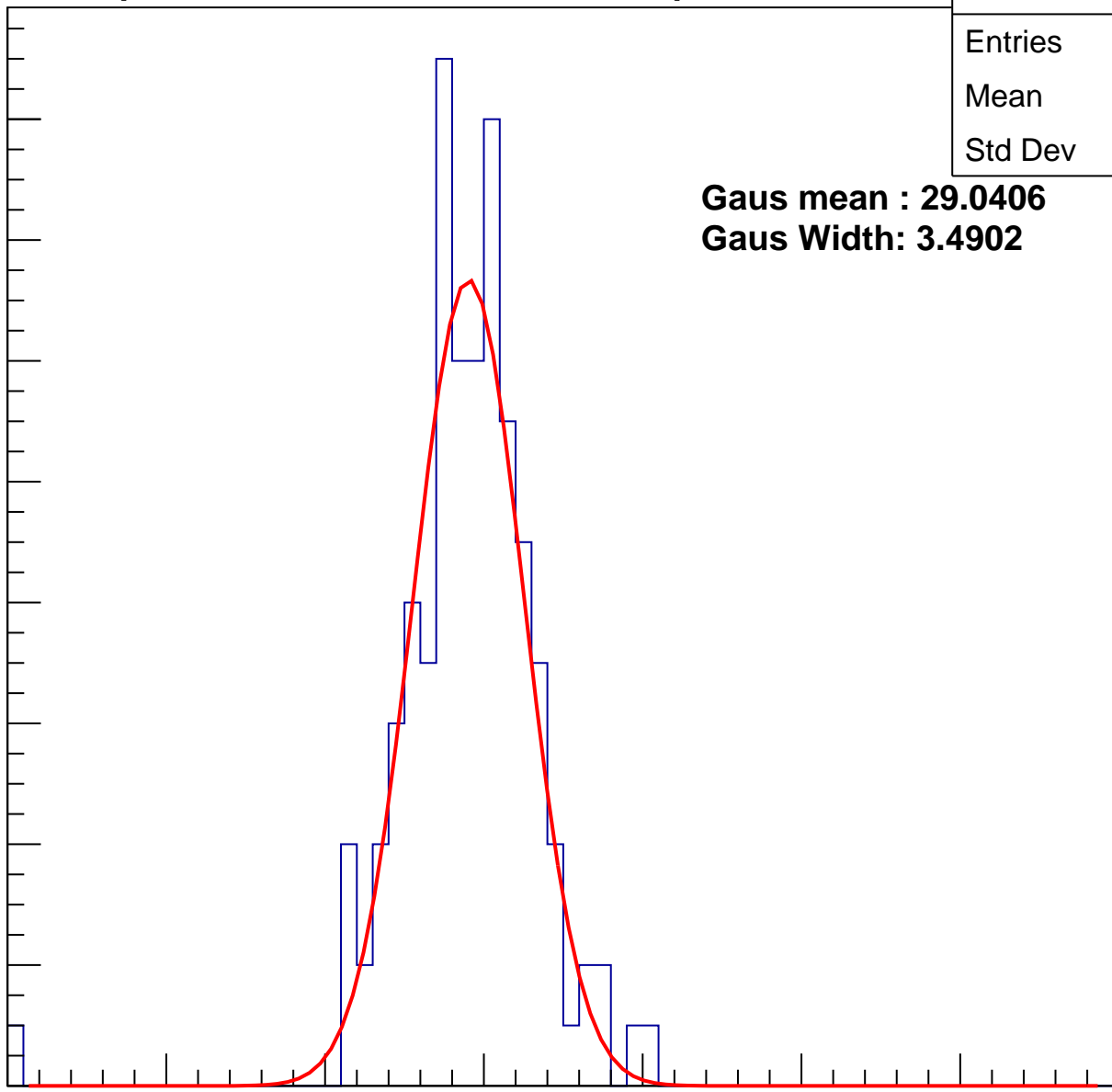
**Gaus Width: 3.4902**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch75, adc1

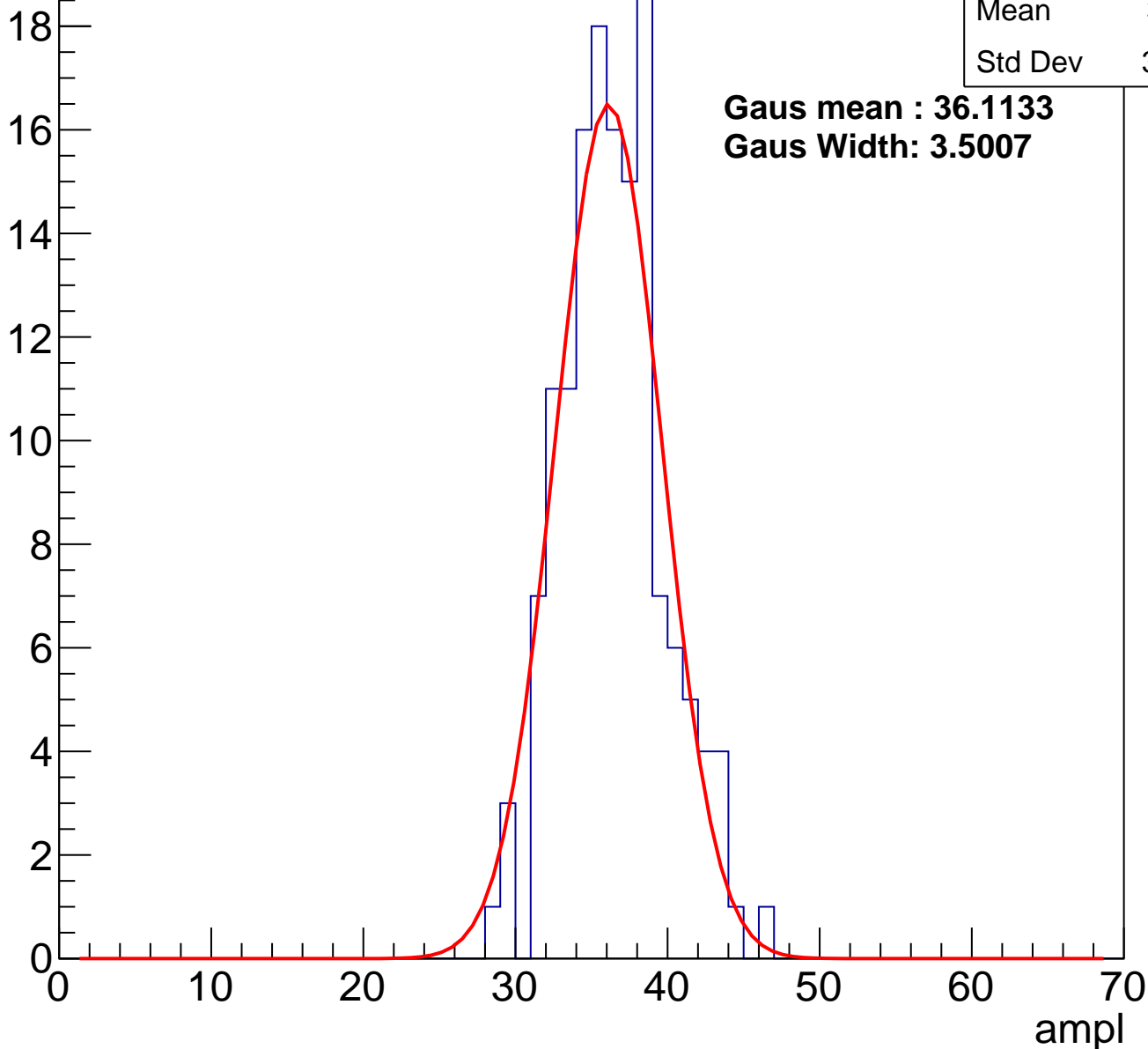
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	36.01
Std Dev	3.345

**Gaus mean : 36.1133**

**Gaus Width: 3.5007**

Entry



# B0L101S, U23-ch75, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	143
Mean	43.14
Std Dev	3.334

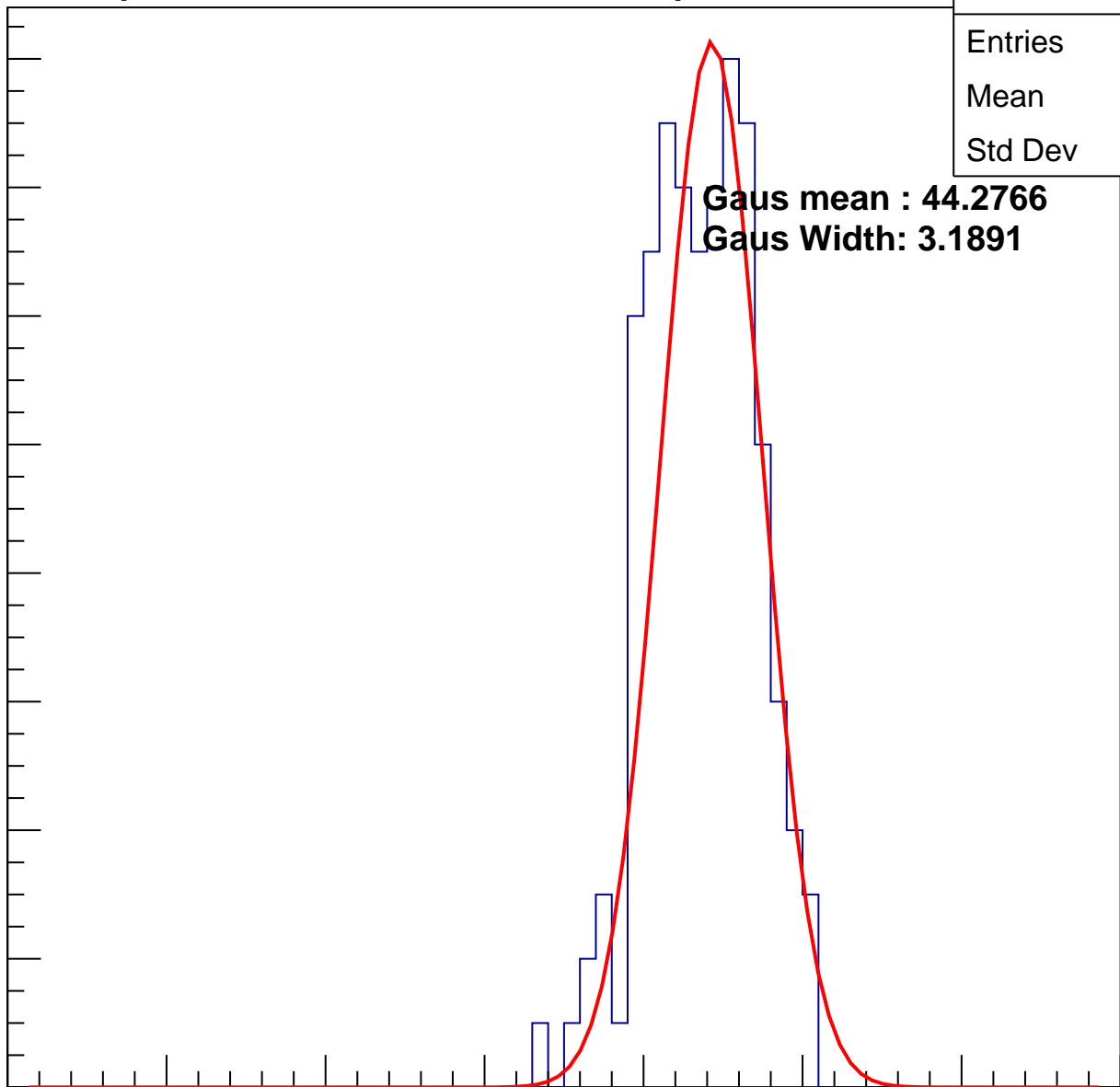
**Gaus mean : 44.2766**

**Gaus Width: 3.1891**

0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch75, adc3

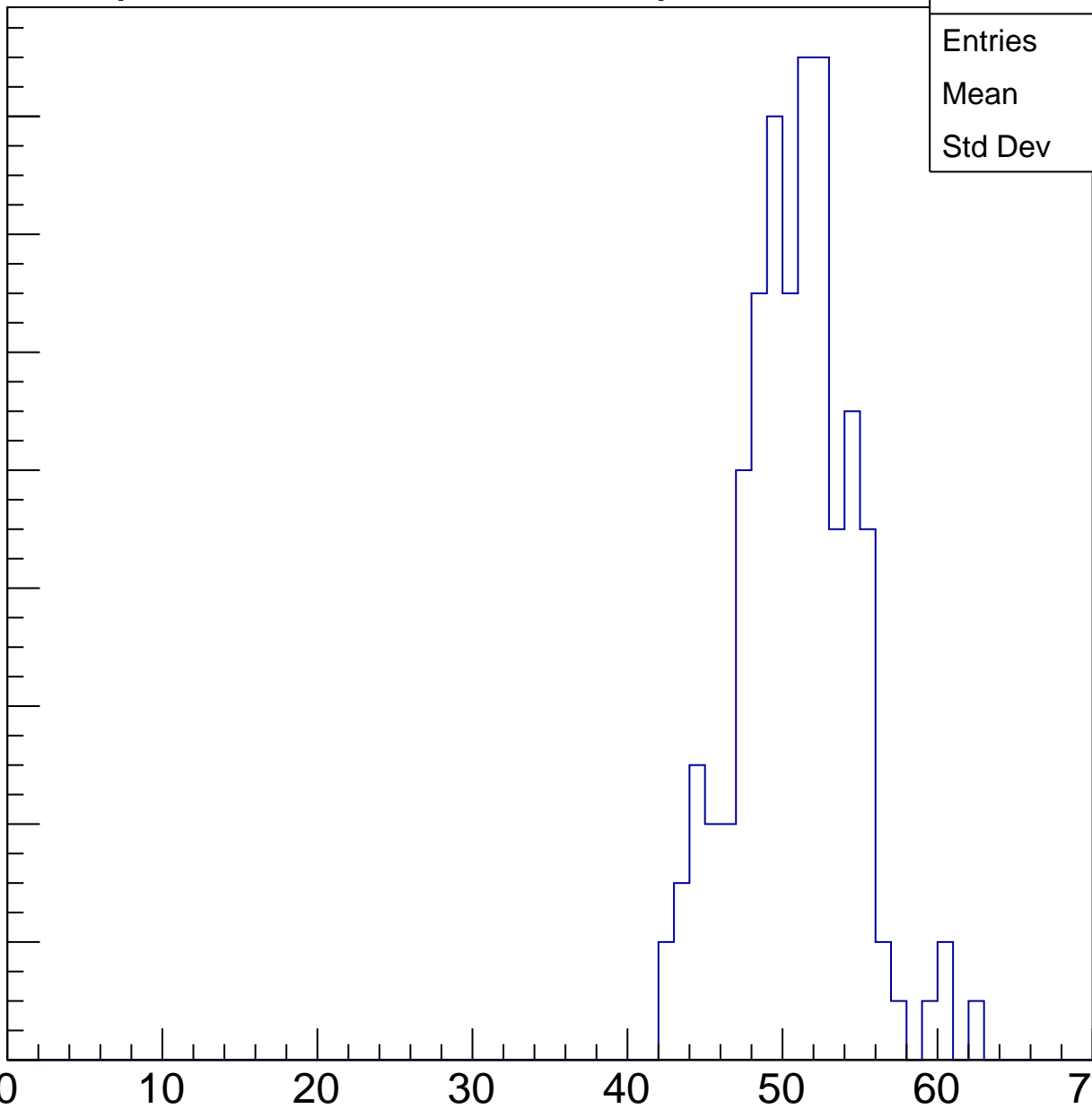
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	140
Mean	50.37
Std Dev	3.655

ampl

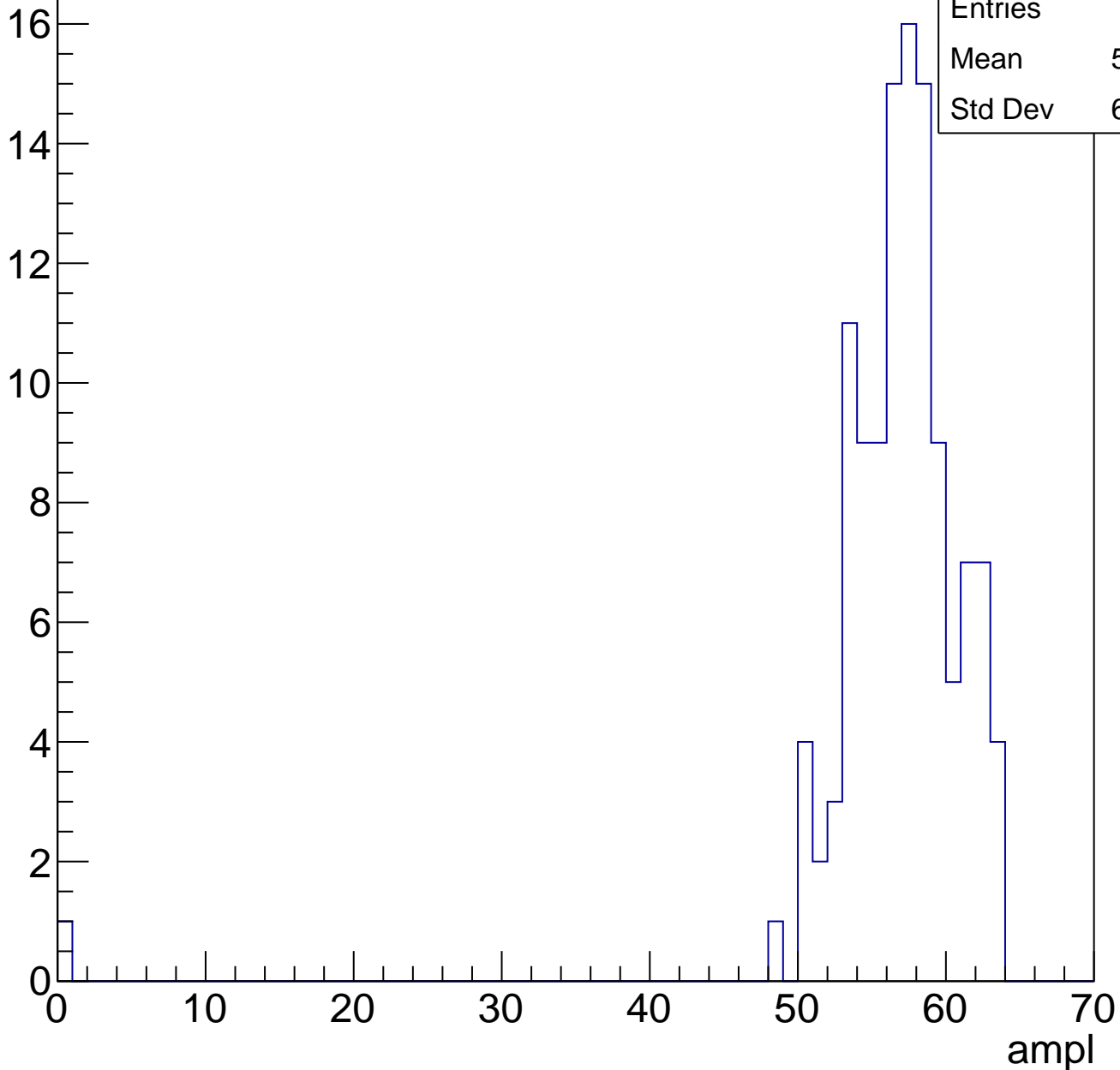


# B0L101S, U23-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	56.24
Std Dev	6.134

Entry



# B0L101S, U23-ch75, adc5

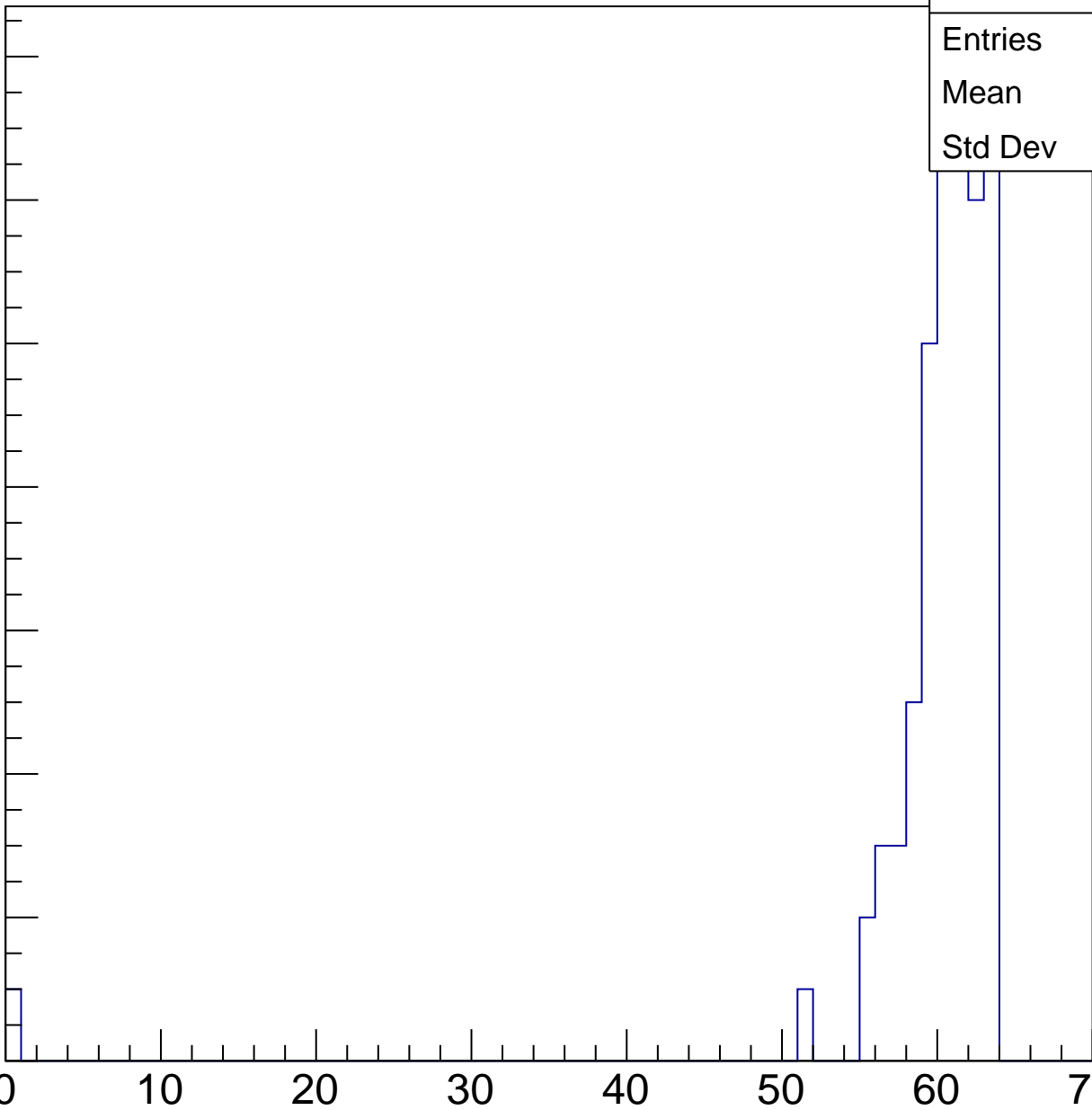
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	77
Mean	59.47
Std Dev	7.209

ampl



# B0L101S, U23-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch76, adc0

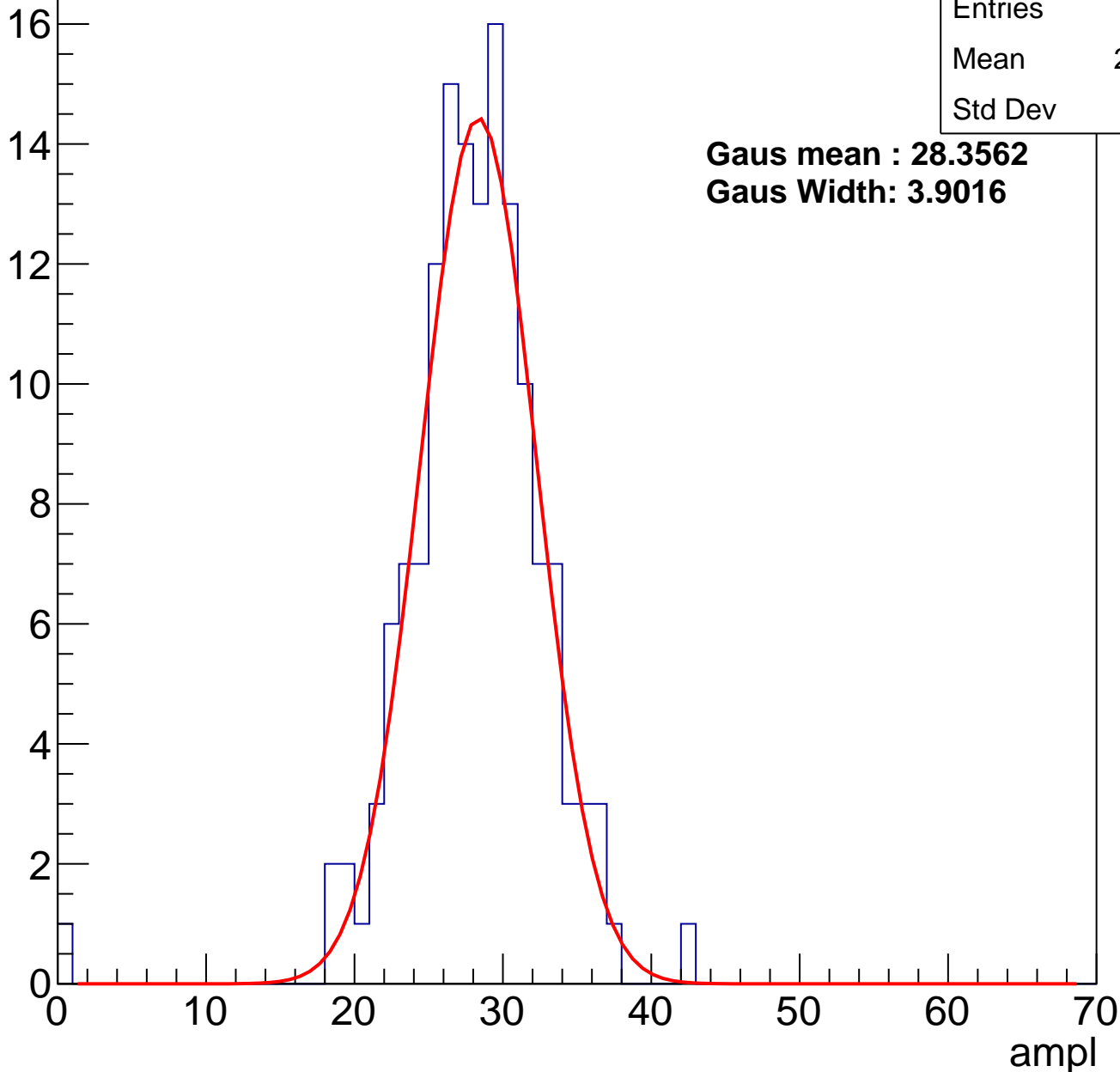
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	27.64
Std Dev	4.66

**Gaus mean : 28.3562**

**Gaus Width: 3.9016**

Entry



# B0L101S, U23-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	35.05
Std Dev	3.897

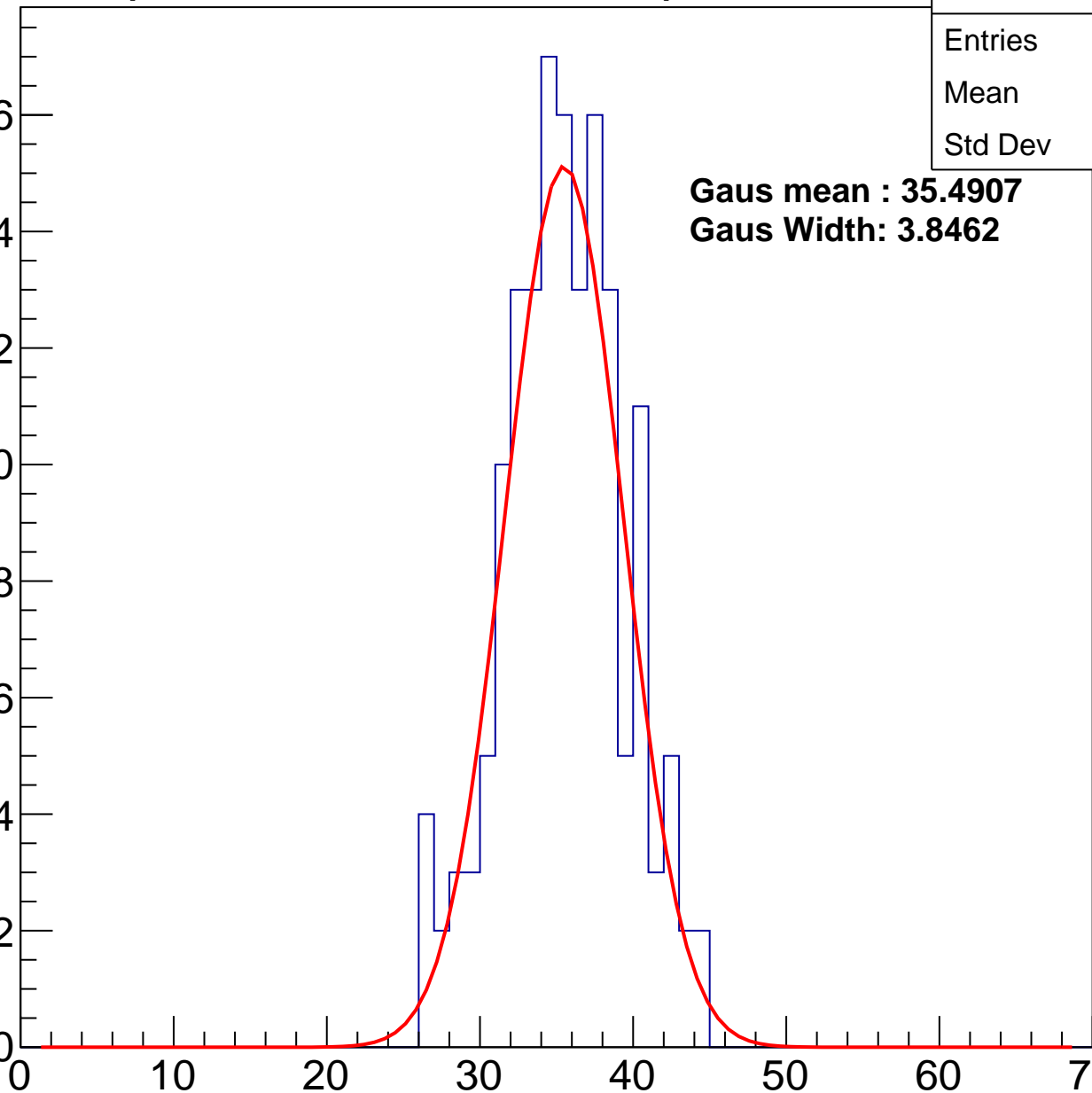
**Gaus mean : 35.4907**

**Gaus Width: 3.8462**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch76, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

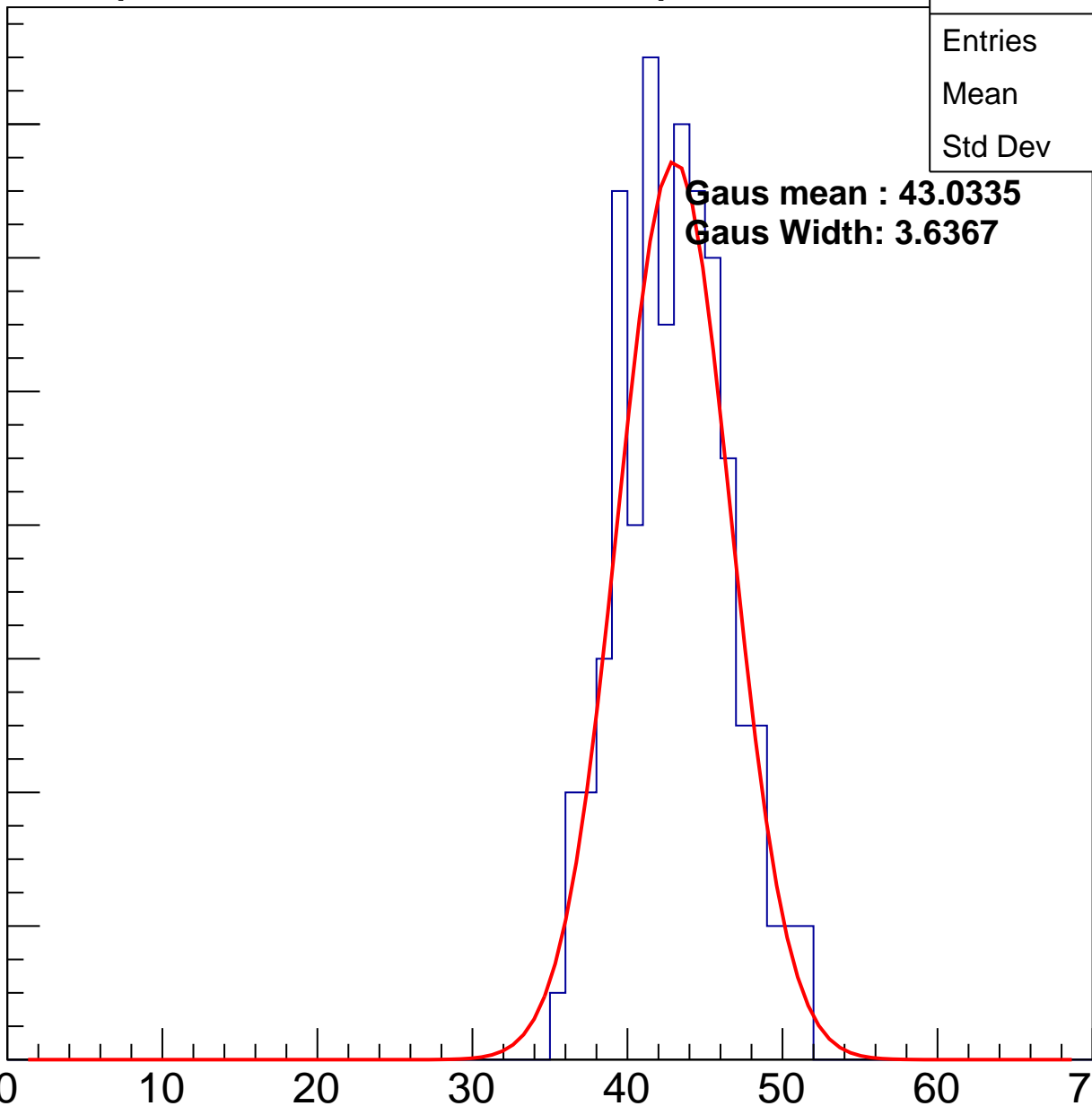
14  
12  
10  
8  
6  
4  
2  
0

Entries	126
Mean	42.56
Std Dev	3.479

**Gaus mean : 43.0335**

**Gaus Width: 3.6367**

ampl



# B0L101S, U23-ch76, adc3

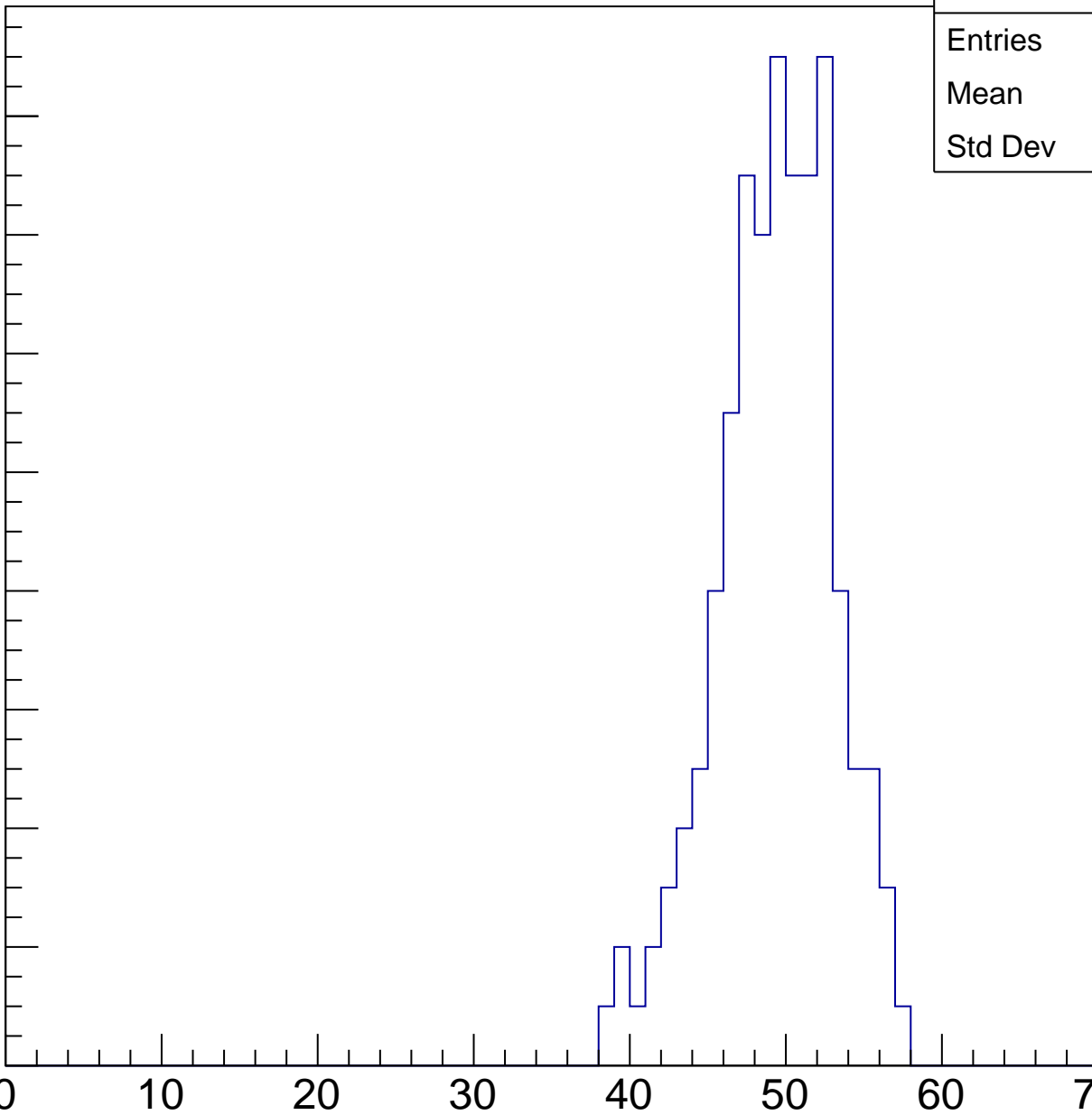
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	152
Mean	48.85
Std Dev	3.731

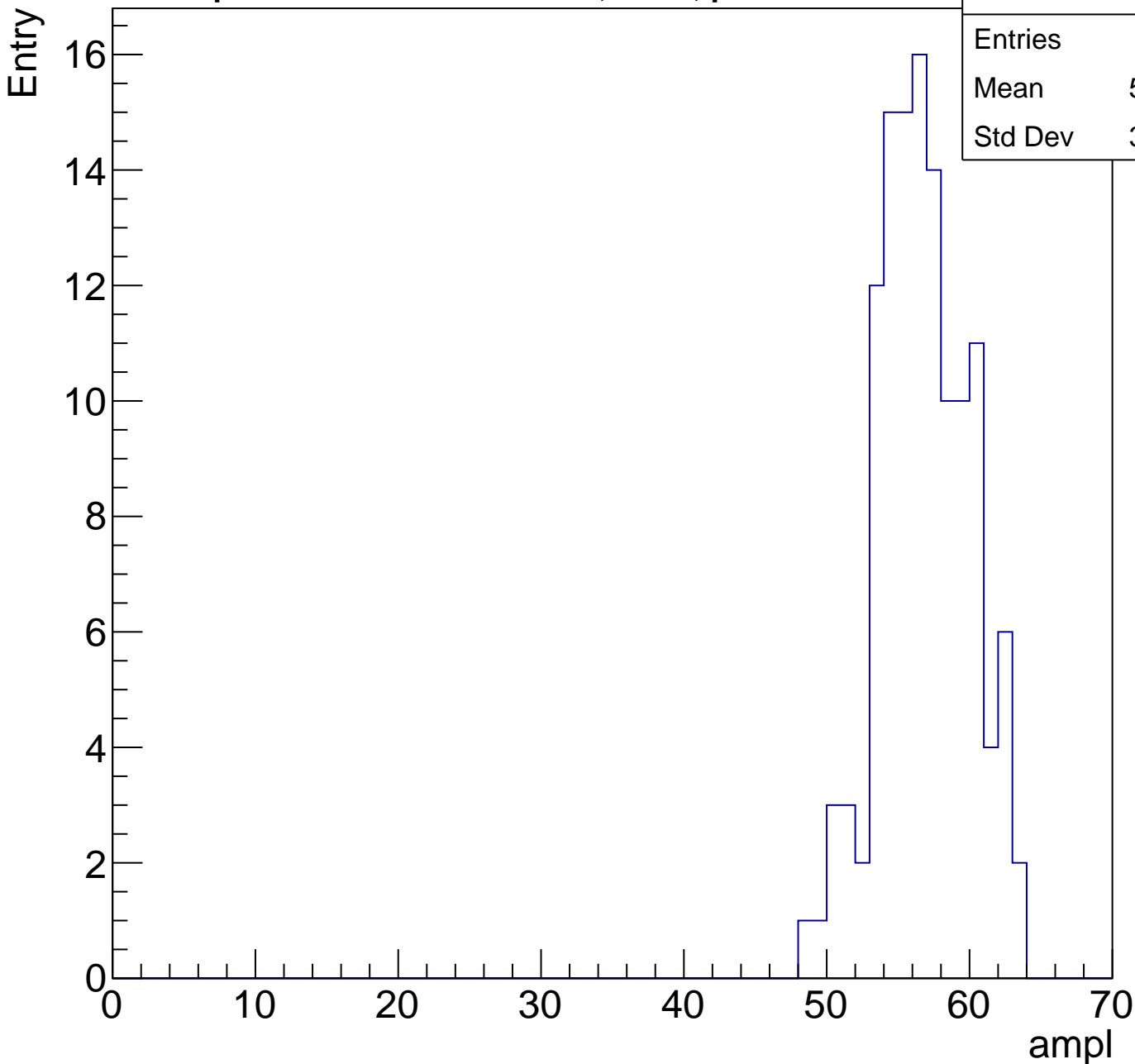
ampl



# B0L101S, U23-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	56.33
Std Dev	3.139



# B0L101S, U23-ch76, adc5

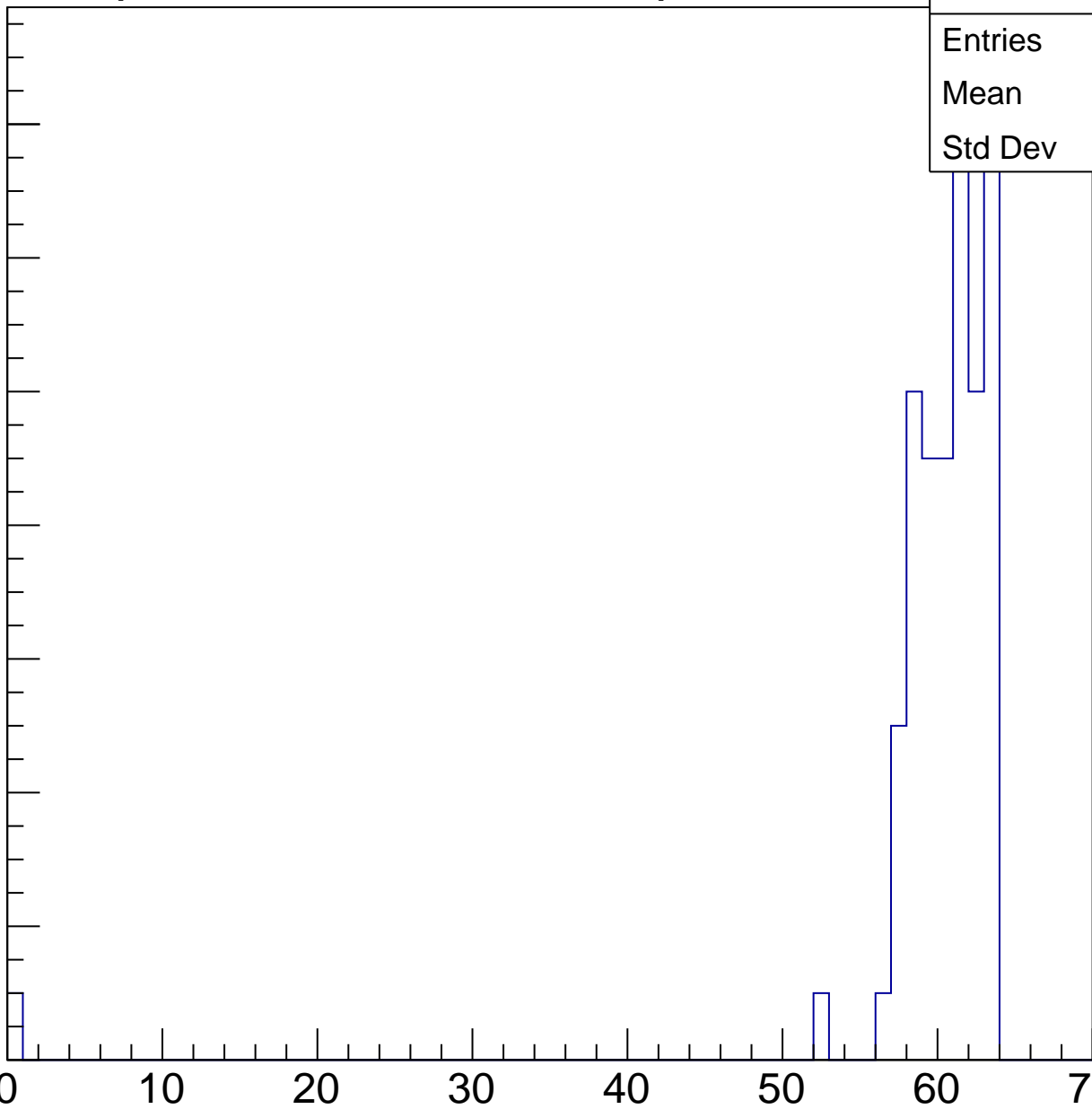
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	75
Mean	59.48
Std Dev	7.243

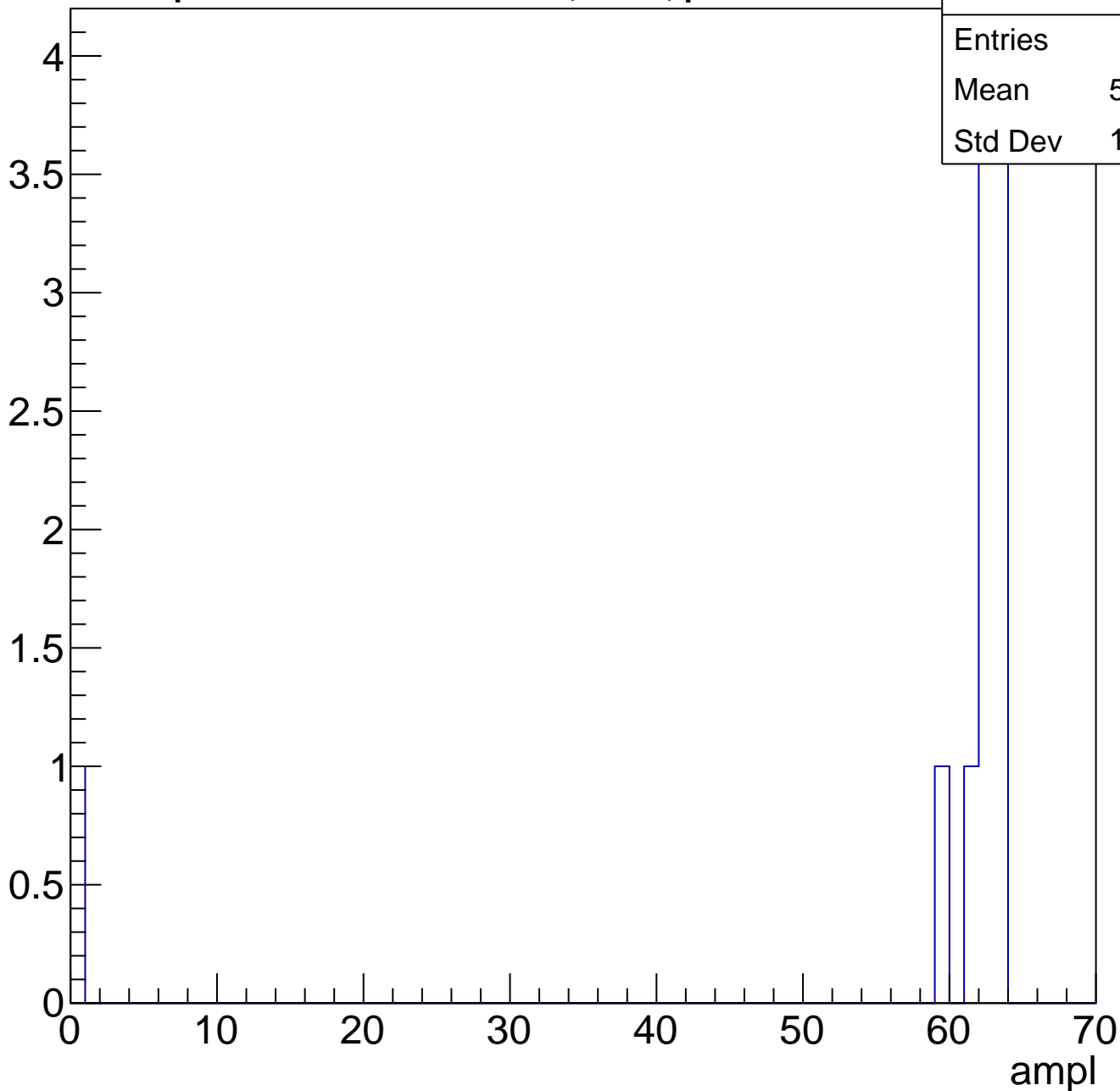
ampl



# B0L101S, U23-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	27.94
Std Dev	3.388

**Gaus mean : 28.9361**

**Gaus Width: 3.0164**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

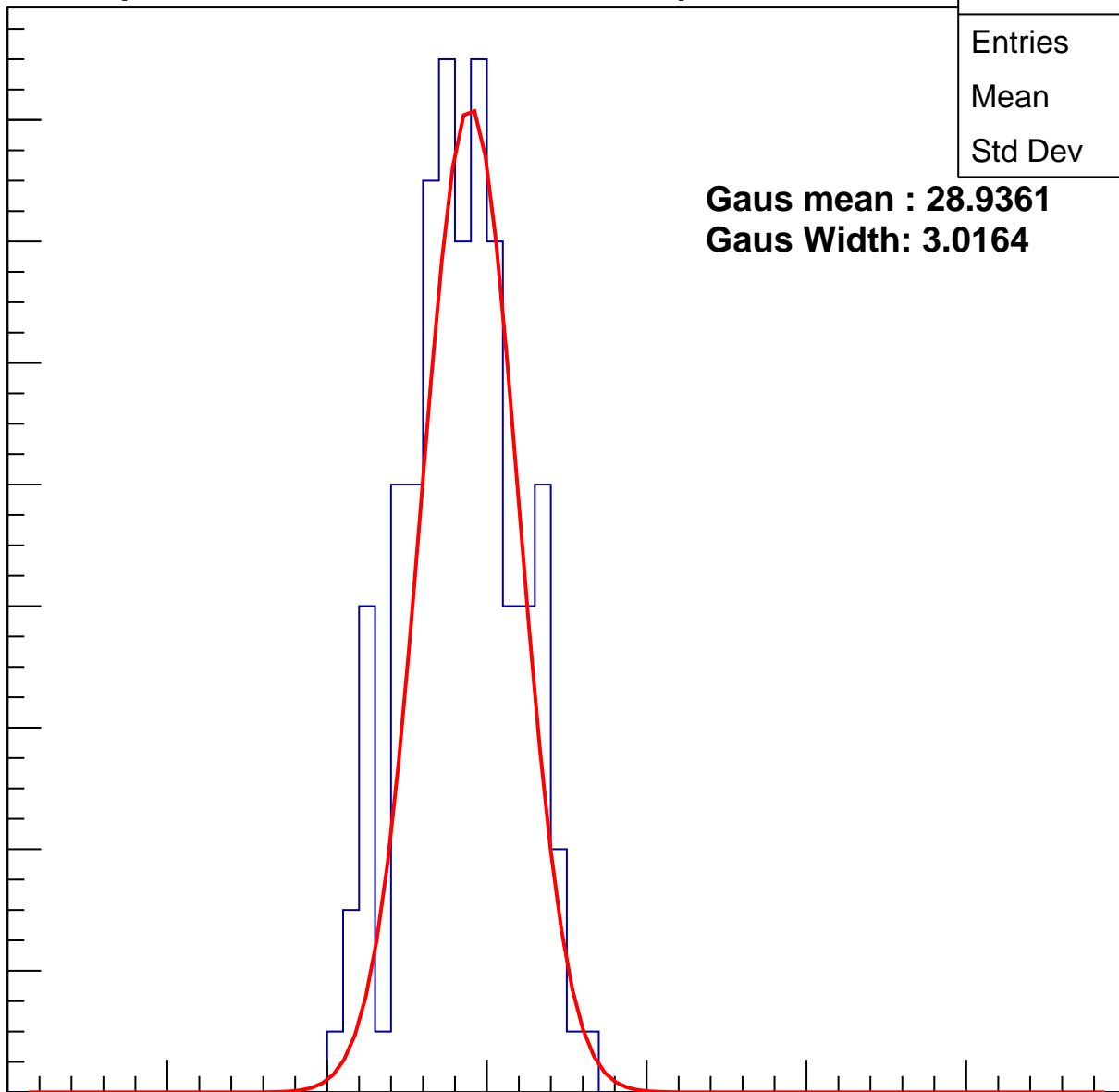
40

50

60

70

ampl



# B0L101S, U23-ch77, adc1

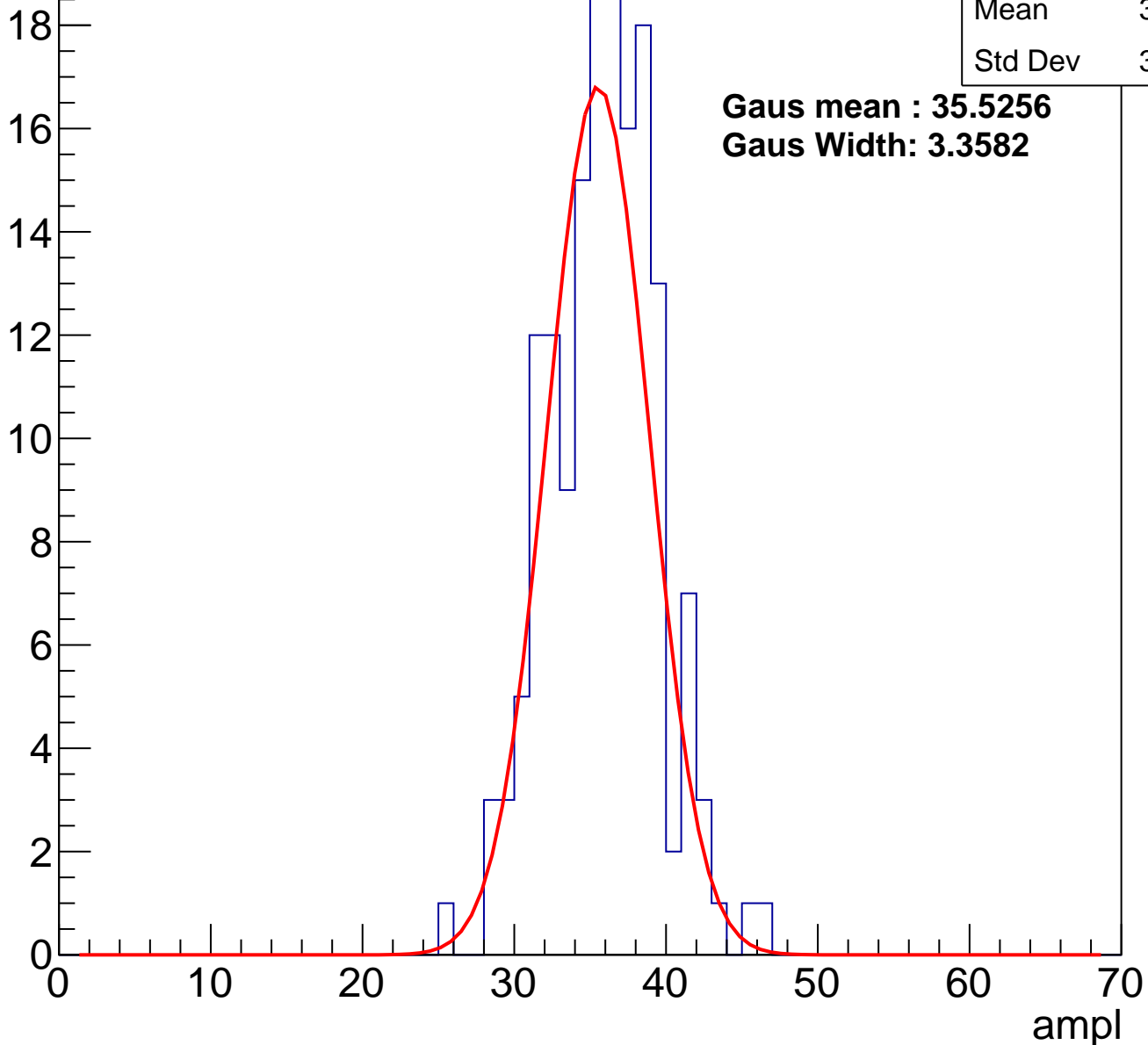
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	35.42
Std Dev	3.526

**Gaus mean : 35.5256**

**Gaus Width: 3.3582**

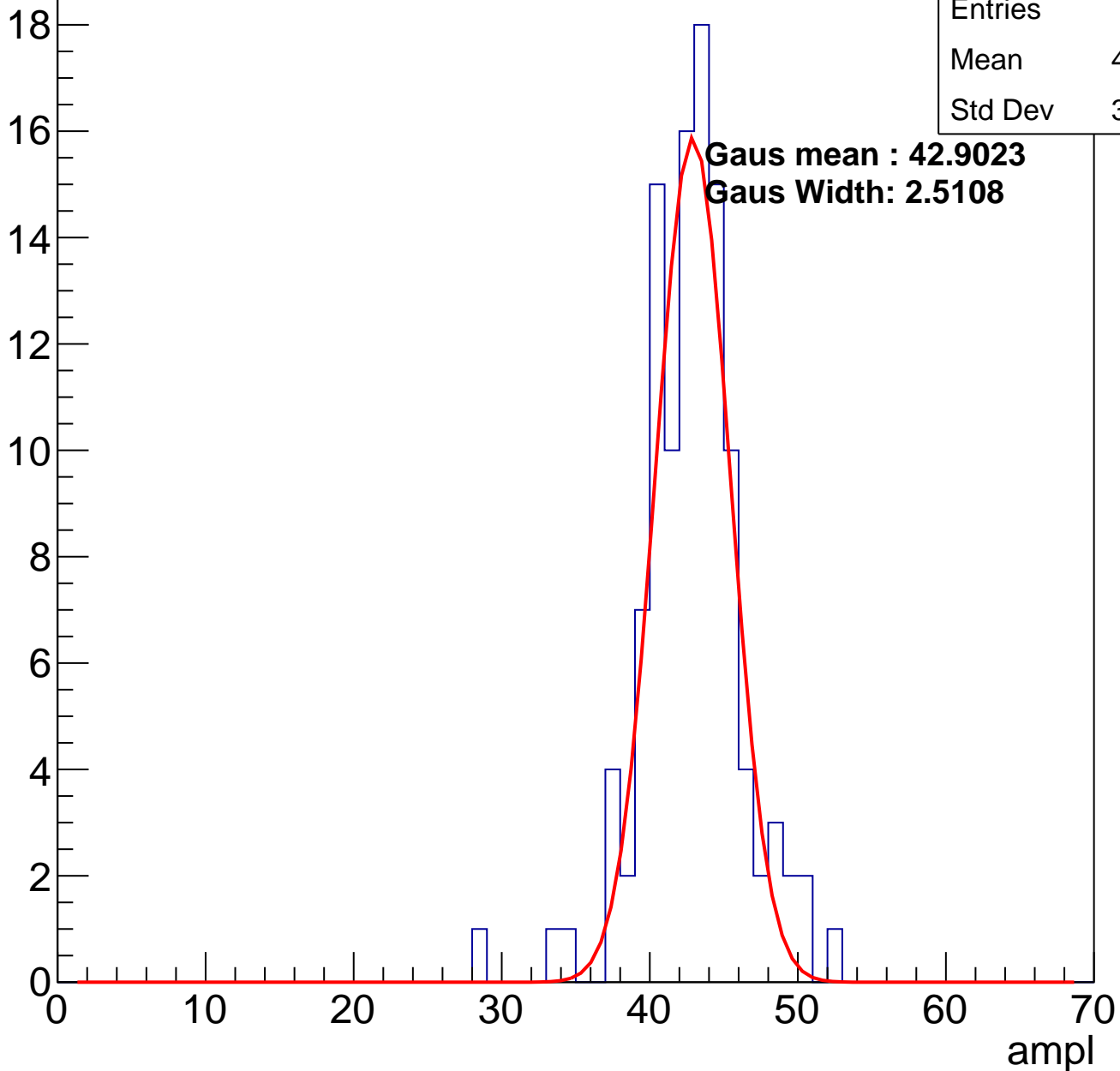
Entry



# B0L101S, U23-ch77, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

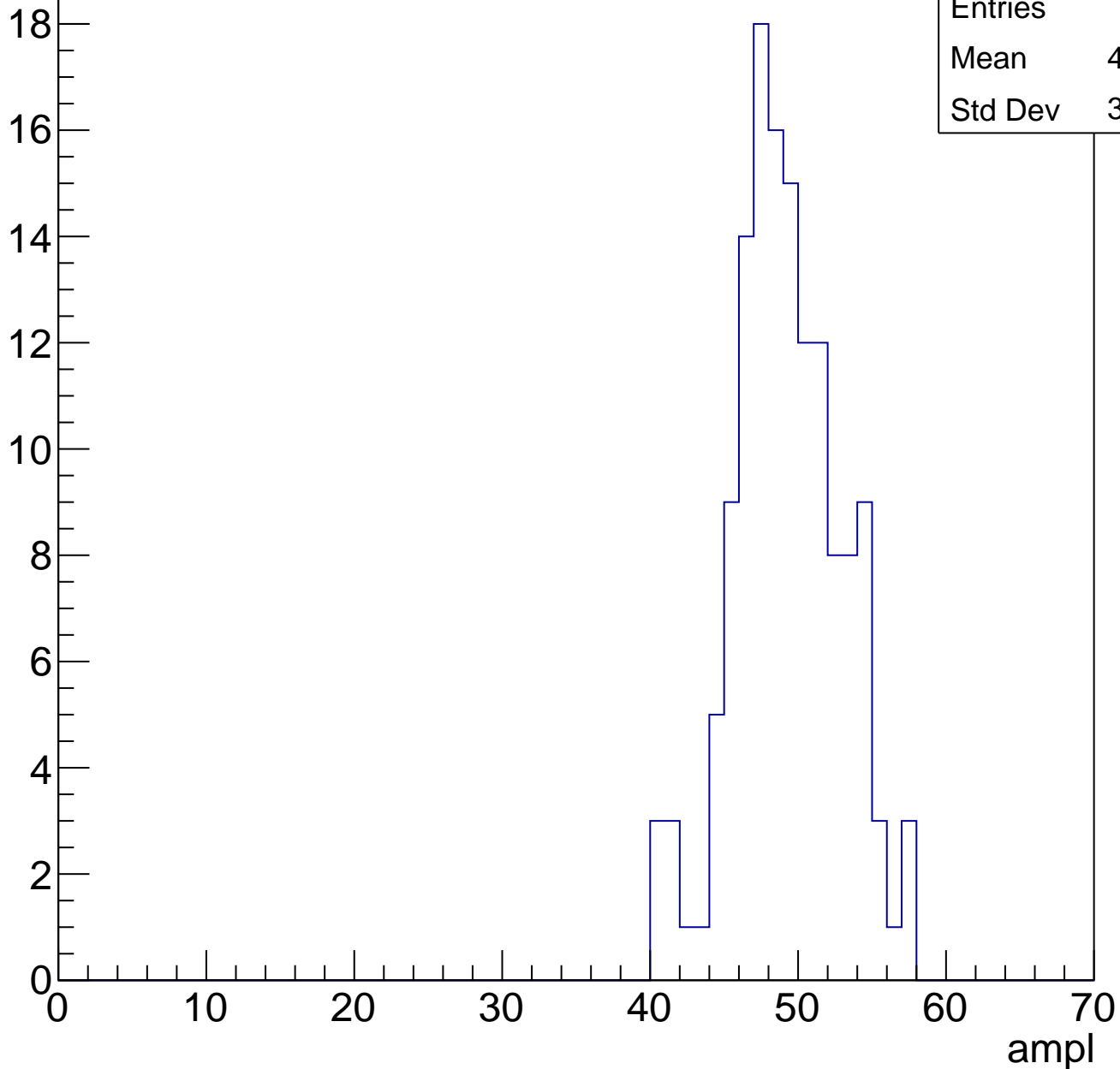
Entry



# B0L101S, U23-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	141
Mean	48.77
Std Dev	3.602

# B0L101S, U23-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	55.03
Std Dev	3.493

Entry

12

10

8

6

4

2

0

0

10

20

30

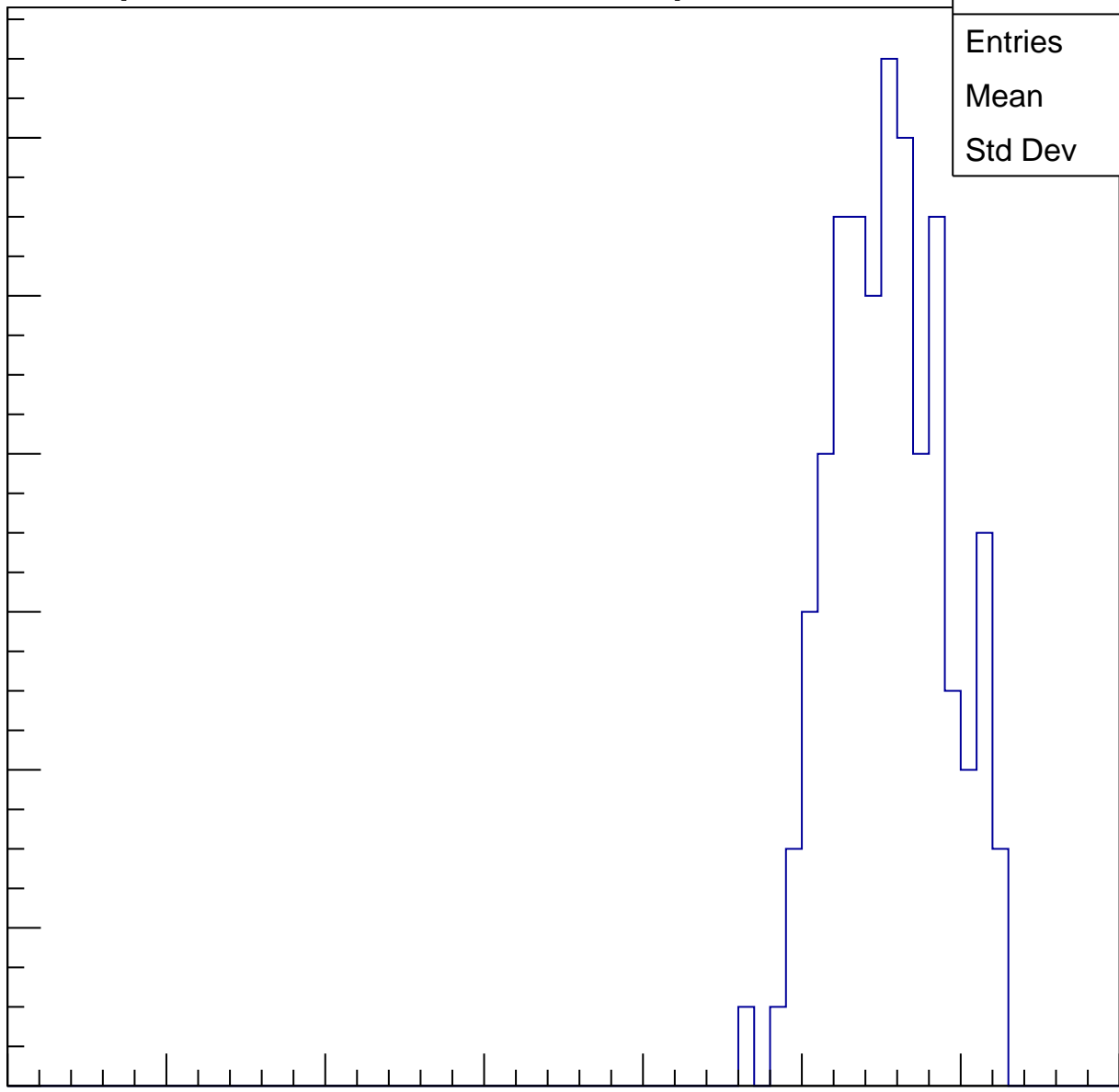
40

50

60

70

ampl



# B0L101S, U23-ch77, adc5

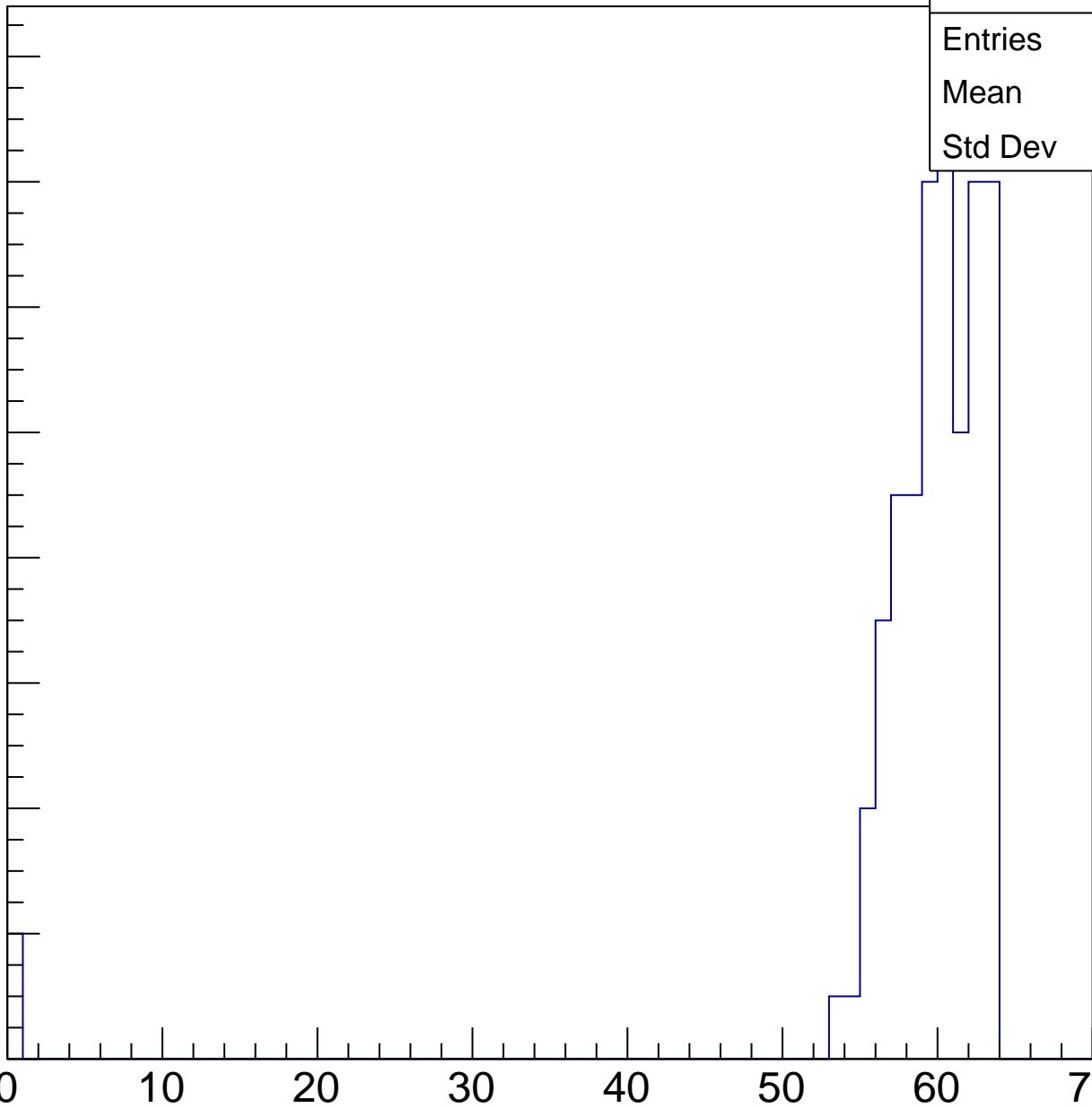
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	101
Mean	58.42
Std Dev	8.655

ampl

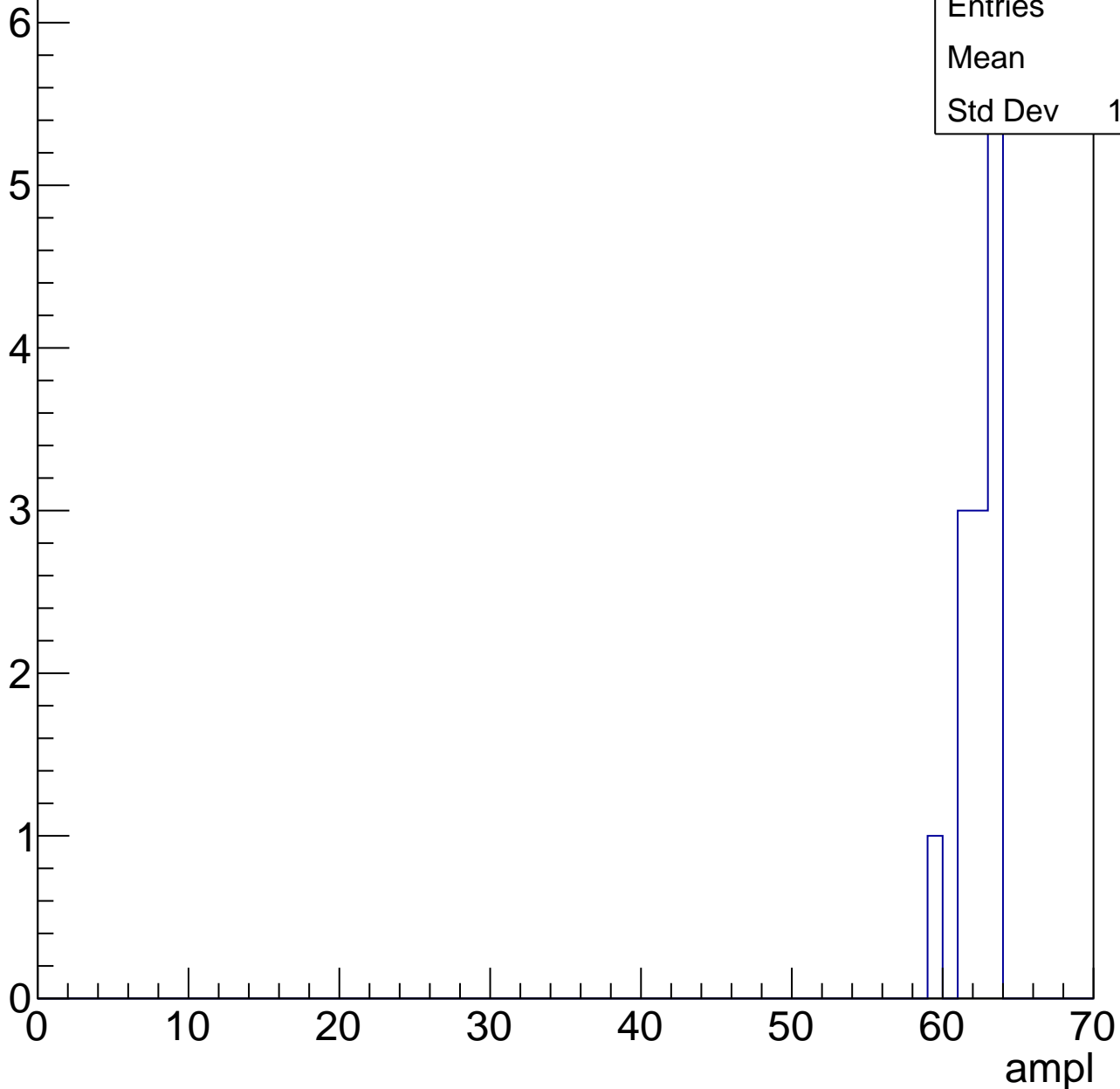


# B0L101S, U23-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	13
Mean	62
Std Dev	1.177





# B0L101S, U23-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch78, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	27.65
Std Dev	3.842

**Gaus mean : 28.3260**

**Gaus Width: 3.8730**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

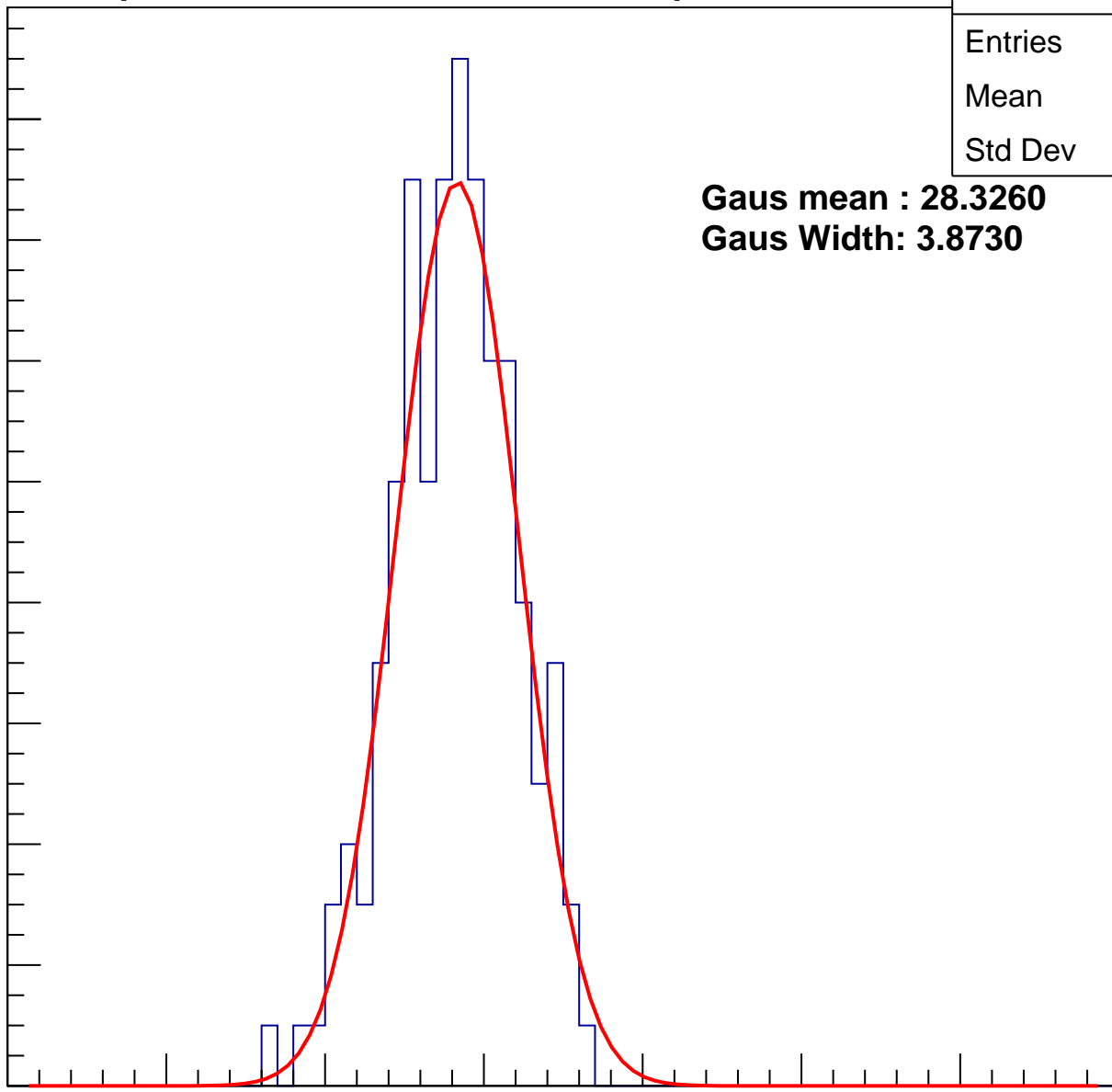
40

50

60

70

ampl



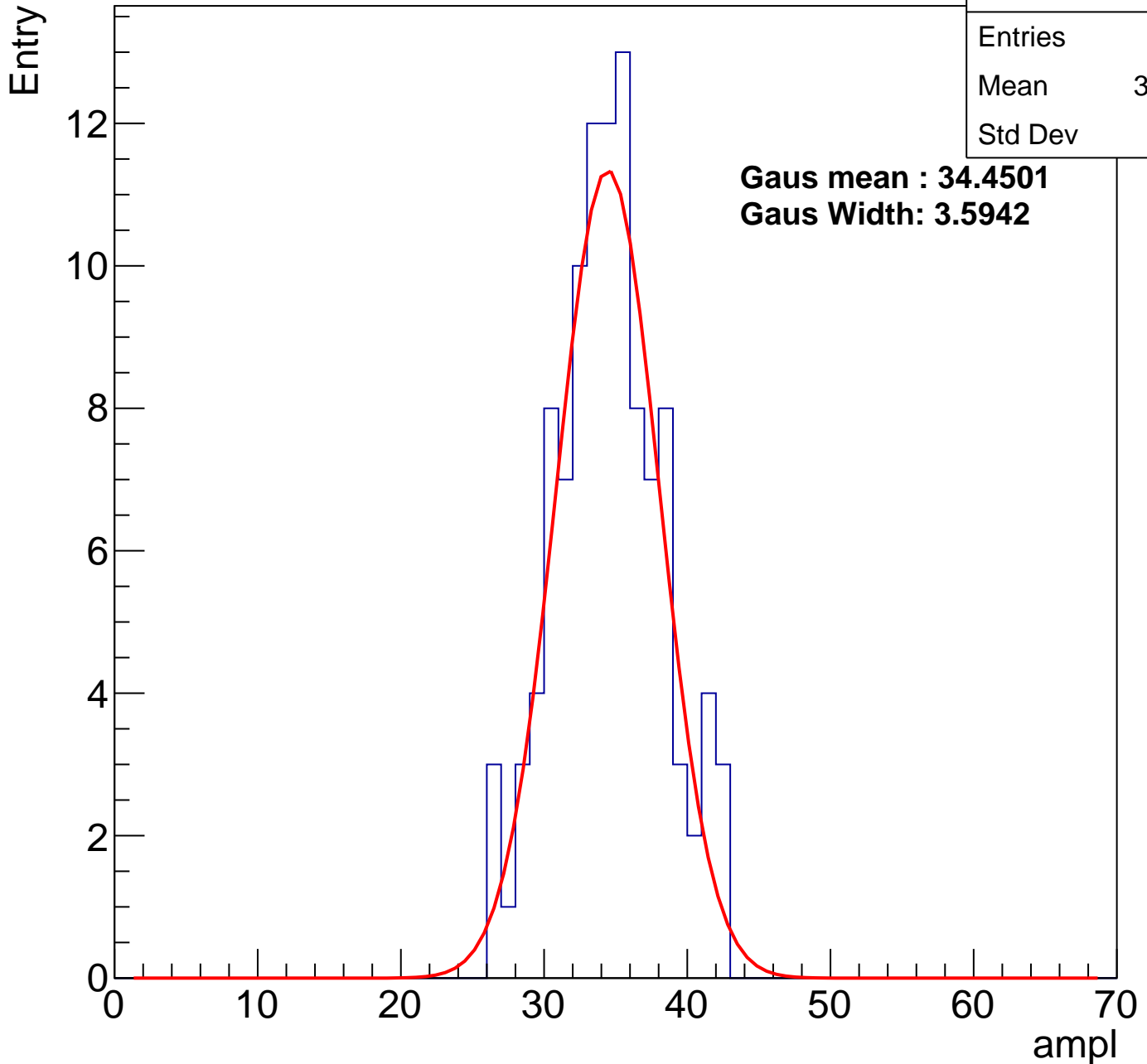
# B0L101S, U23-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	34.06
Std Dev	3.68

**Gaus mean : 34.4501**

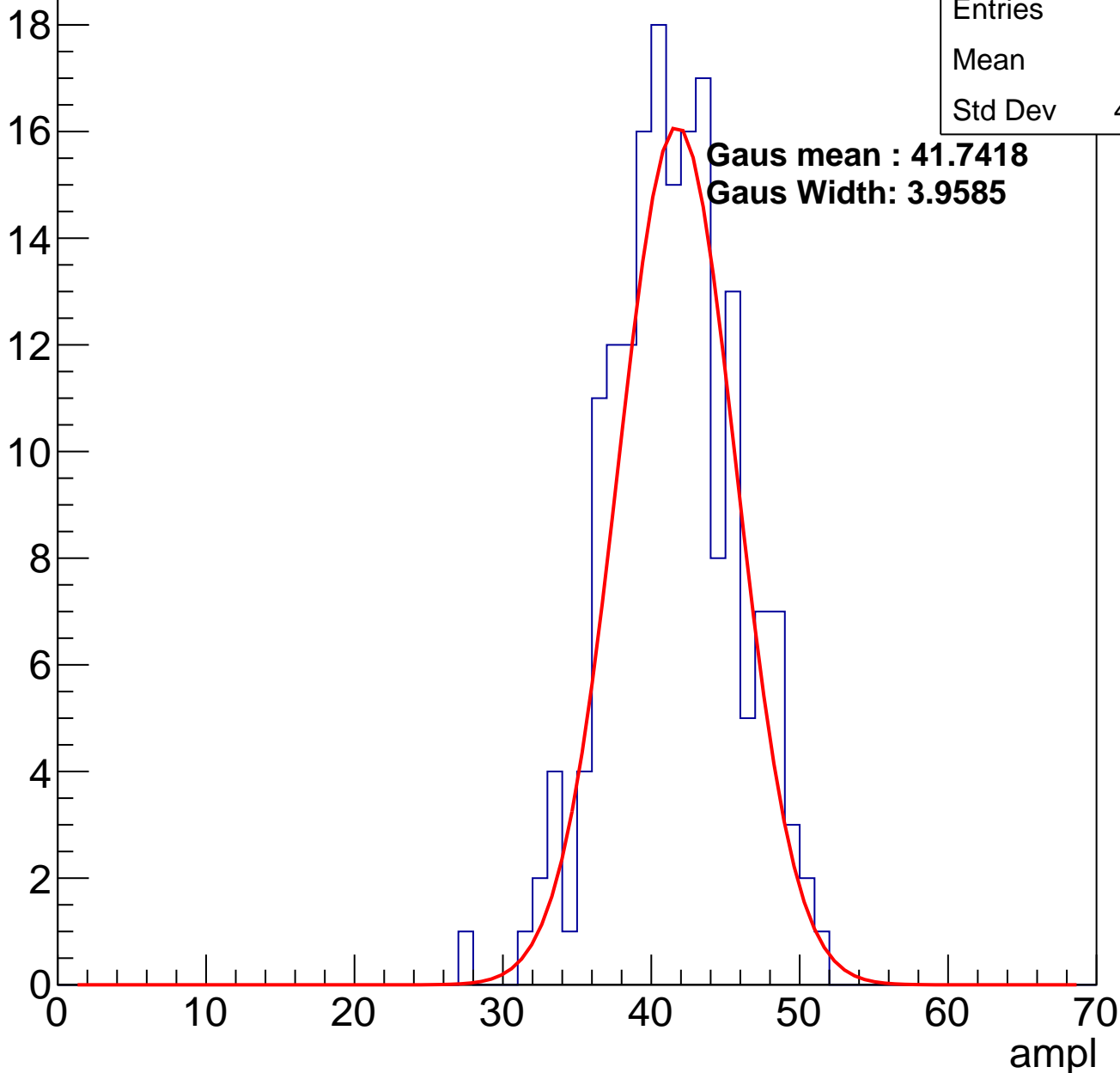
**Gaus Width: 3.5942**



# B0L101S, U23-ch78, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

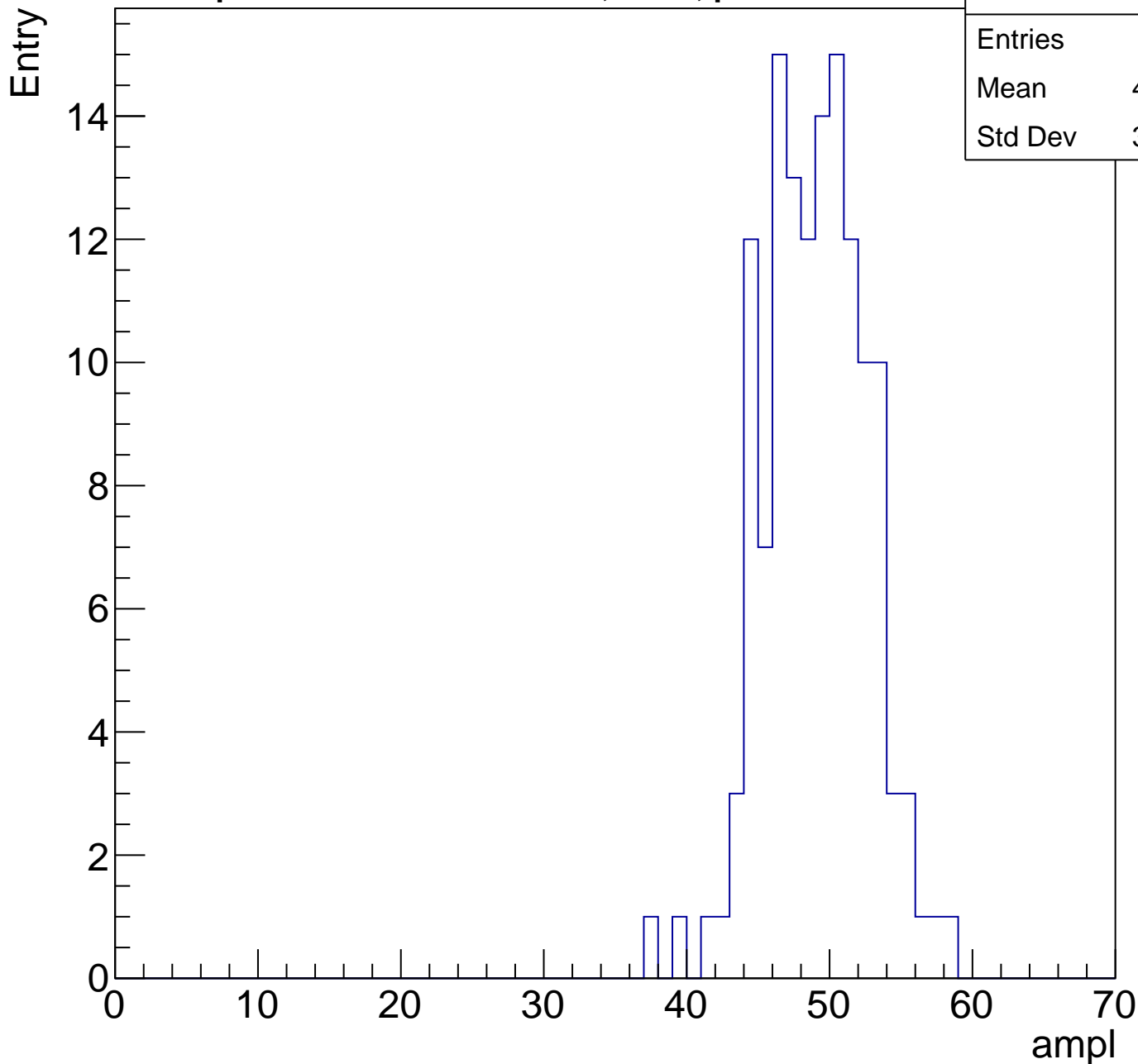
Entry



# B0L101S, U23-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

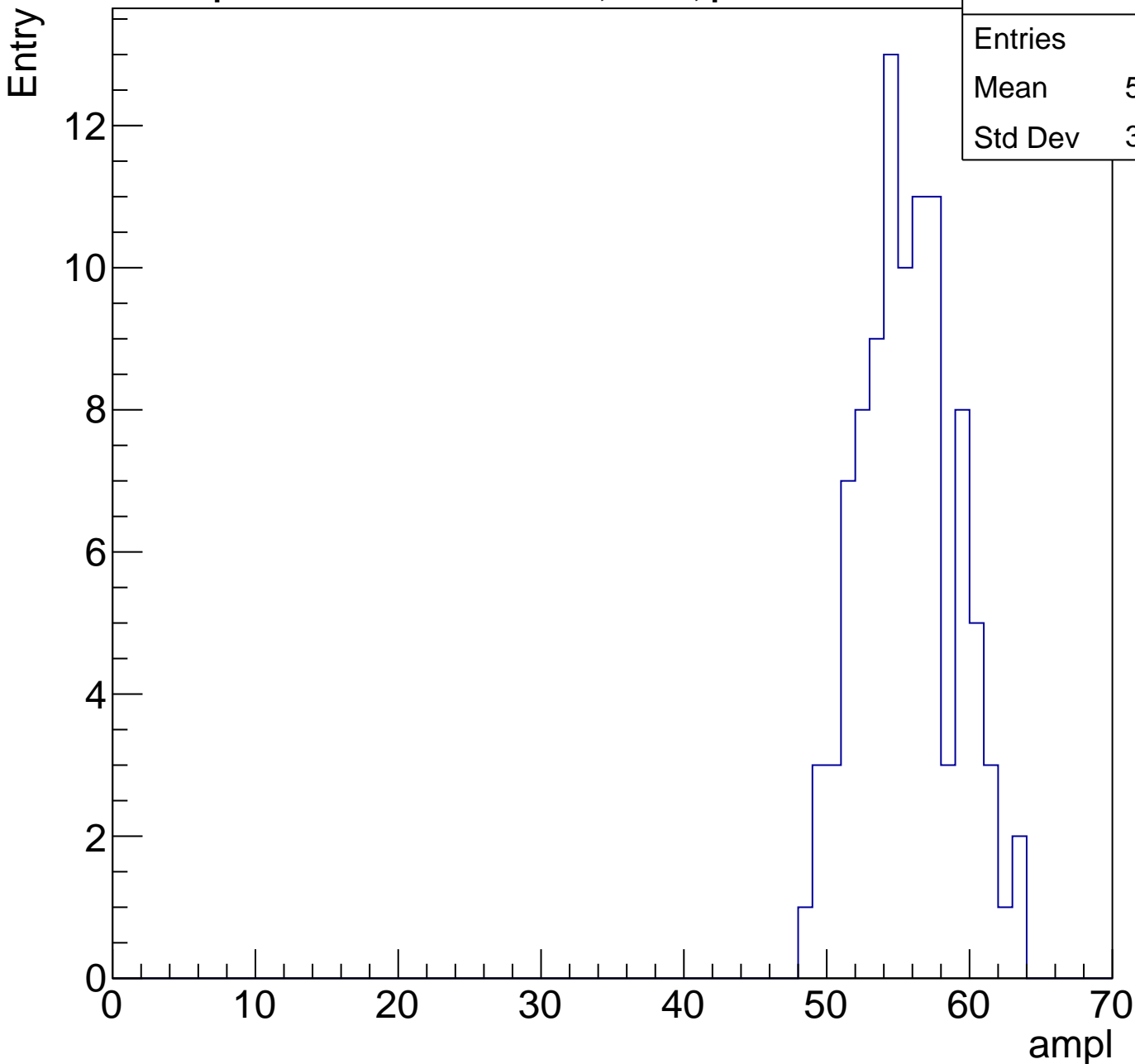
Entries	136
Mean	48.56
Std Dev	3.576



# B0L101S, U23-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	98
Mean	55.17
Std Dev	3.323



# B0L101S, U23-ch78, adc5

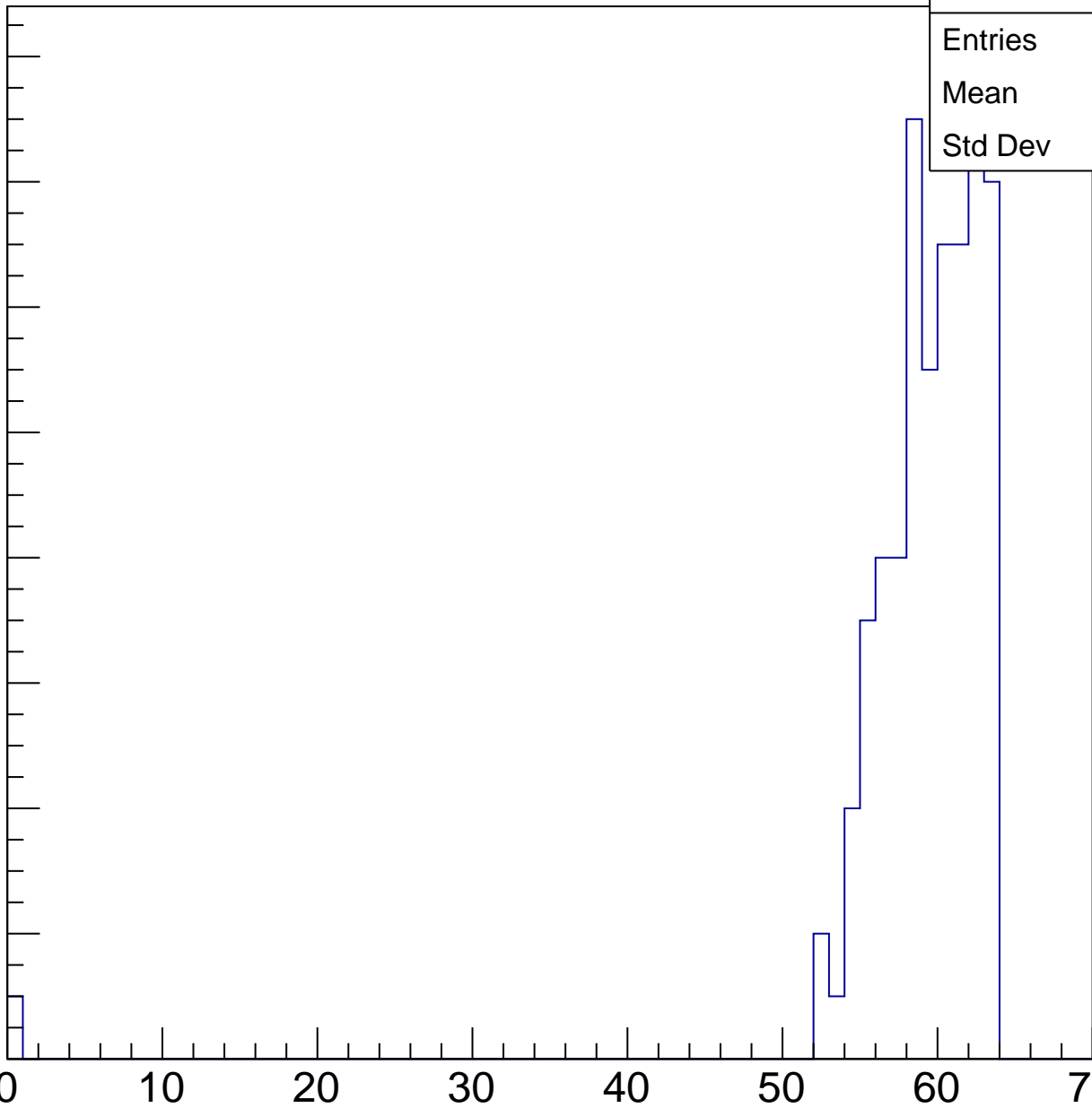
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	113
Mean	58.65
Std Dev	6.216

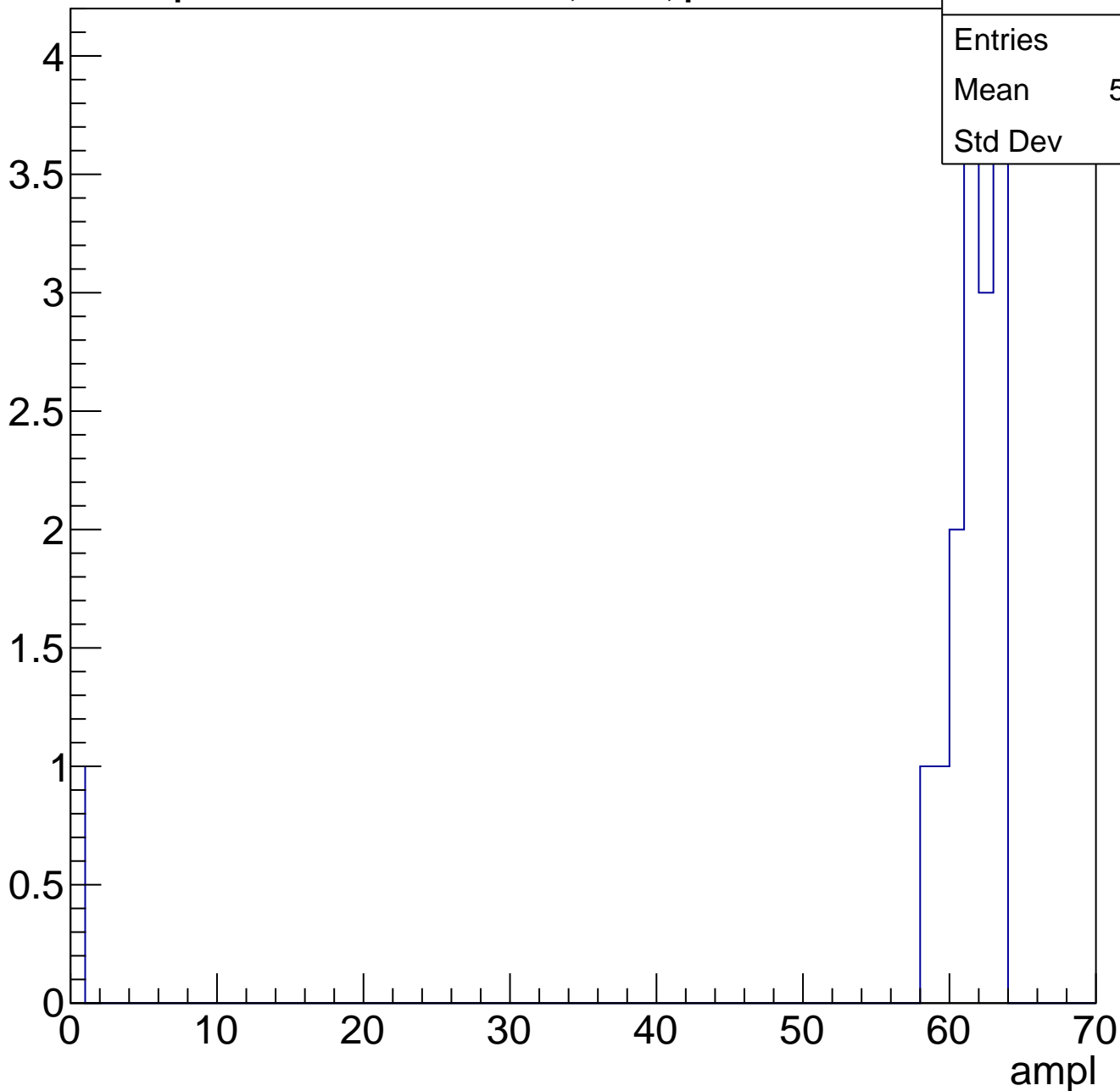
ampl



# B0L101S, U23-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



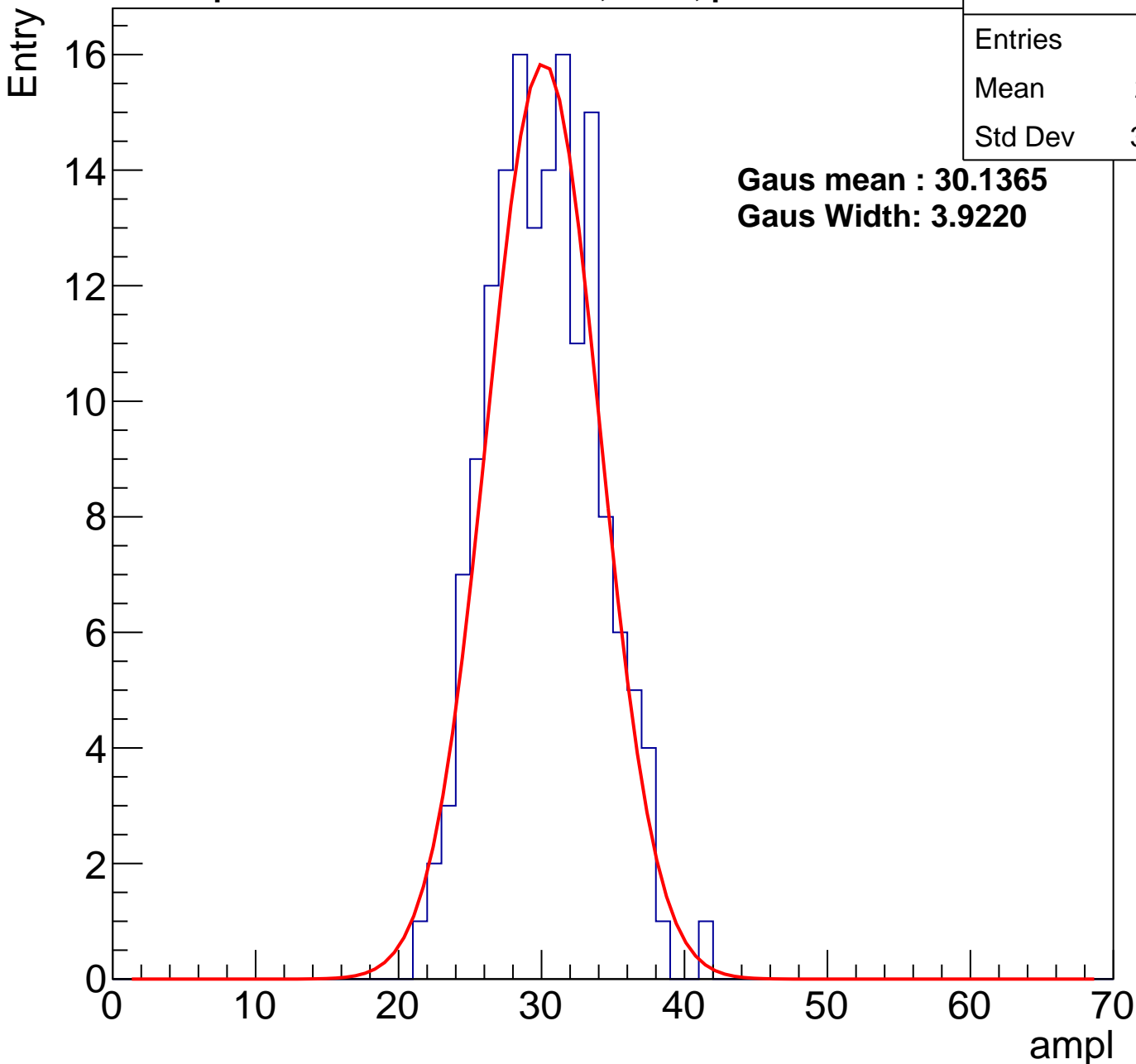
Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch79, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	29.71
Std Dev	3.772

**Gaus mean : 30.1365**  
**Gaus Width: 3.9220**



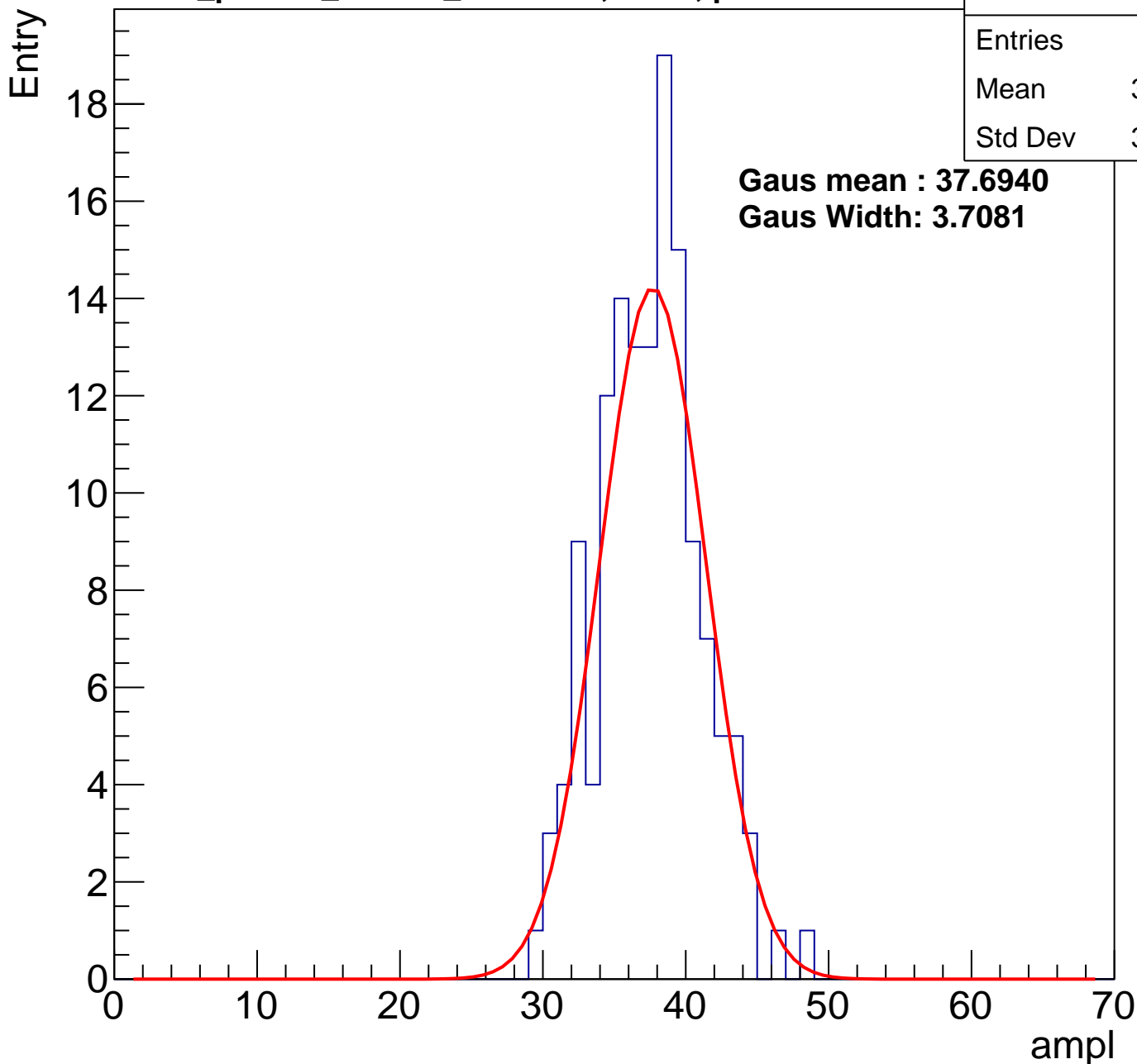
# B0L101S, U23-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	37.07
Std Dev	3.557

**Gaus mean : 37.6940**

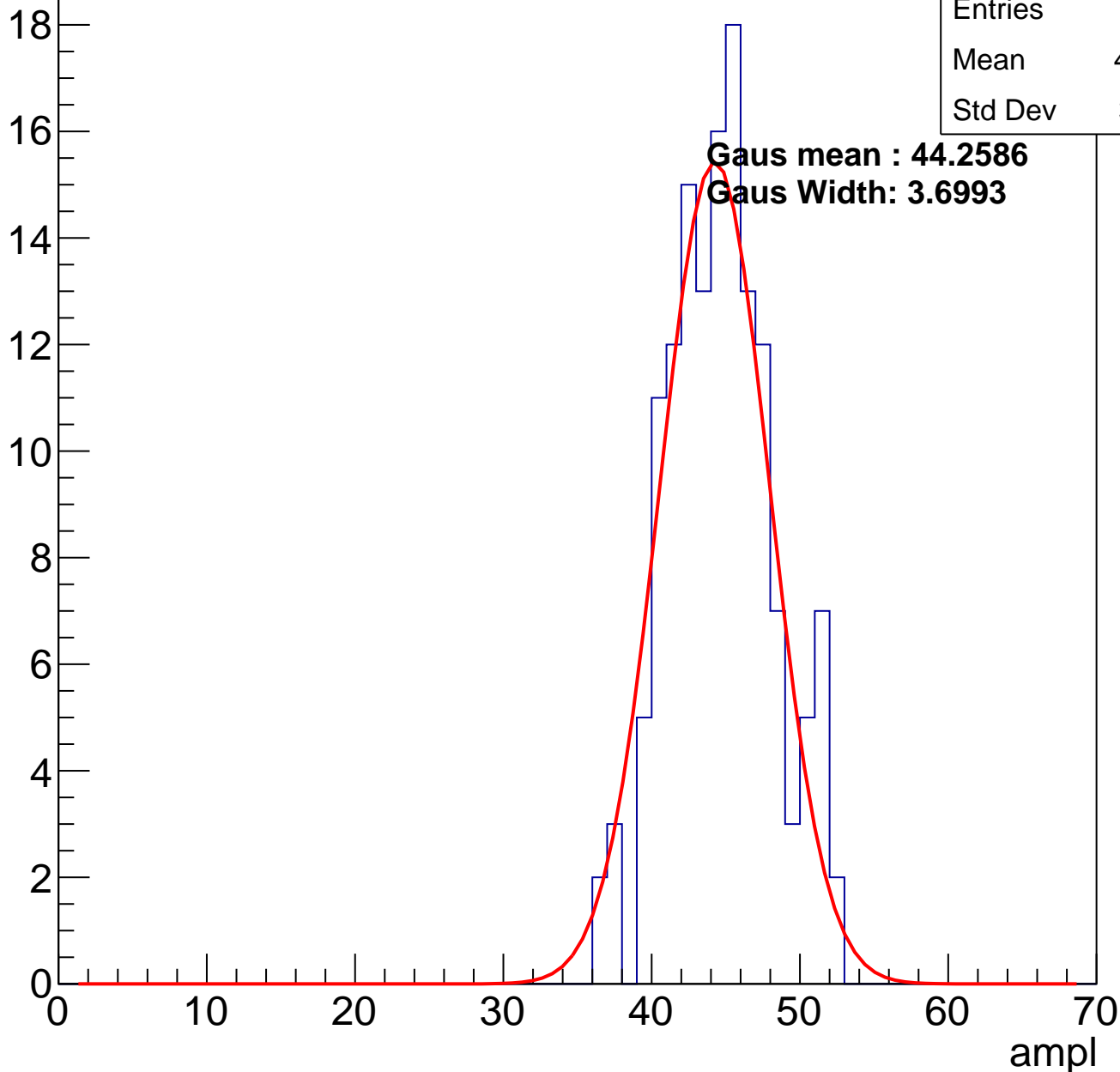
**Gaus Width: 3.7081**



# B0L101S, U23-ch79, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

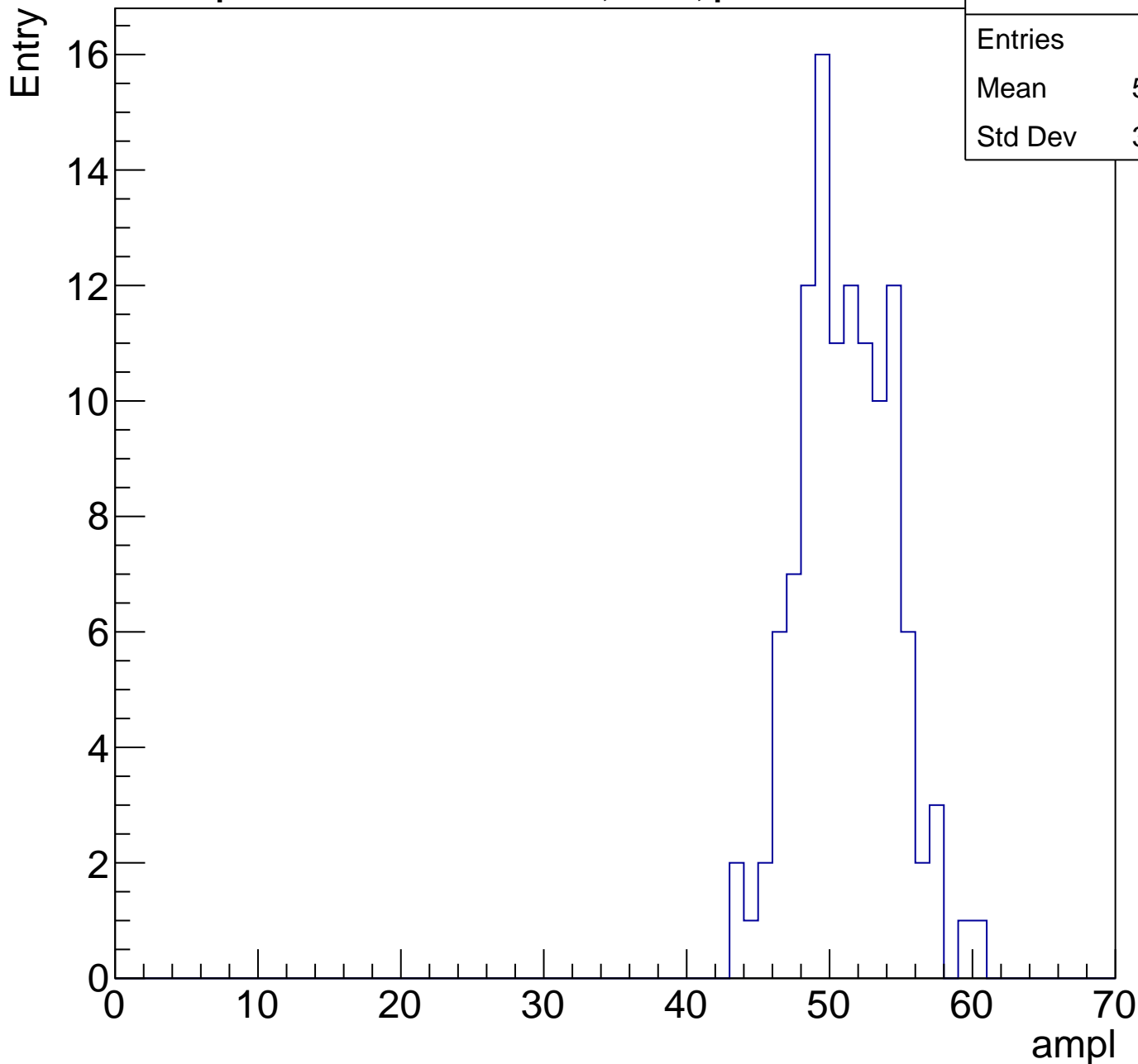
Entry



# B0L101S, U23-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	50.69
Std Dev	3.285



# B0L101S, U23-ch79, adc4

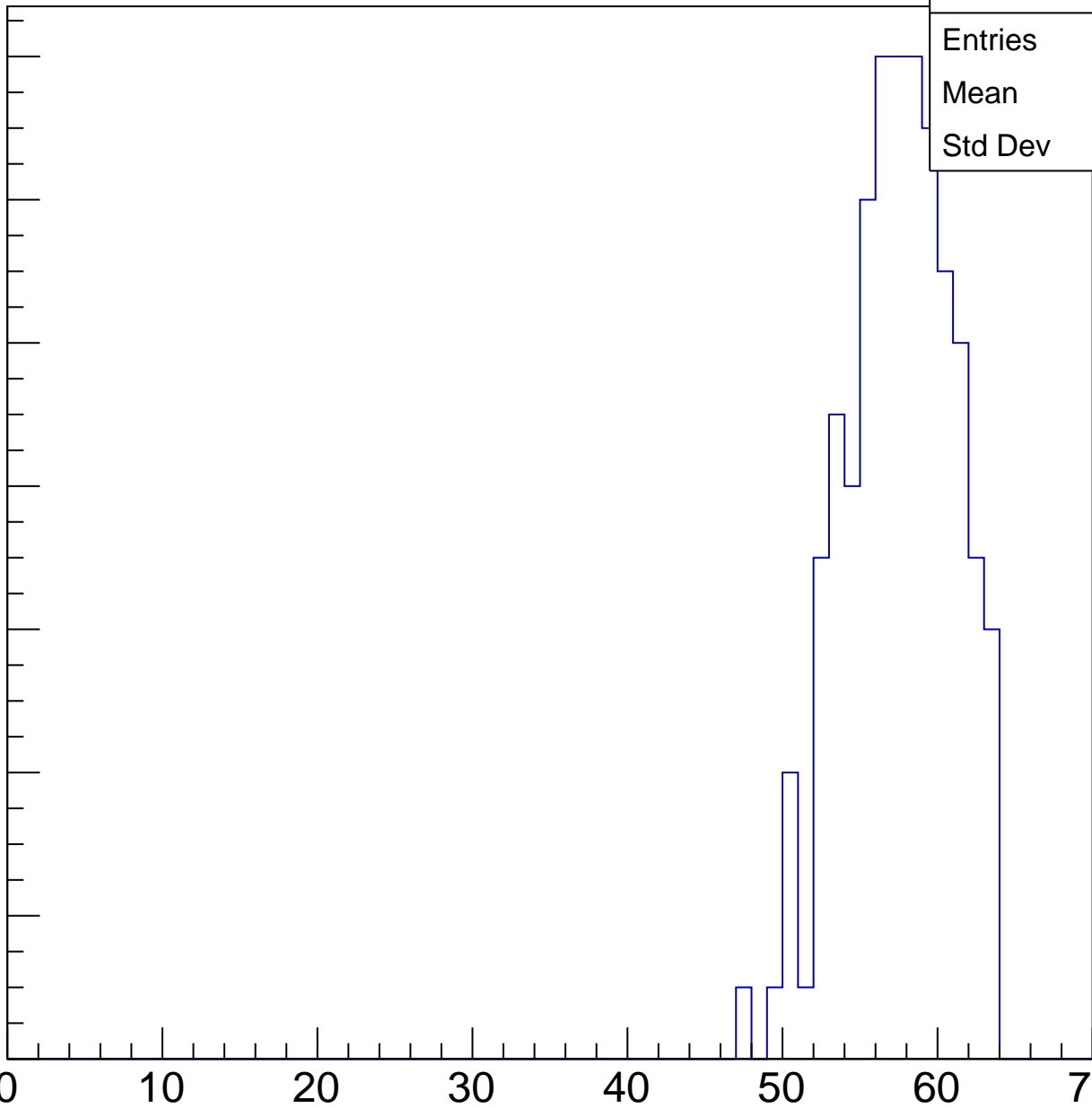
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	56.99
Std Dev	3.443

ampl



# B0L101S, U23-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

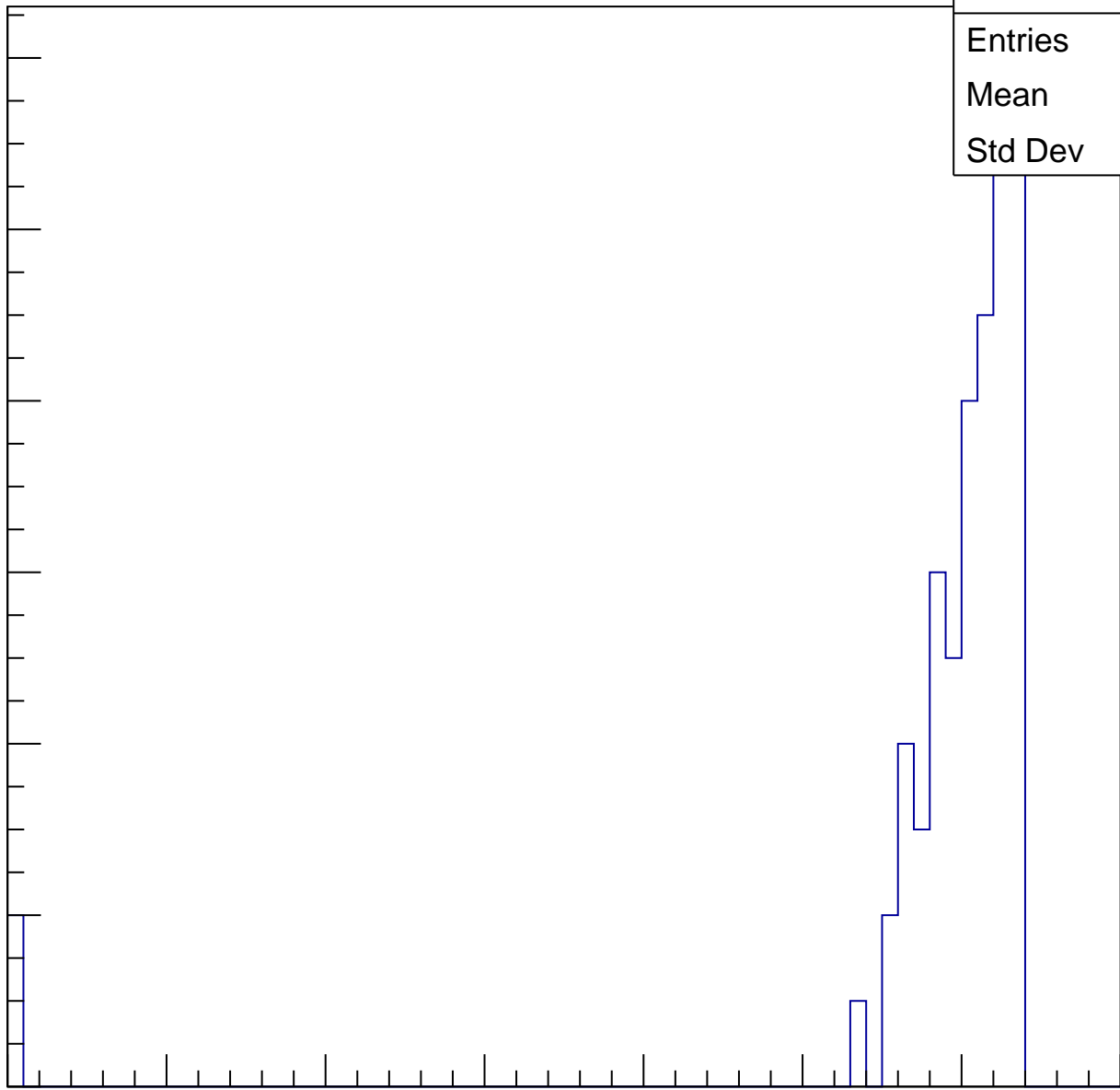
2

0

Entries	63
Mean	58.21
Std Dev	10.82

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	29.35
Std Dev	3.471

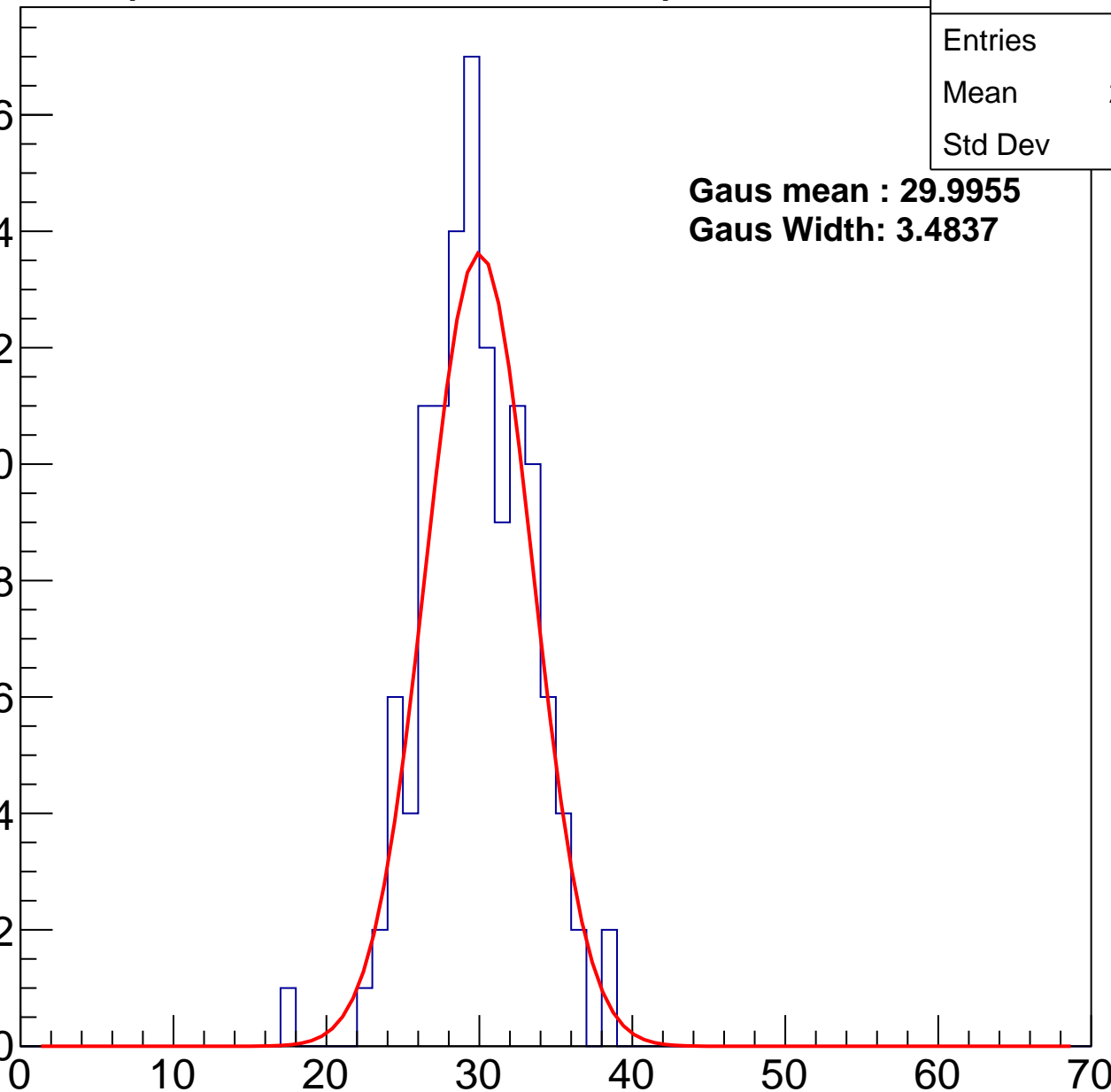
**Gaus mean : 29.9955**

**Gaus Width: 3.4837**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	36.78
Std Dev	4.089

**Gaus mean : 37.3268**

**Gaus Width: 4.0957**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

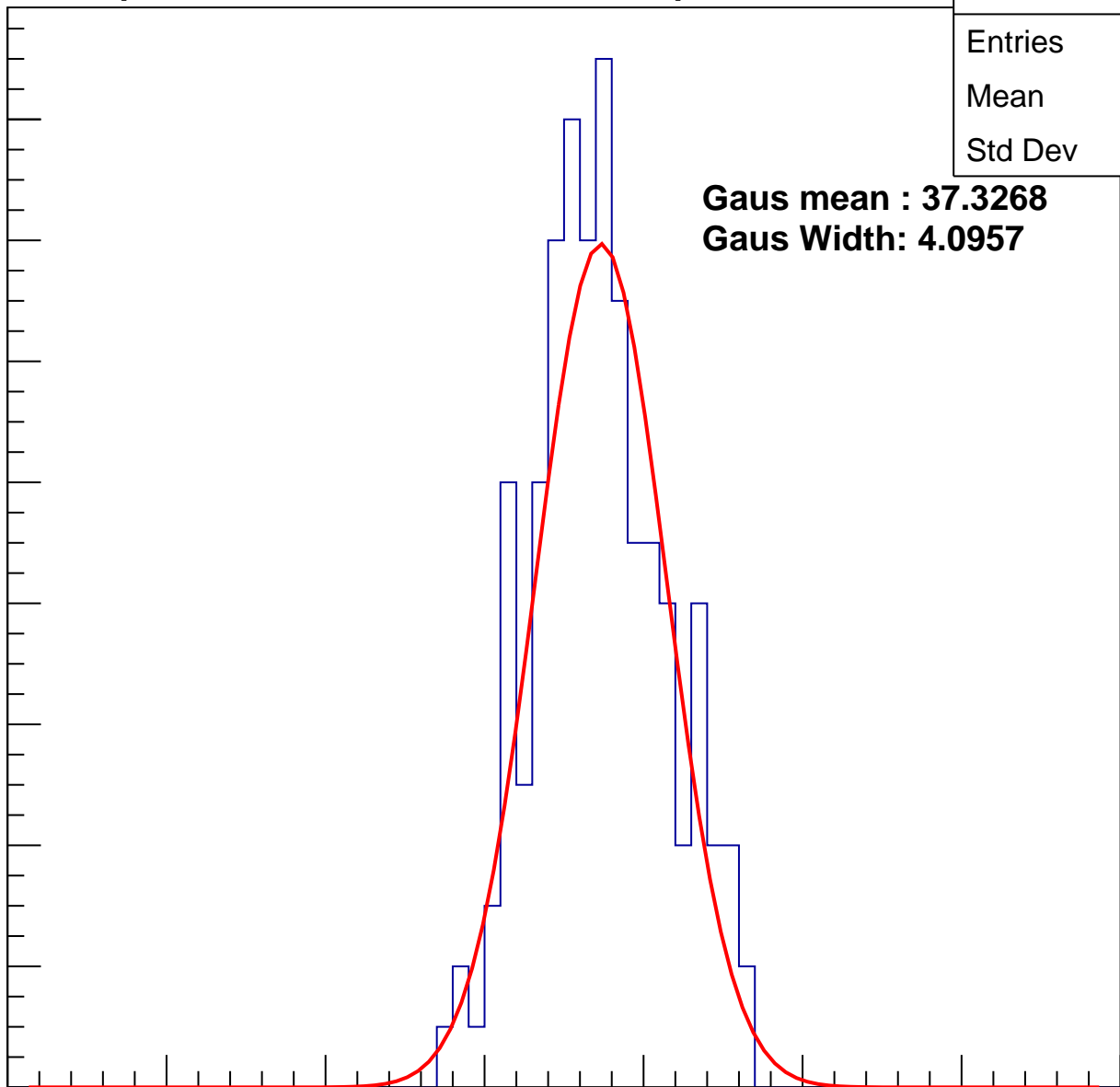
40

50

60

70

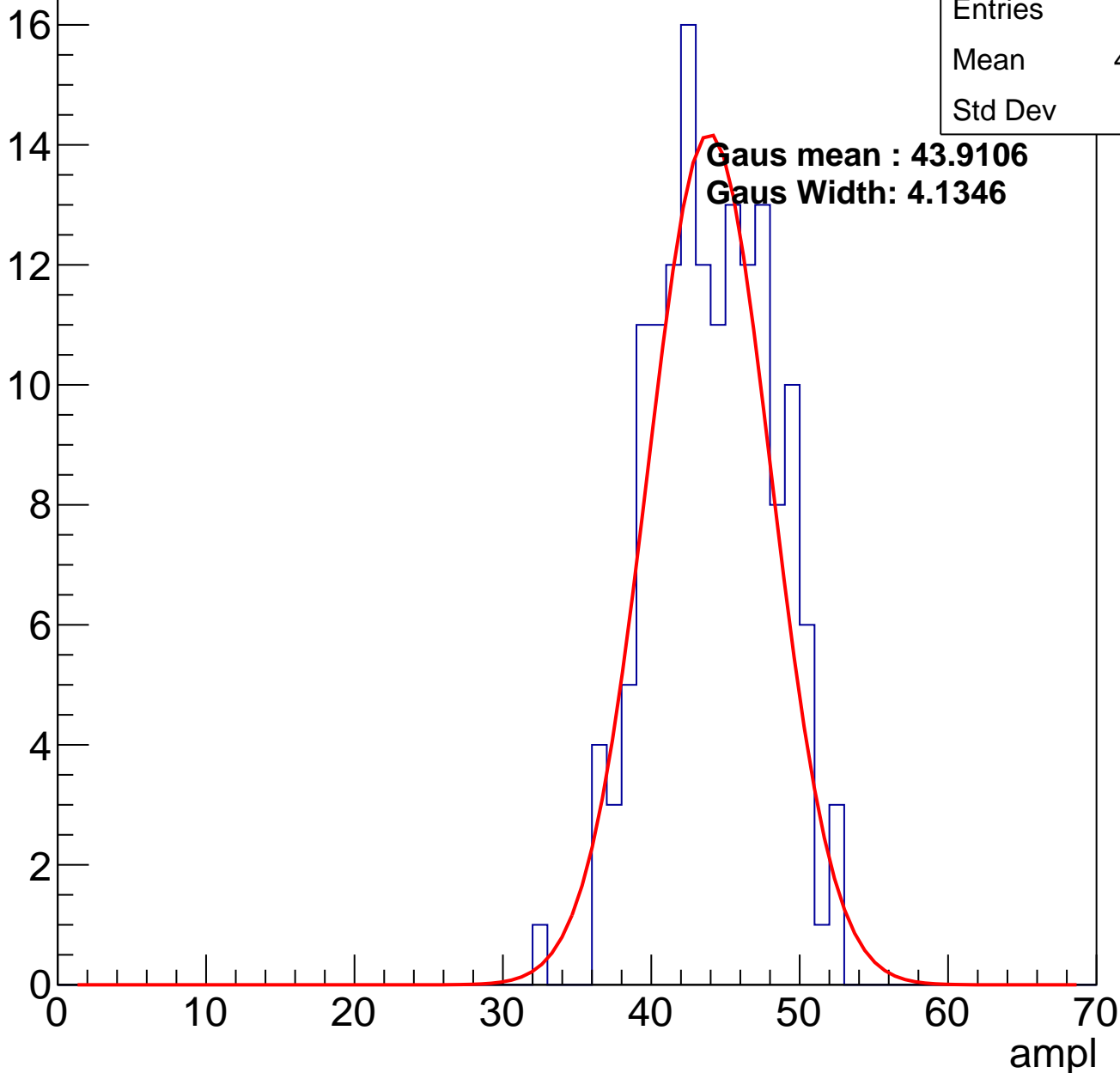
ampl



# B0L101S, U23-ch80, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	50.81
Std Dev	3.549

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

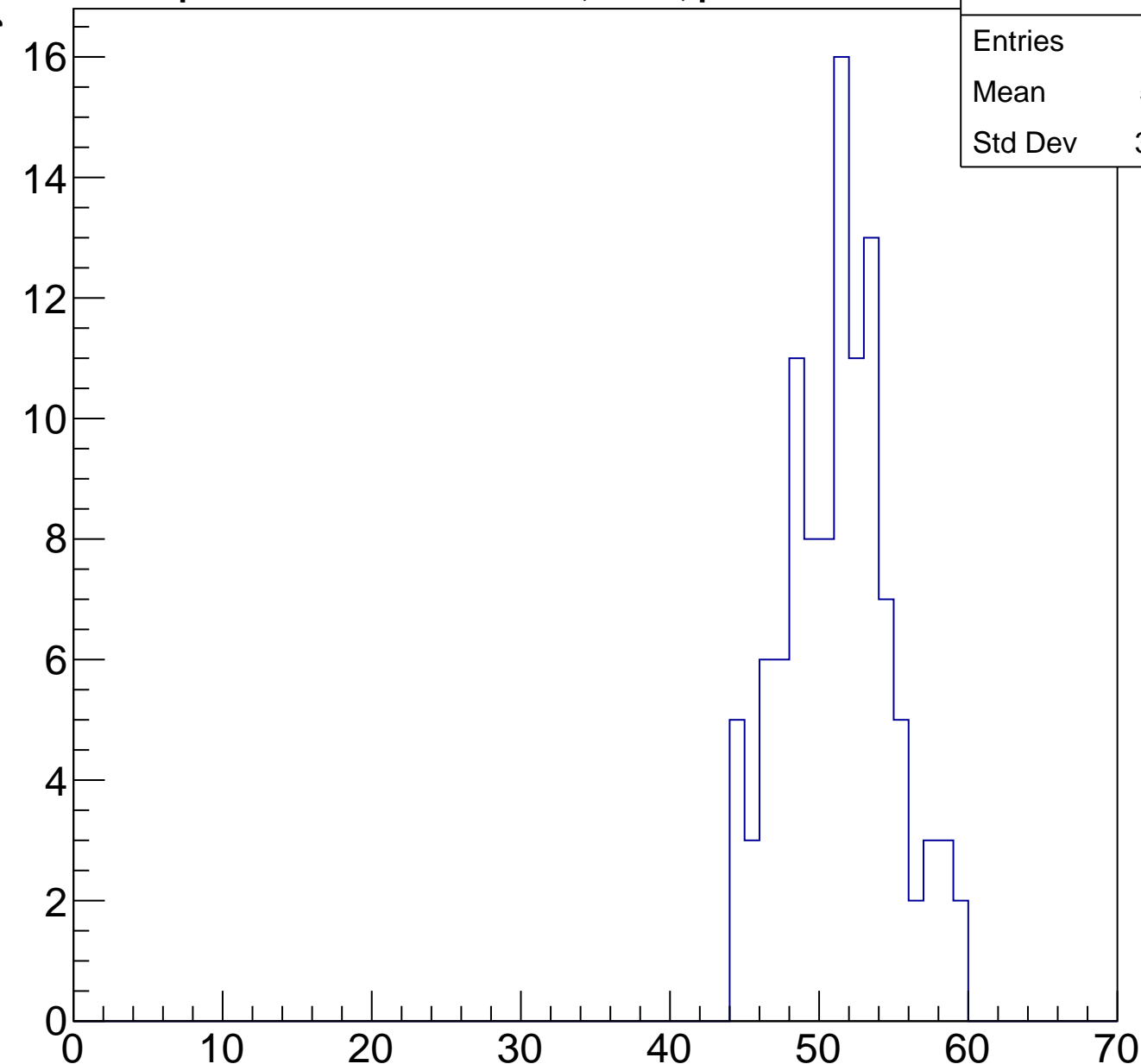
30

40

50

60

ampl



# B0L101S, U23-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

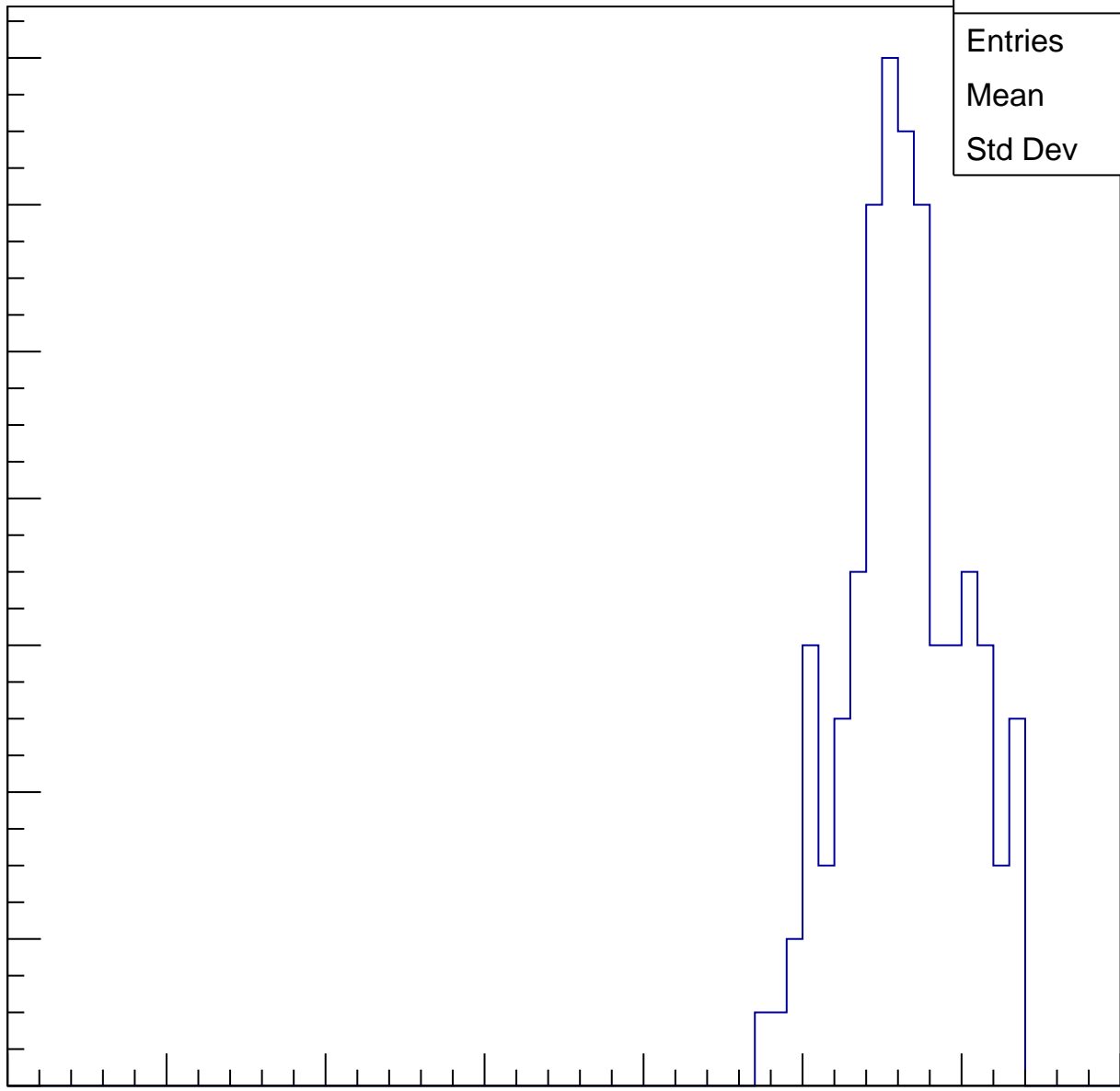
Entries	109
Mean	55.93
Std Dev	3.639

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch80, adc5

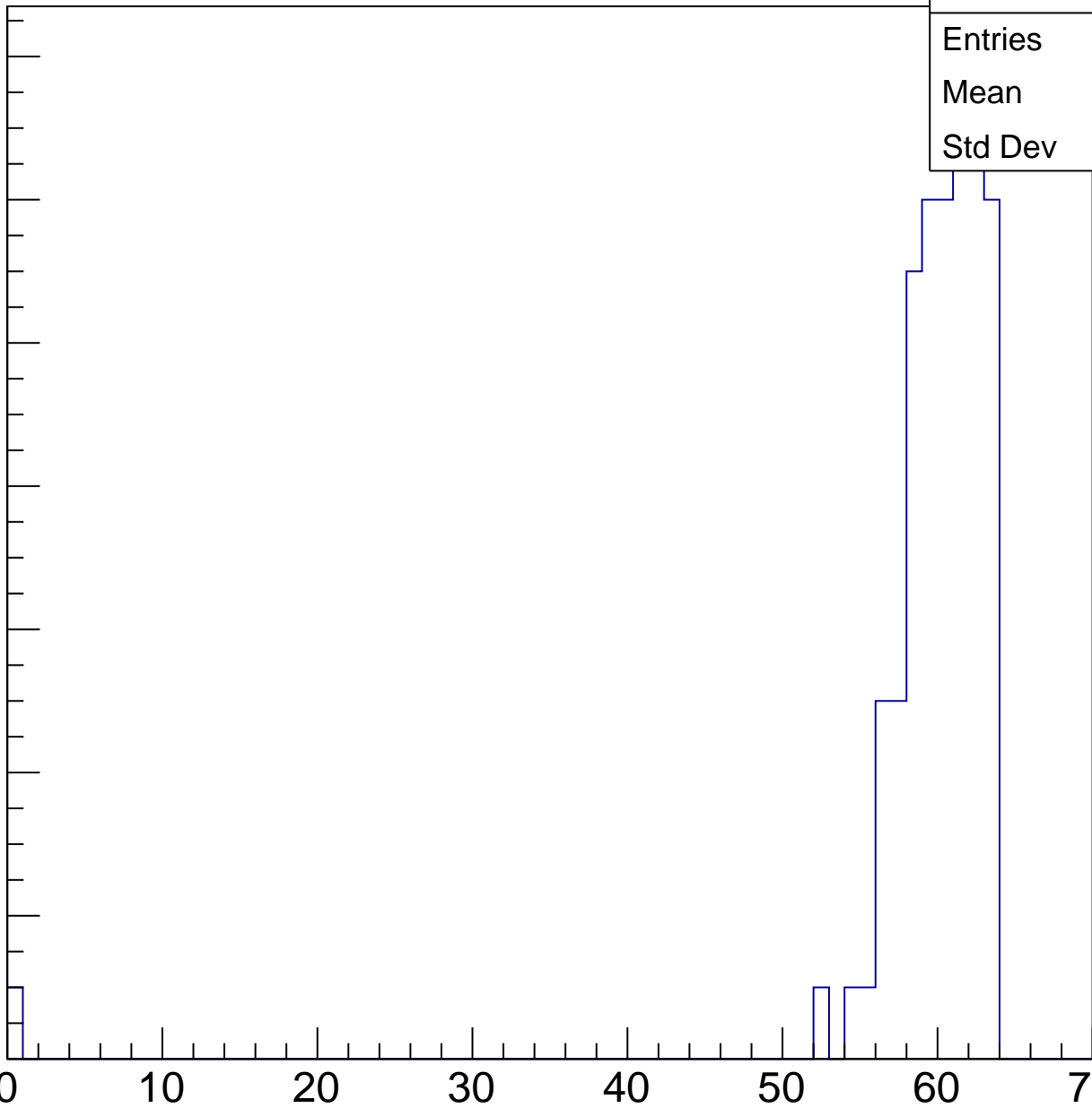
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	89
Mean	59.21
Std Dev	6.728

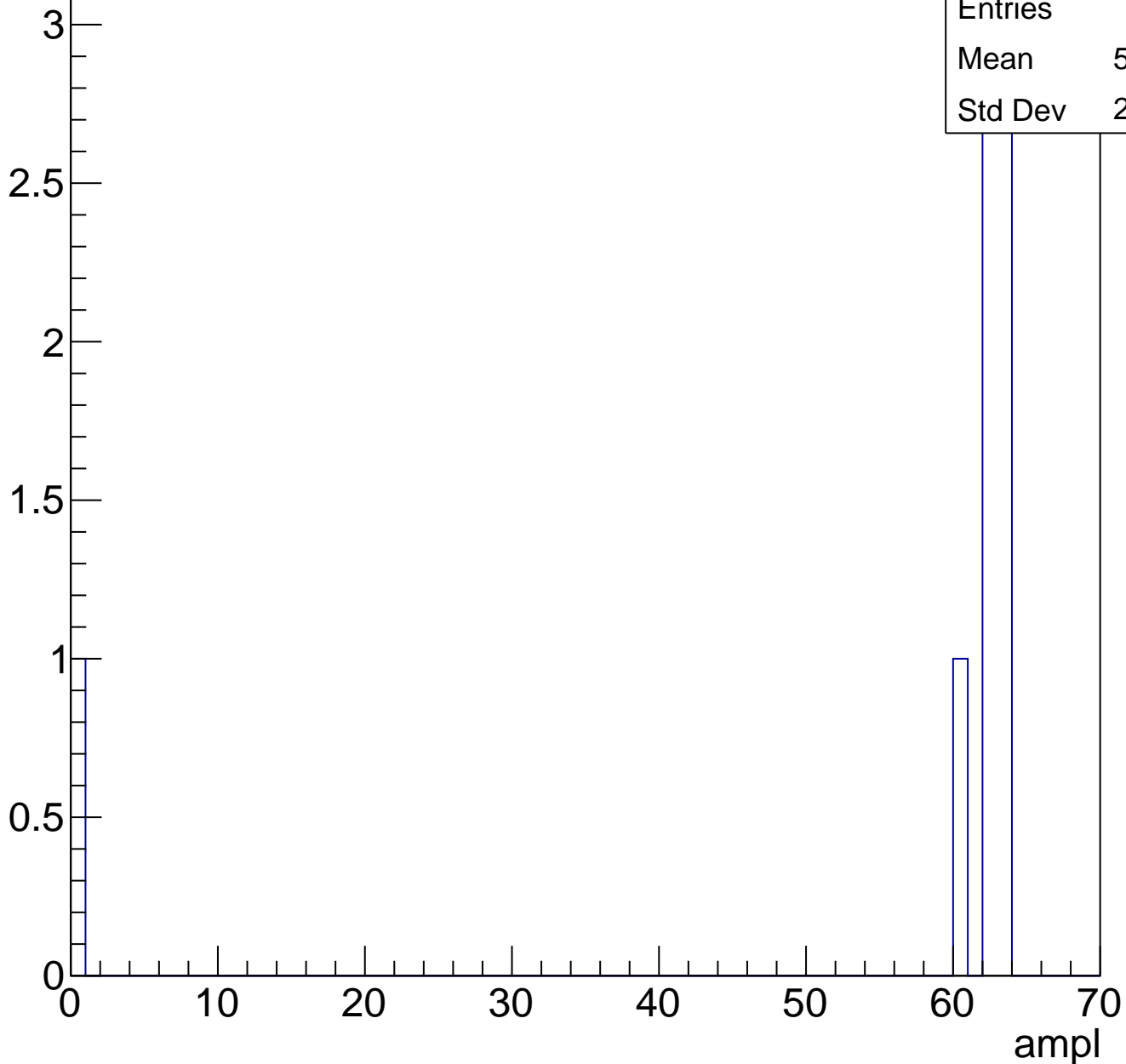
ampl



# B0L101S, U23-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch81, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	28.13
Std Dev	3.391

**Gaus mean : 29.0502**

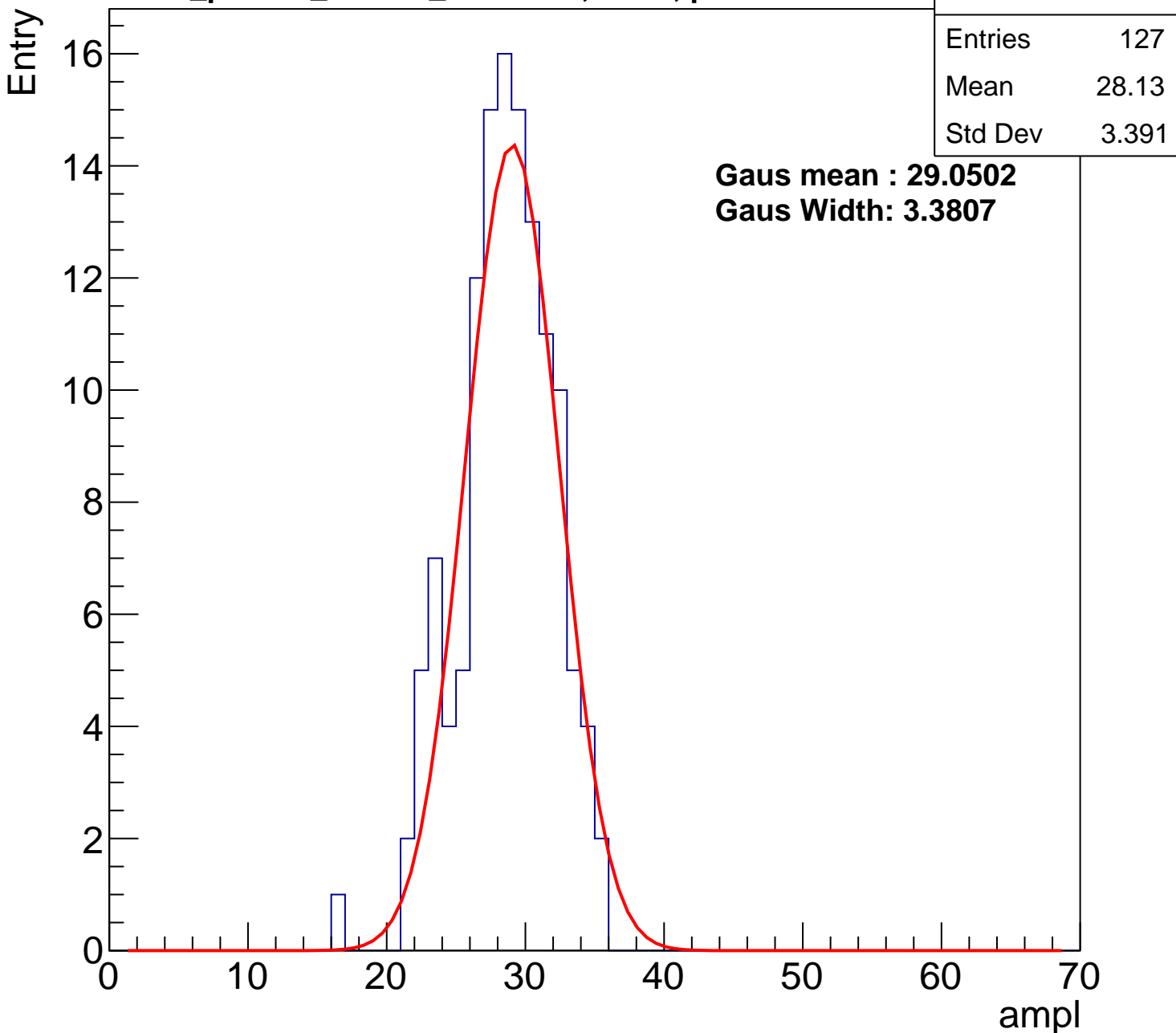
**Gaus Width: 3.3807**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	35.42
Std Dev	3.75

**Gaus mean : 35.6786**

**Gaus Width: 3.6338**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

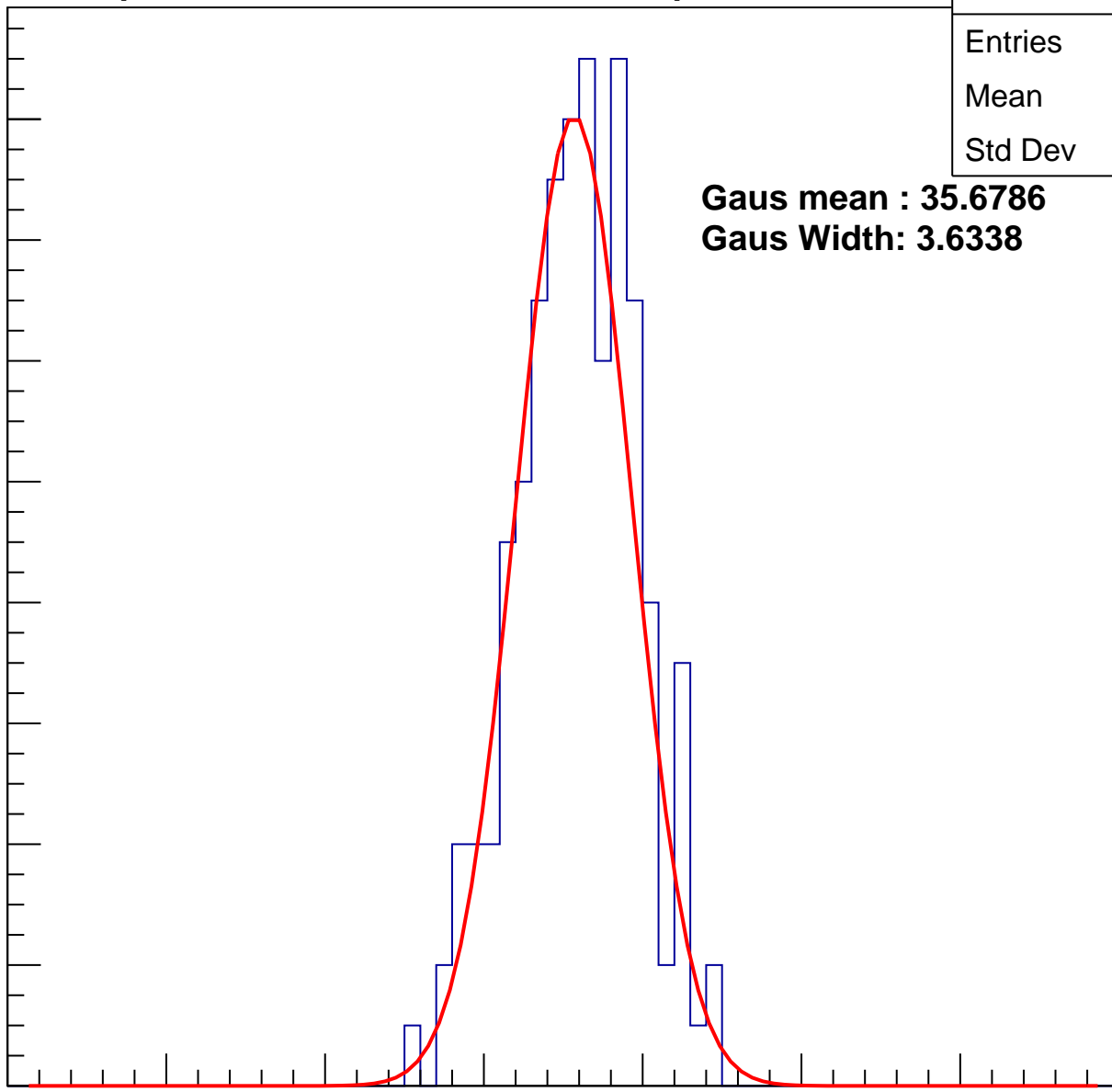
40

50

60

70

ampl



# B0L101S, U23-ch81, adc2

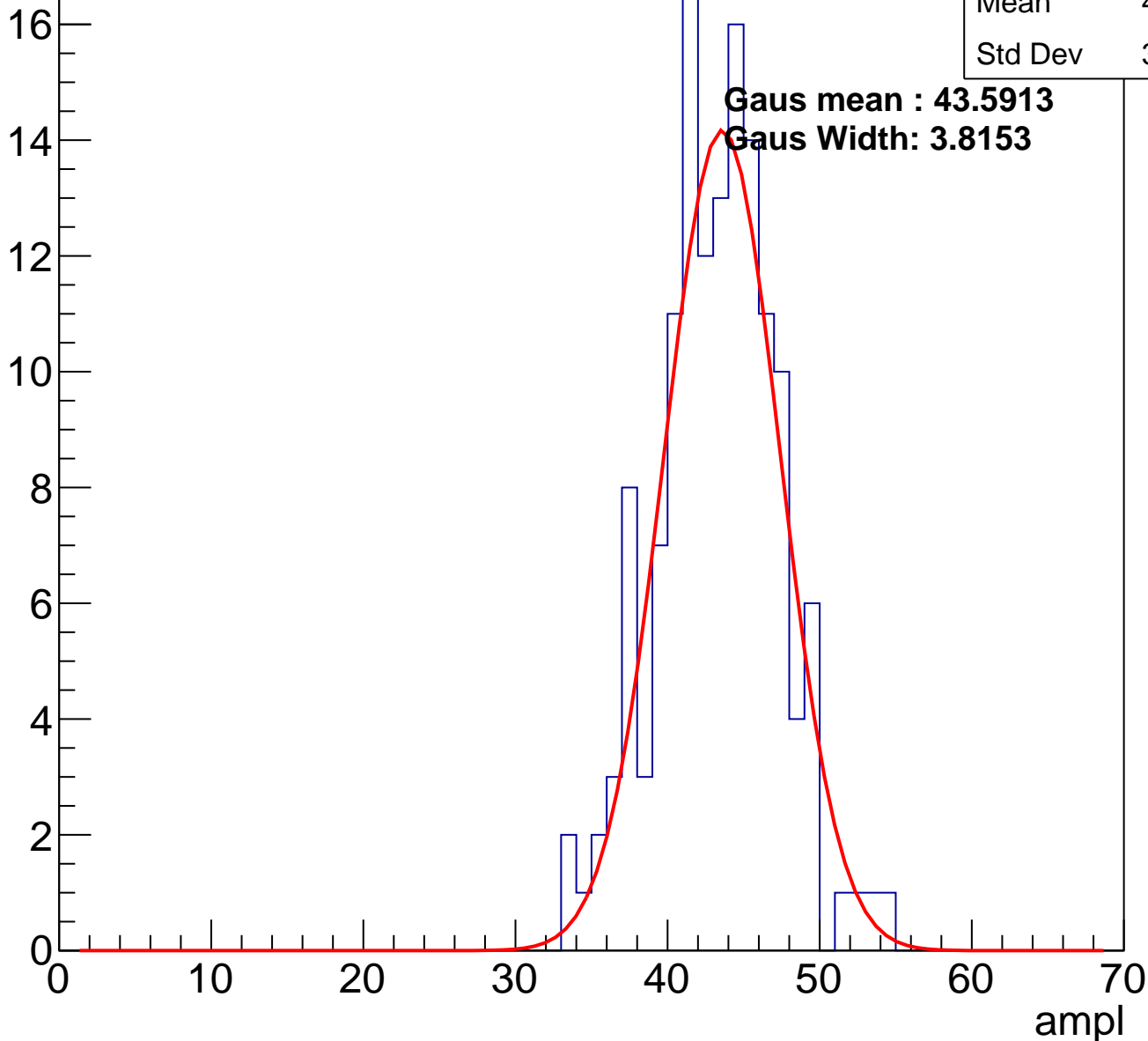
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	144
Mean	42.83
Std Dev	3.925

**Gaus mean : 43.5913**

**Gaus Width: 3.8153**



# B0L101S, U23-ch81, adc3

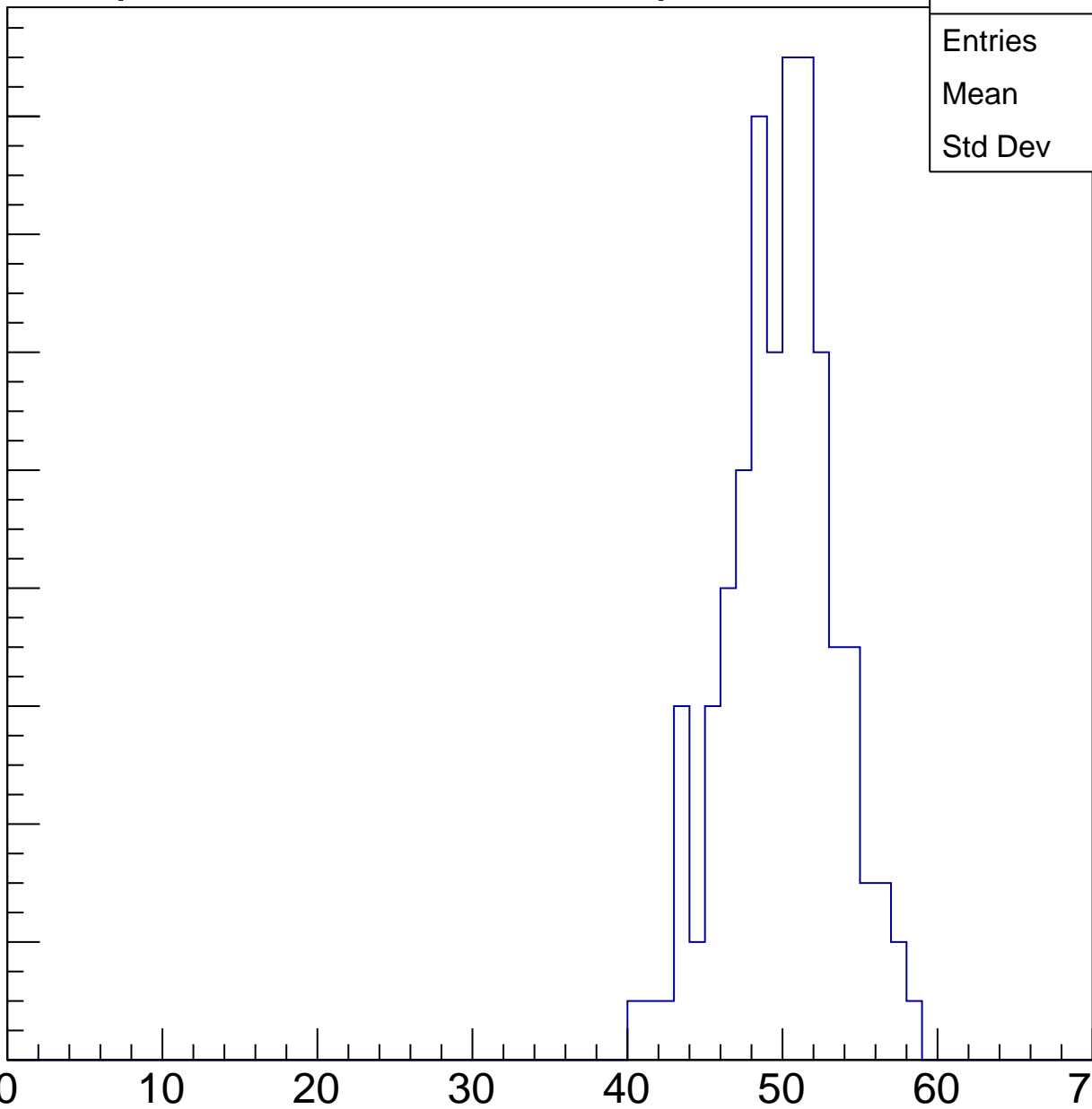
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	49.45
Std Dev	3.482

ampl

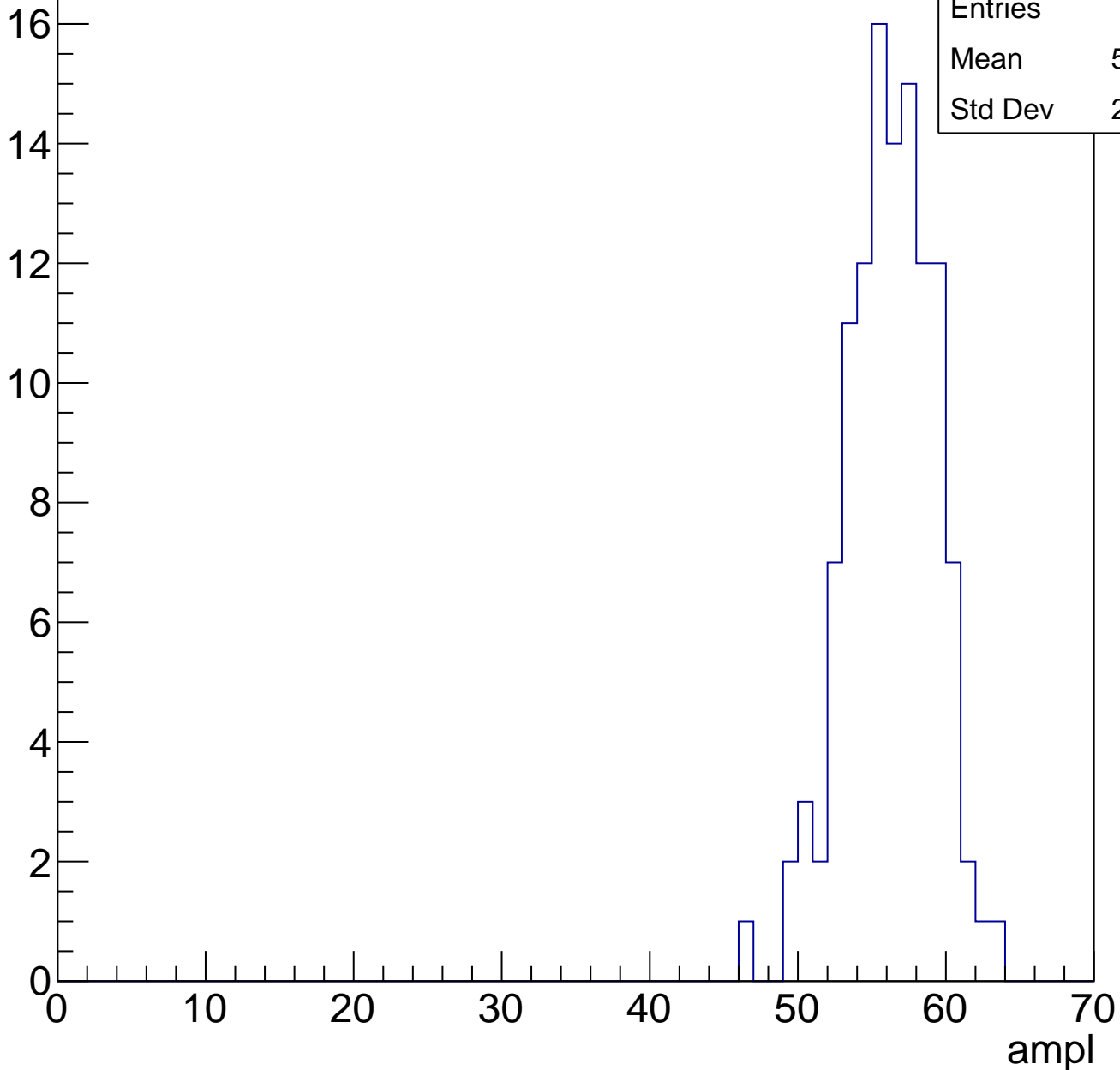


# B0L101S, U23-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	55.77
Std Dev	2.973

Entry



# B0L101S, U23-ch81, adc5

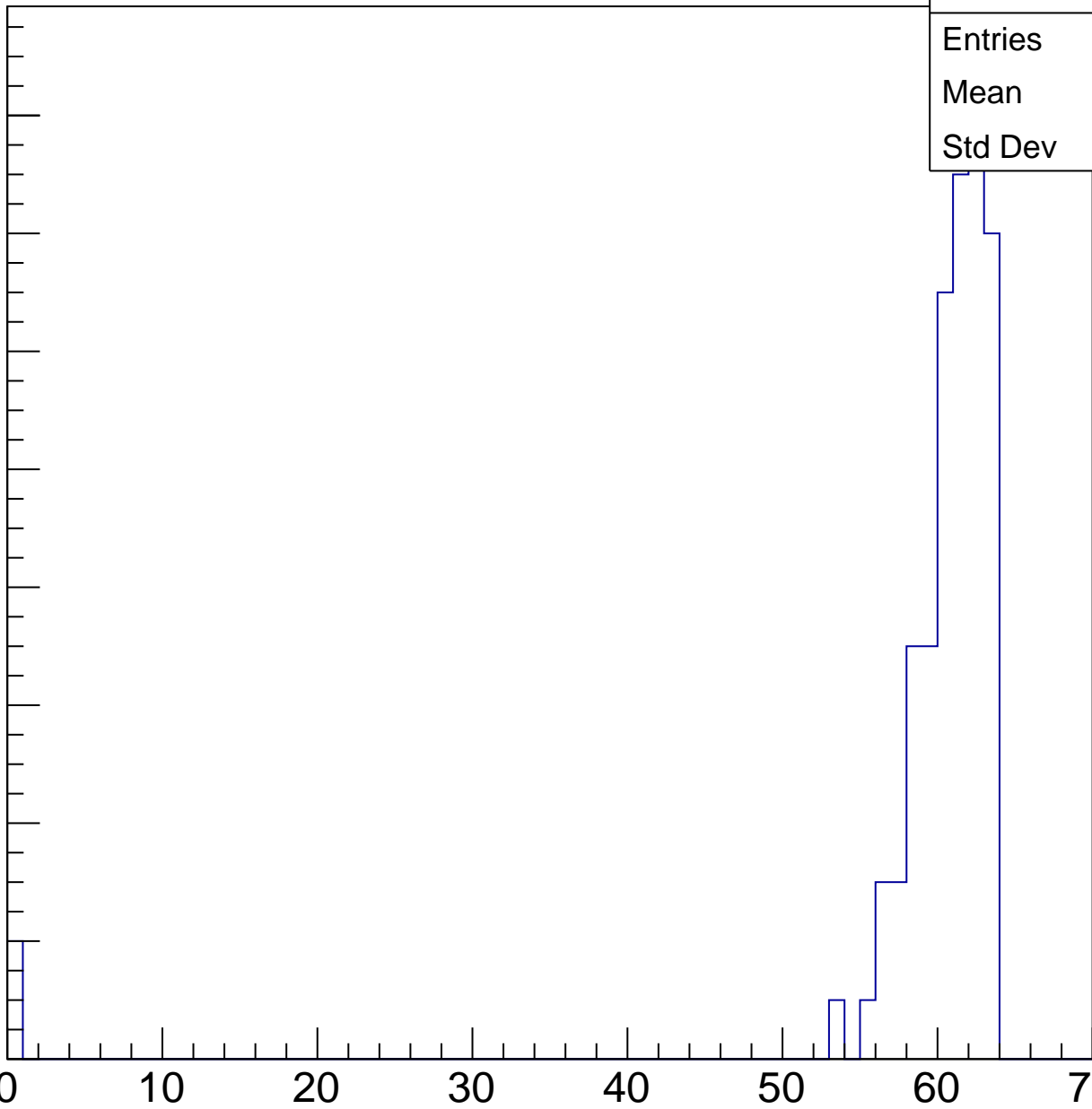
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	59
Std Dev	9.512

ampl

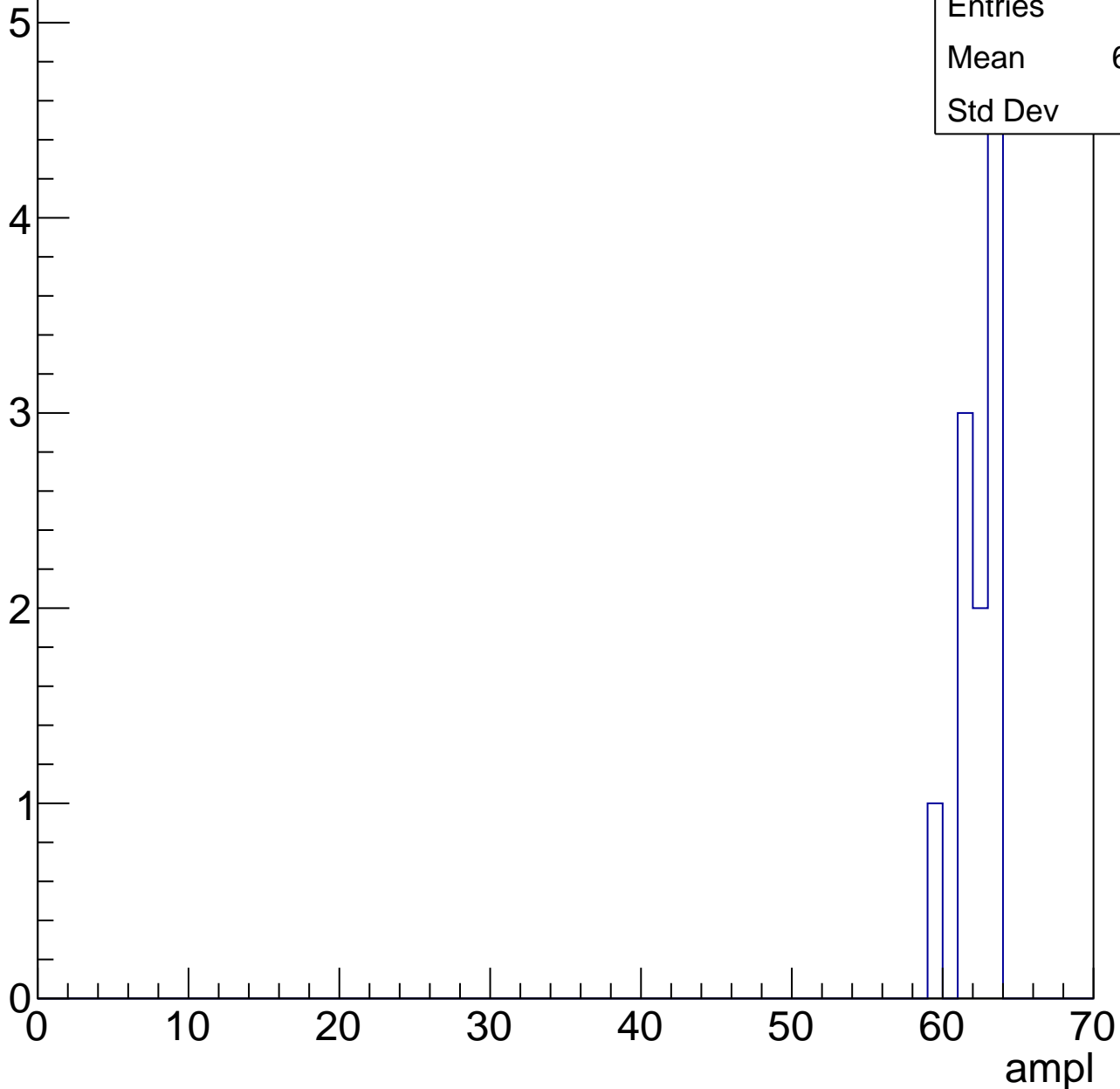


# B0L101S, U23-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	11
Mean	61.91
Std Dev	1.24





# B0L101S, U23-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch82, adc0

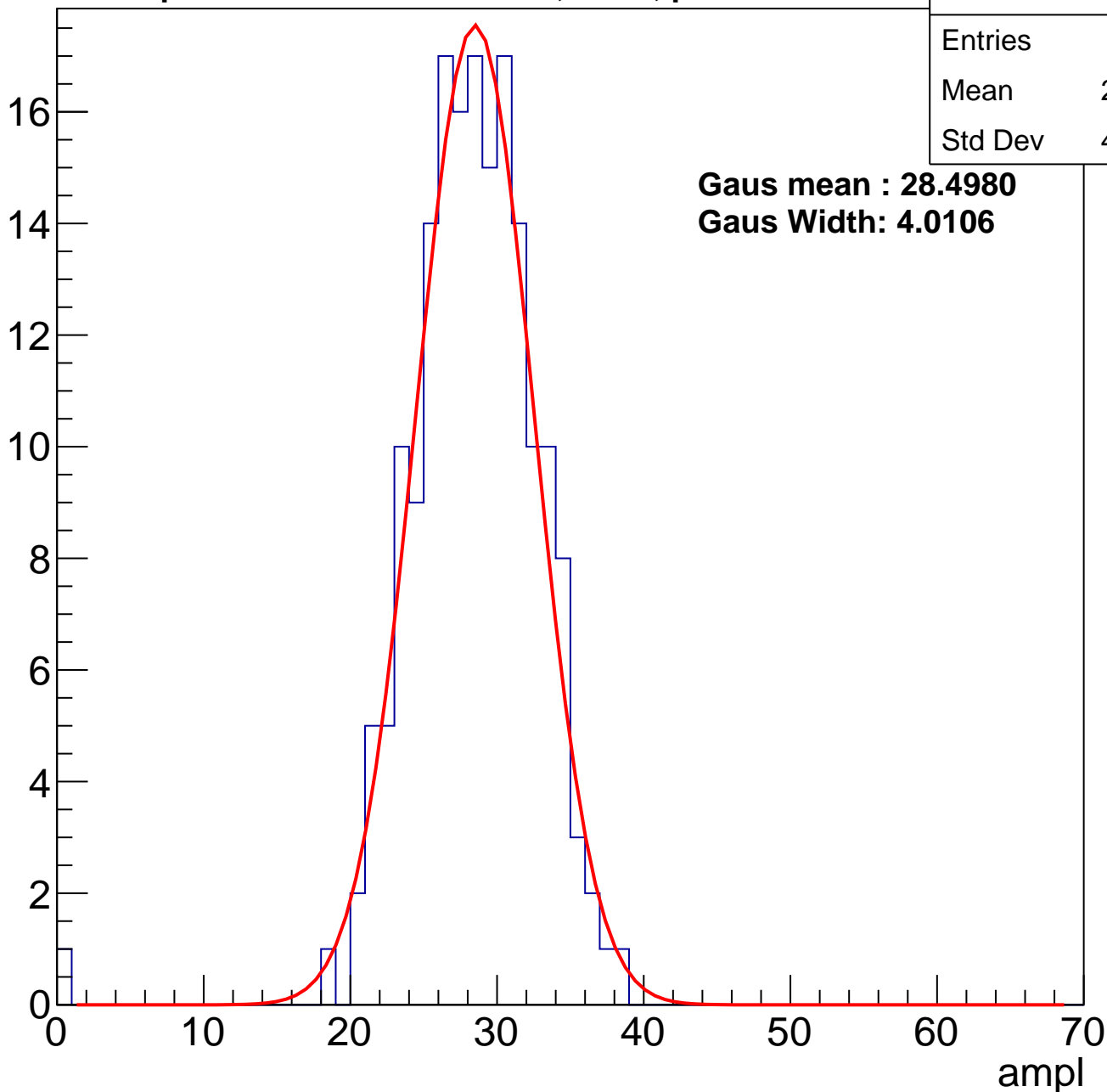
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	178
Mean	27.93
Std Dev	4.373

**Gaus mean : 28.4980**

**Gaus Width: 4.0106**

Entry



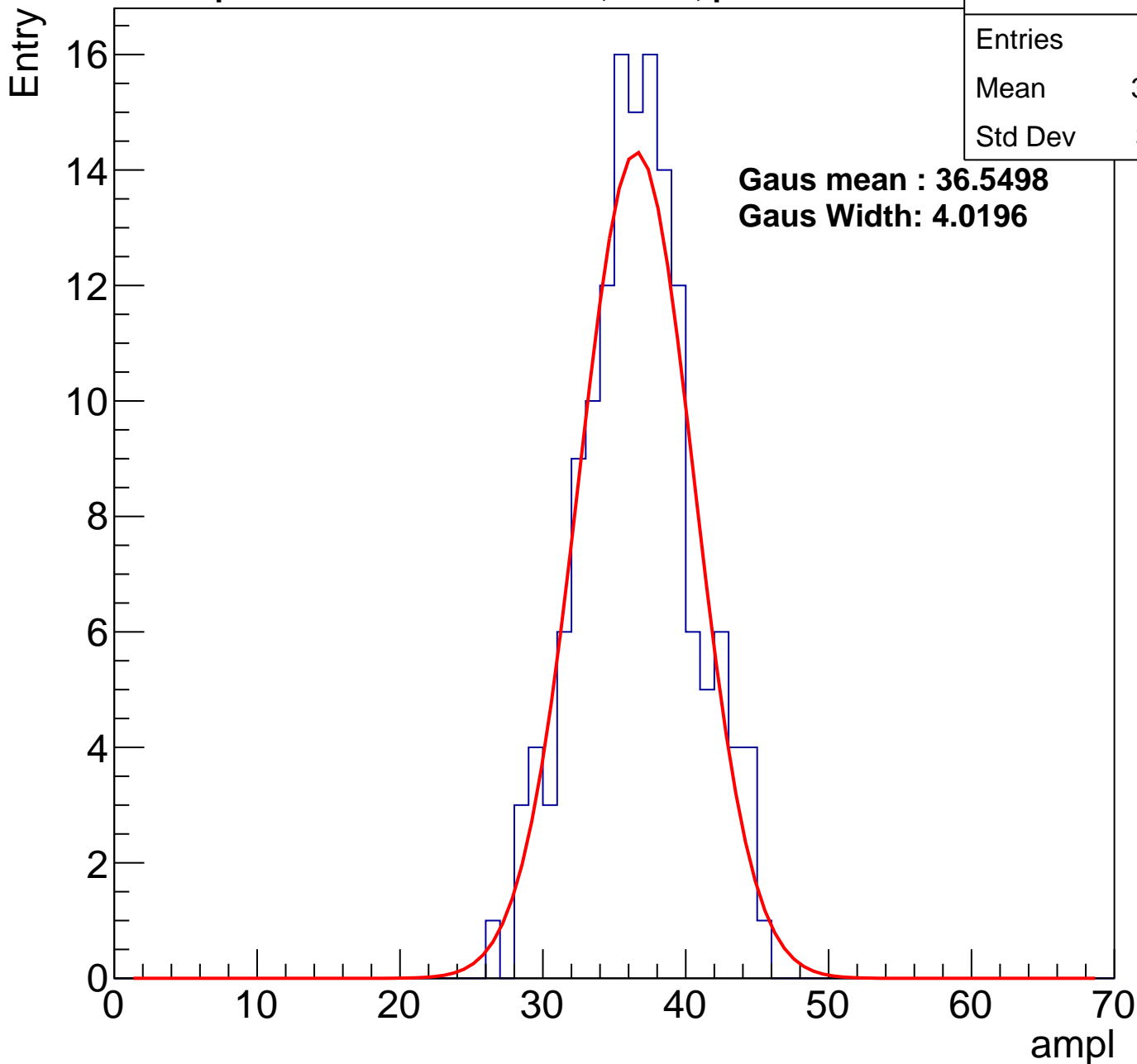
# B0L101S, U23-ch82, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	36.12
Std Dev	3.861

**Gaus mean : 36.5498**

**Gaus Width: 4.0196**



# B0L101S, U23-ch82, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	42.35
Std Dev	3.328

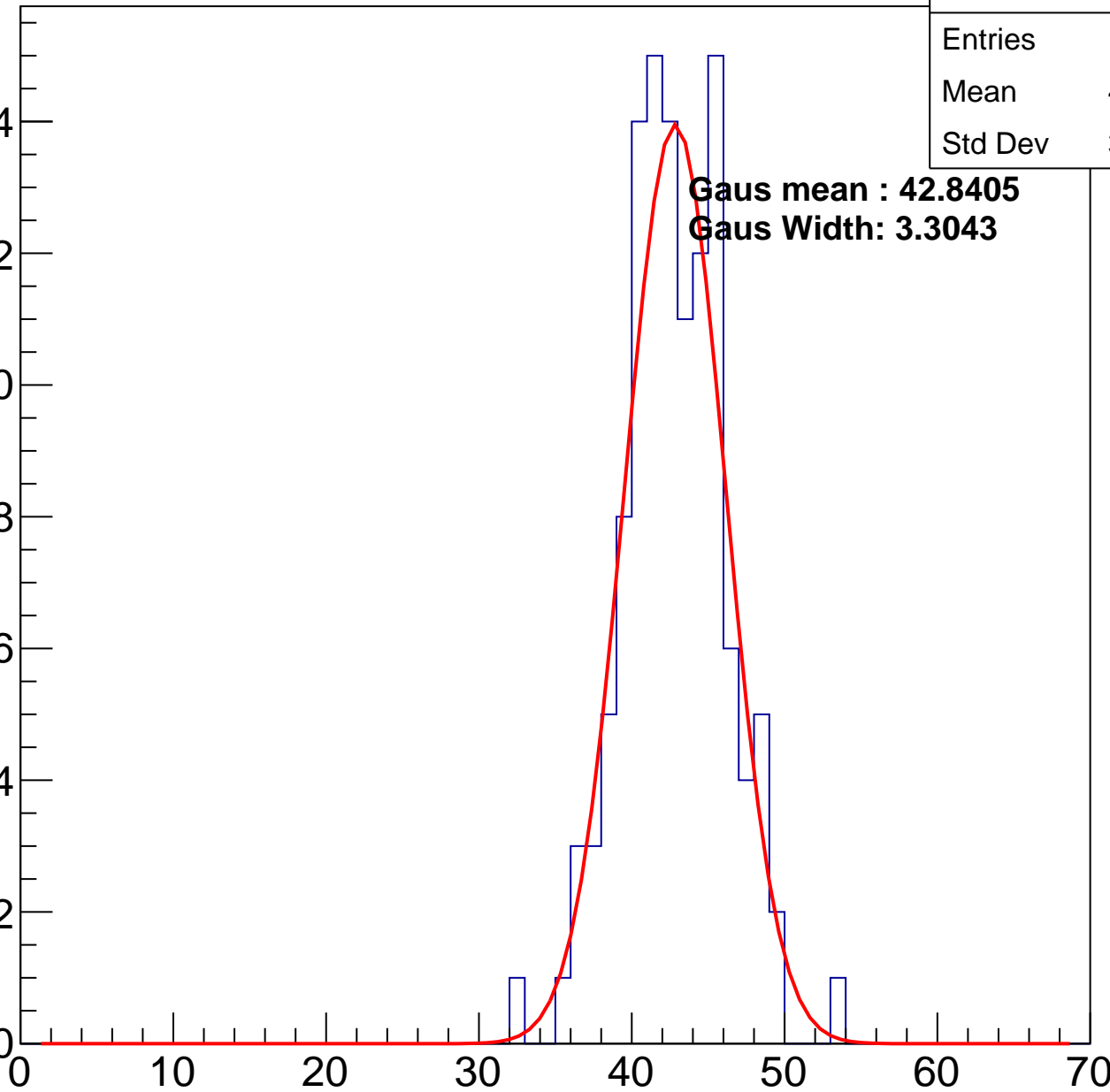
**Gaus mean : 42.8405**

**Gaus Width: 3.3043**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch82, adc3

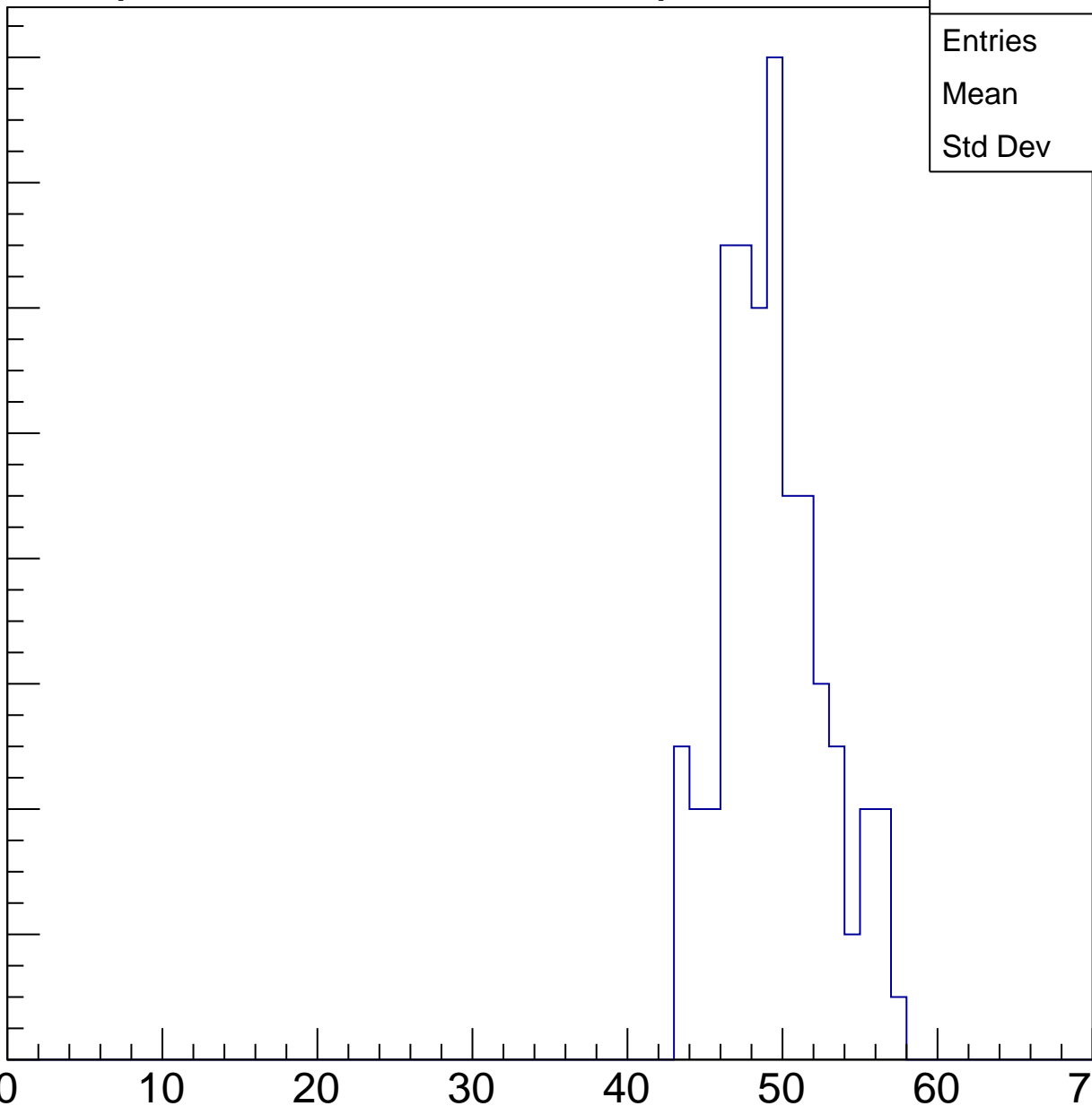
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	107
Mean	48.93
Std Dev	3.289

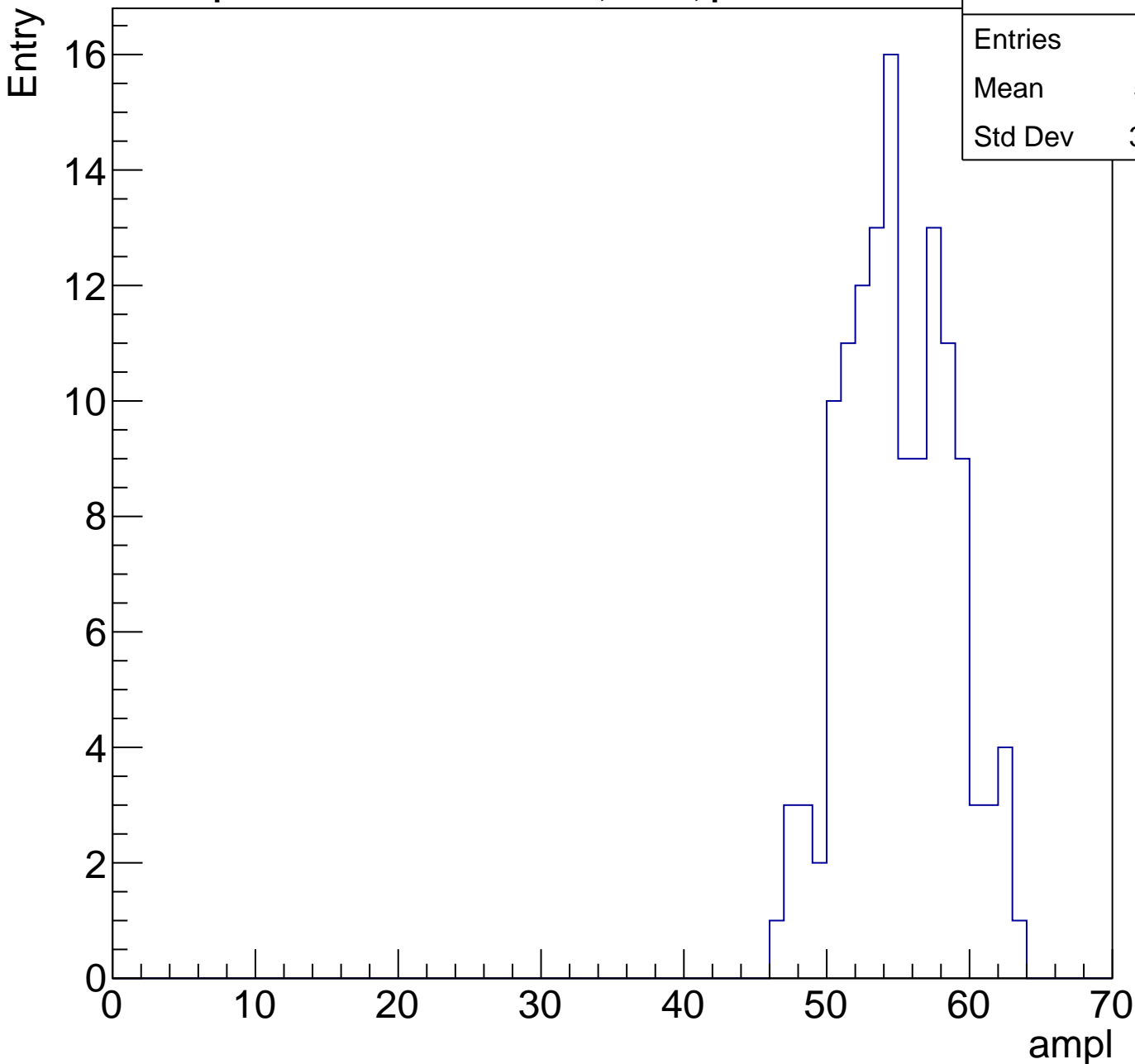
ampl



# B0L101S, U23-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

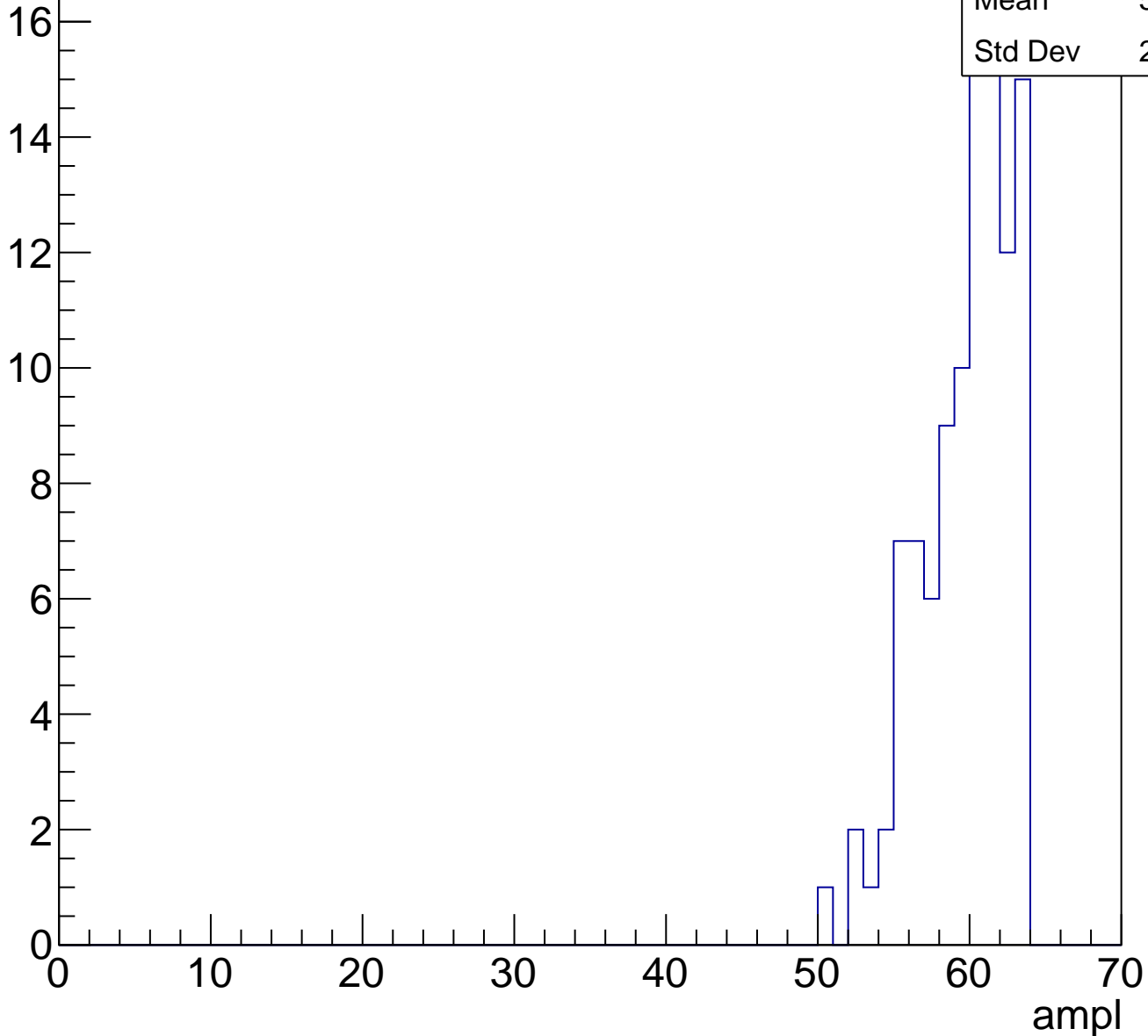
Entries	133
Mean	54.51
Std Dev	3.688



# B0L101S, U23-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

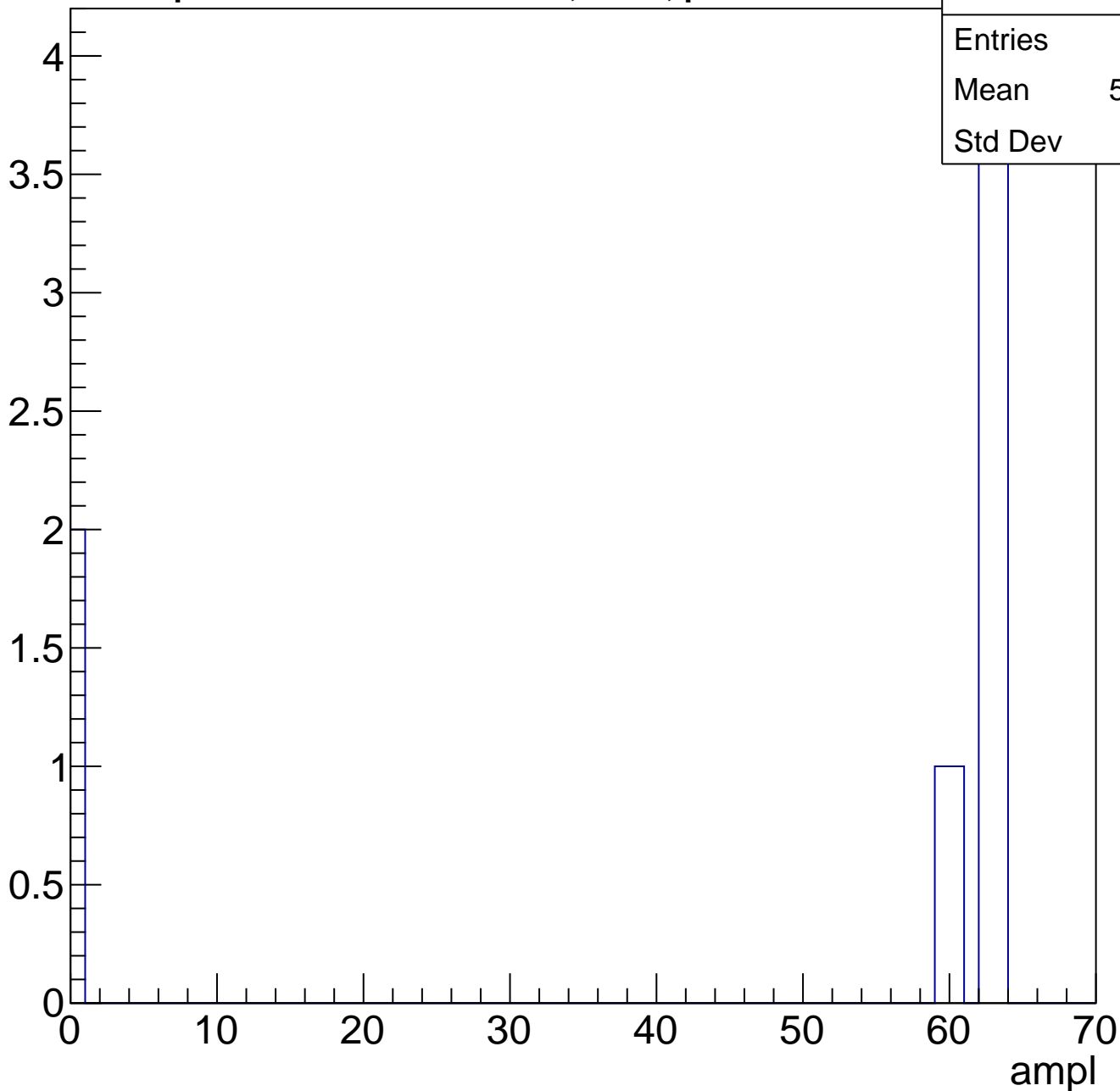
Entry



# B0L101S, U23-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch83, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	28.14
Std Dev	4.147

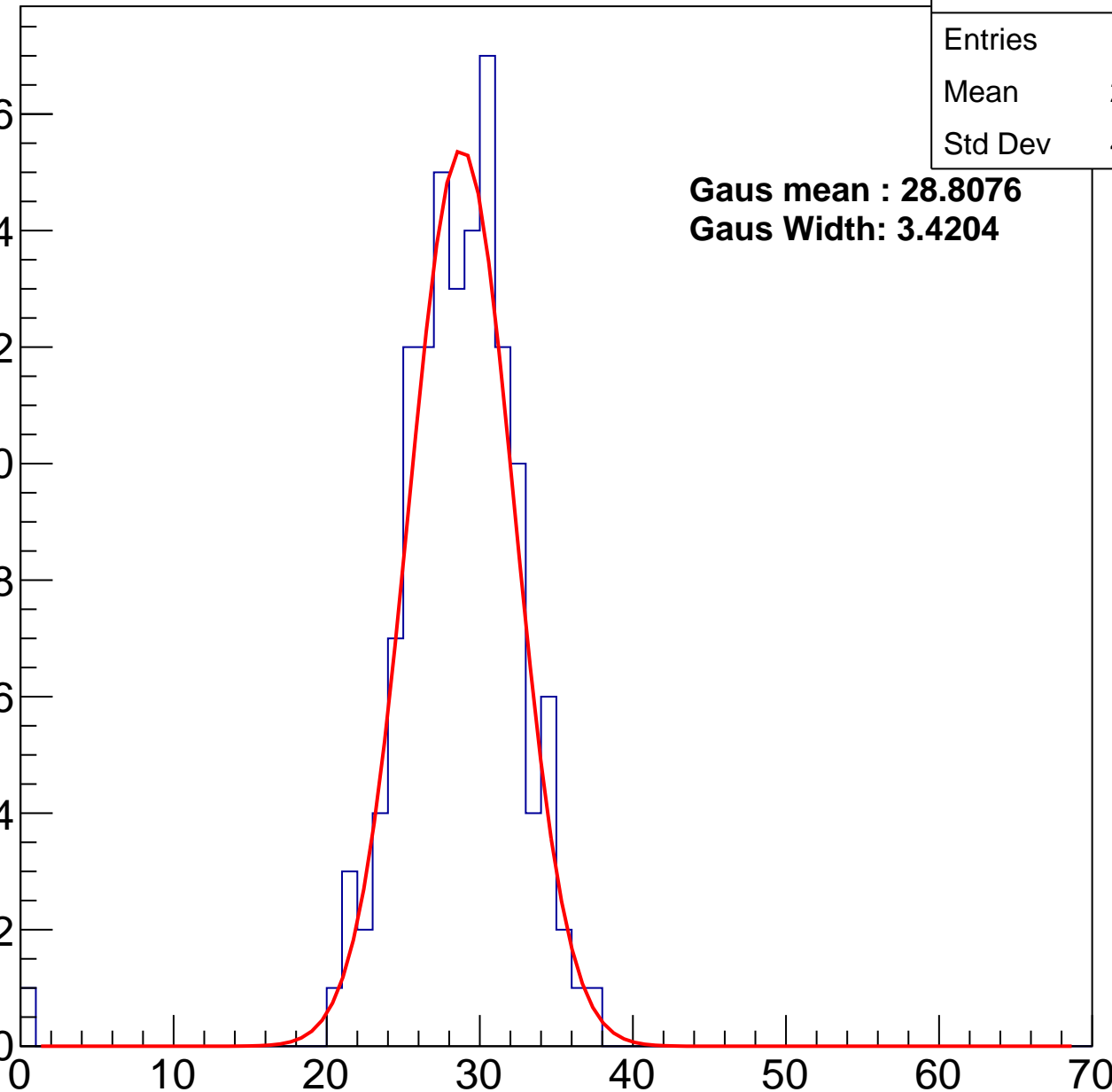
**Gaus mean : 28.8076**

**Gaus Width: 3.4204**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch83, adc1

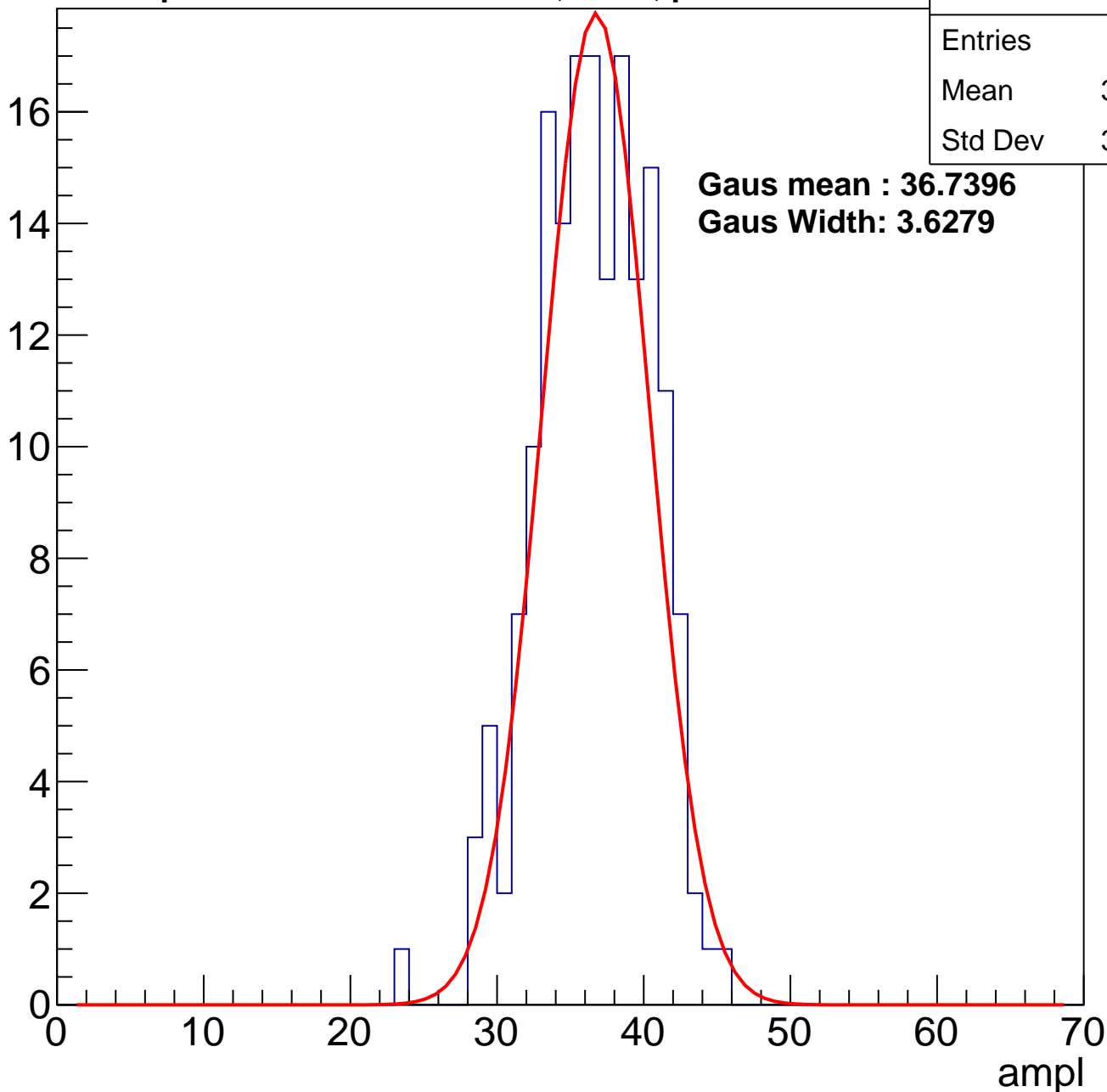
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	172
Mean	36.13
Std Dev	3.763

**Gaus mean : 36.7396**

**Gaus Width: 3.6279**

Entry



# B0L101S, U23-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

128

Mean

43.61

Std Dev

3.408

**Gaus mean : 44.5207**

**Gaus Width: 3.3924**

0

10

20

30

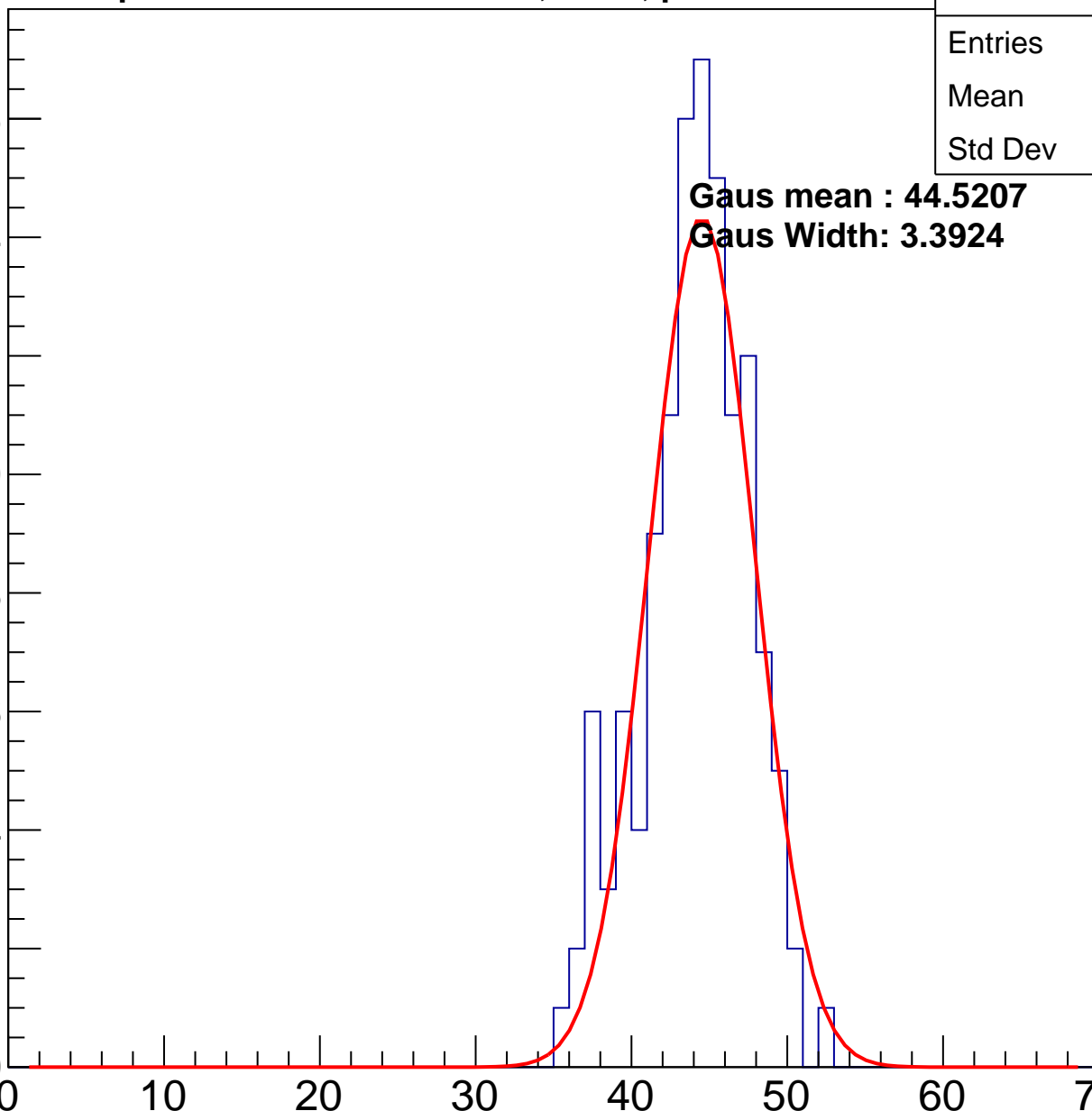
40

50

60

70

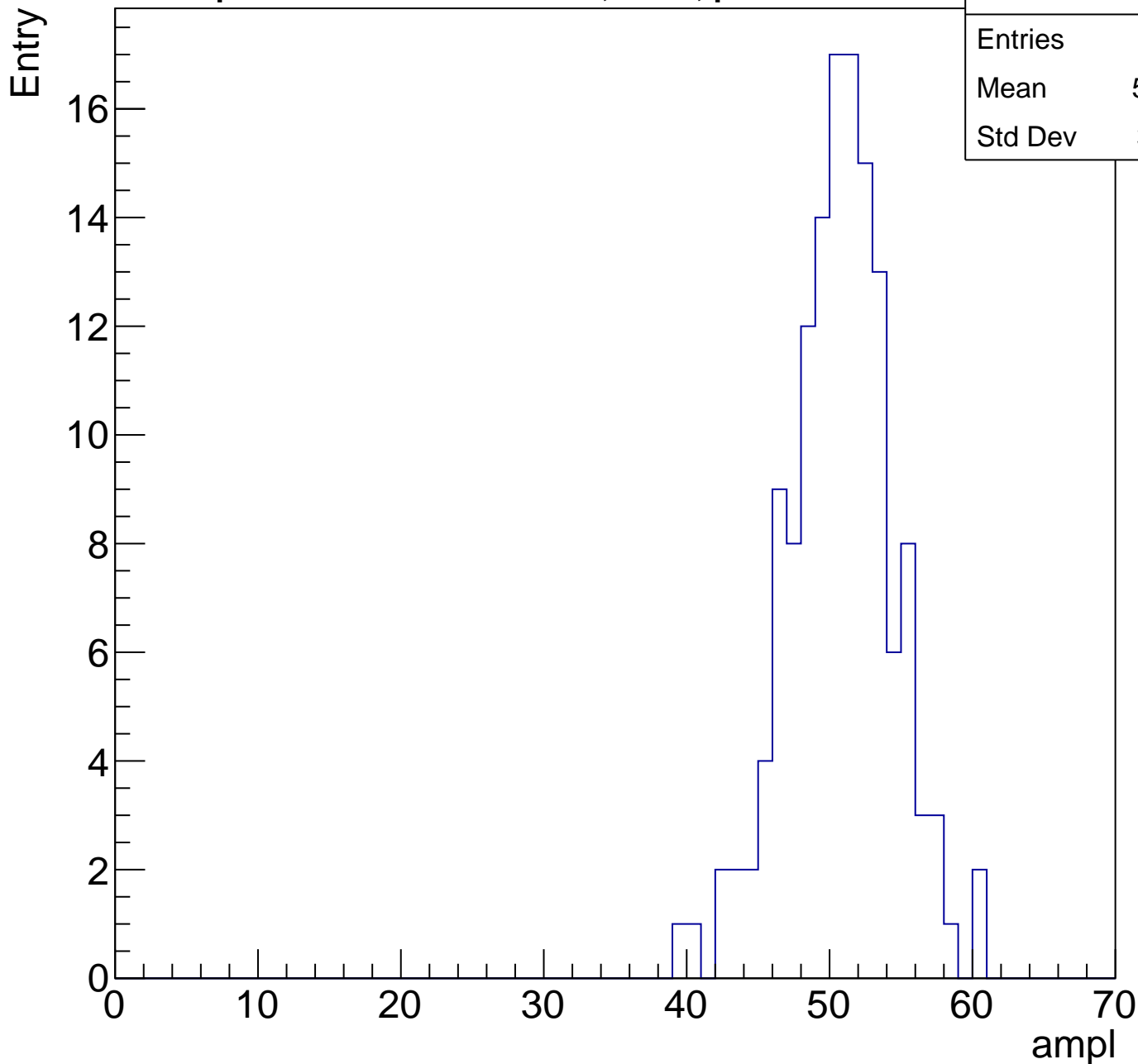
ampl



# B0L101S, U23-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	50.26
Std Dev	3.681



# B0L101S, U23-ch83, adc4

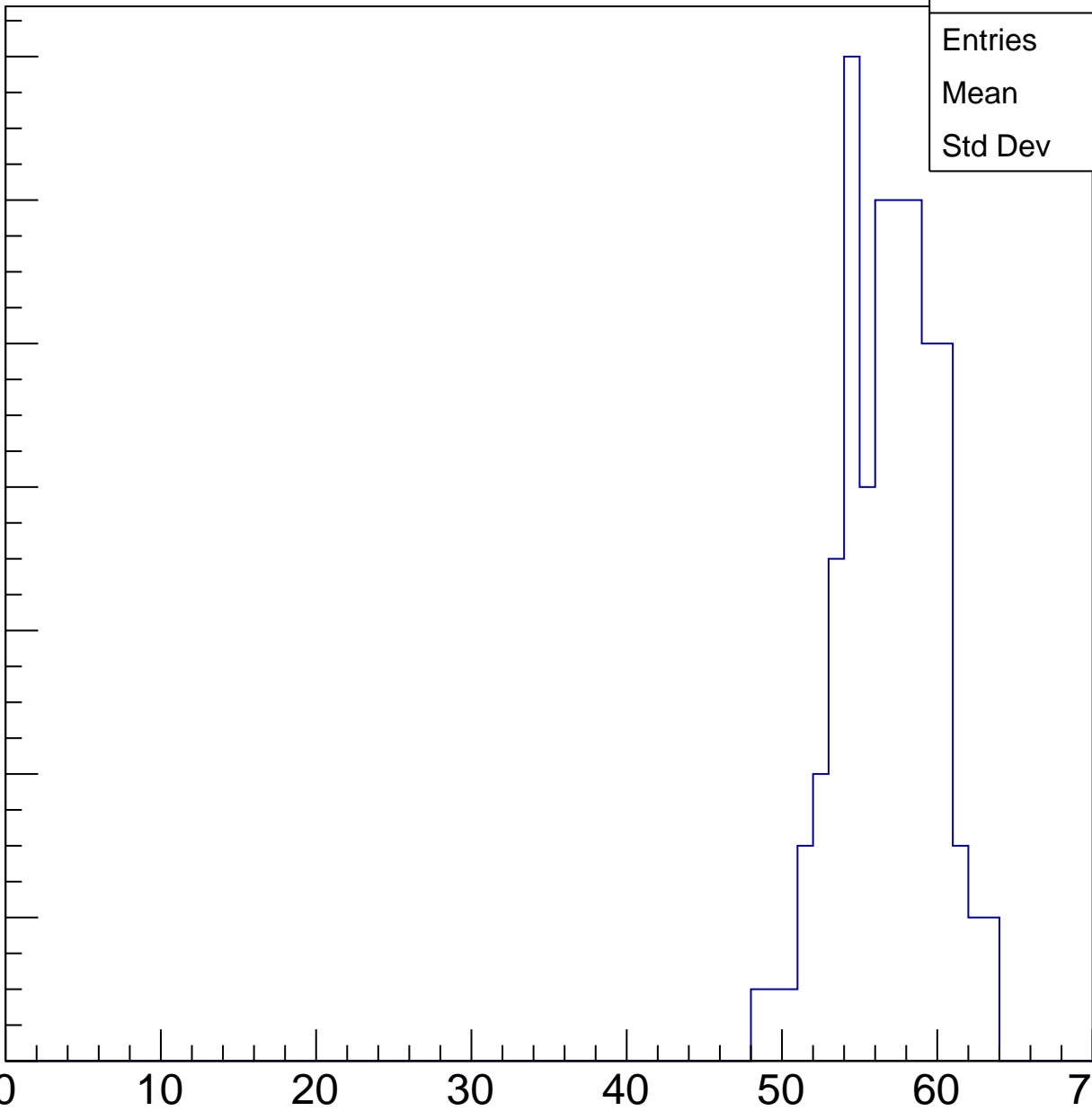
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	102
Mean	56.37
Std Dev	3.061

ampl



# B0L101S, U23-ch83, adc5

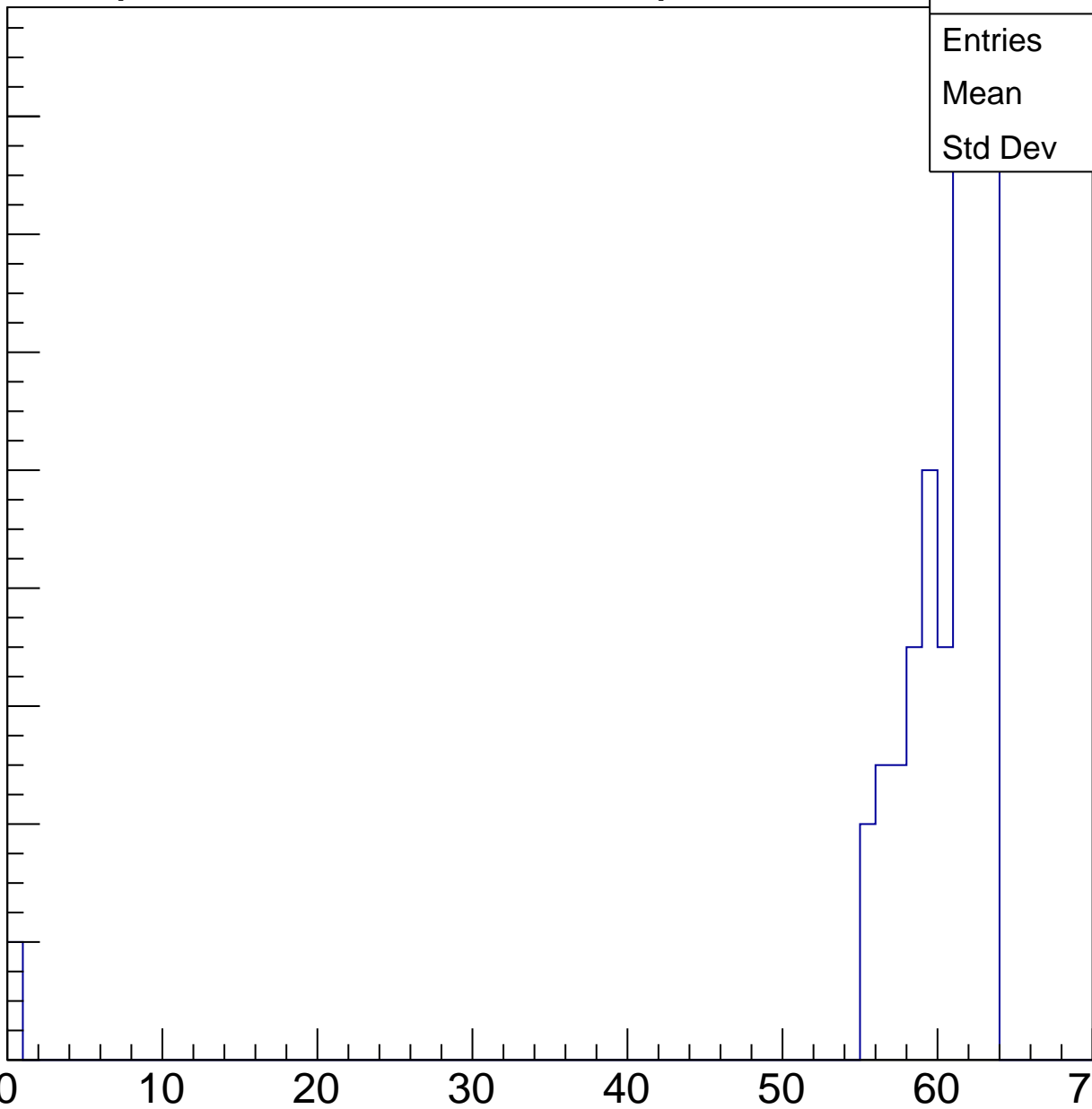
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	90
Mean	58.89
Std Dev	9.178

ampl



# B0L101S, U23-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	27.96
Std Dev	3.333

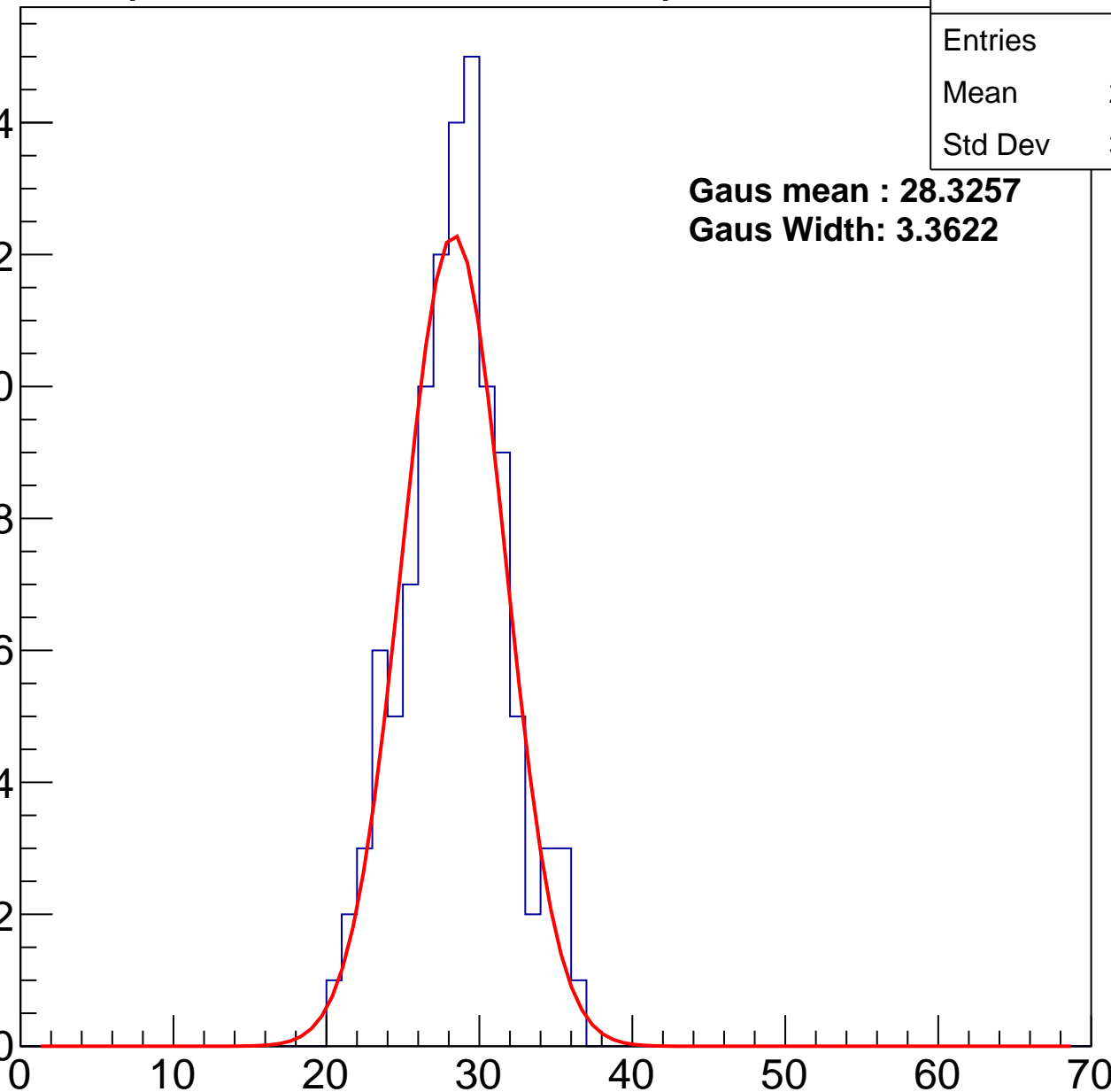
**Gaus mean : 28.3257**

**Gaus Width: 3.3622**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

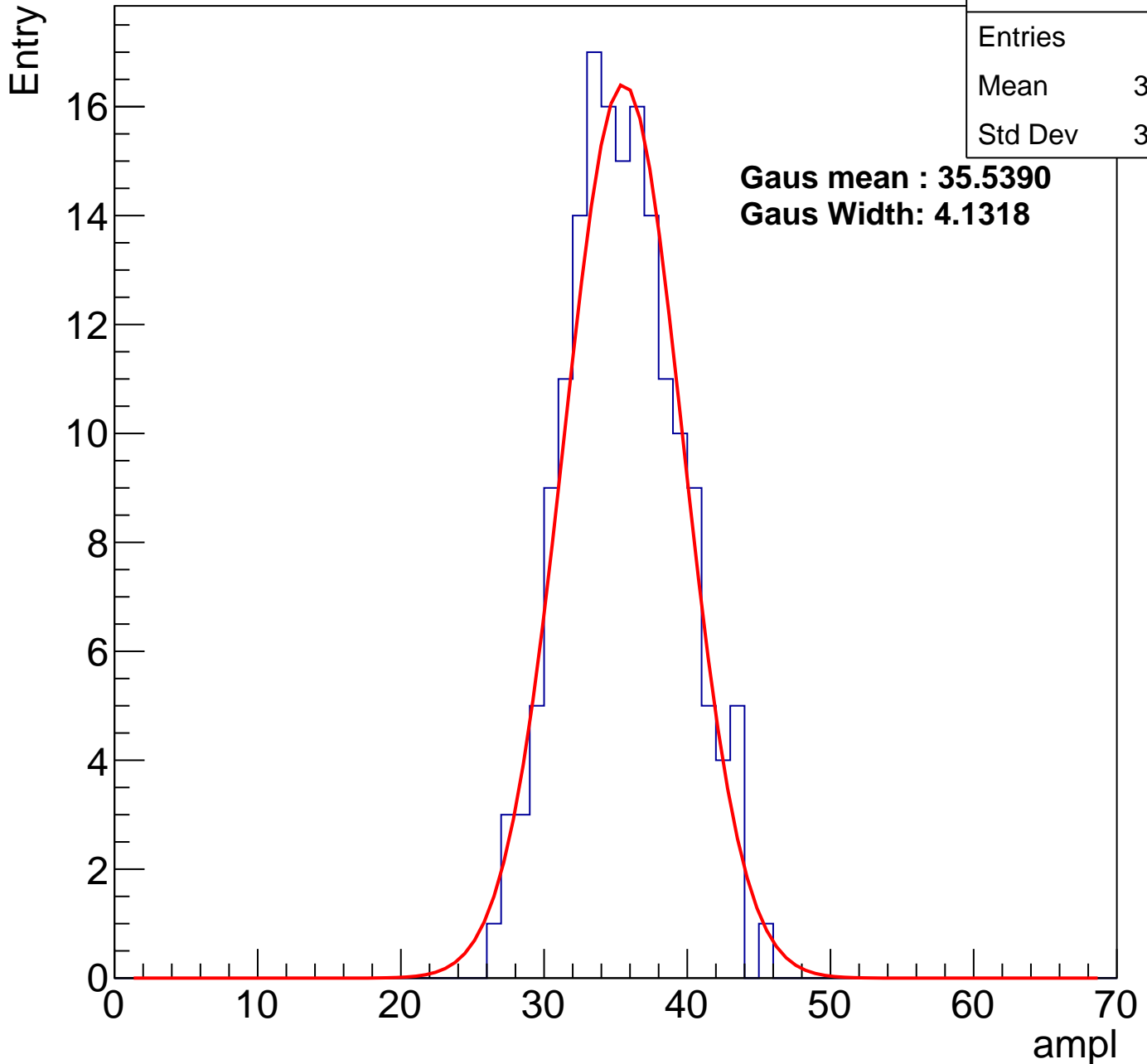


# B0L101S, U23-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	35.03
Std Dev	3.894

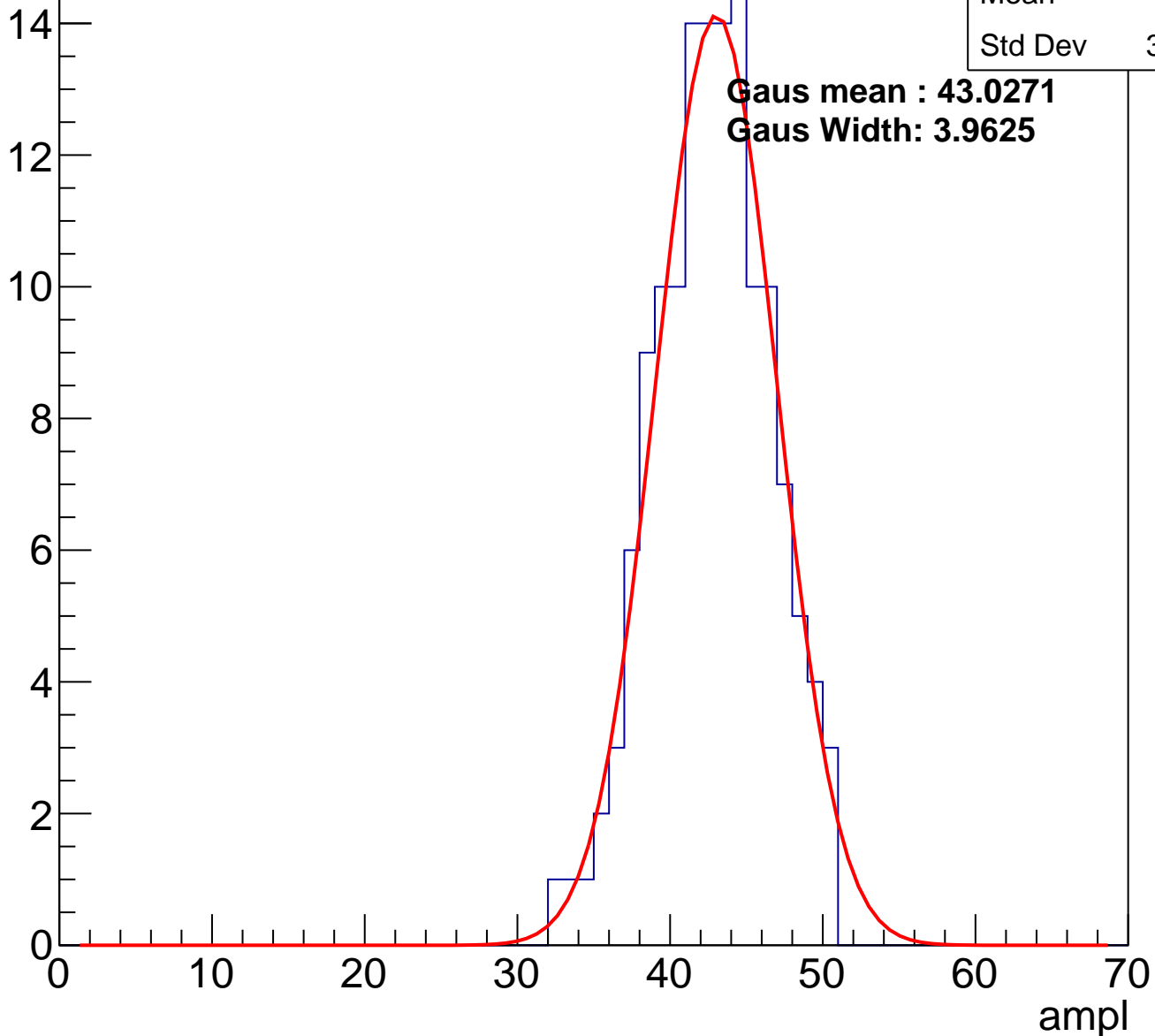
**Gaus mean : 35.5390**  
**Gaus Width: 4.1318**



# B0L101S, U23-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

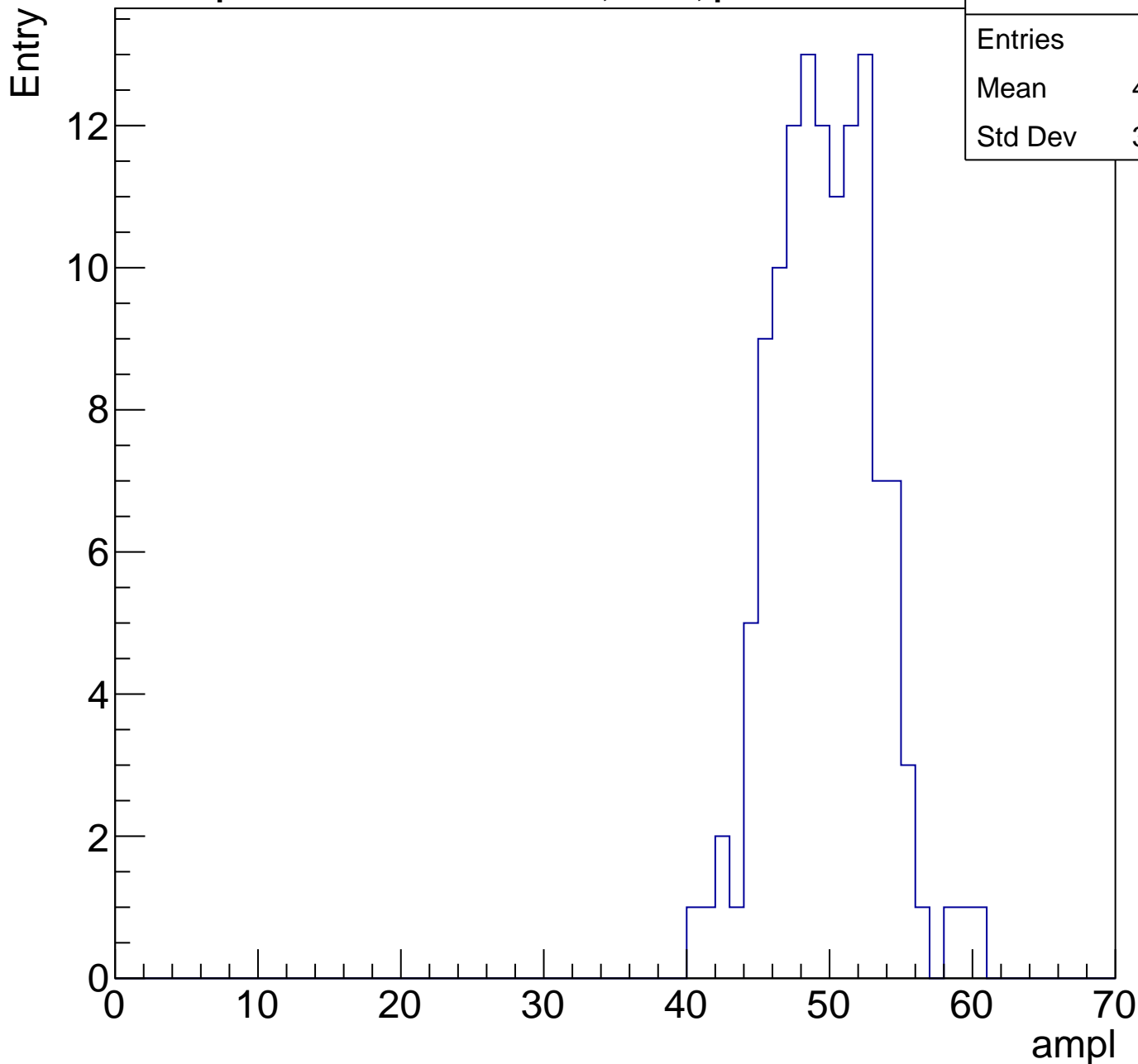
Entry



# B0L101S, U23-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	49.22
Std Dev	3.624



# B0L101S, U23-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

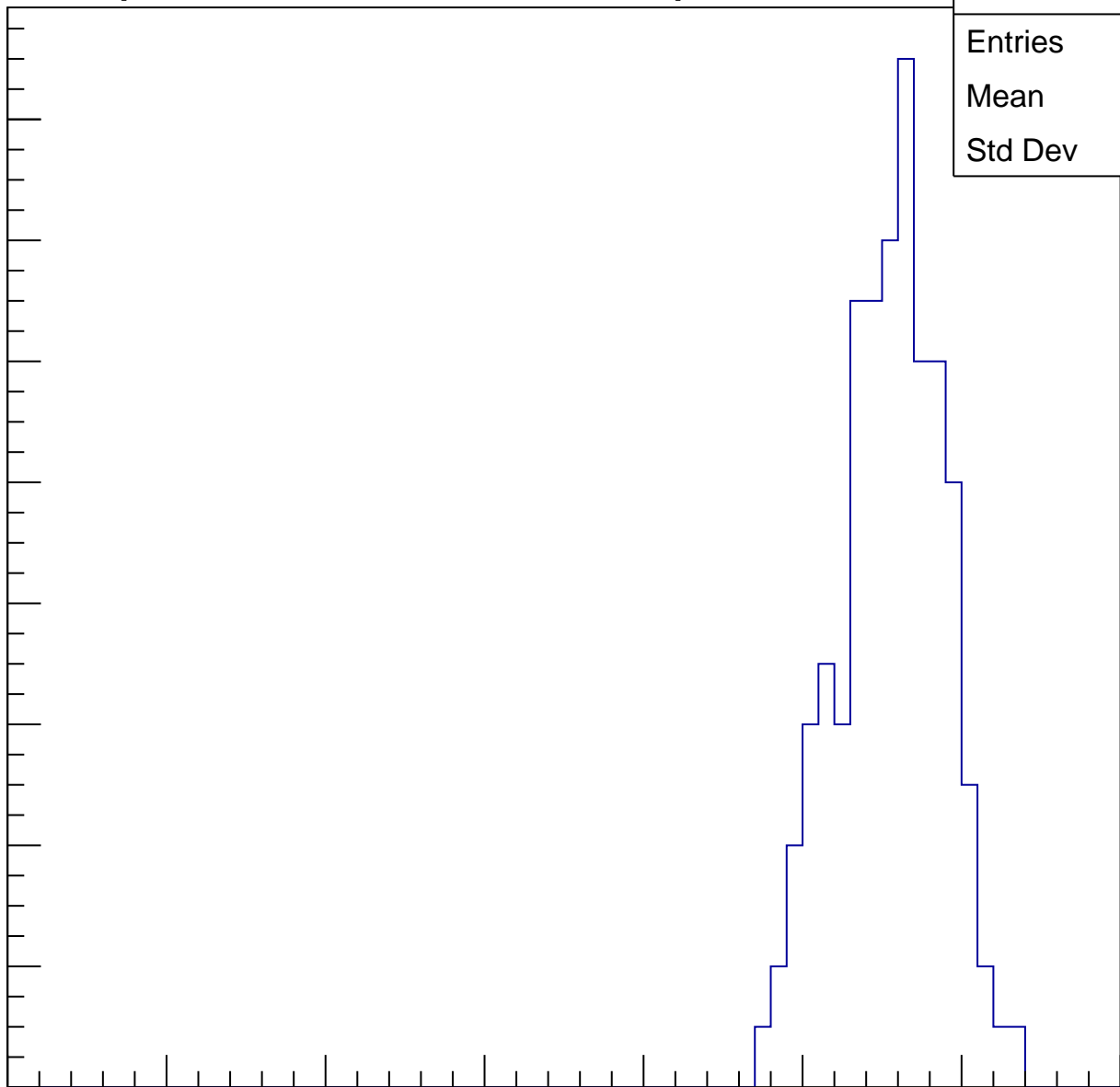
Entries	126
Mean	55.06
Std Dev	3.248

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch84, adc5

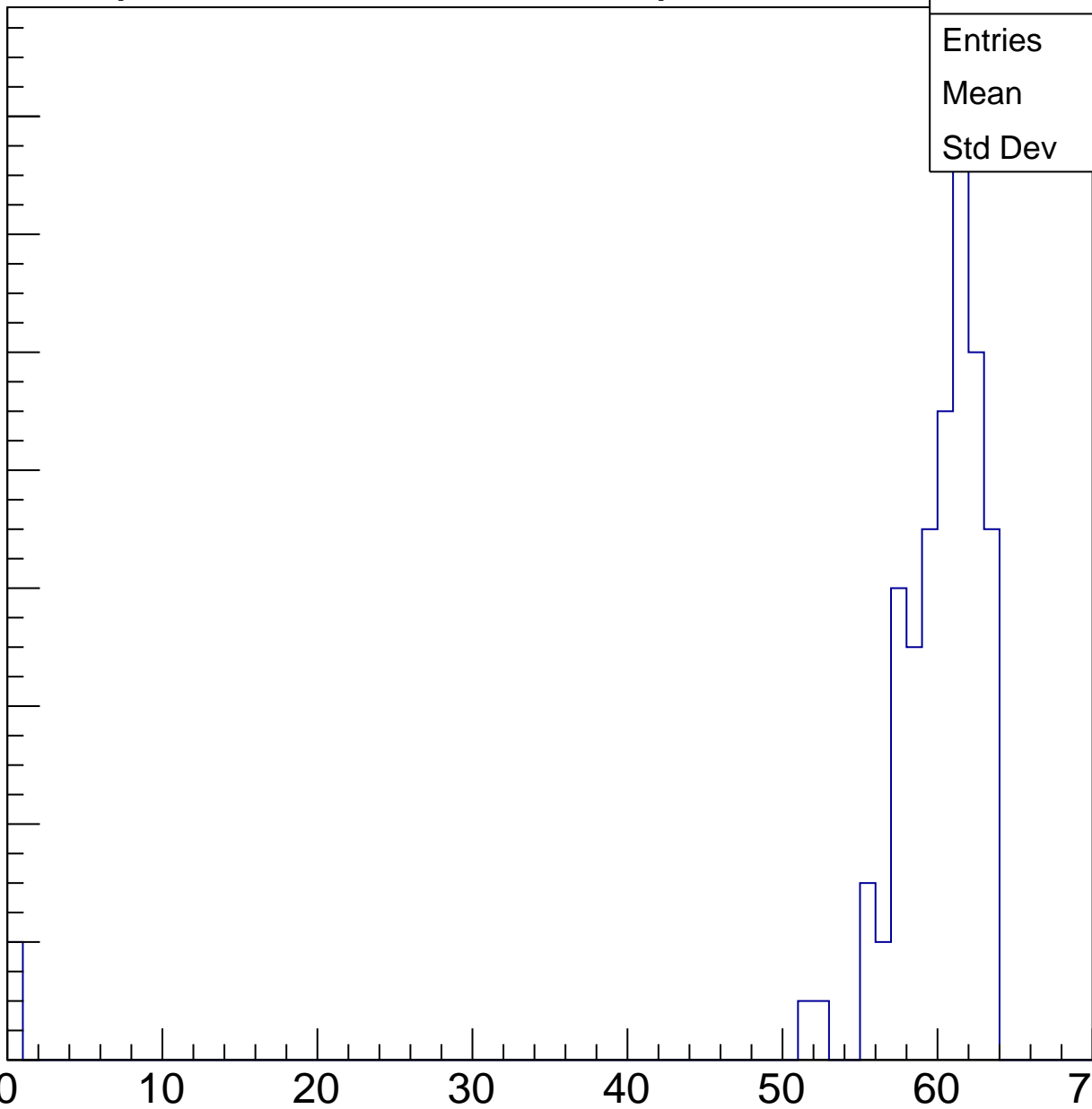
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	82
Mean	58.3
Std Dev	9.545

ampl

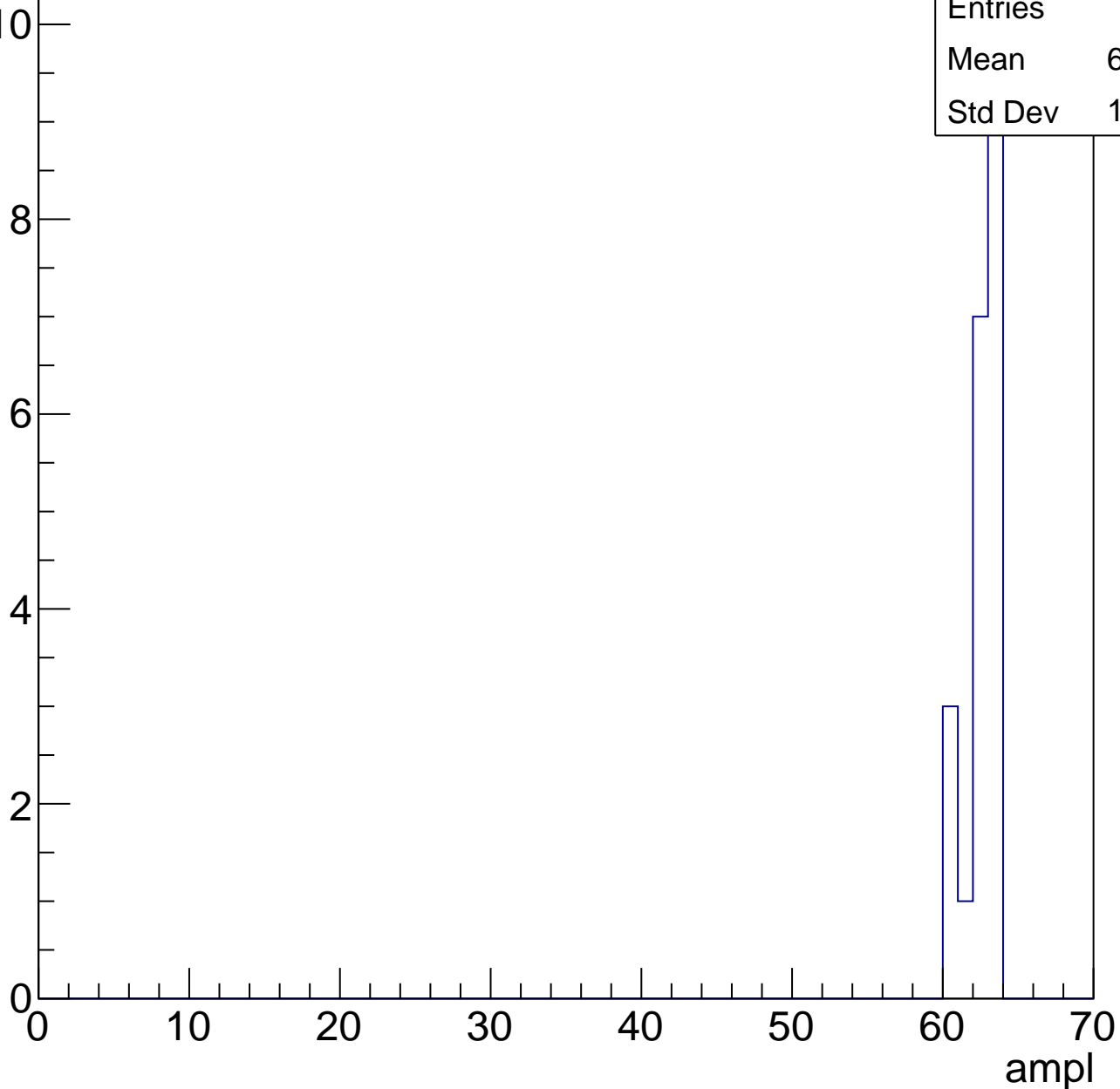


# B0L101S, U23-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	21
Mean	62.14
Std Dev	1.037





# B0L101S, U23-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

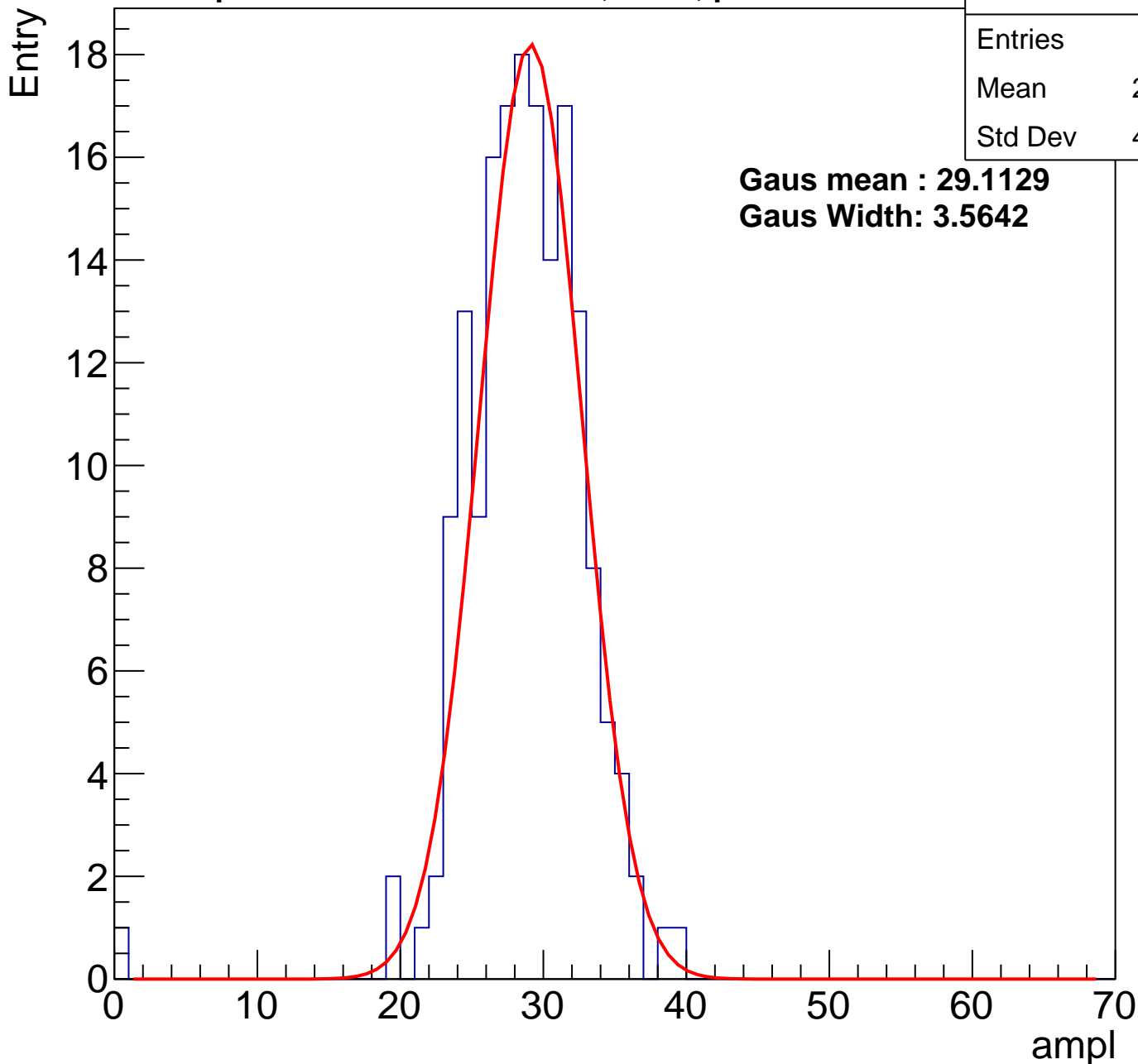
# B0L101S, U23-ch85, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	170
Mean	28.26
Std Dev	4.207

**Gaus mean : 29.1129**

**Gaus Width: 3.5642**



# B0L101S, U23-ch85, adc1

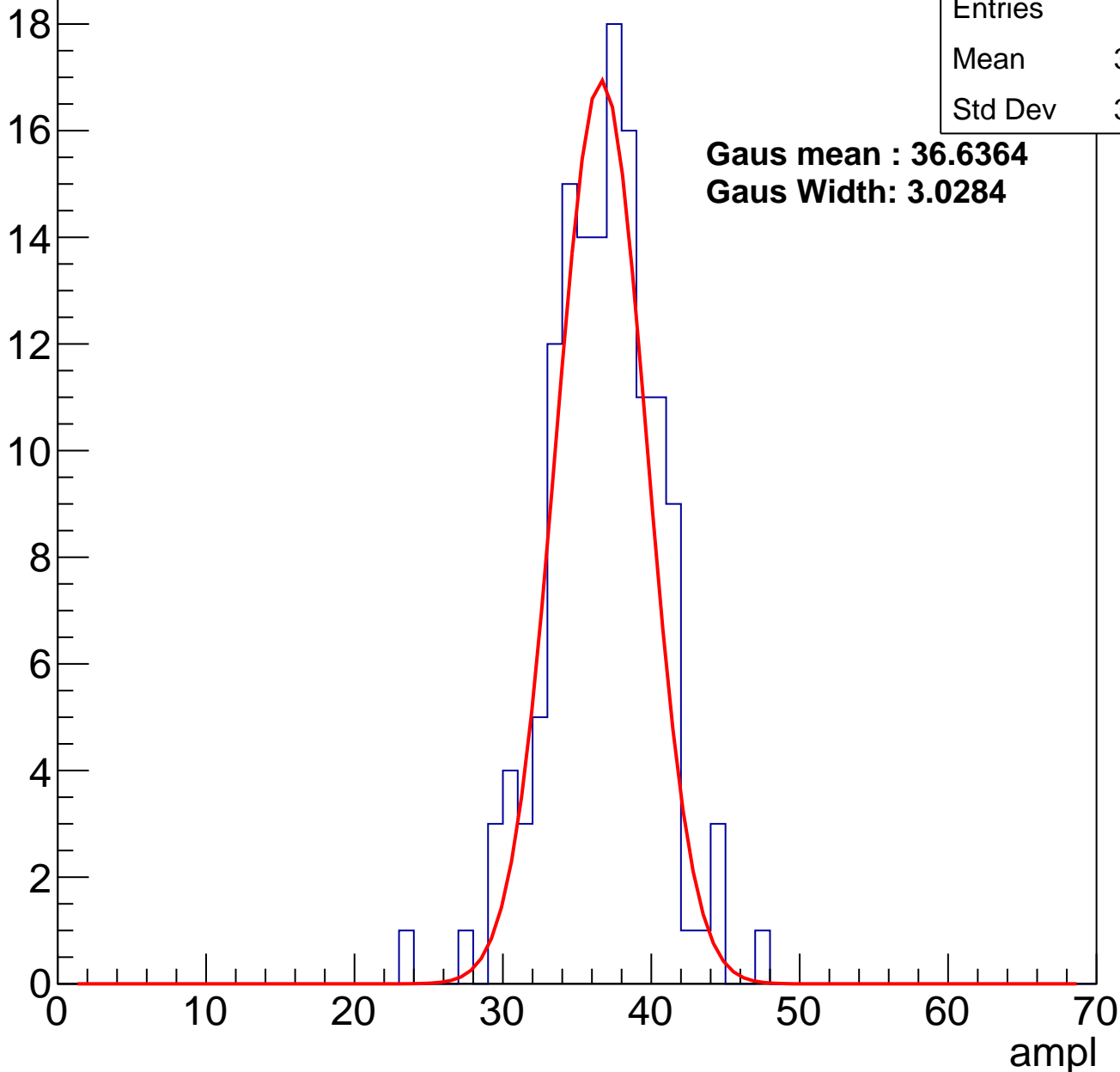
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	36.27
Std Dev	3.595

**Gaus mean : 36.6364**

**Gaus Width: 3.0284**

Entry



# B0L101S, U23-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

97

Mean

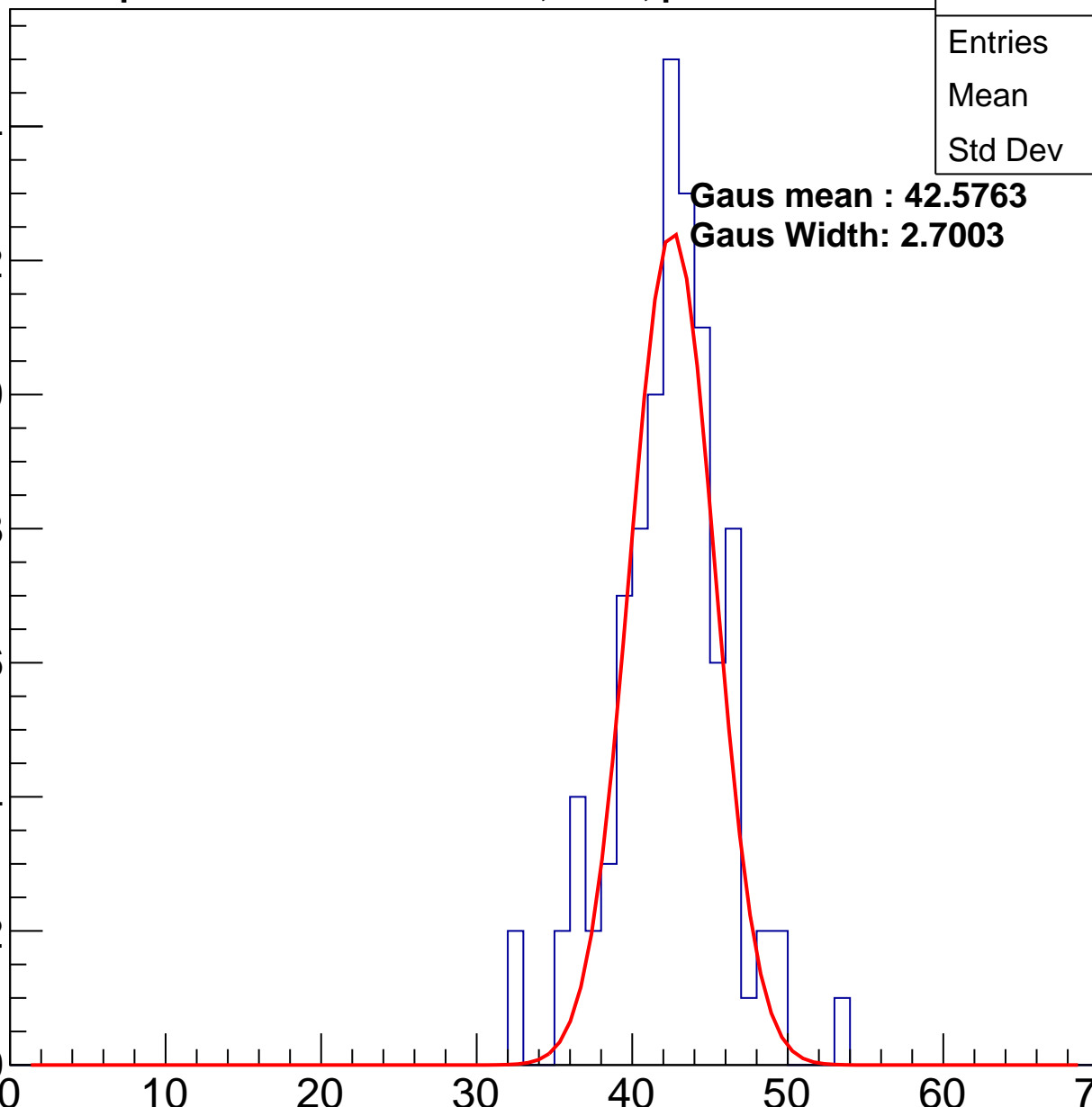
42

Std Dev

3.517

**Gaus mean : 42.5763**

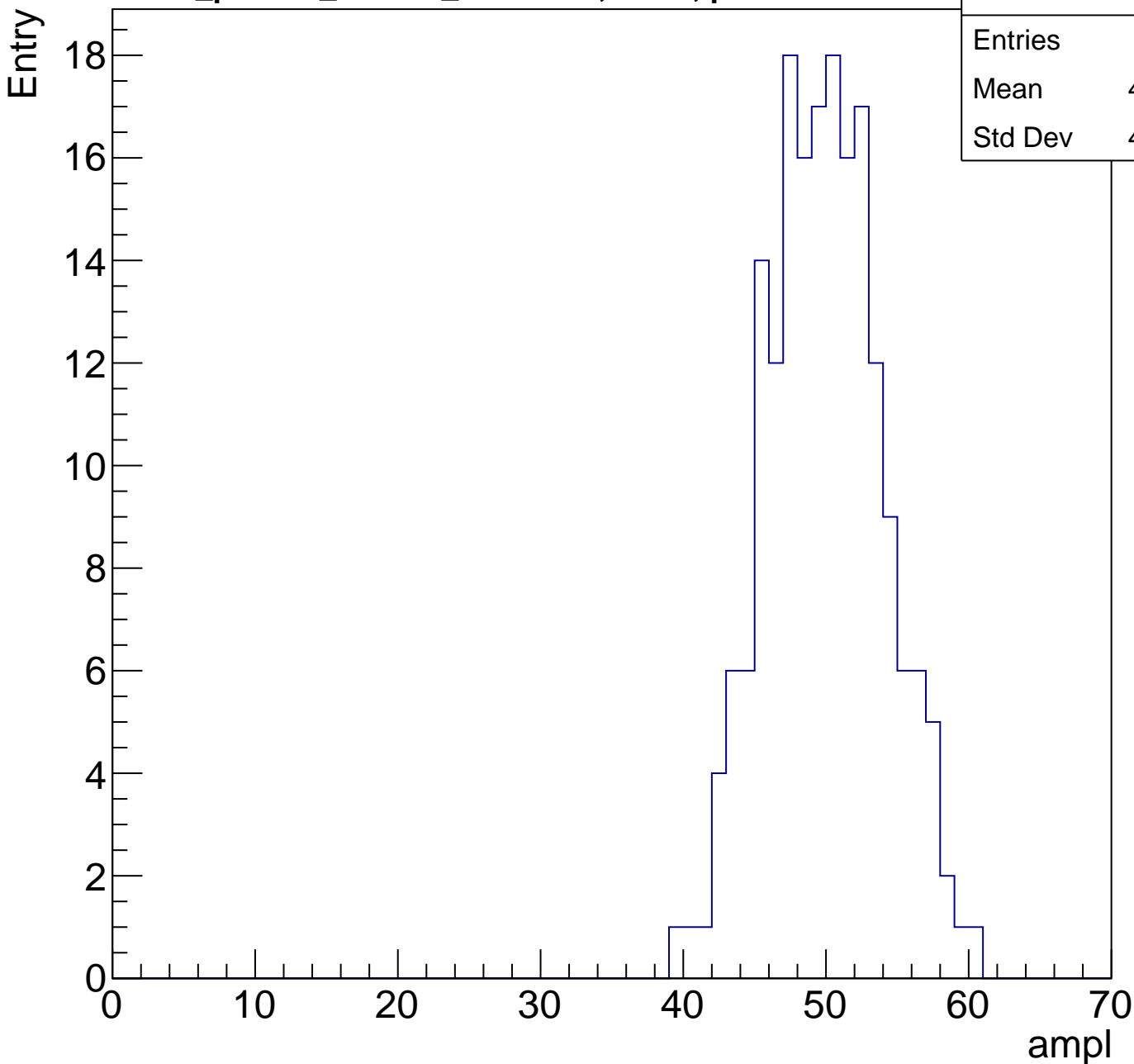
**Gaus Width: 2.7003**



# B0L101S, U23-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	189
Mean	49.46
Std Dev	4.019



# B0L101S, U23-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	99
Mean	56.29
Std Dev	3.307

Entry

12

10

8

6

4

2

0

0

10

20

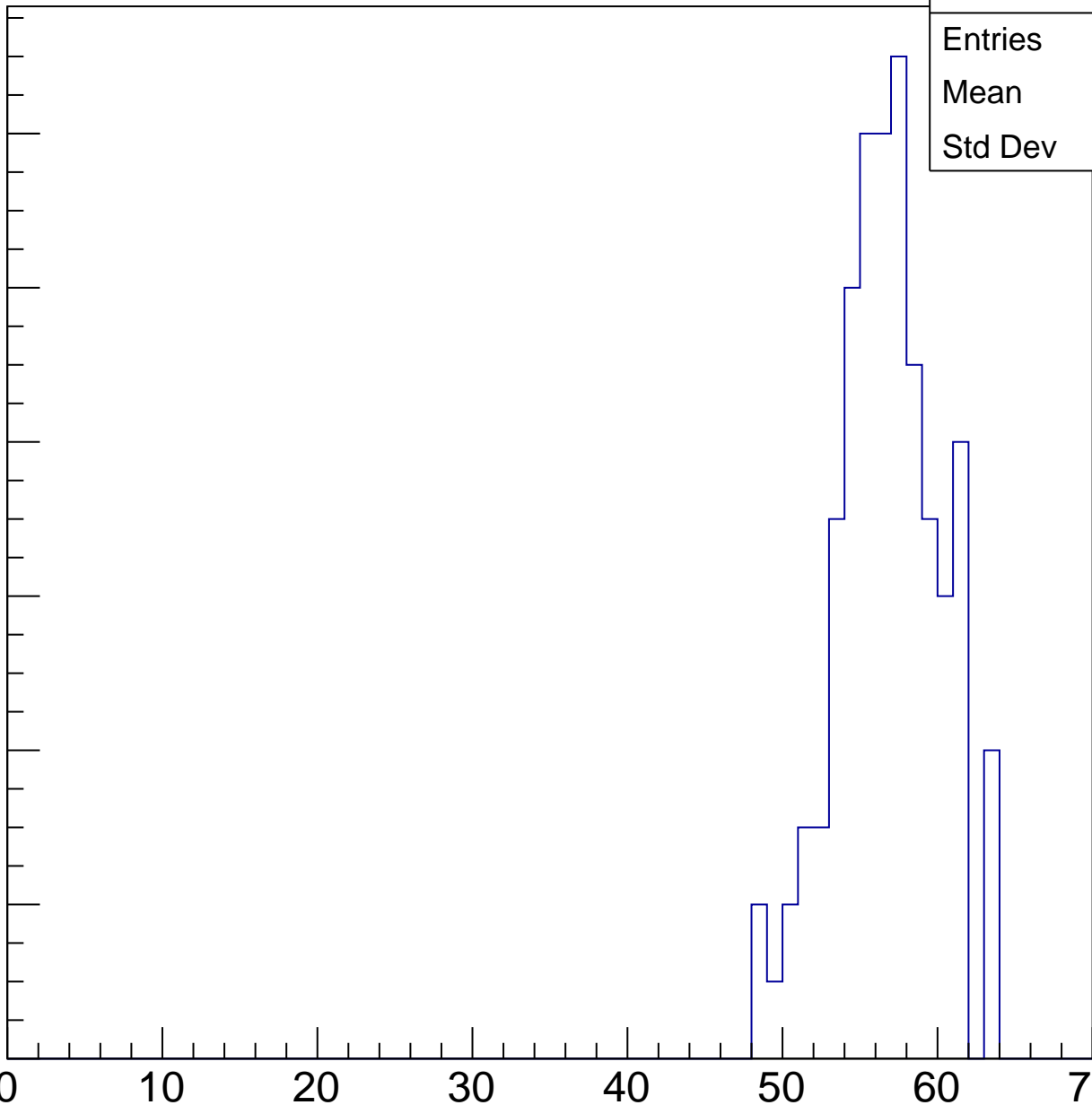
30

40

50

60

ampl



# B0L101S, U23-ch85, adc5

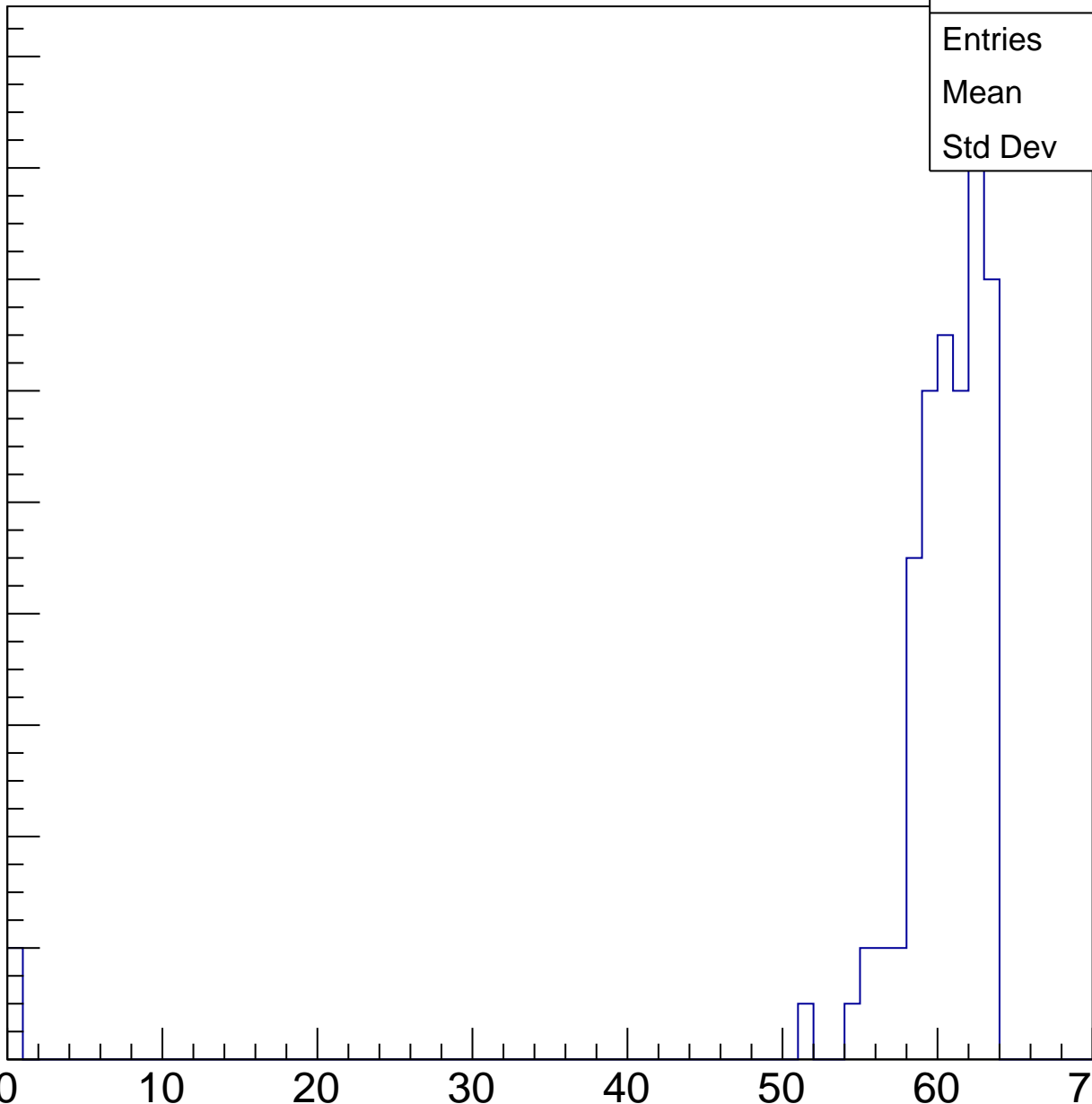
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	58.88
Std Dev	9.271

ampl



# B0L101S, U23-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B0L101S, U23-ch86, adc0

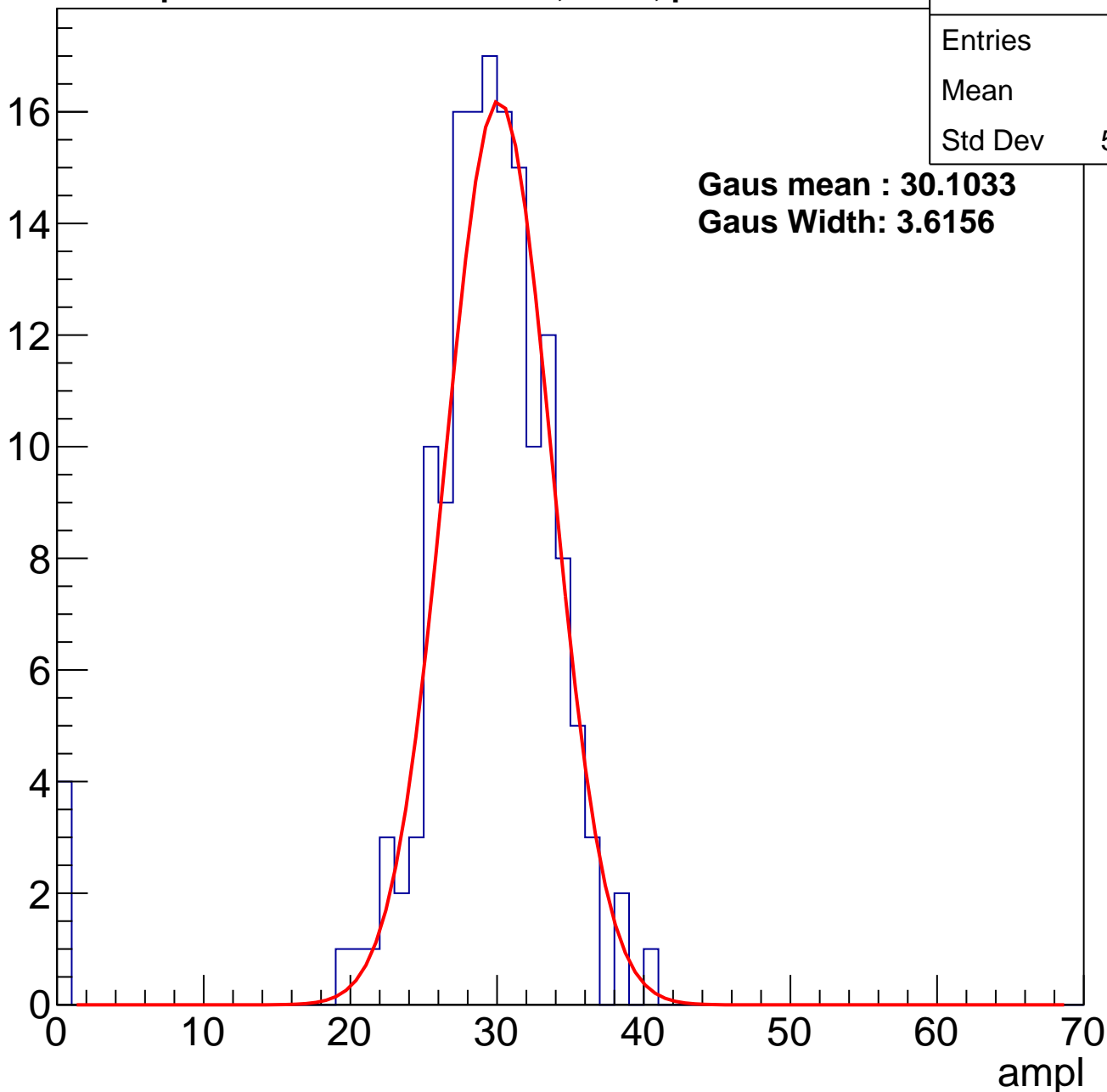
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	28.6
Std Dev	5.884

**Gaus mean : 30.1033**

**Gaus Width: 3.6156**

Entry



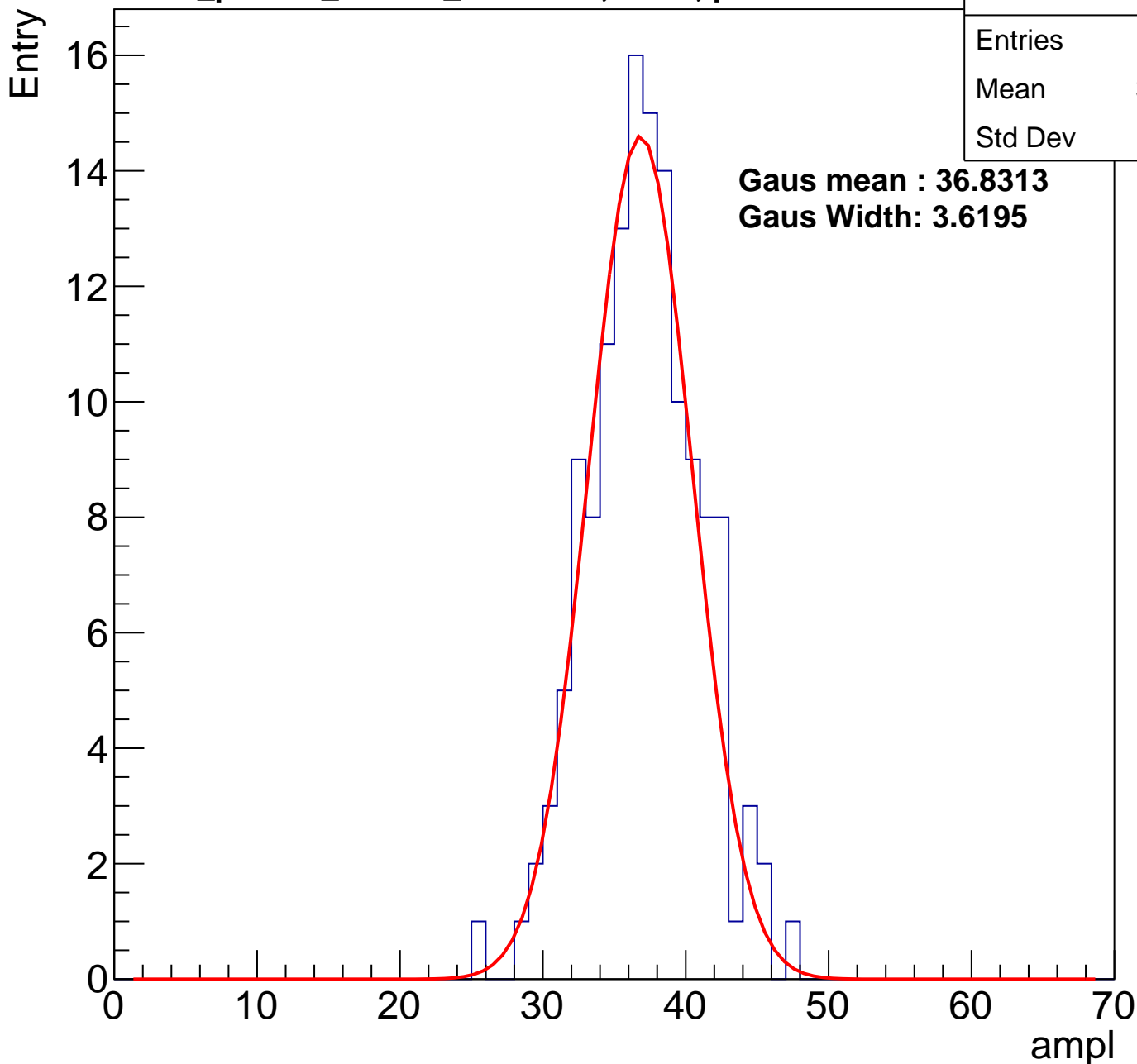
# B0L101S, U23-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	36.61
Std Dev	3.83

**Gaus mean : 36.8313**

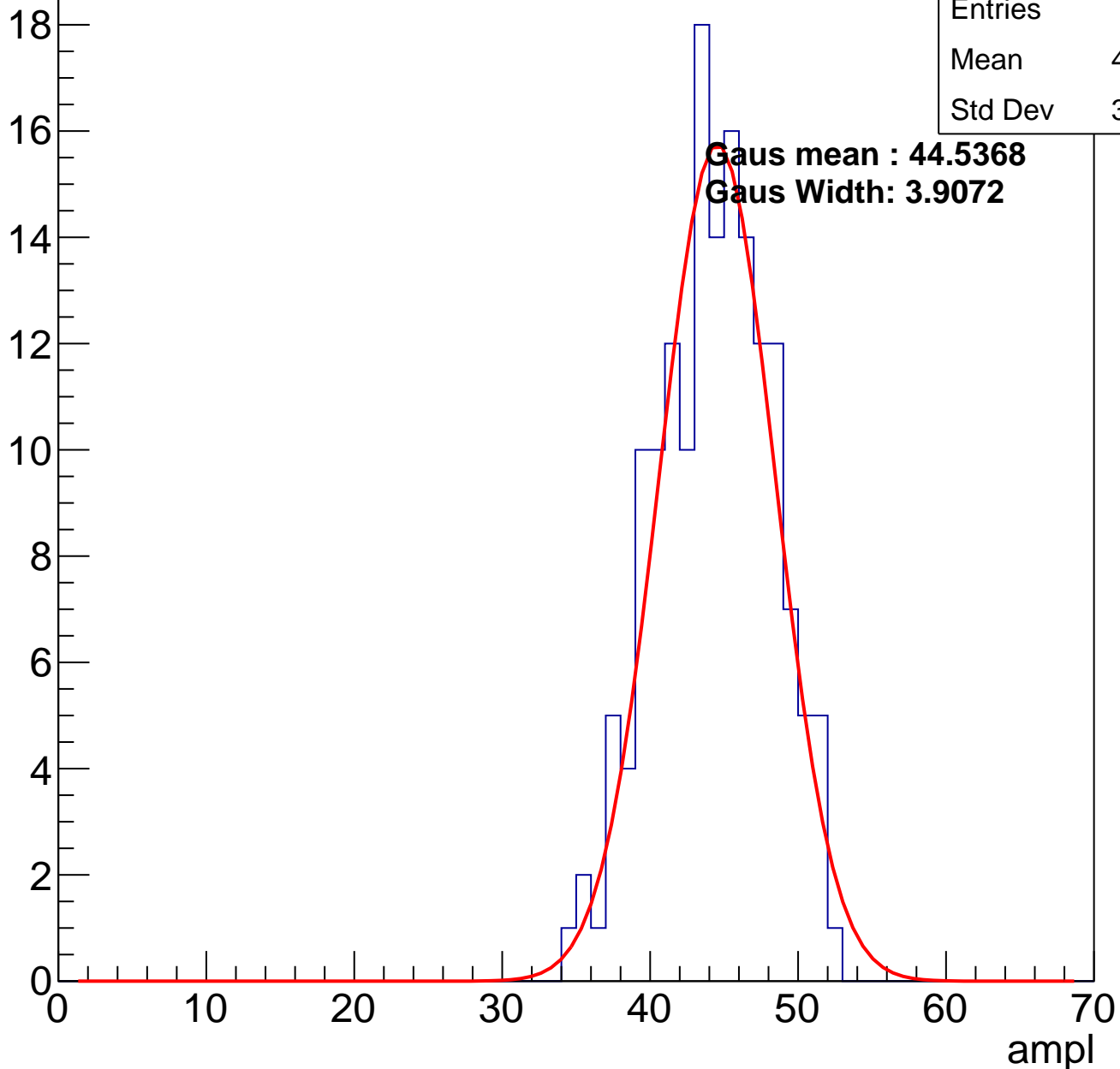
**Gaus Width: 3.6195**



# B0L101S, U23-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

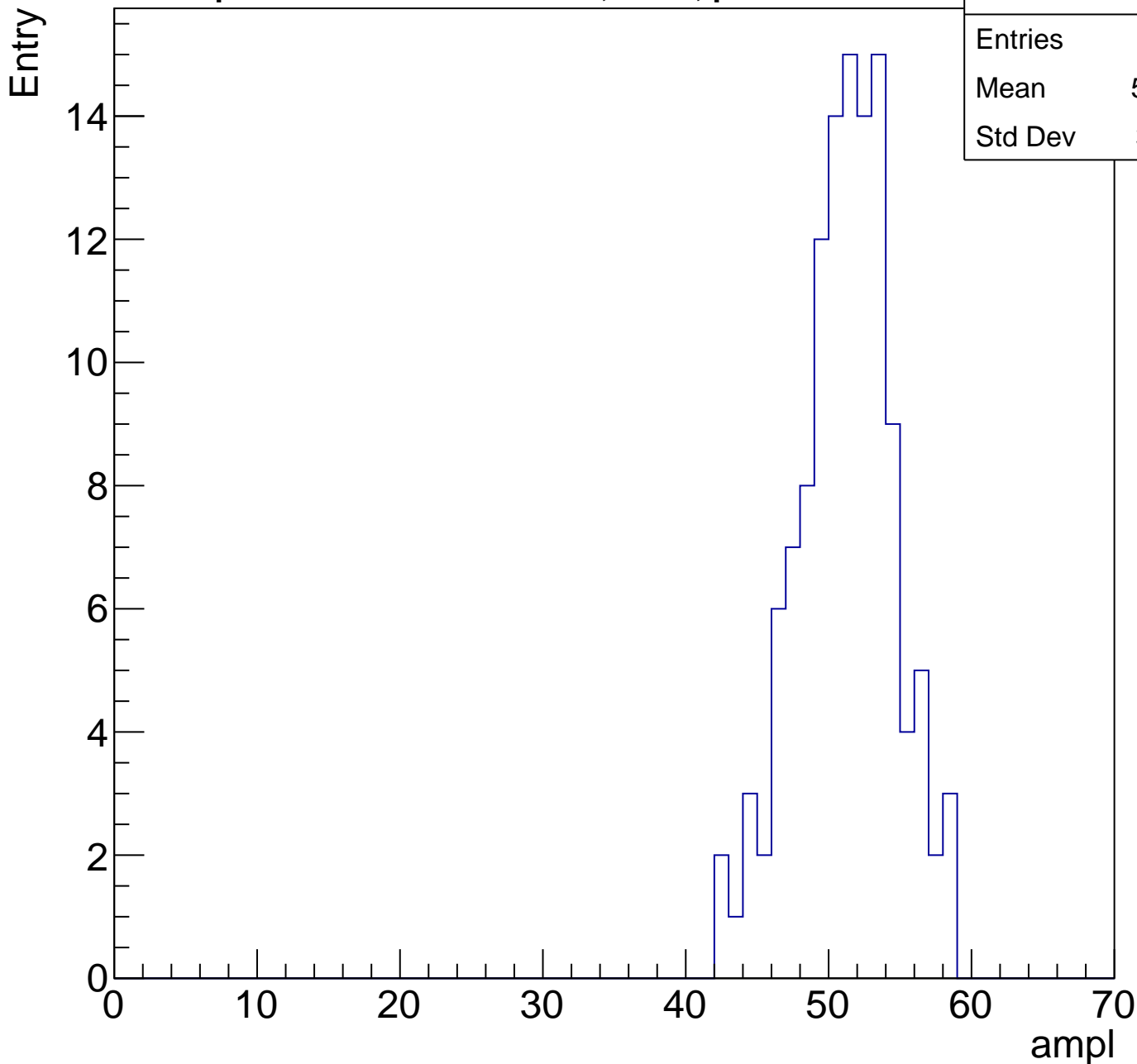
Entry



# B0L101S, U23-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	50.72
Std Dev	3.381



# B0L101S, U23-ch86, adc4

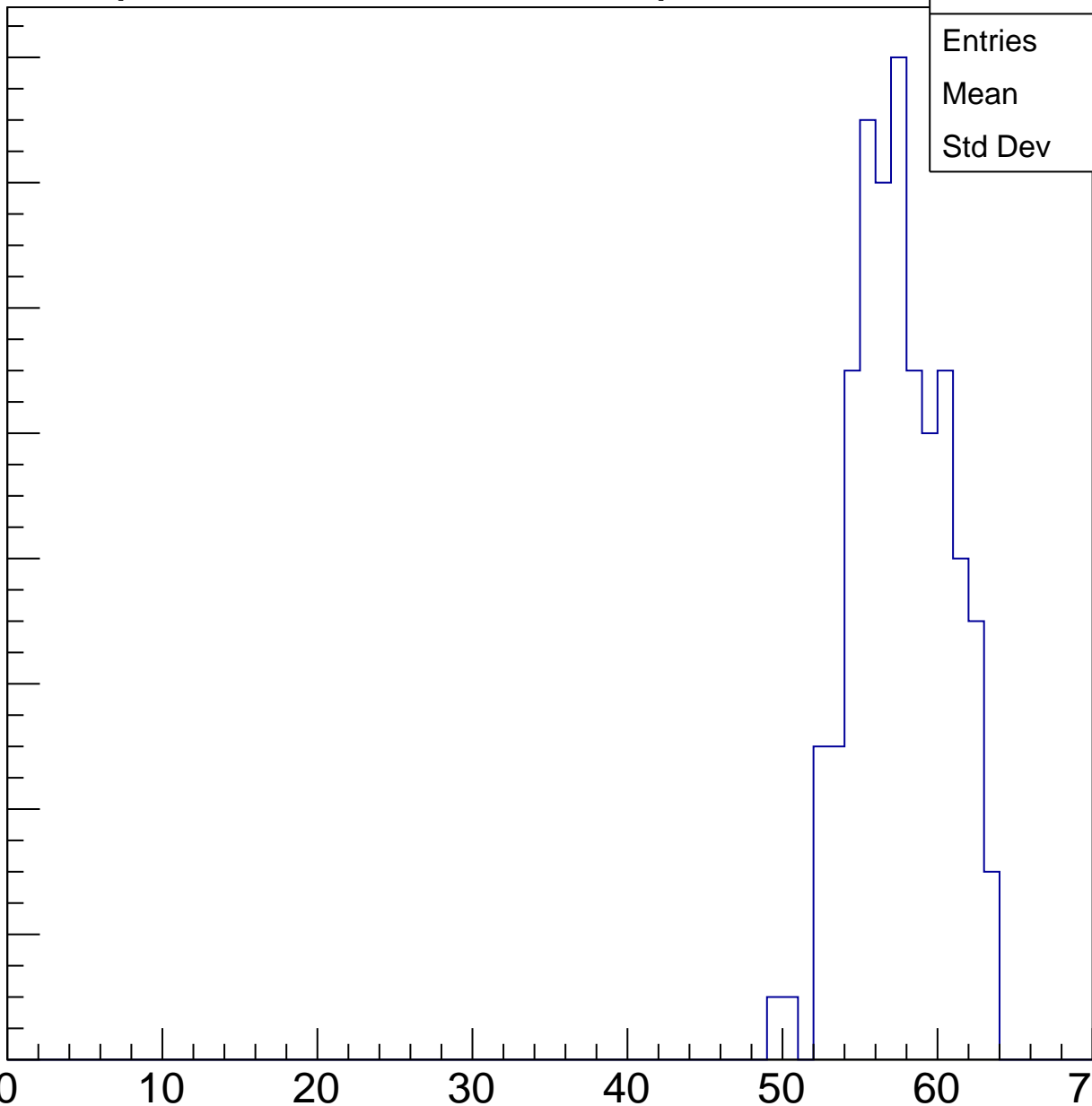
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	118
Mean	57.1
Std Dev	2.984

ampl



# B0L101S, U23-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

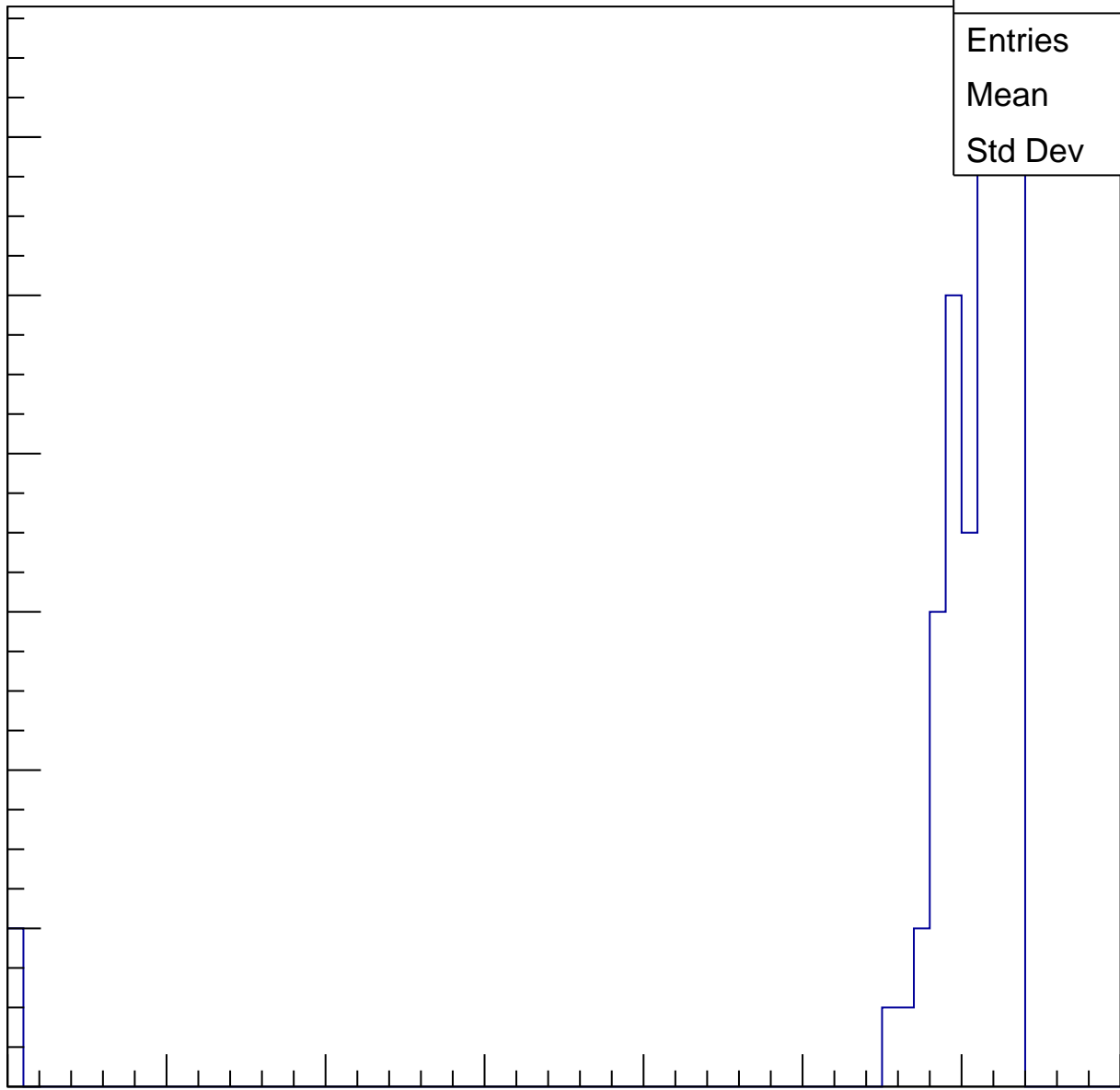
2

0

Entries	66
Mean	58.76
Std Dev	10.57

ampl

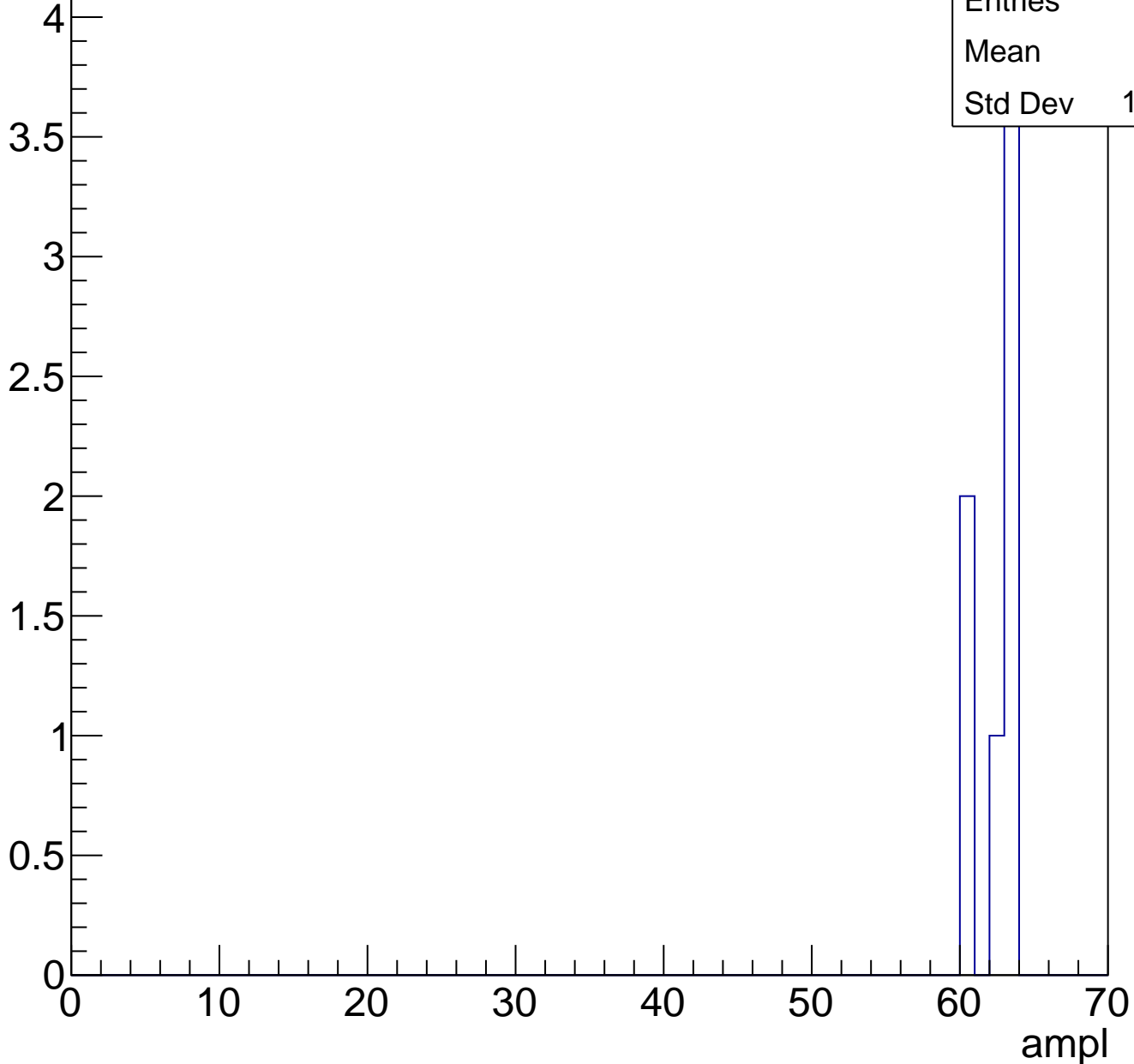
0 10 20 30 40 50 60 70



# B0L101S, U23-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

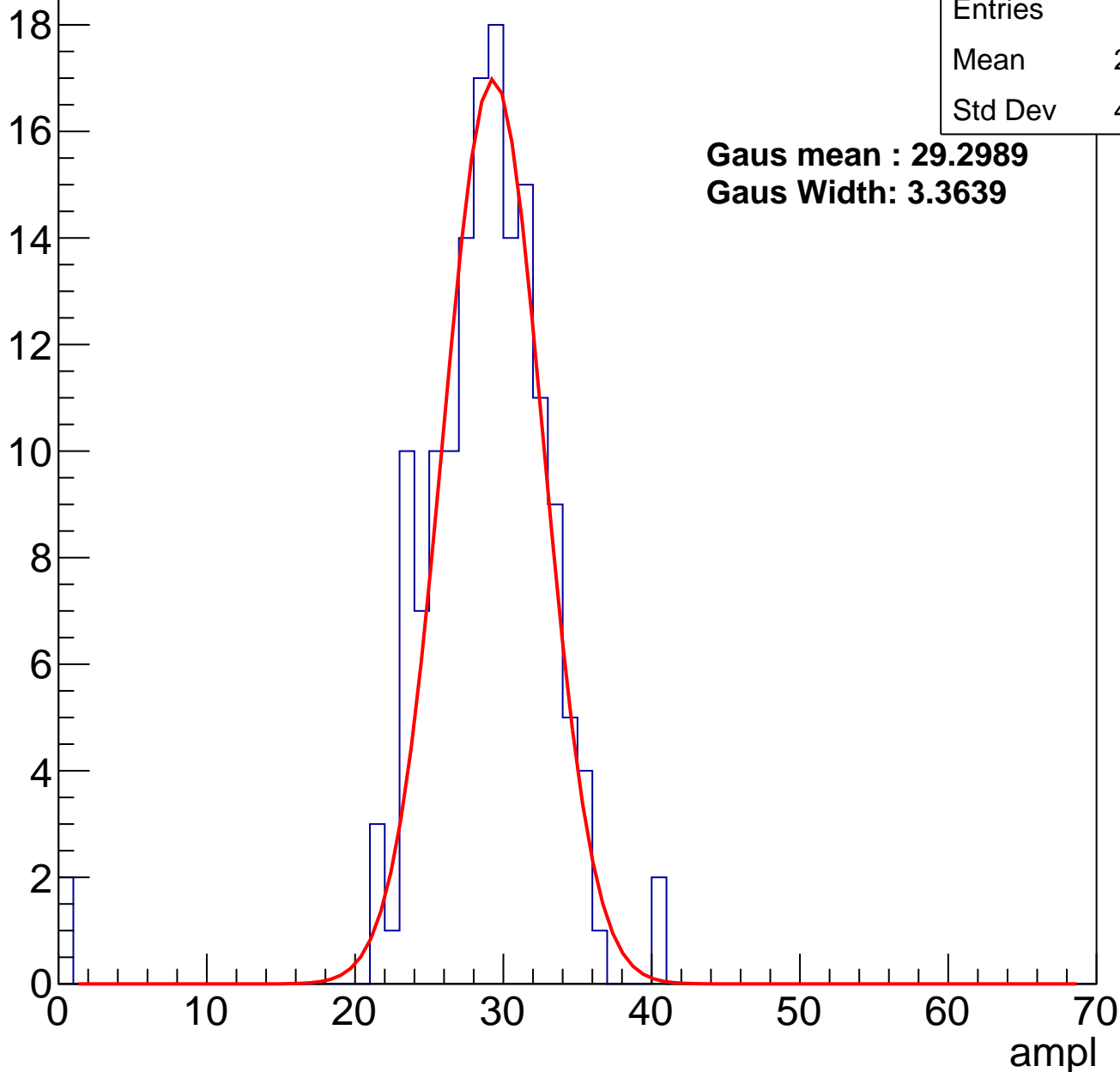


Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch87, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch87, adc1

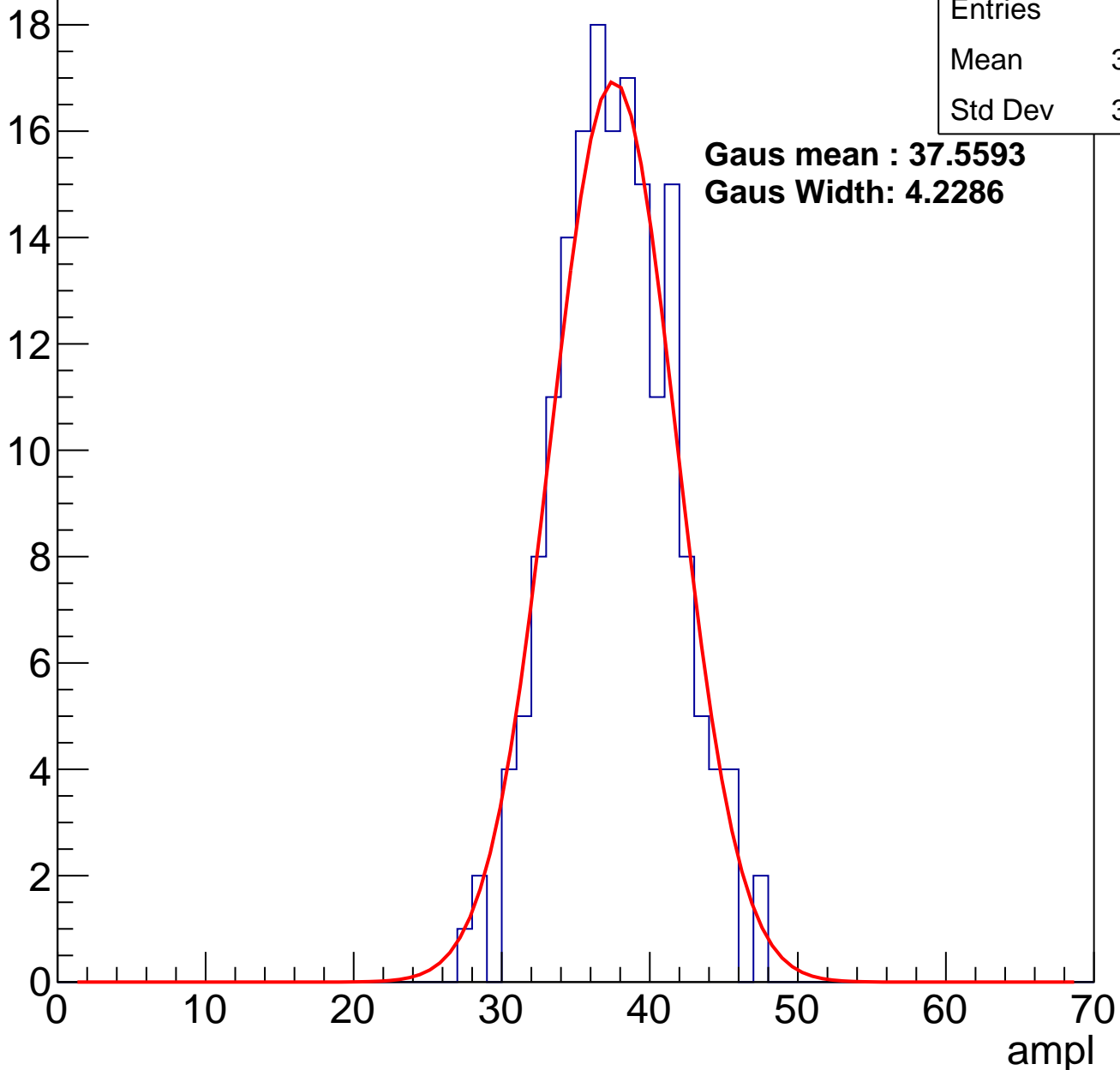
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	176
Mean	37.16
Std Dev	3.892

**Gaus mean : 37.5593**

**Gaus Width: 4.2286**

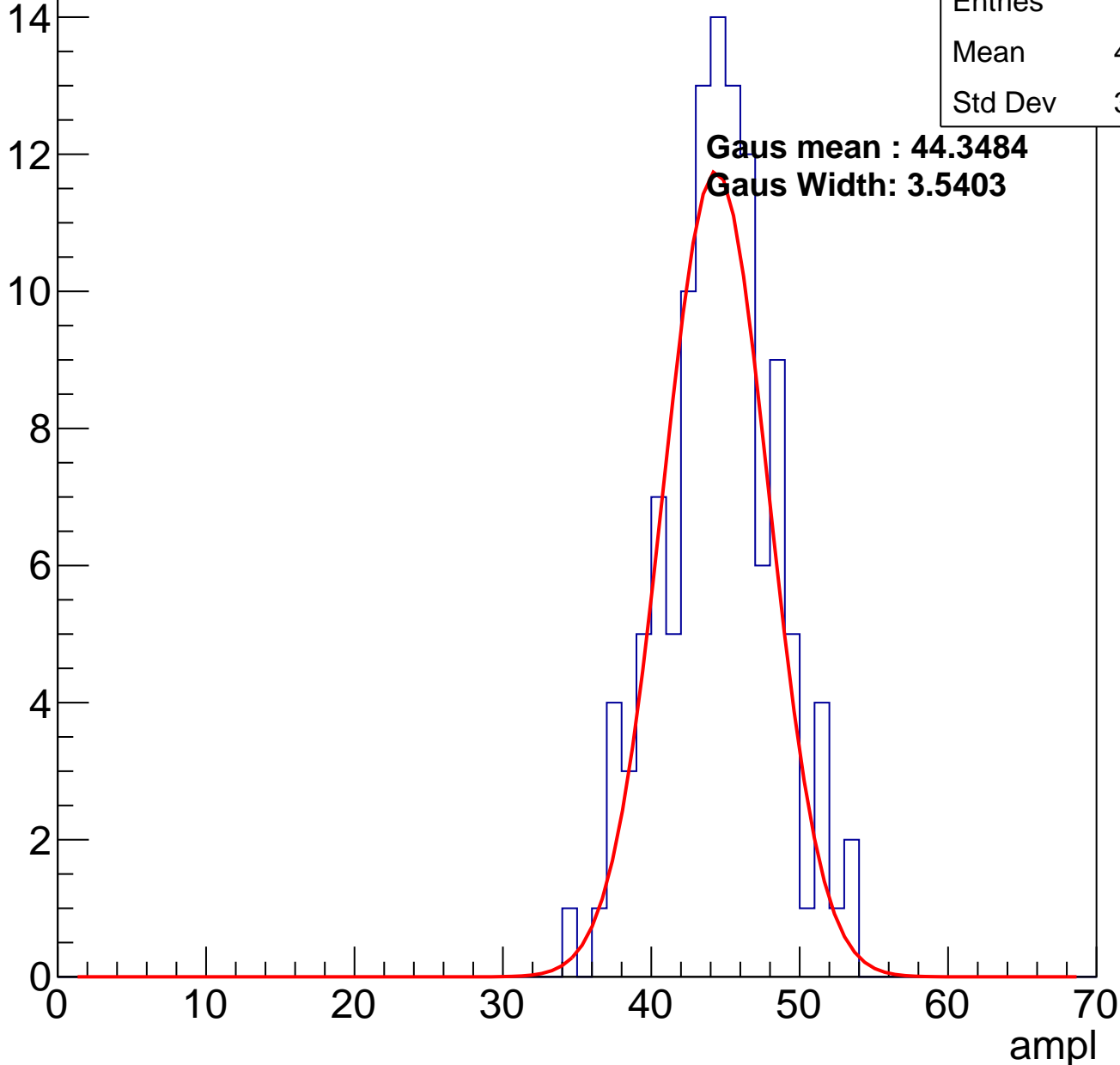
Entry



# B0L101S, U23-ch87, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

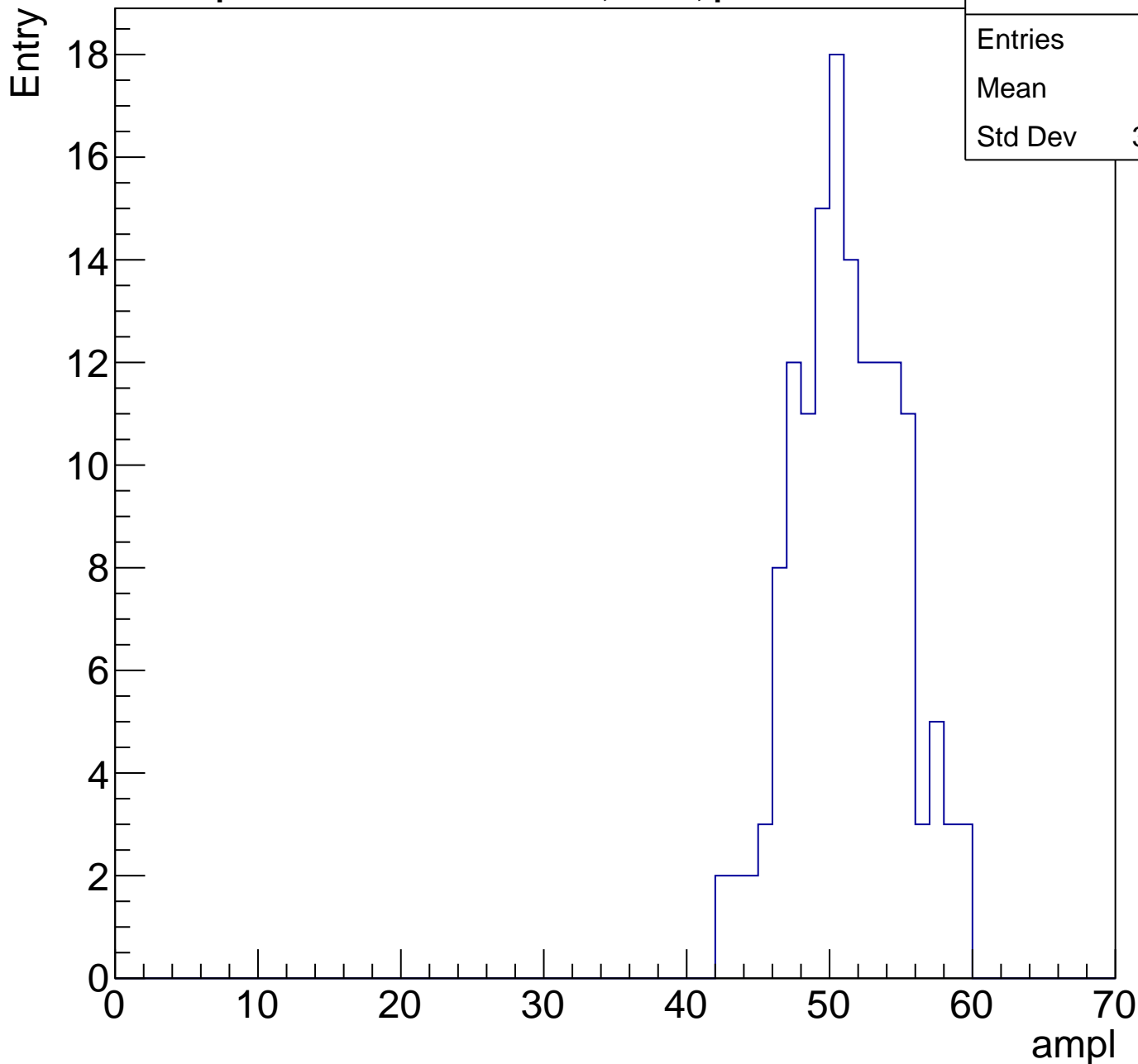
Entry



# B0L101S, U23-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	50.8
Std Dev	3.674



# B0L101S, U23-ch87, adc4

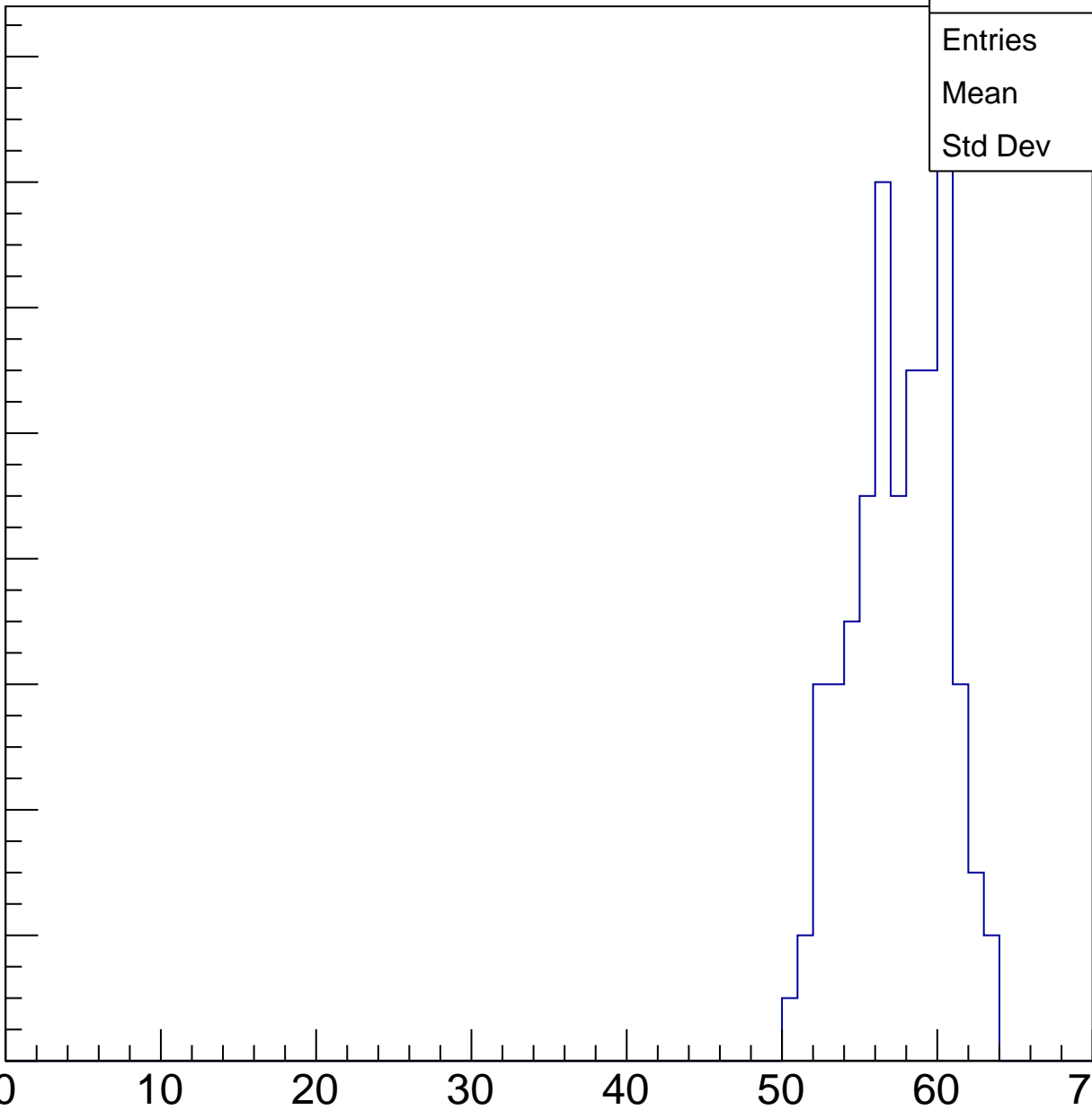
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	103
Mean	57.06
Std Dev	2.991

ampl



# B0L101S, U23-ch87, adc5

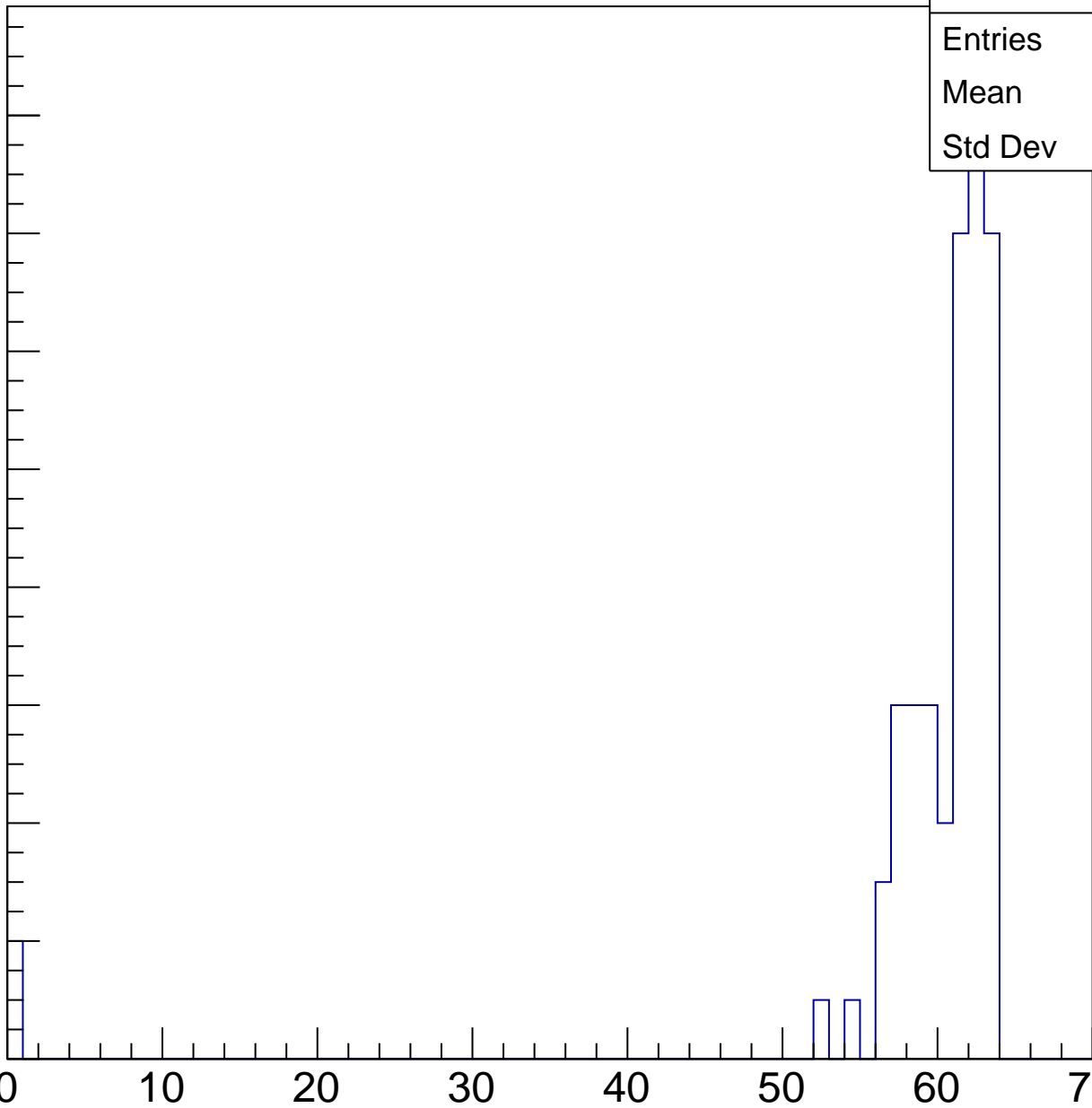
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	74
Mean	58.76
Std Dev	10.08

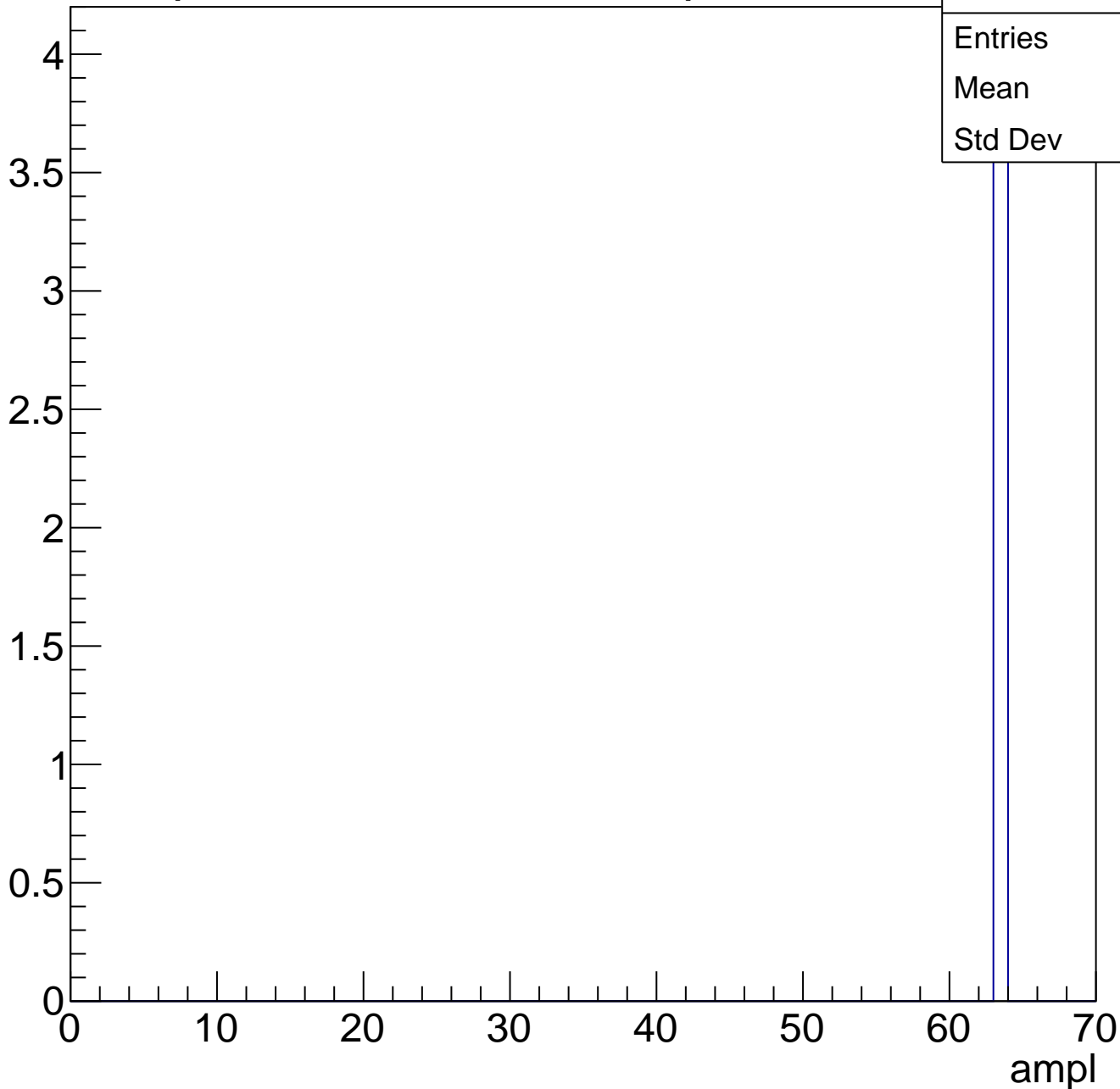
ampl



# B0L101S, U23-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch88, adc0

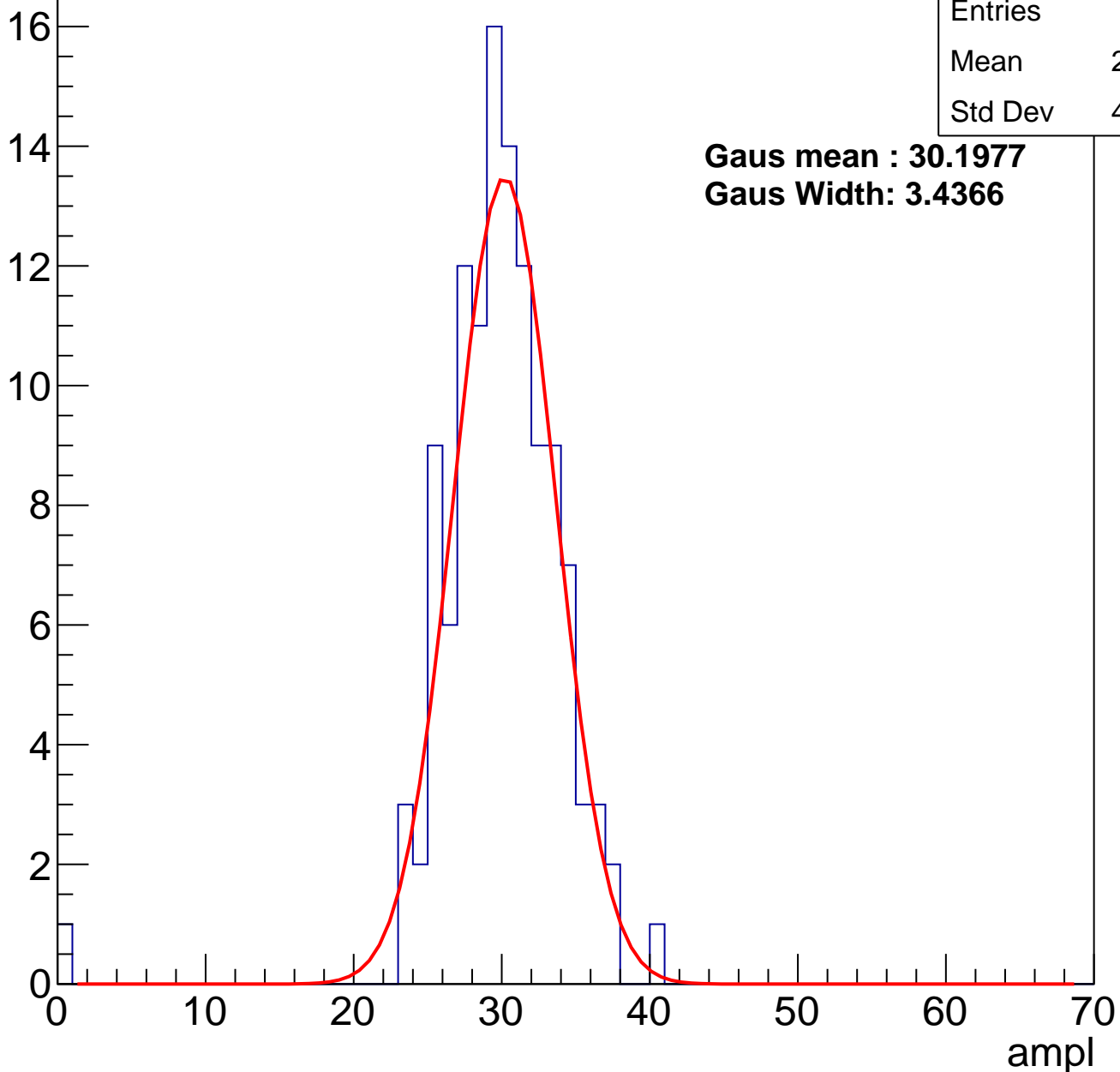
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	29.47
Std Dev	4.278

**Gaus mean : 30.1977**

**Gaus Width: 3.4366**

Entry

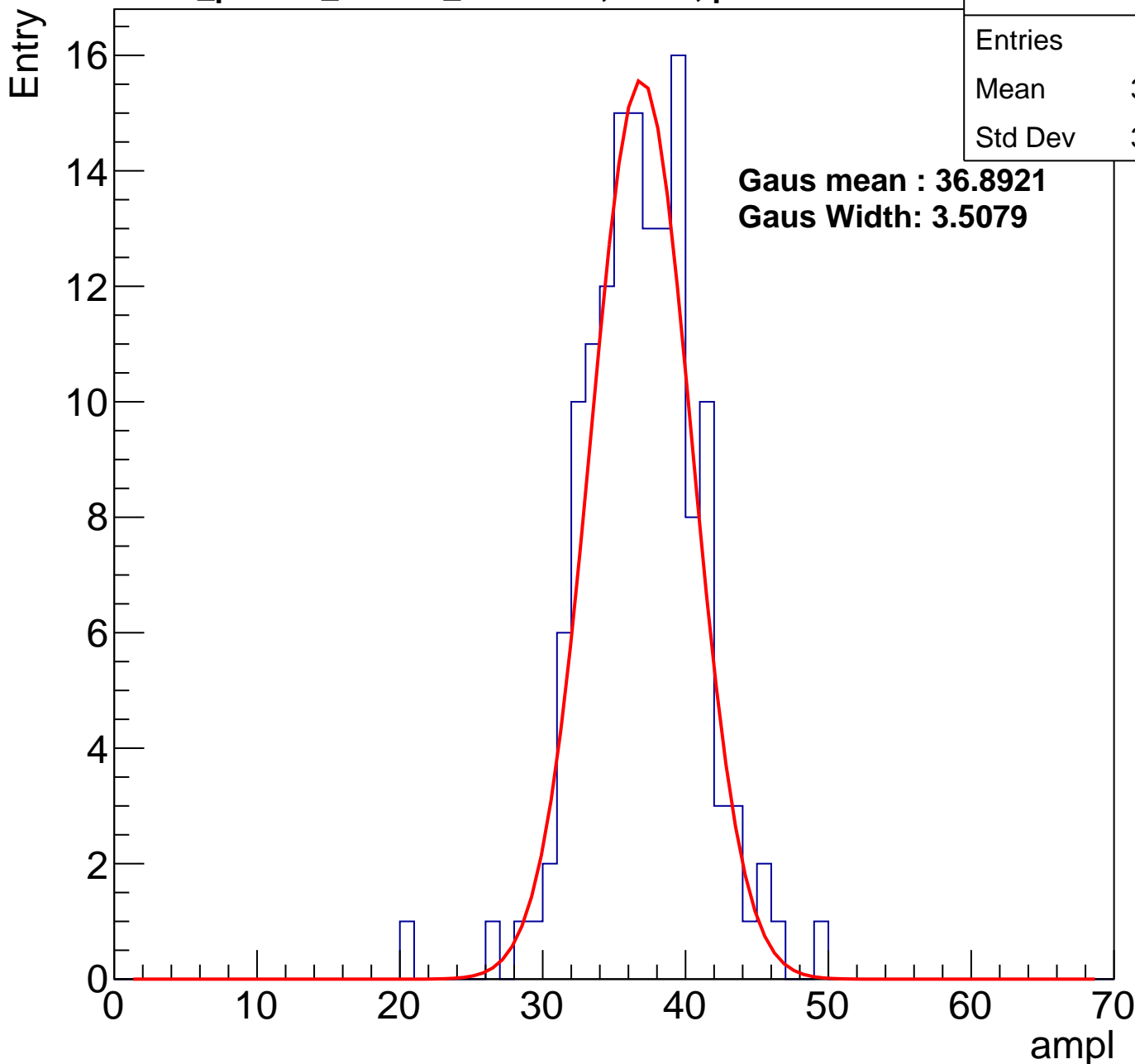


# B0L101S, U23-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	36.42
Std Dev	3.989

**Gaus mean : 36.8921**  
**Gaus Width: 3.5079**



# B0L101S, U23-ch88, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

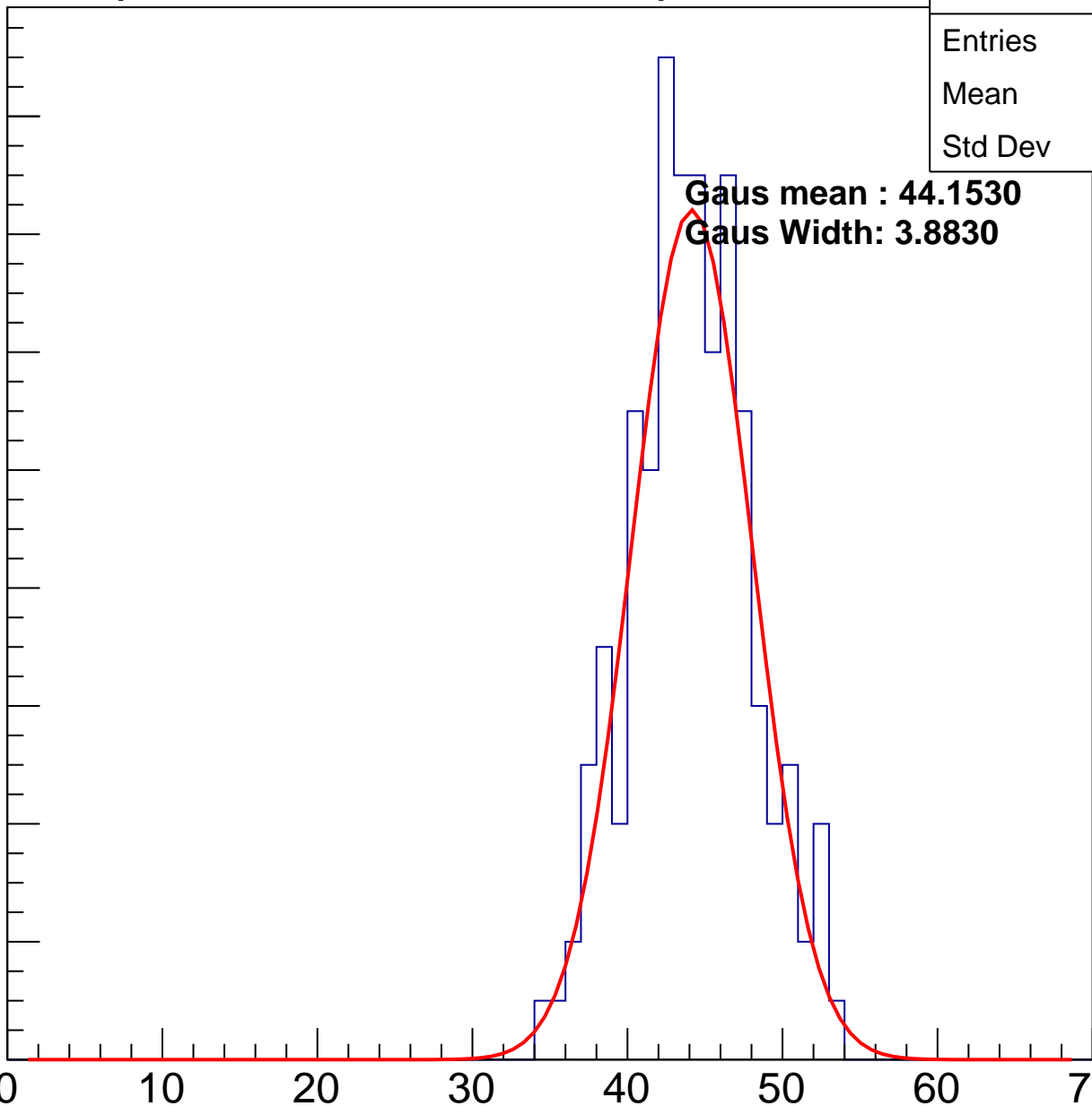
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	148
Mean	43.66
Std Dev	3.872

**Gaus mean : 44.1530**

**Gaus Width: 3.8830**

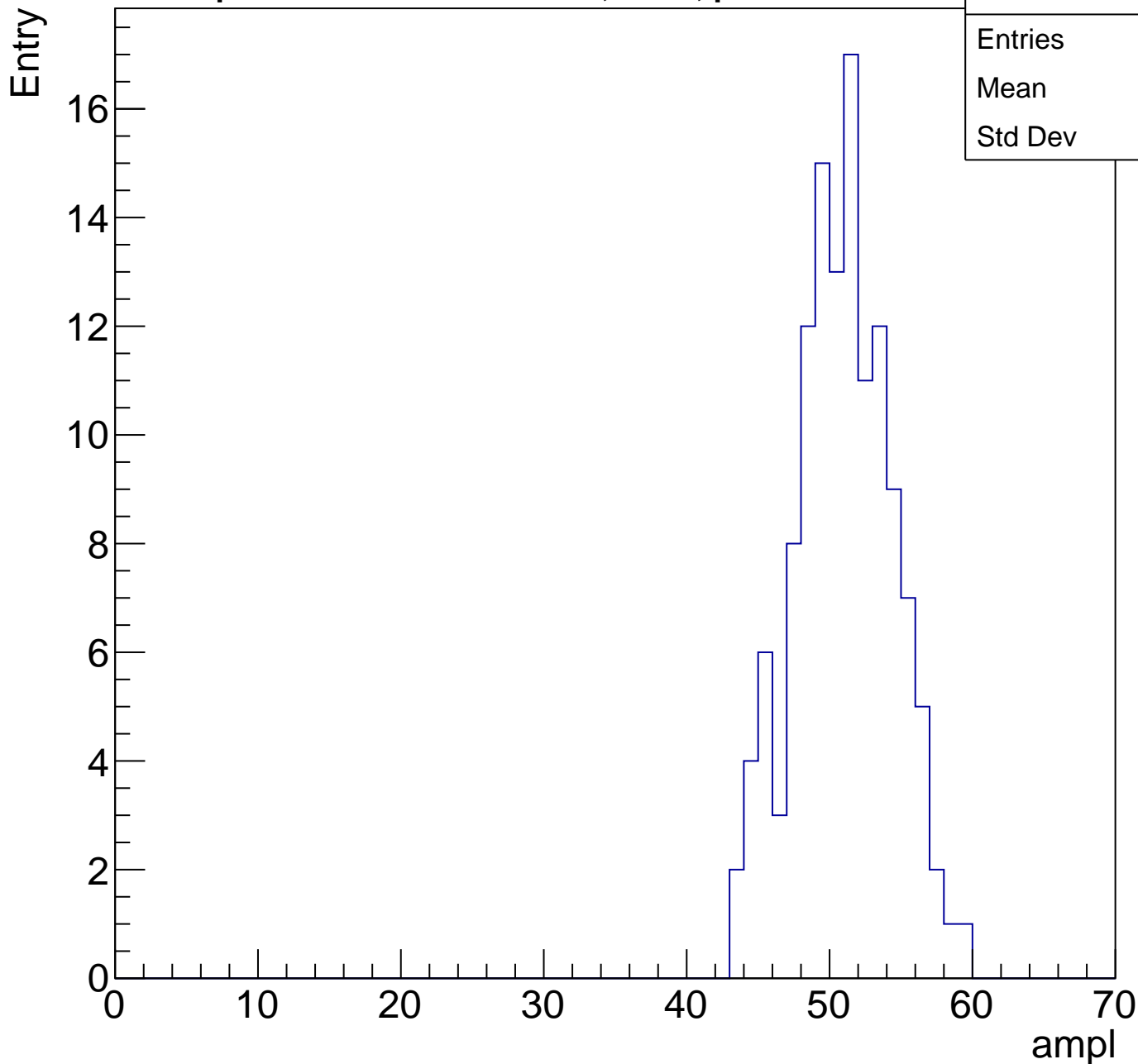
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	50.5
Std Dev	3.37



# B0L101S, U23-ch88, adc4

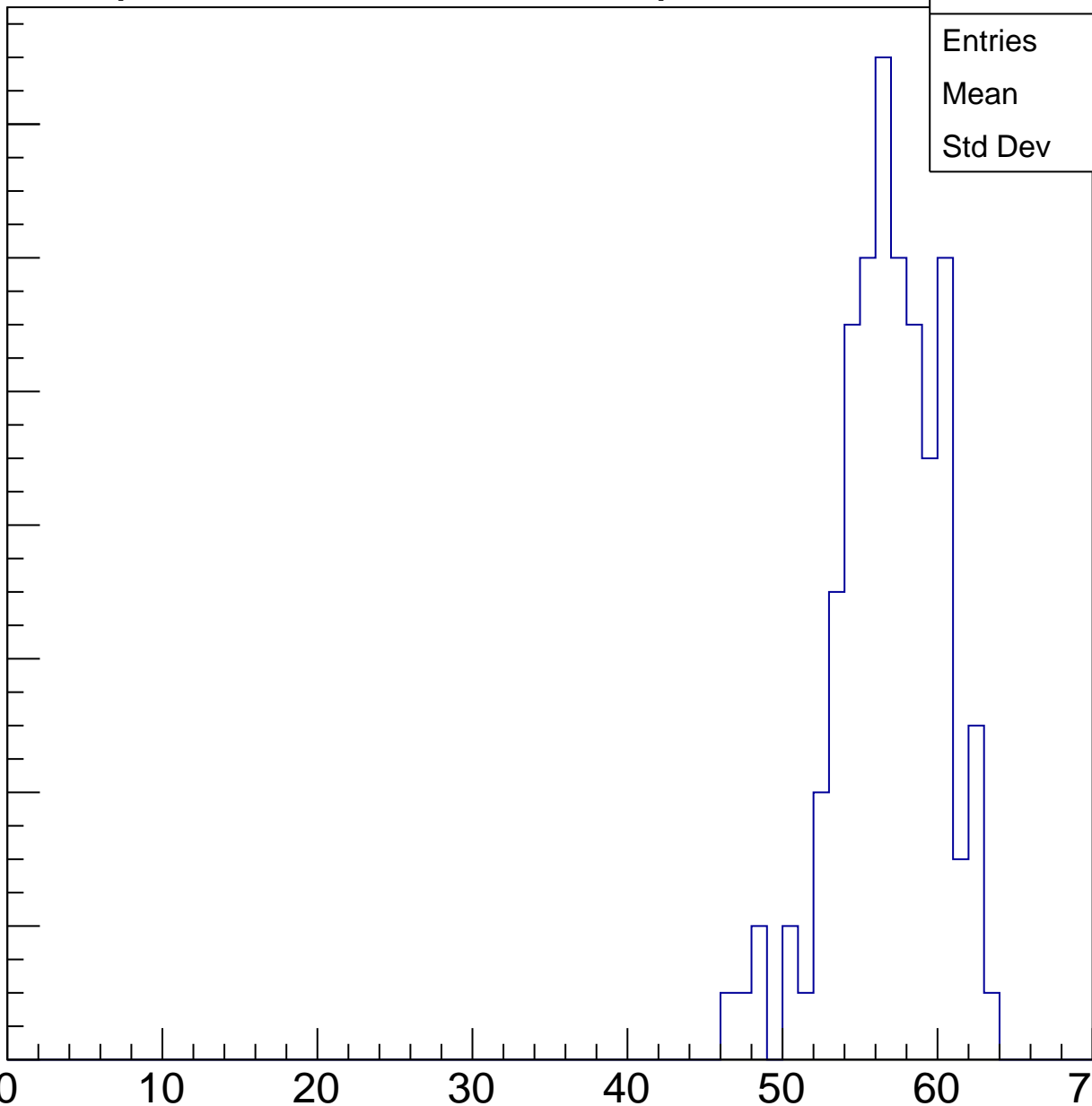
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	109
Mean	56.35
Std Dev	3.33

ampl



# B0L101S, U23-ch88, adc5

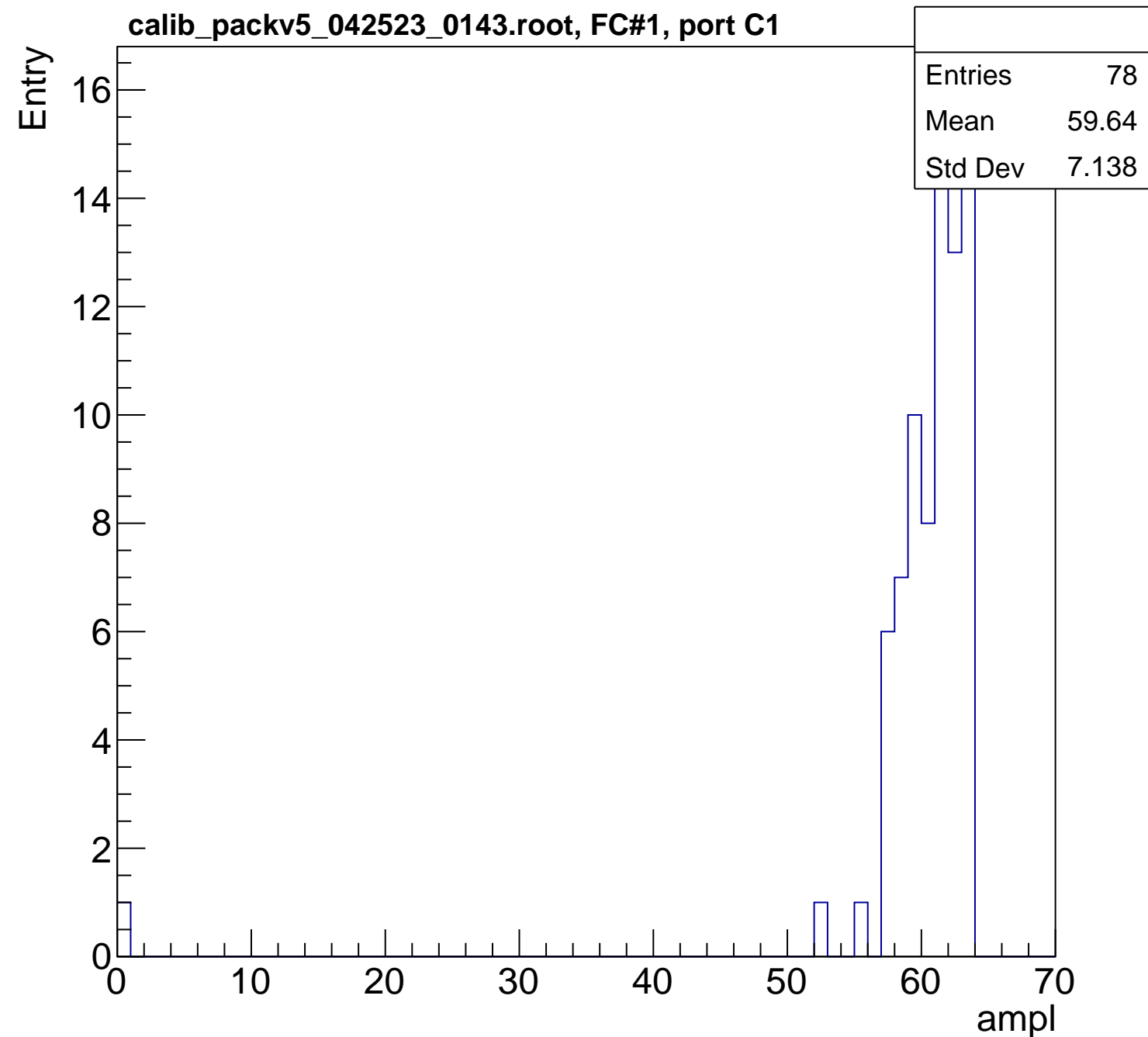
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	78
Mean	59.64
Std Dev	7.138

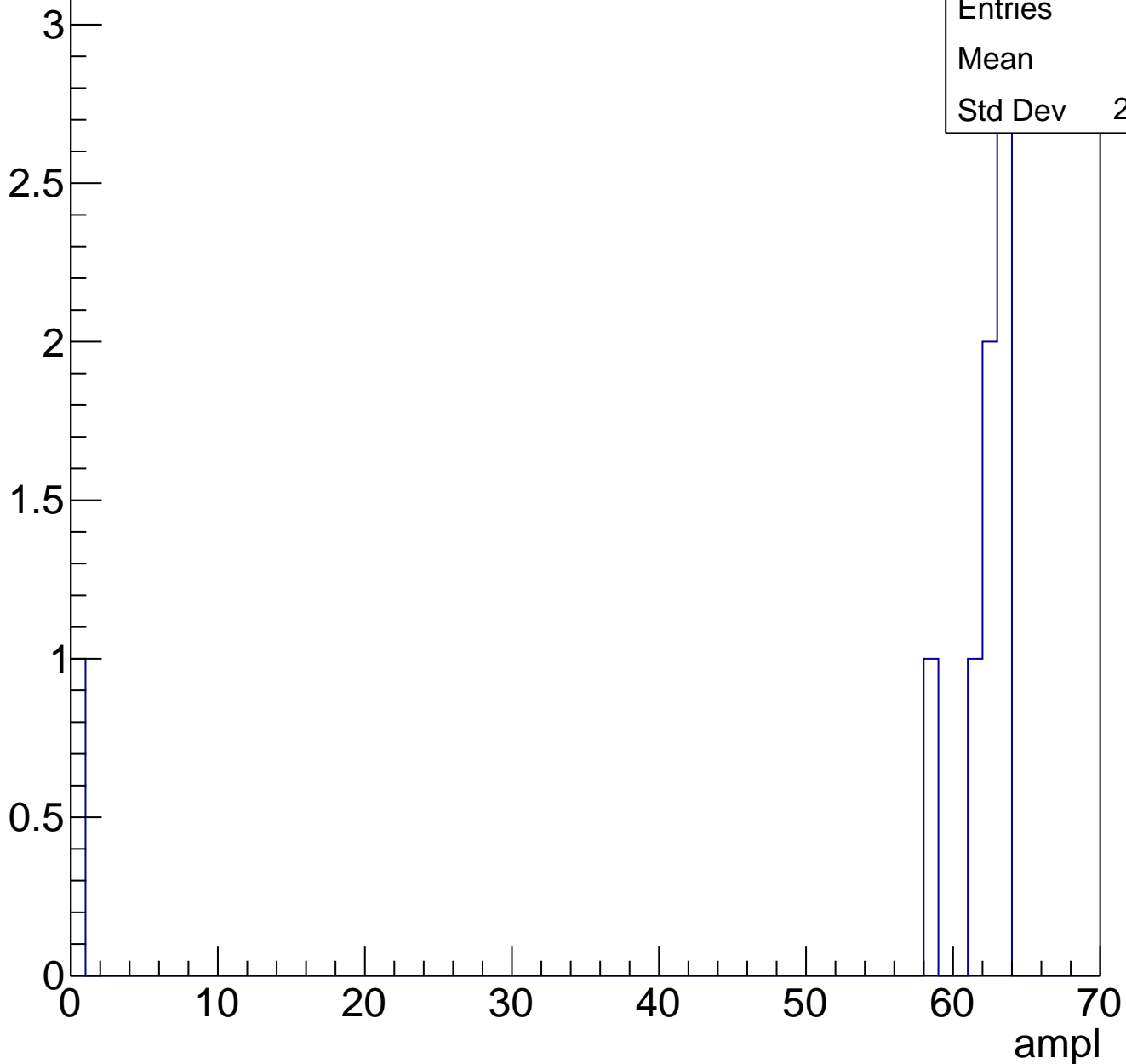
ampl



# B0L101S, U23-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch89, adc0

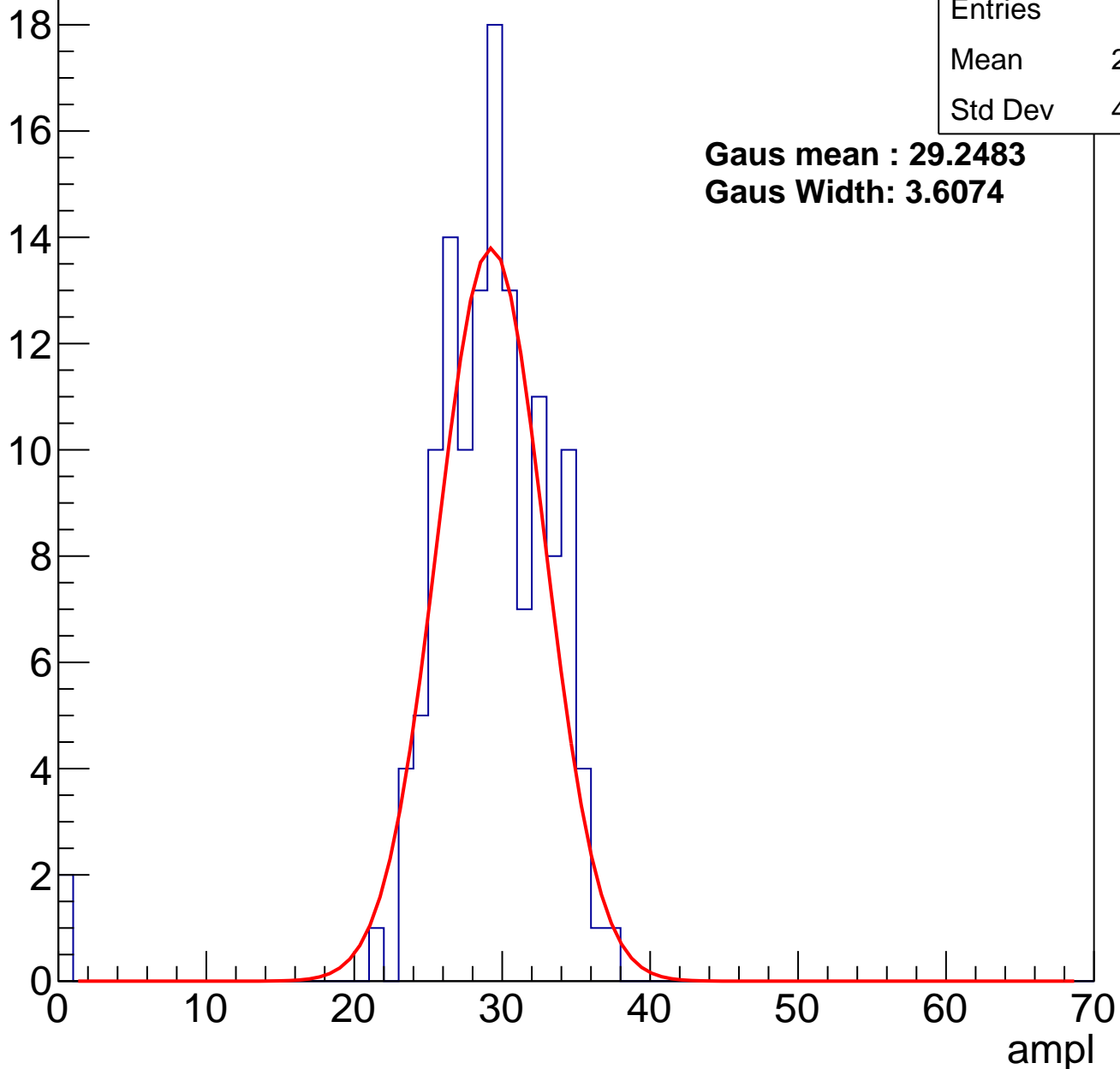
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	28.63
Std Dev	4.855

**Gaus mean : 29.2483**

**Gaus Width: 3.6074**

Entry



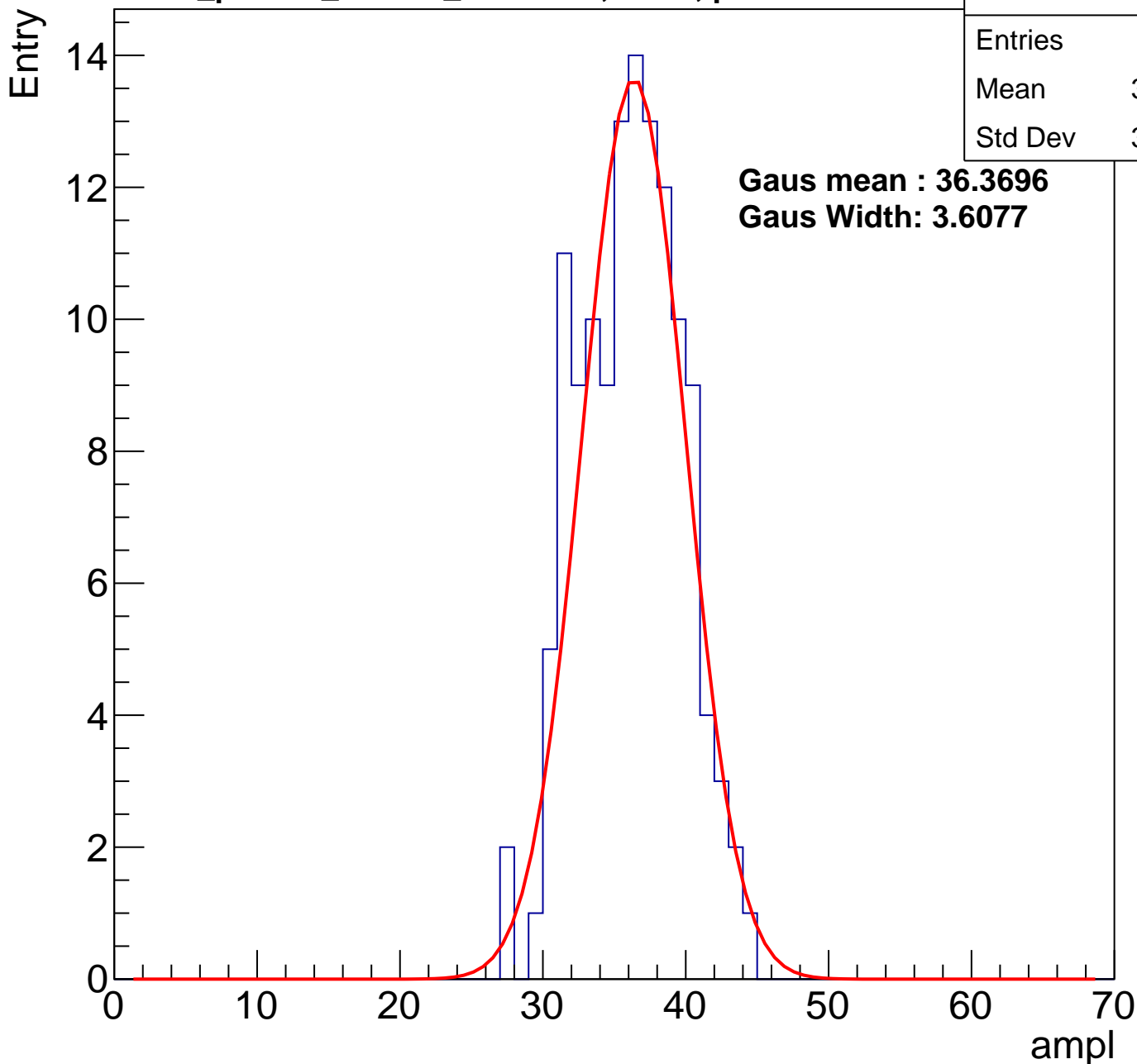
# B0L101S, U23-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	35.66
Std Dev	3.534

**Gaus mean : 36.3696**

**Gaus Width: 3.6077**



# B0L101S, U23-ch89, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

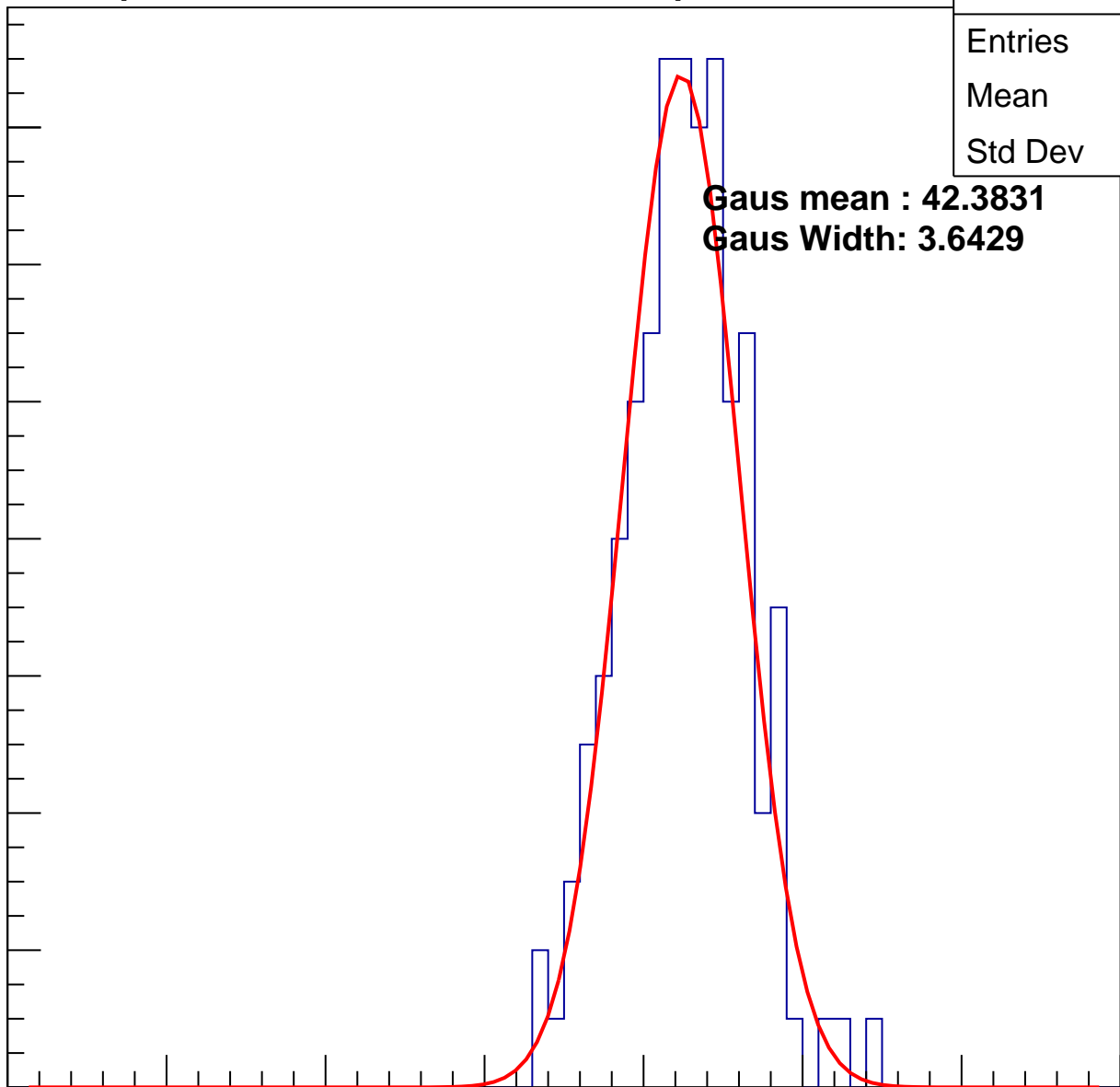
70

ampl

Entries	141
Mean	42.09
Std Dev	3.808

**Gaus mean : 42.3831**

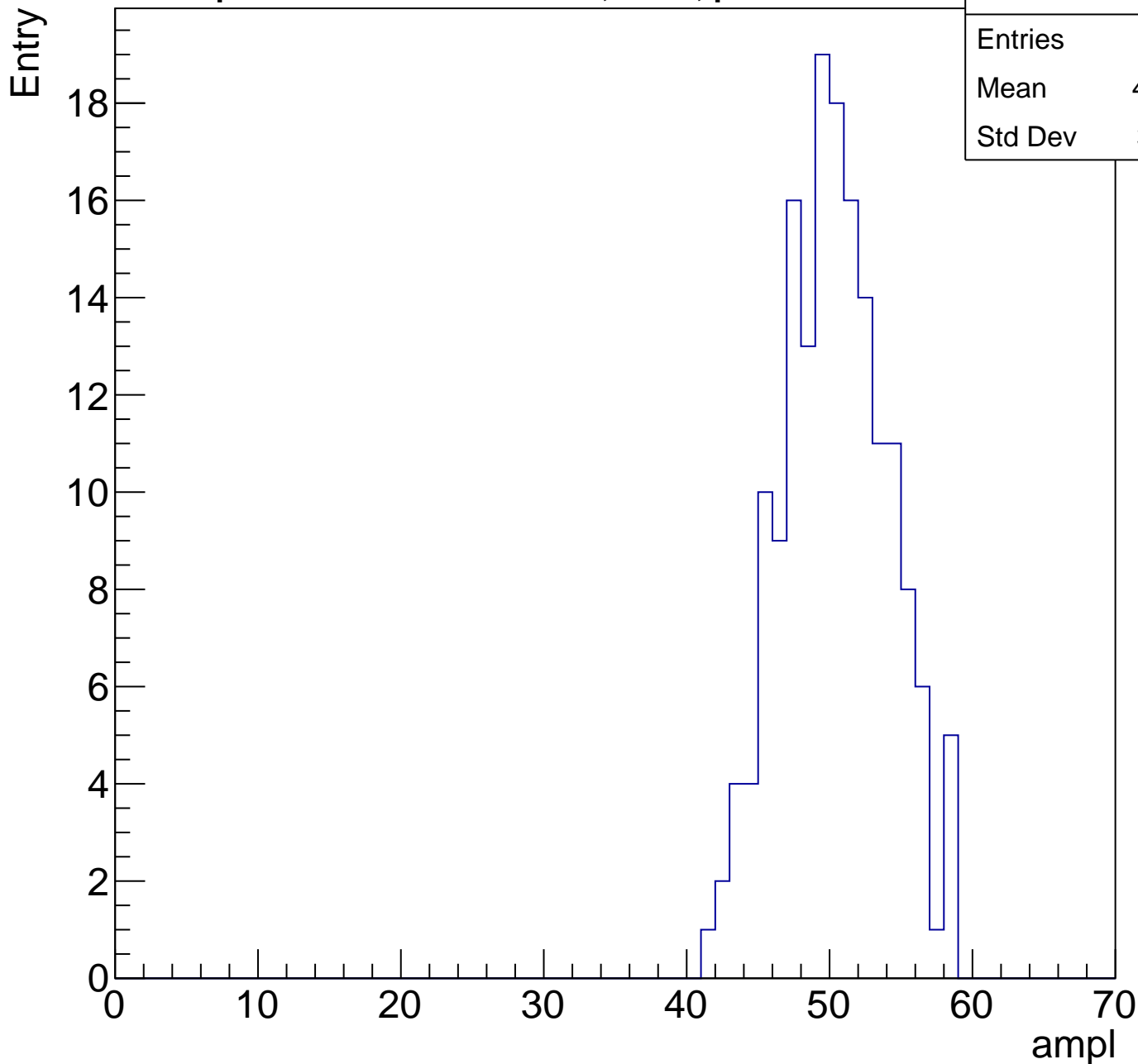
**Gaus Width: 3.6429**



# B0L101S, U23-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	168
Mean	49.93
Std Dev	3.691



# B0L101S, U23-ch89, adc4

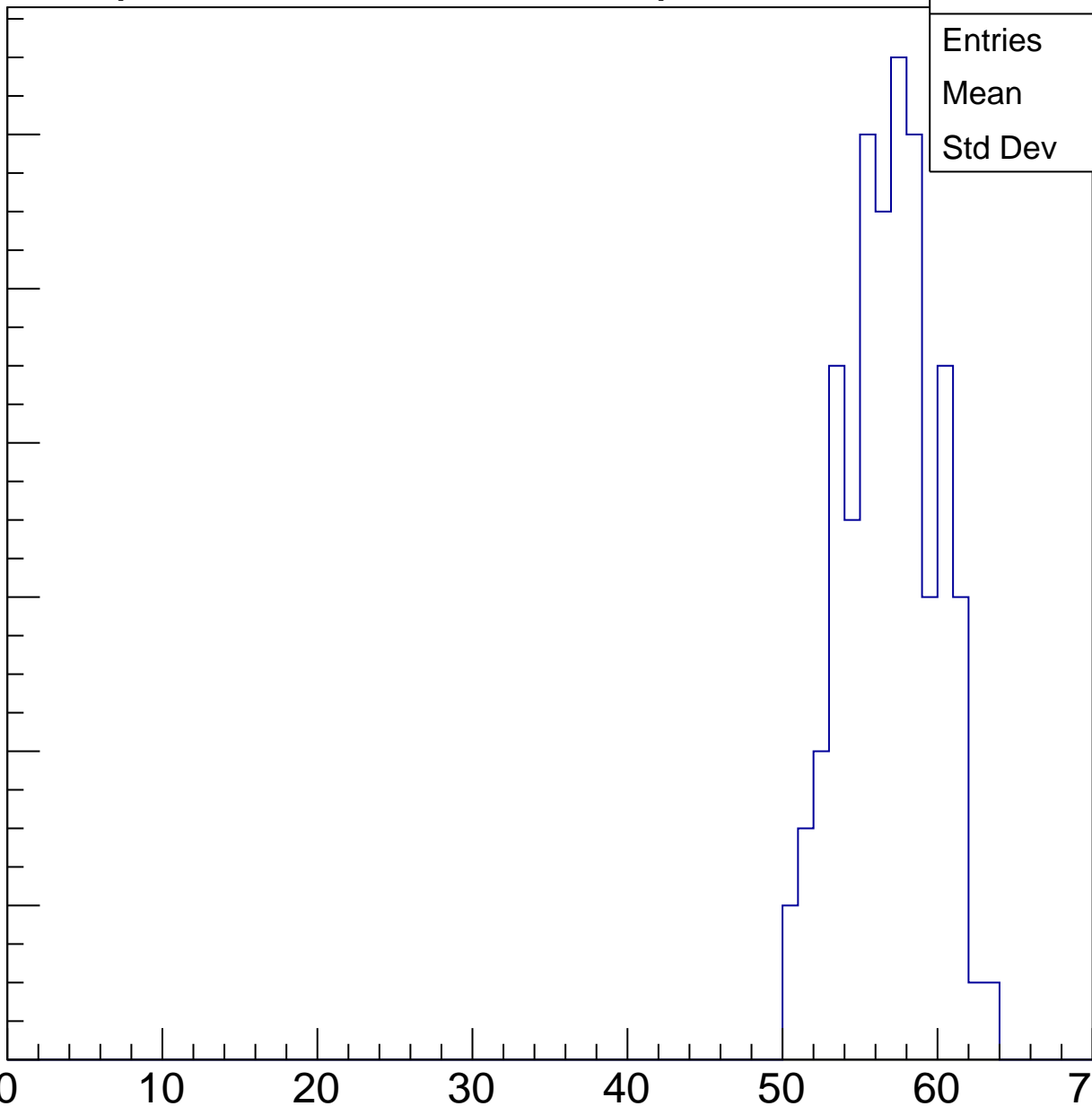
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	96
Mean	56.4
Std Dev	2.906

ampl



# B0L101S, U23-ch89, adc5

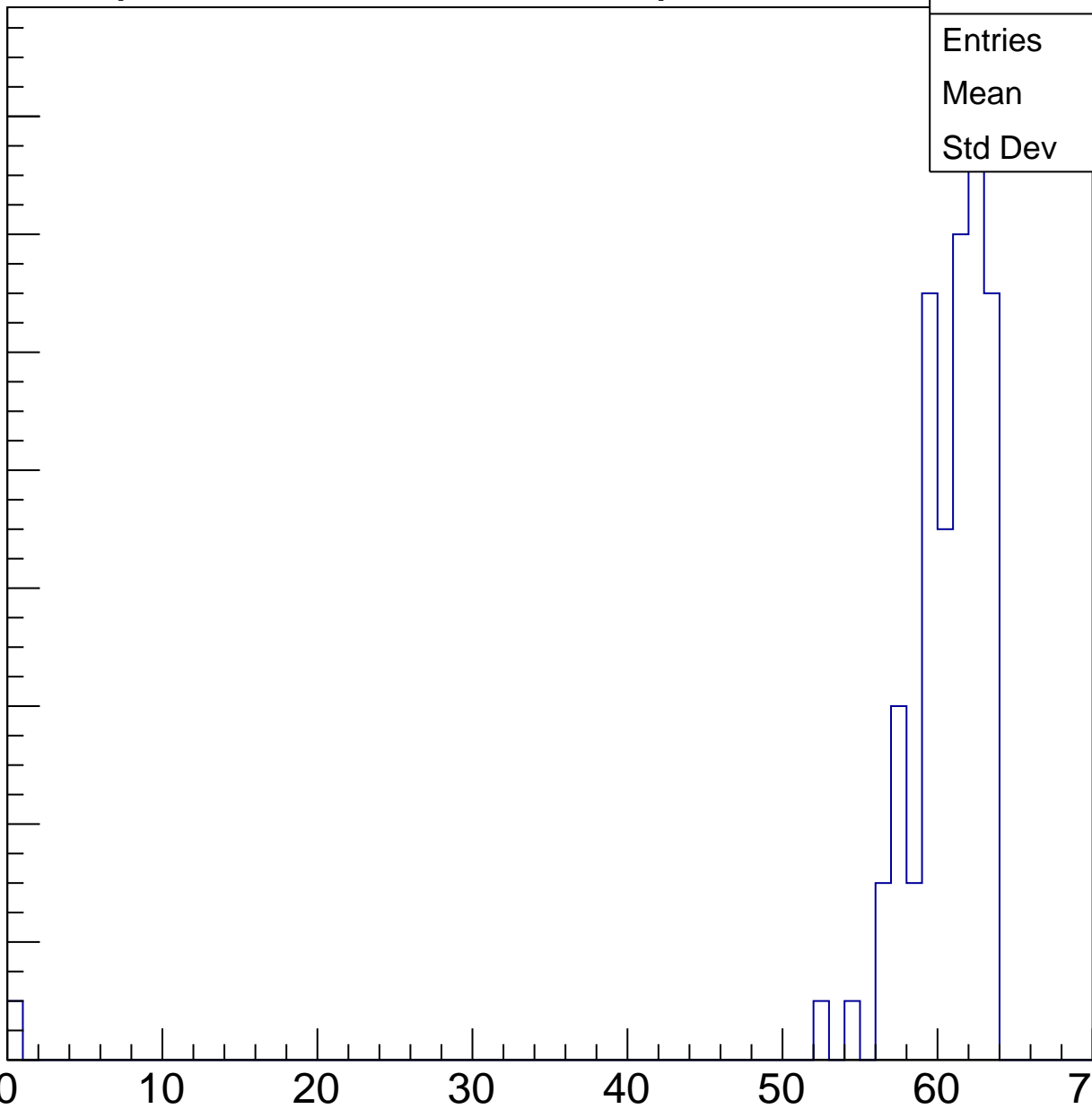
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	59.56
Std Dev	7.036

ampl

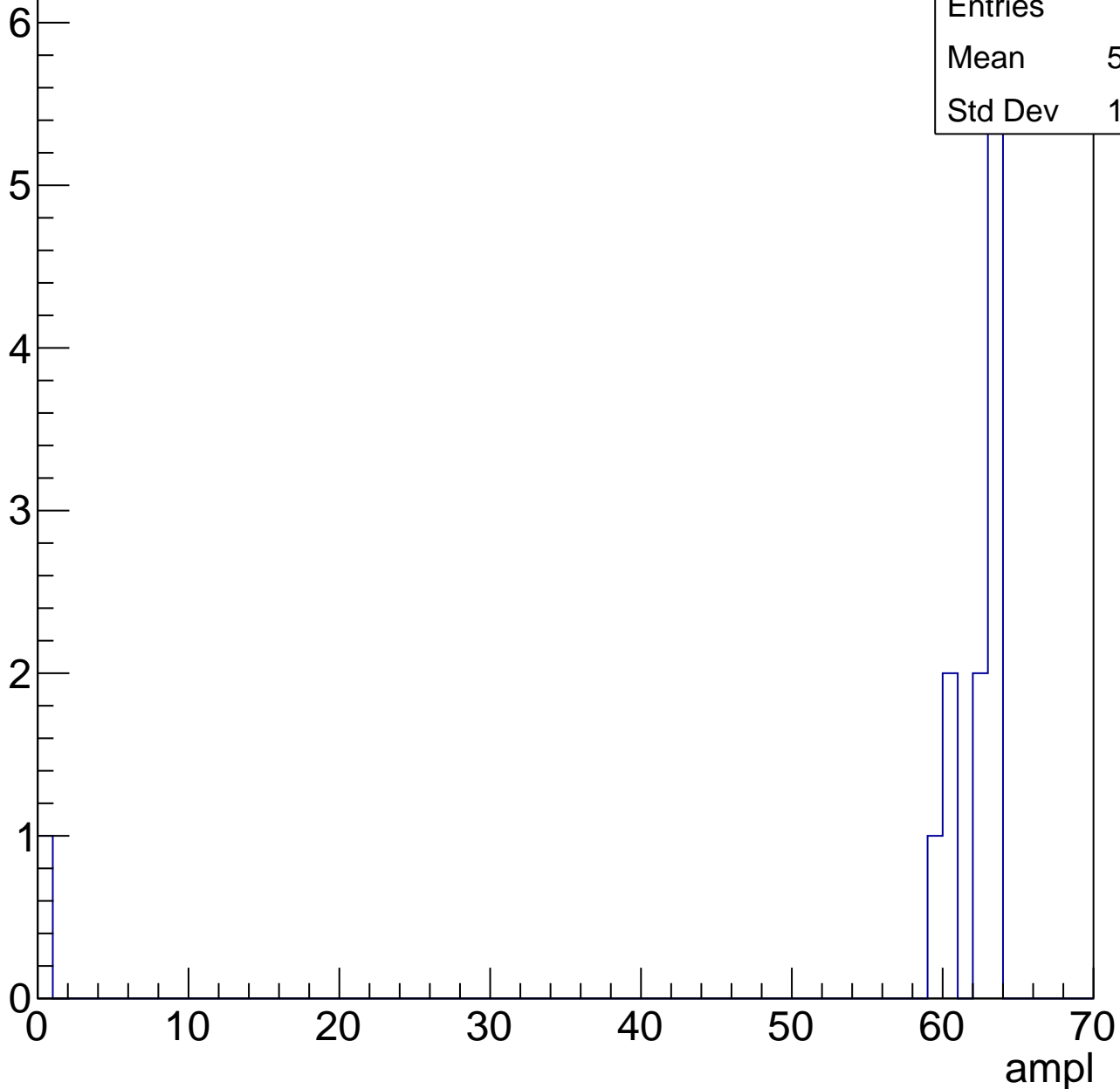


# B0L101S, U23-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.75
Std Dev	17.17





# B0L101S, U23-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L101S, U23-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	27.78
Std Dev	4.661

**Gaus mean : 28.4501**

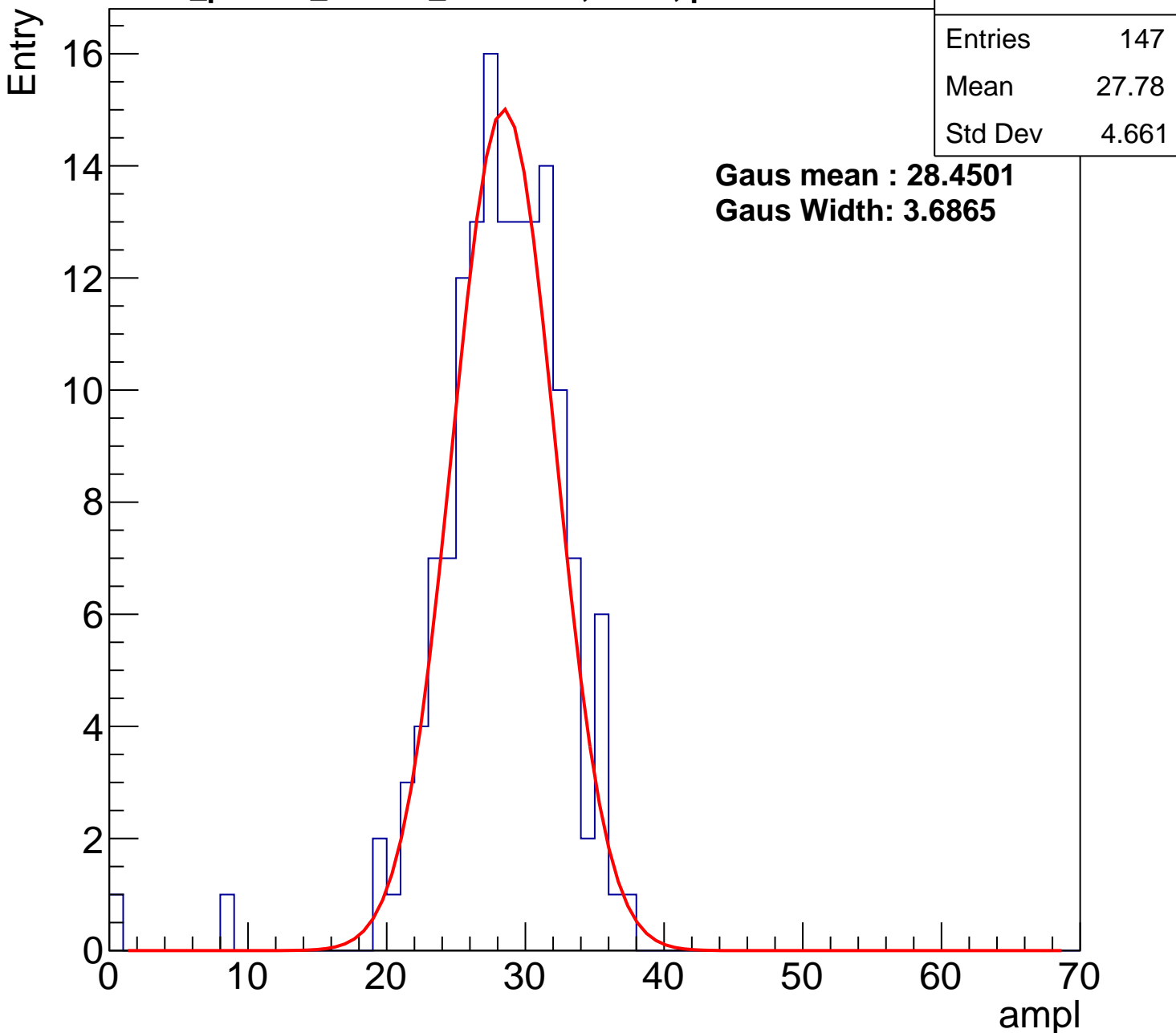
**Gaus Width: 3.6865**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



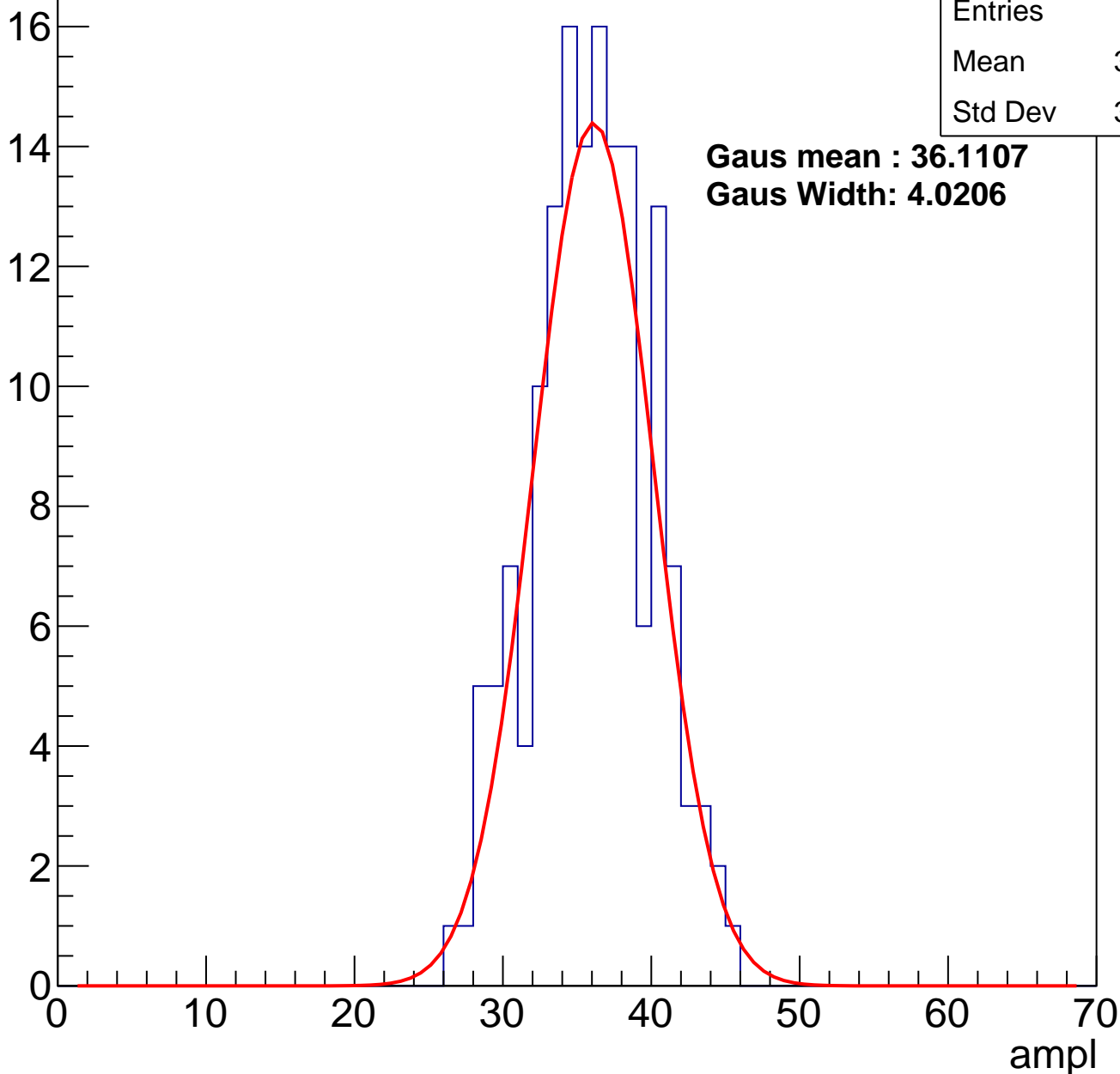
# B0L101S, U23-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	35.55
Std Dev	3.929

**Gaus mean : 36.1107**  
**Gaus Width: 4.0206**

Entry



# B0L101S, U23-ch90, adc2

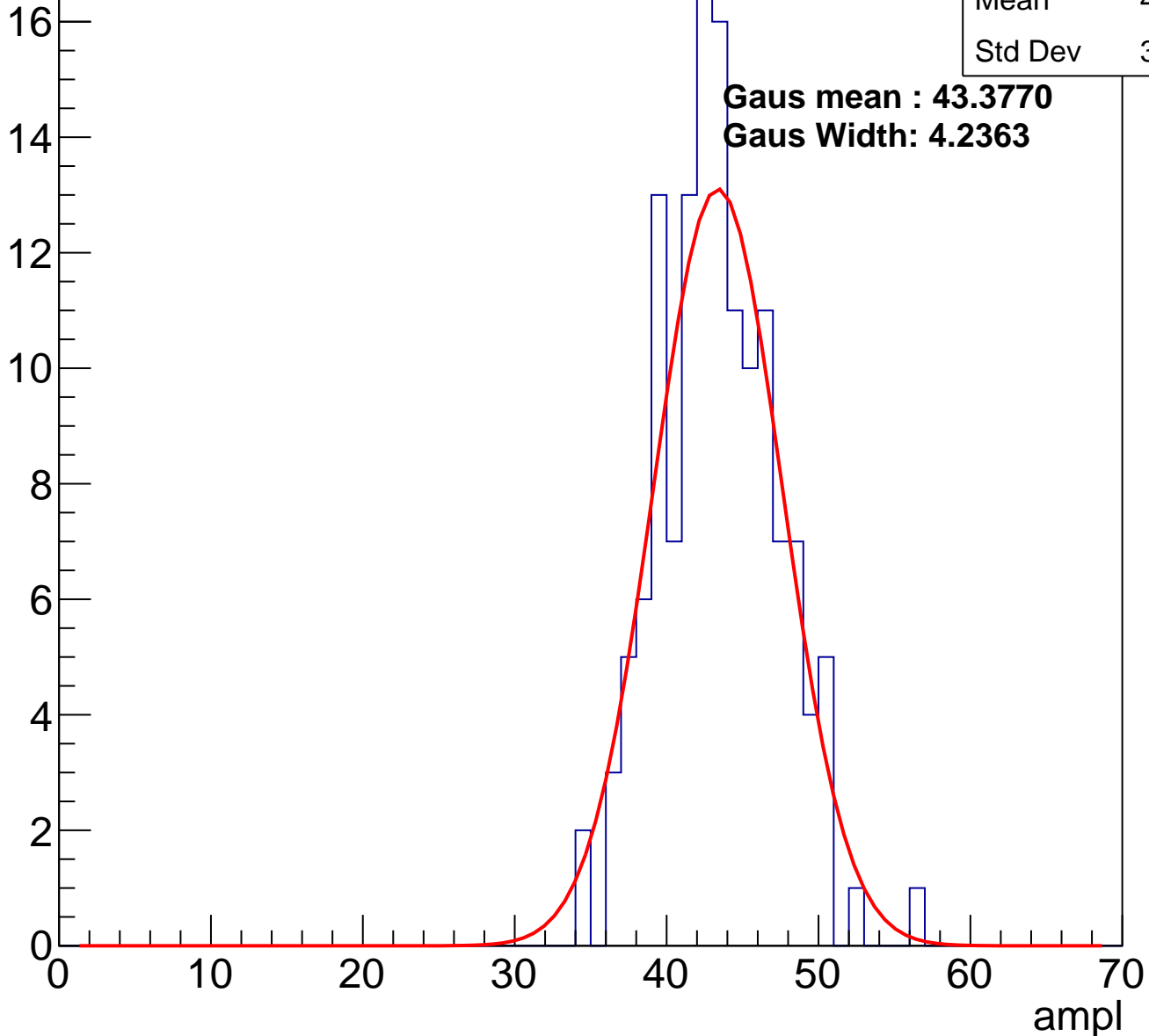
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	139
Mean	42.95
Std Dev	3.845

**Gaus mean : 43.3770**

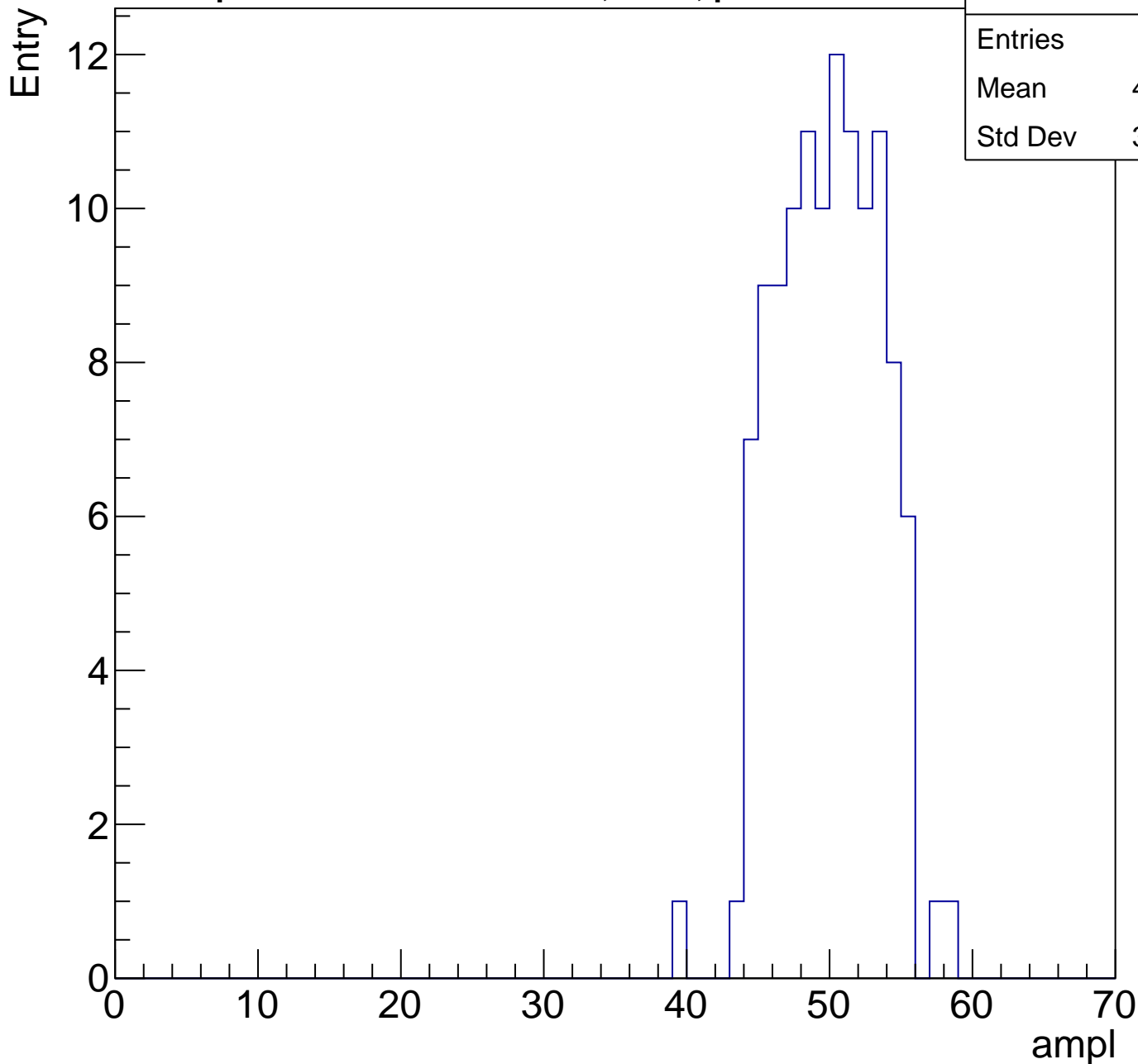
**Gaus Width: 4.2363**



# B0L101S, U23-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	49.47
Std Dev	3.497



# B0L101S, U23-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	55.12
Std Dev	6.026

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

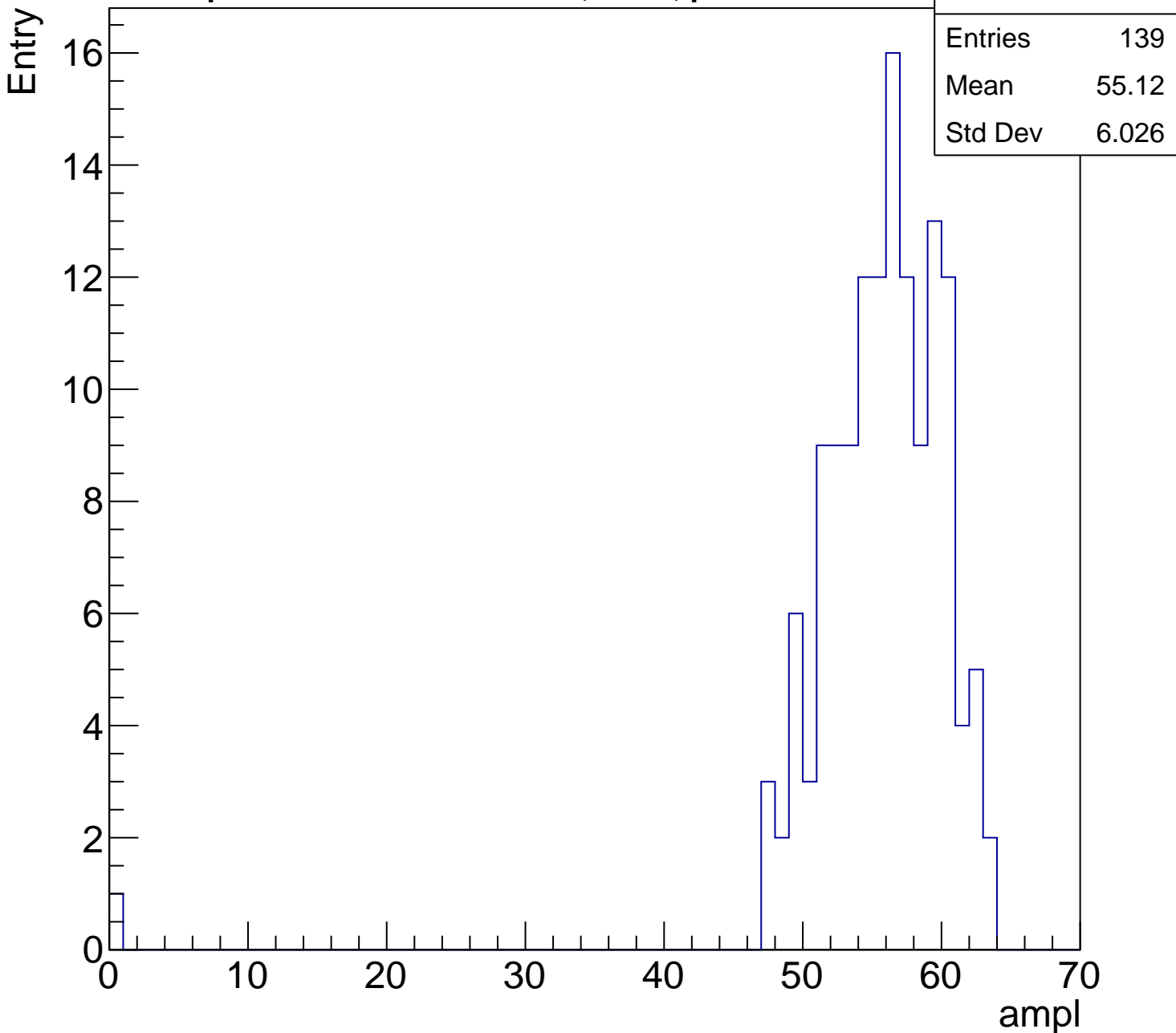
40

50

60

70

ampl



# B0L101S, U23-ch90, adc5

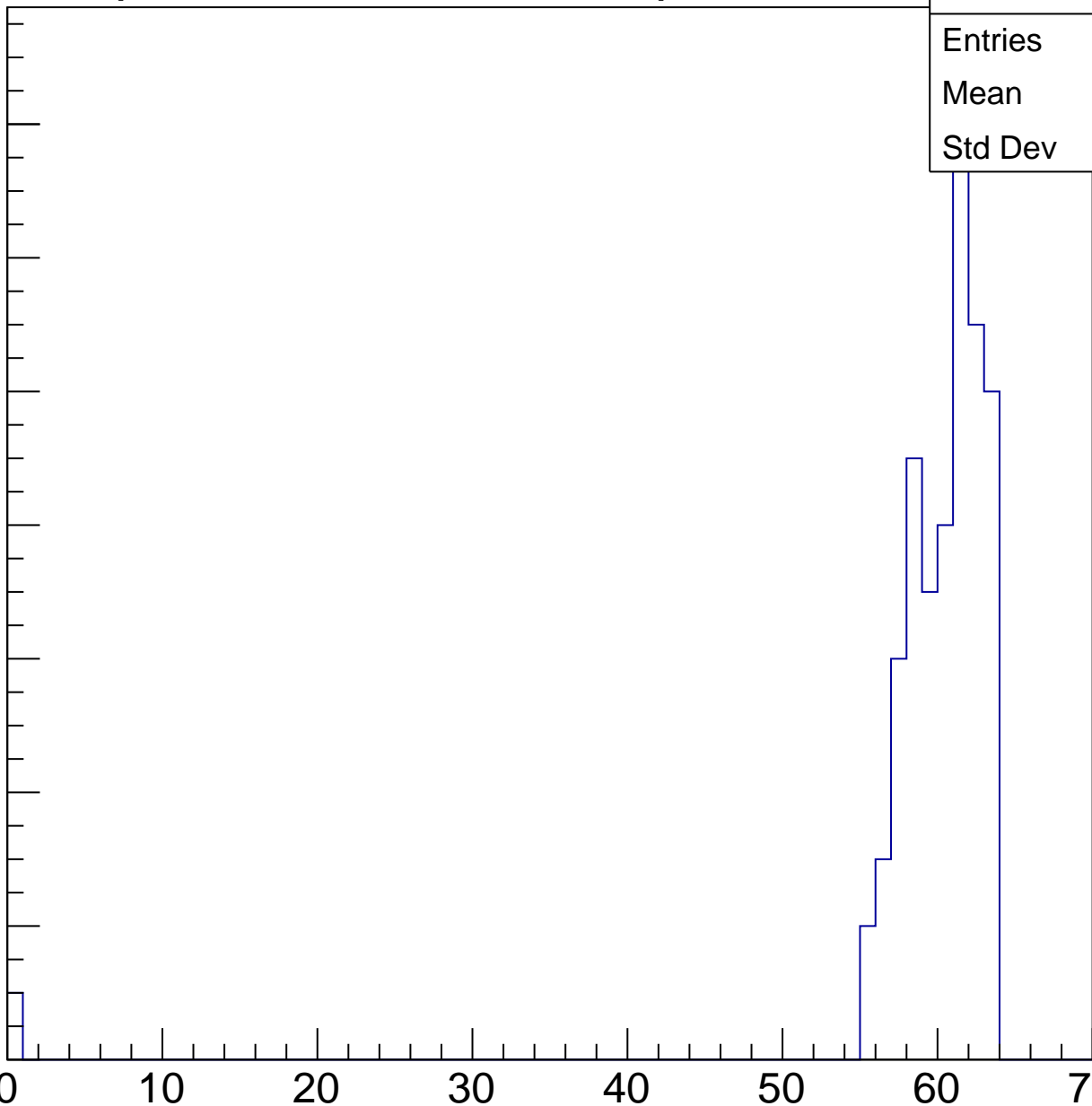
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	59.19
Std Dev	7.357

ampl

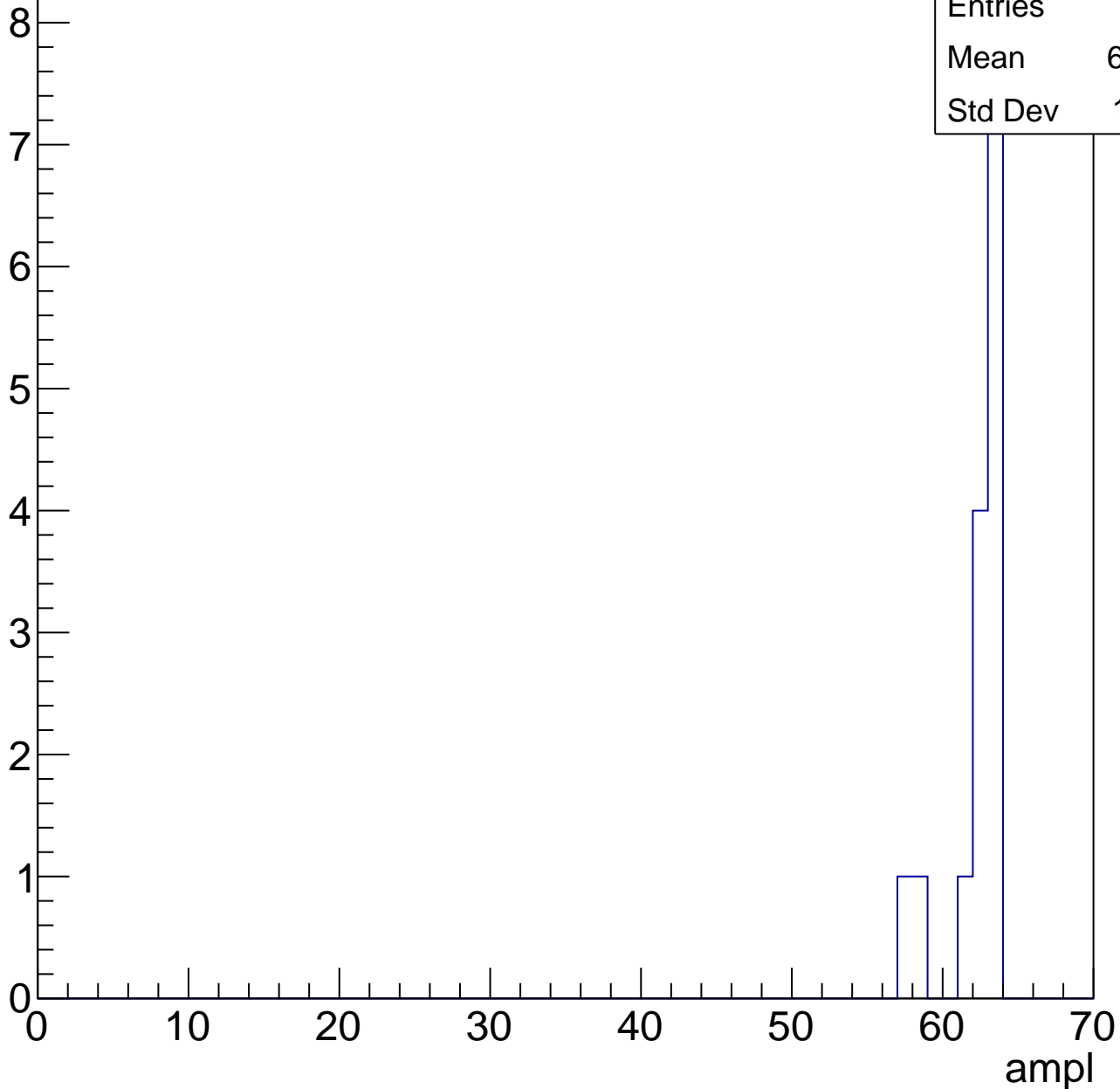


# B0L101S, U23-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.87
Std Dev	1.821

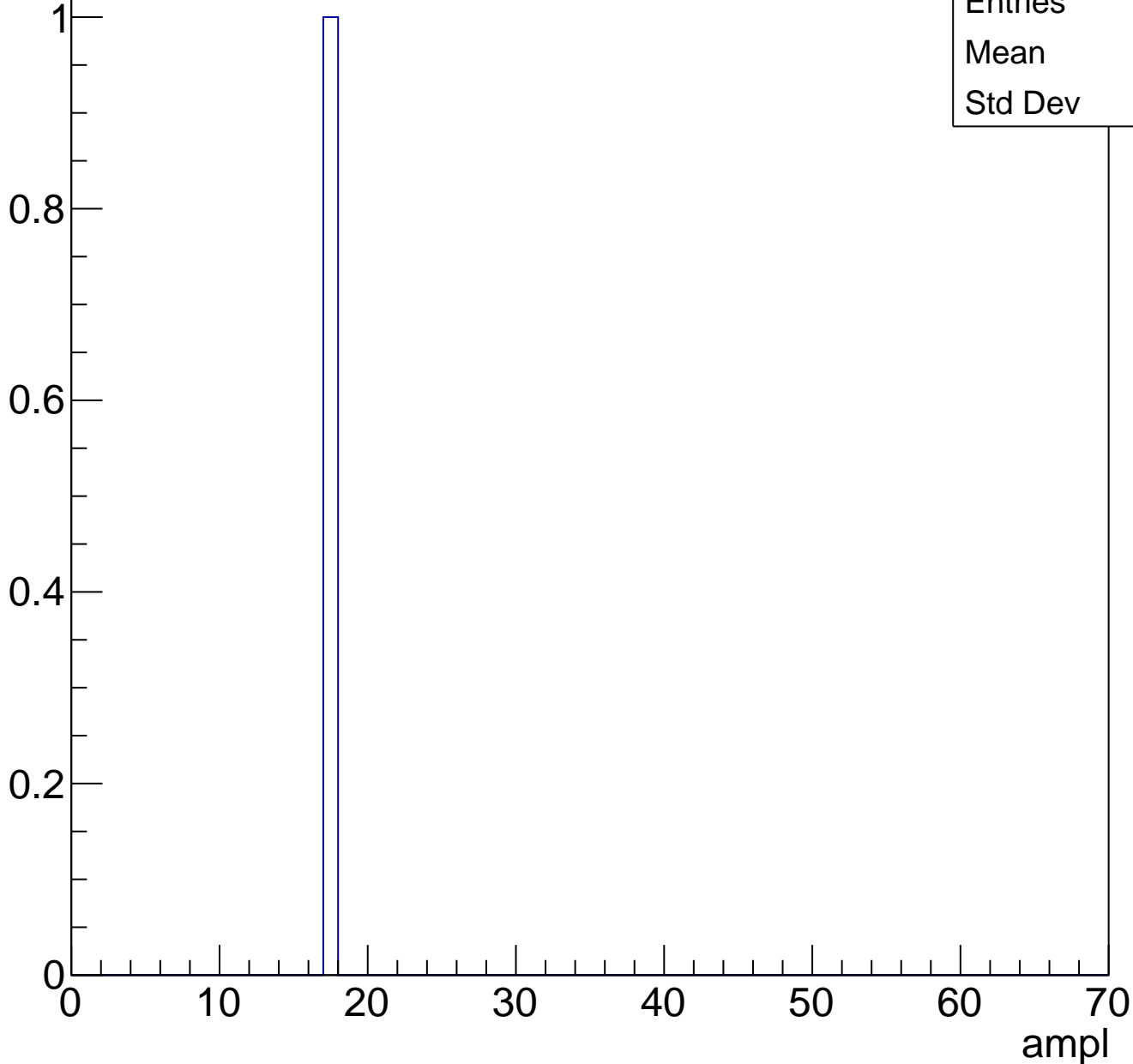




# B0L101S, U23-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch91, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	29.12
Std Dev	4.345

**Gaus mean : 29.6988**

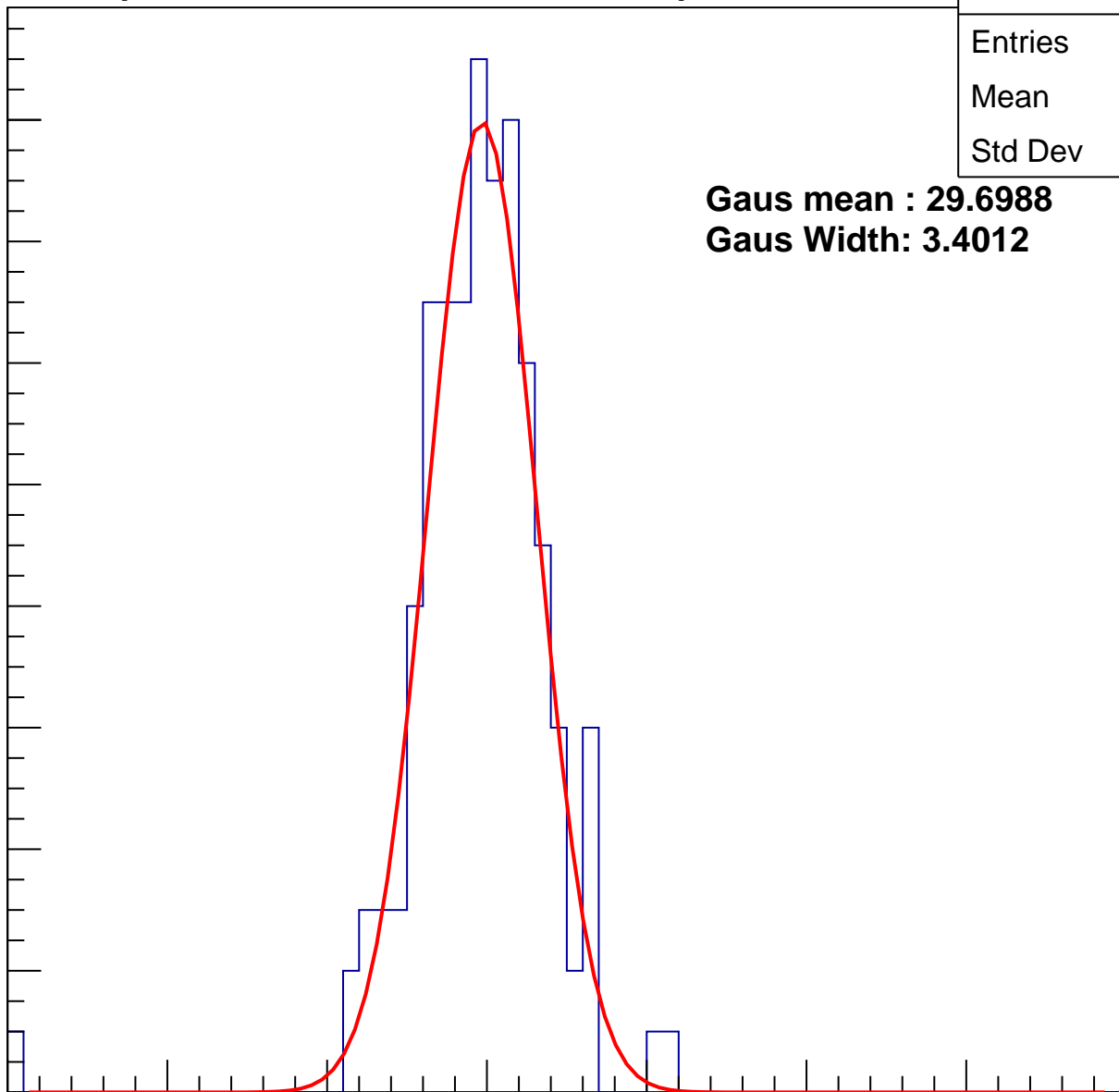
**Gaus Width: 3.4012**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch91, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	36.47
Std Dev	3.623

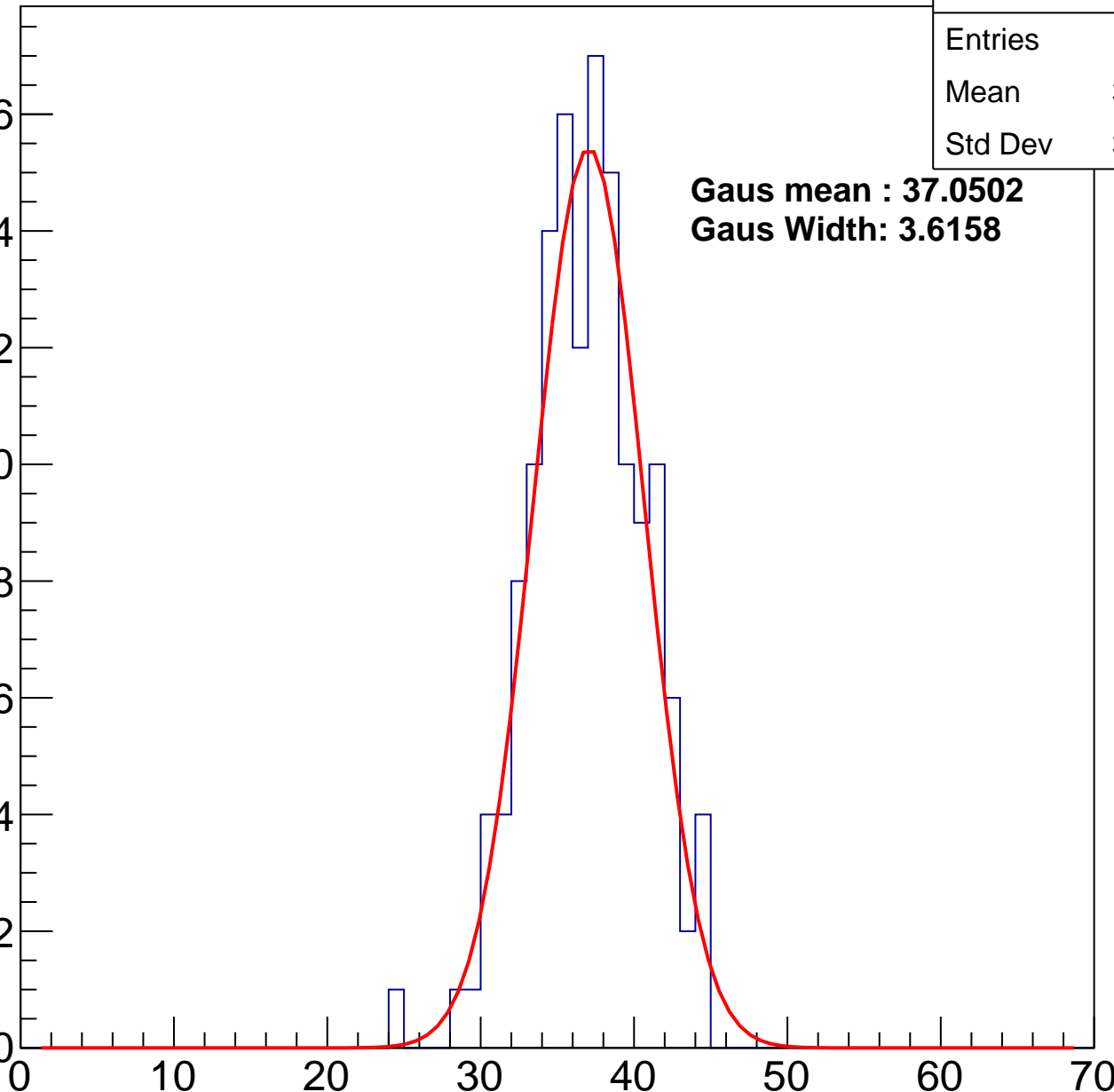
**Gaus mean : 37.0502**

**Gaus Width: 3.6158**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch91, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	119
Mean	42.9
Std Dev	3.544

**Gaus mean : 43.4903**

**Gaus Width: 3.6633**

0

2

4

6

8

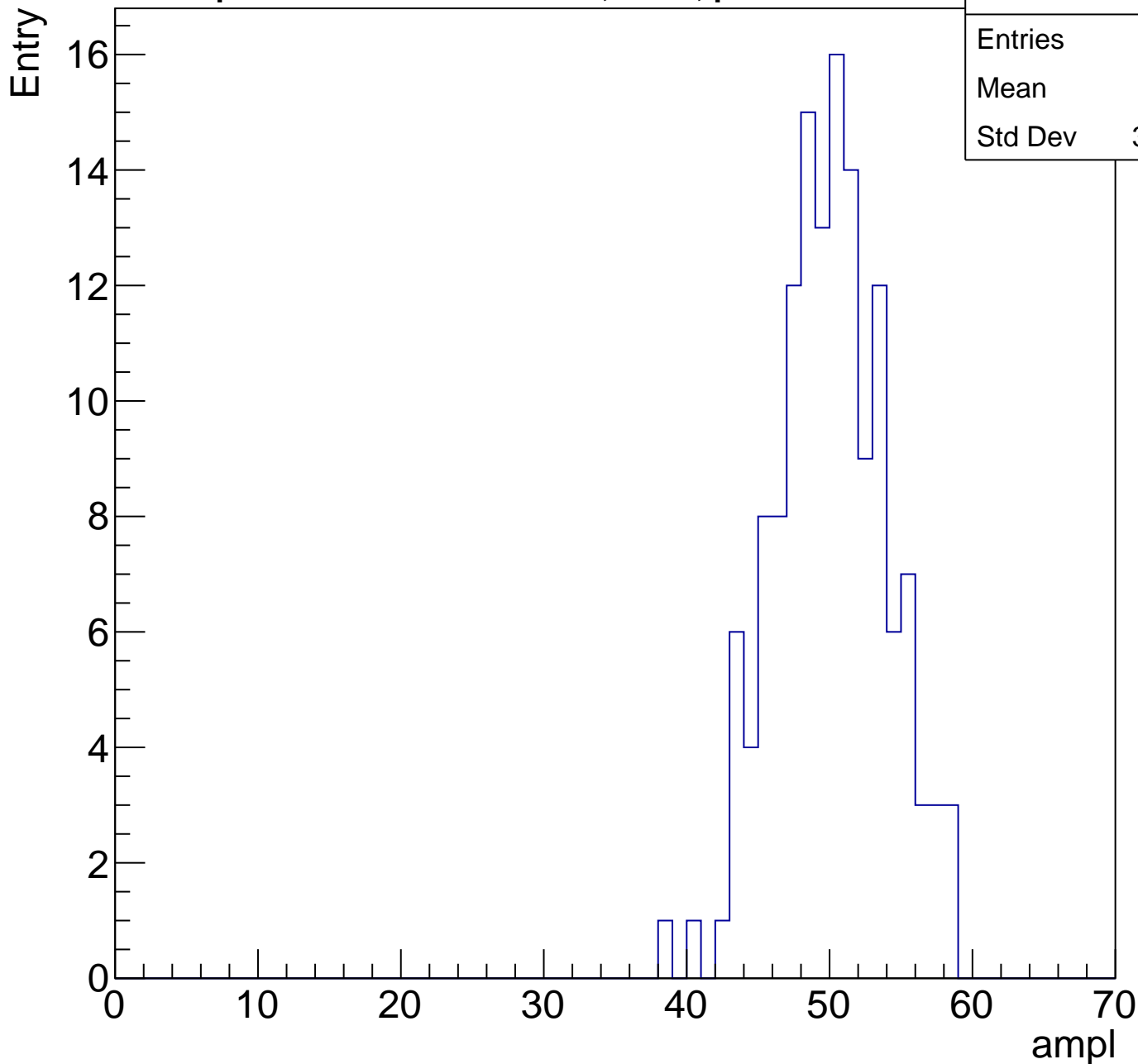
10

12

# B0L101S, U23-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	49.6
Std Dev	3.853



# B0L101S, U23-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

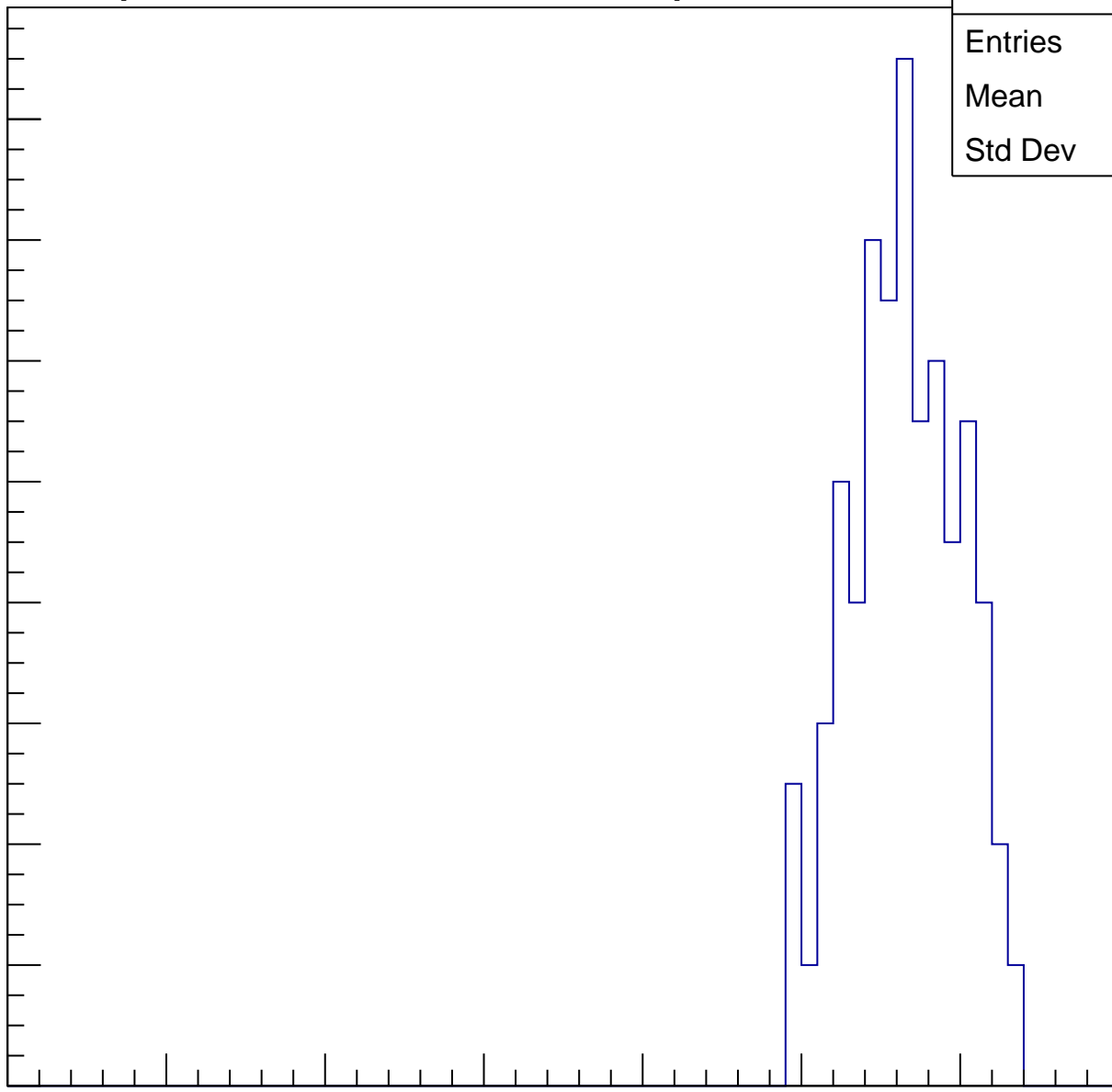
Entries	132
Mean	56.02
Std Dev	3.4

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch91, adc5

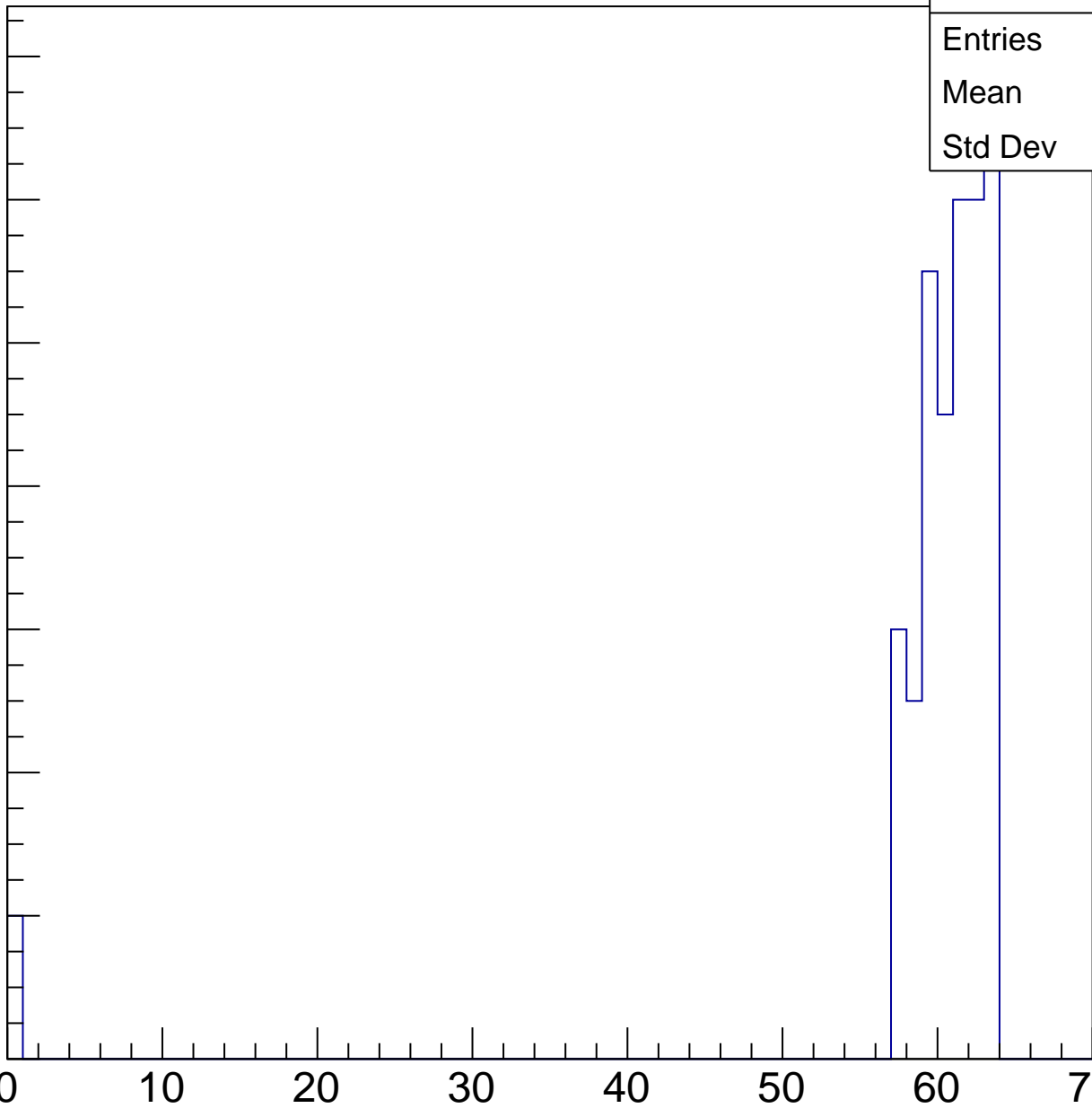
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	71
Mean	58.86
Std Dev	10.19

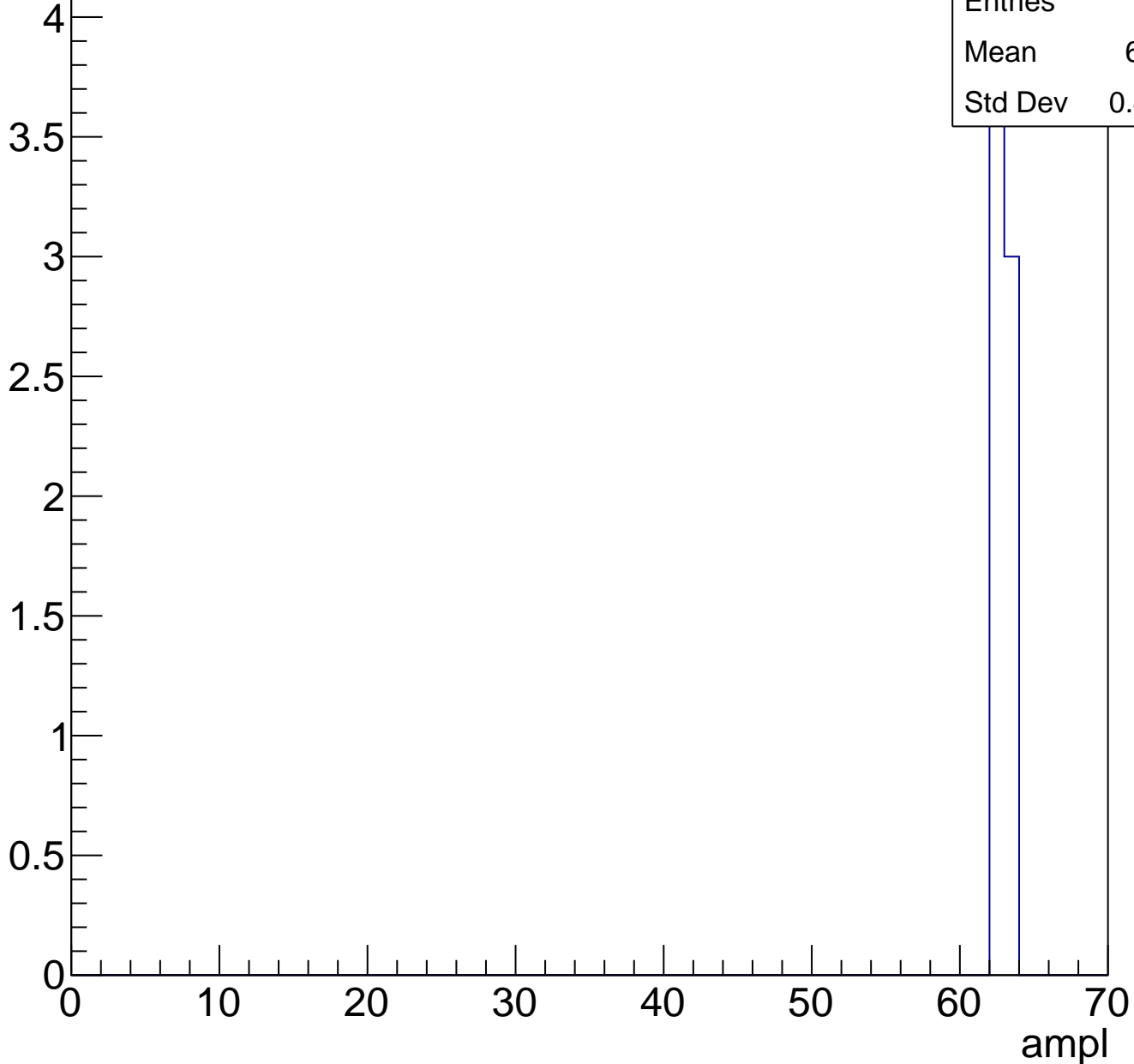
ampl



# B0L101S, U23-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch92, adc0

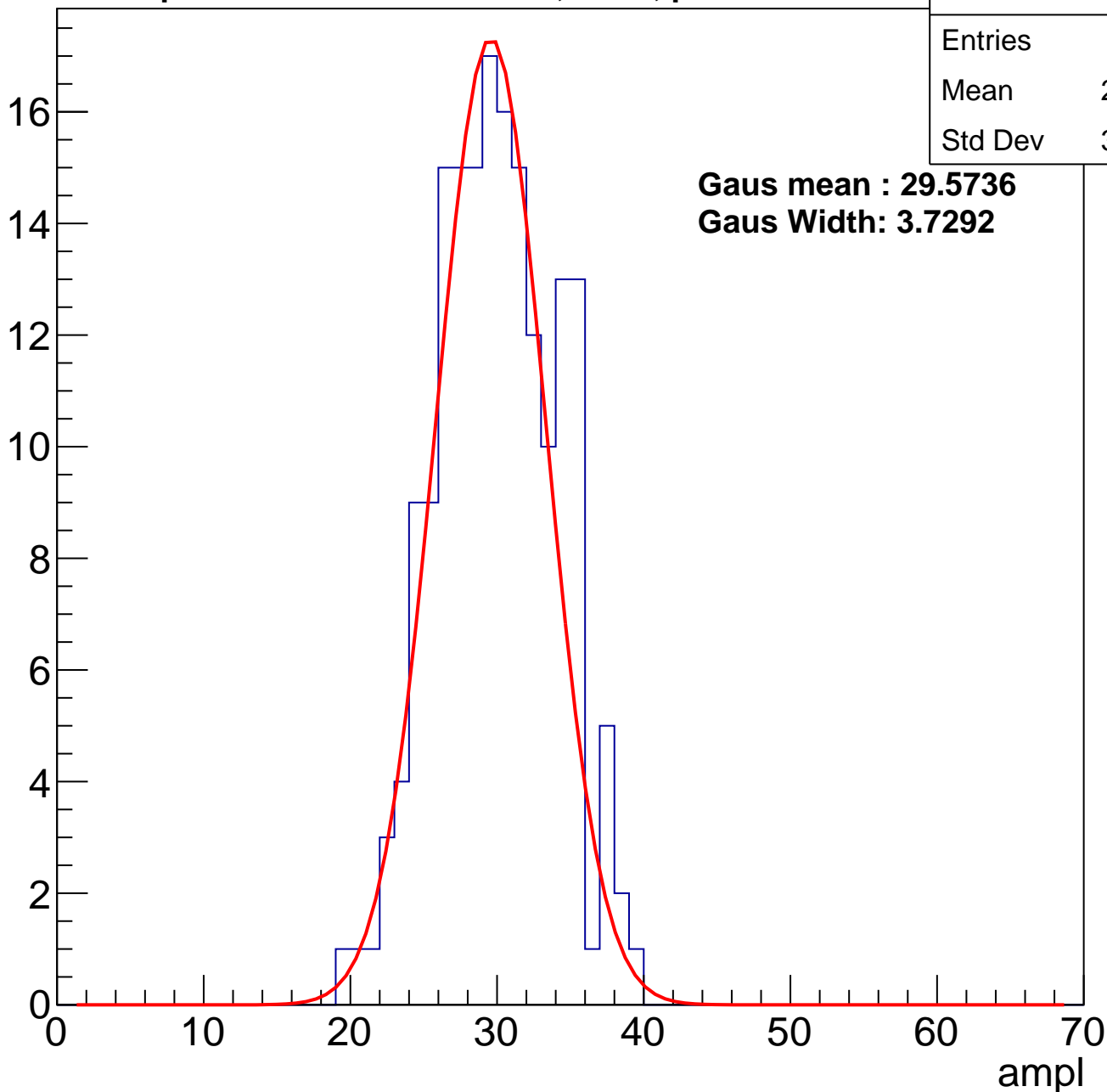
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	178
Mean	29.54
Std Dev	3.987

**Gaus mean : 29.5736**

**Gaus Width: 3.7292**

Entry



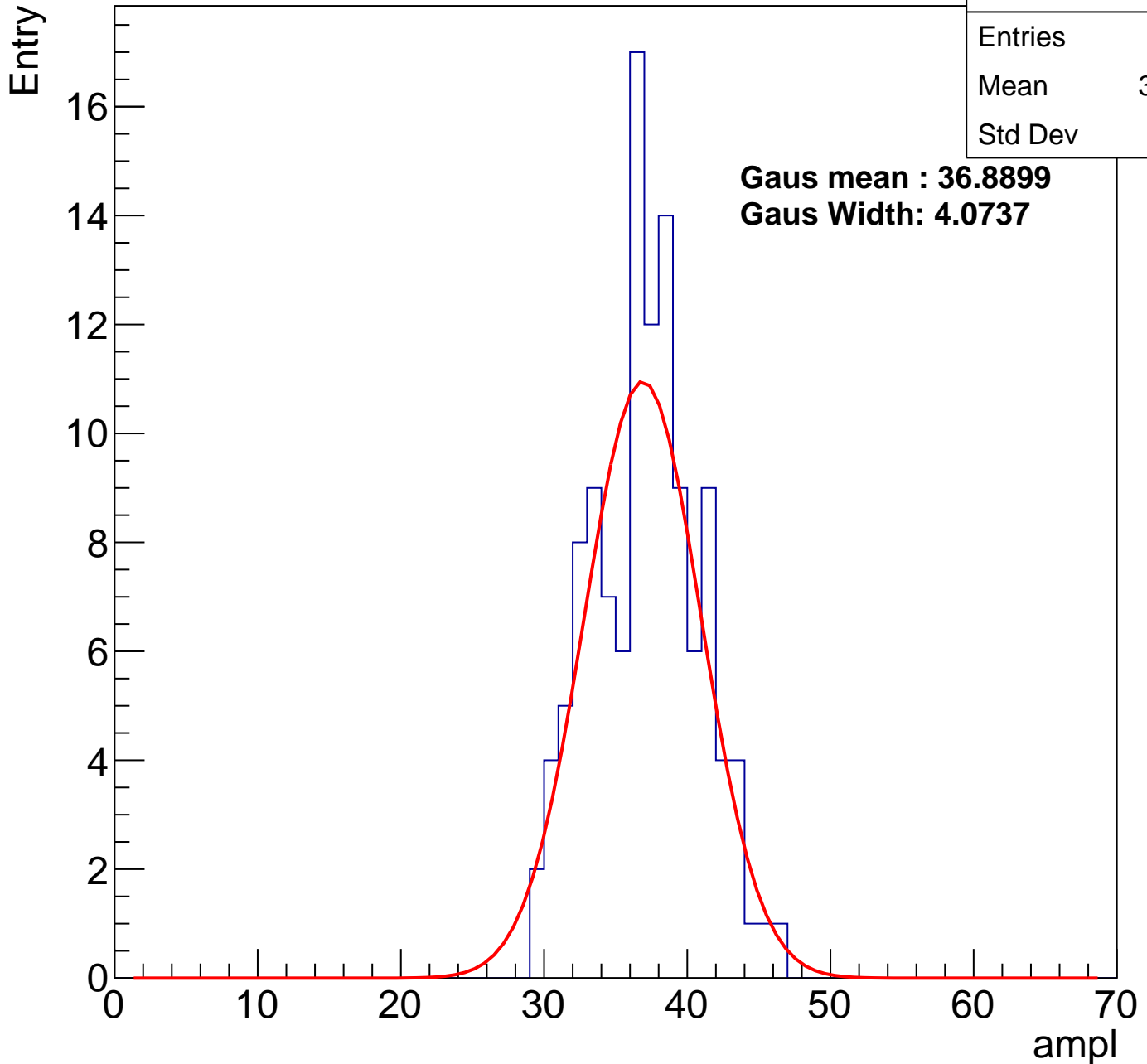
# B0L101S, U23-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	36.61
Std Dev	3.69

**Gaus mean : 36.8899**

**Gaus Width: 4.0737**



# B0L101S, U23-ch92, adc2

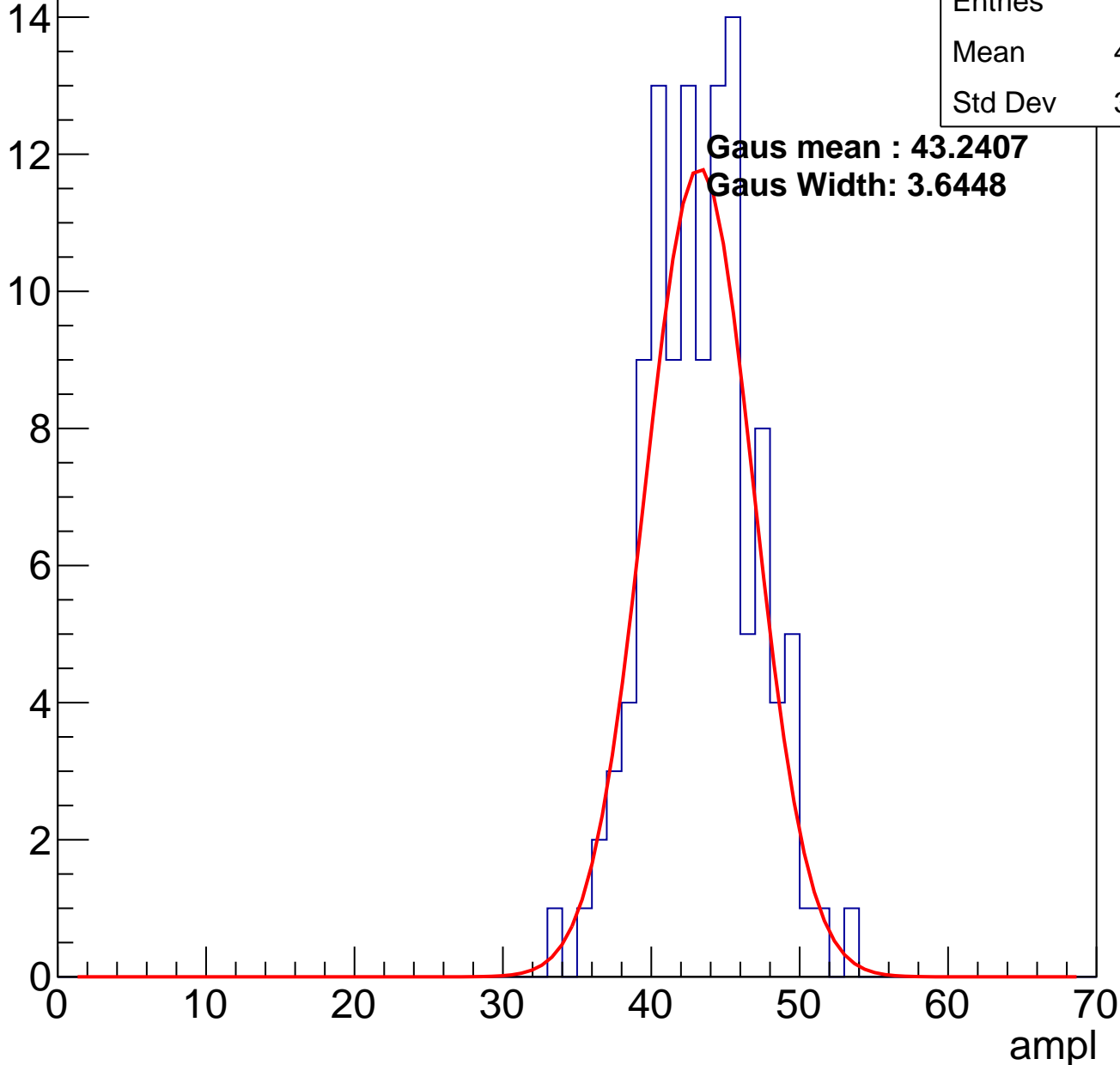
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	42.89
Std Dev	3.612

**Gaus mean : 43.2407**

**Gaus Width: 3.6448**

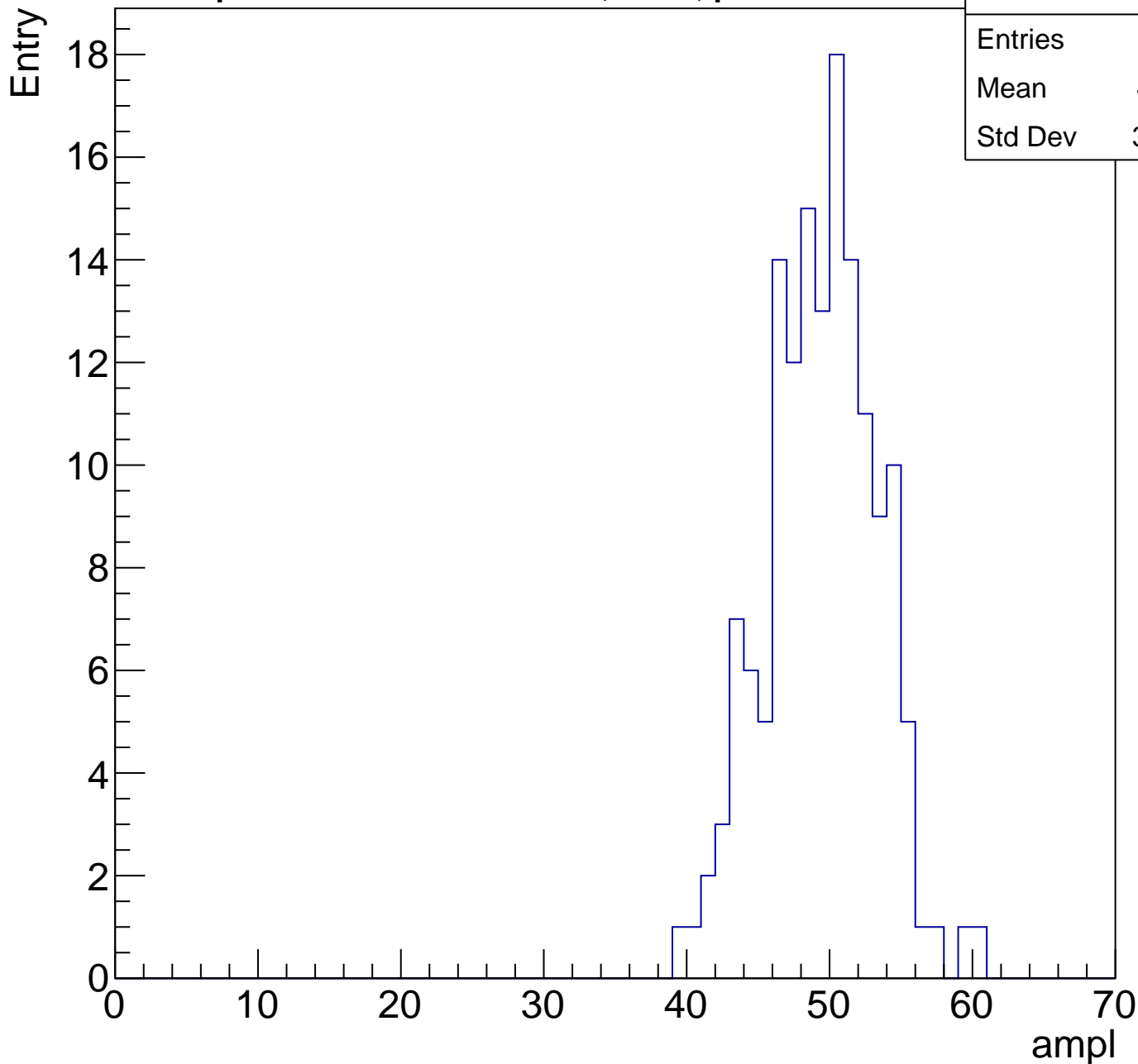
Entry



# B0L101S, U23-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	49.01
Std Dev	3.837



# B0L101S, U23-ch92, adc4

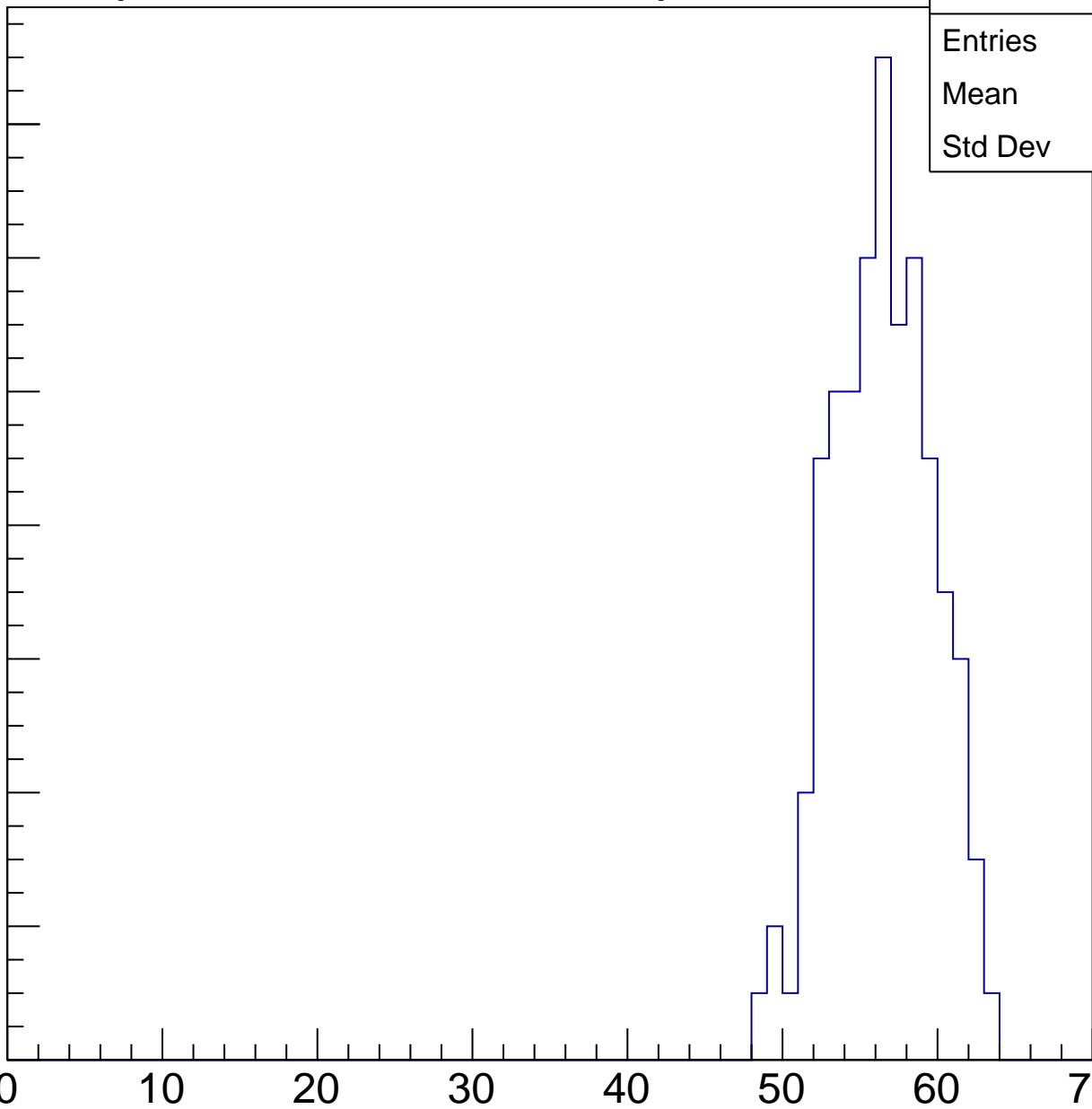
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	113
Mean	55.99
Std Dev	3.18

ampl



# B0L101S, U23-ch92, adc5

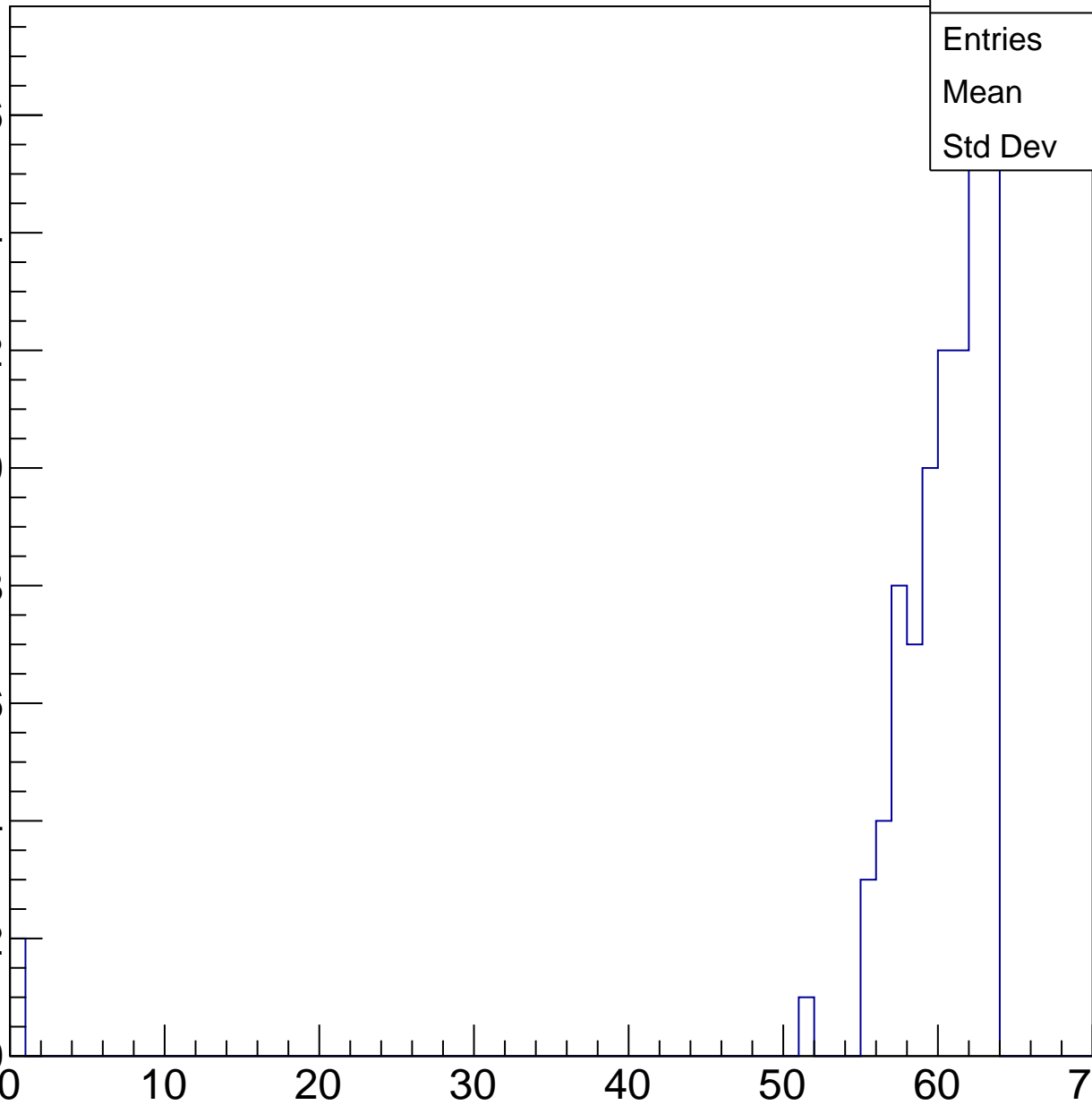
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	92
Mean	58.77
Std Dev	9.101

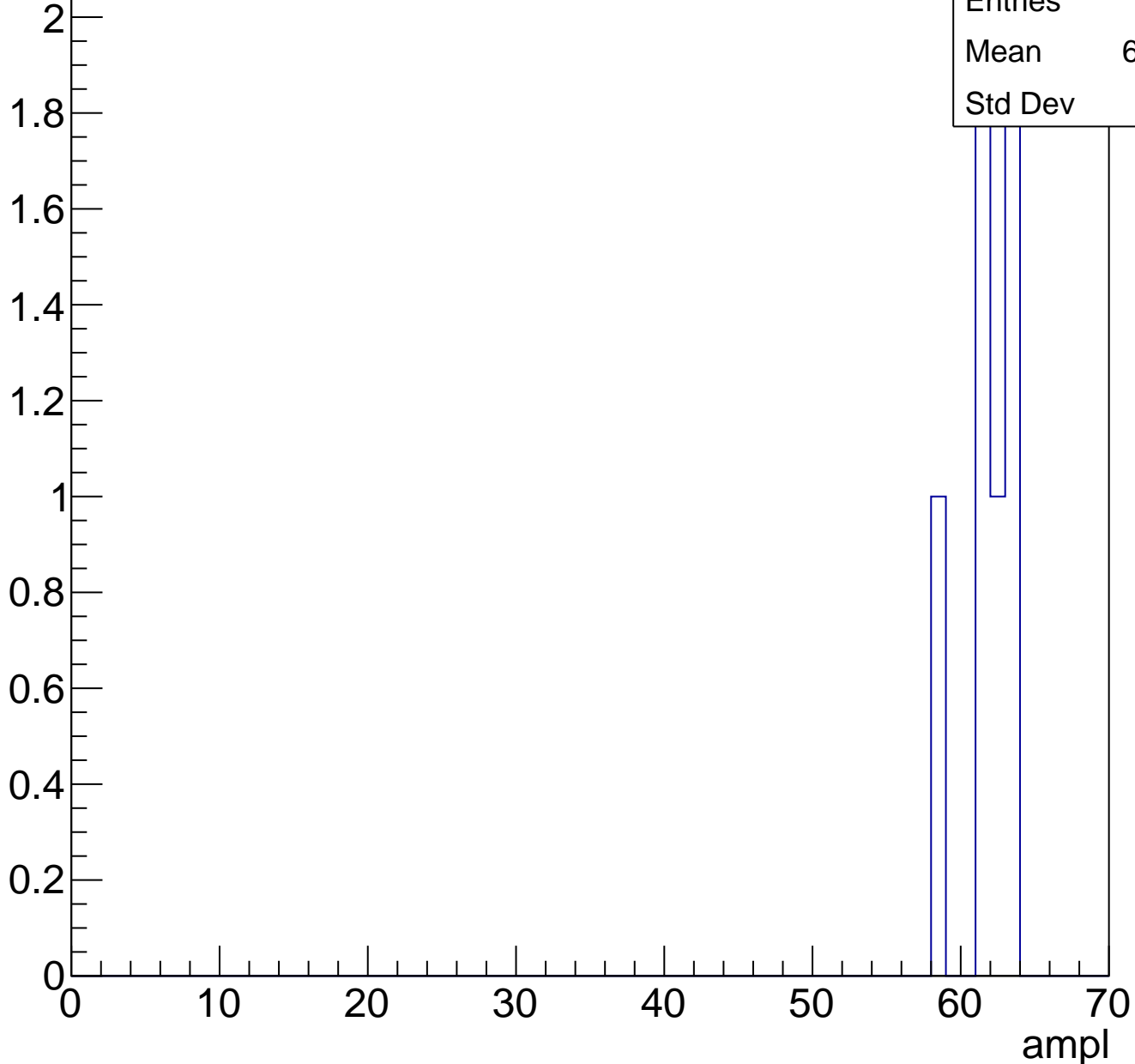
ampl



# B0L101S, U23-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch93, adc0

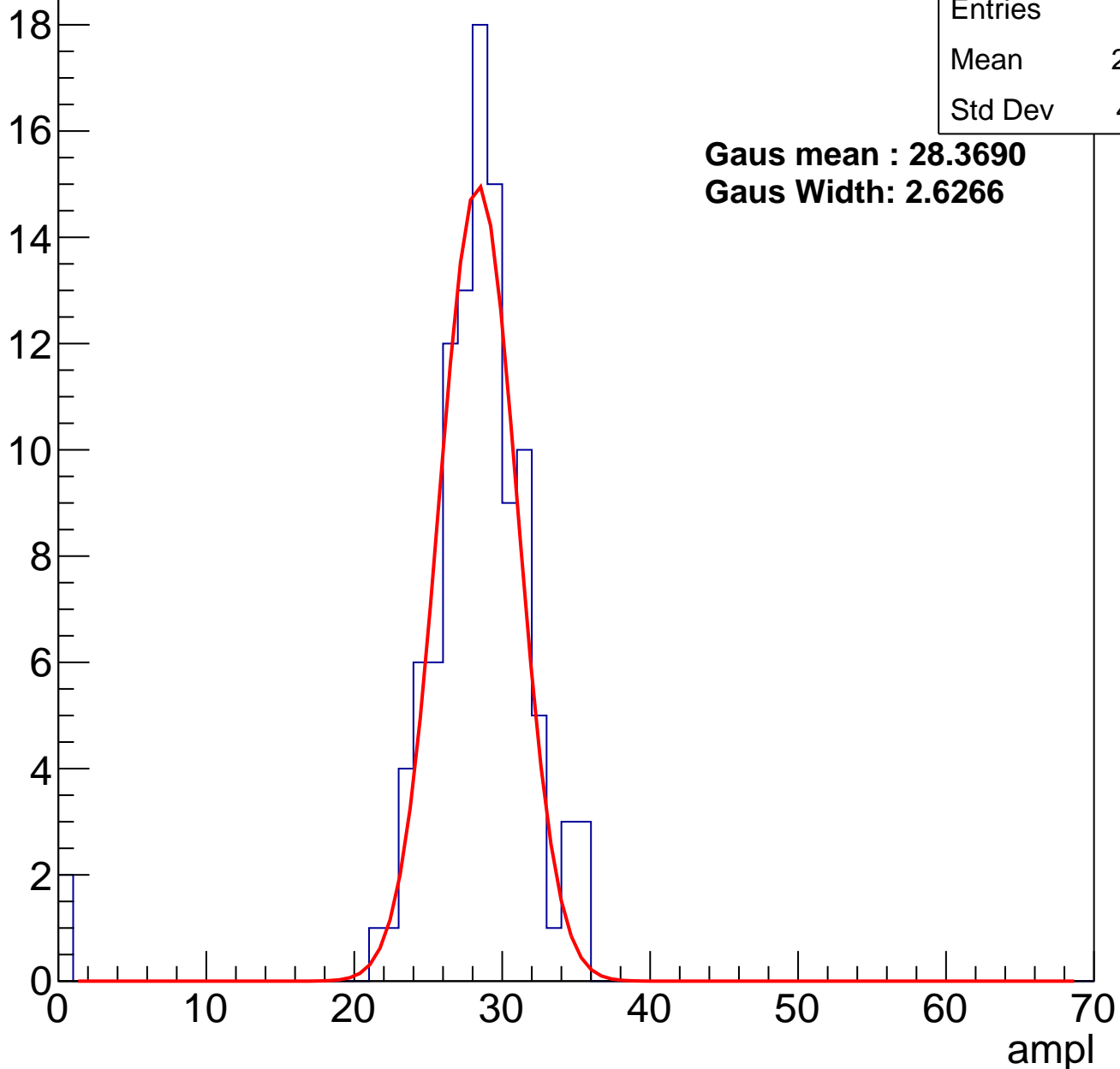
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	27.62
Std Dev	4.741

**Gaus mean : 28.3690**

**Gaus Width: 2.6266**

Entry



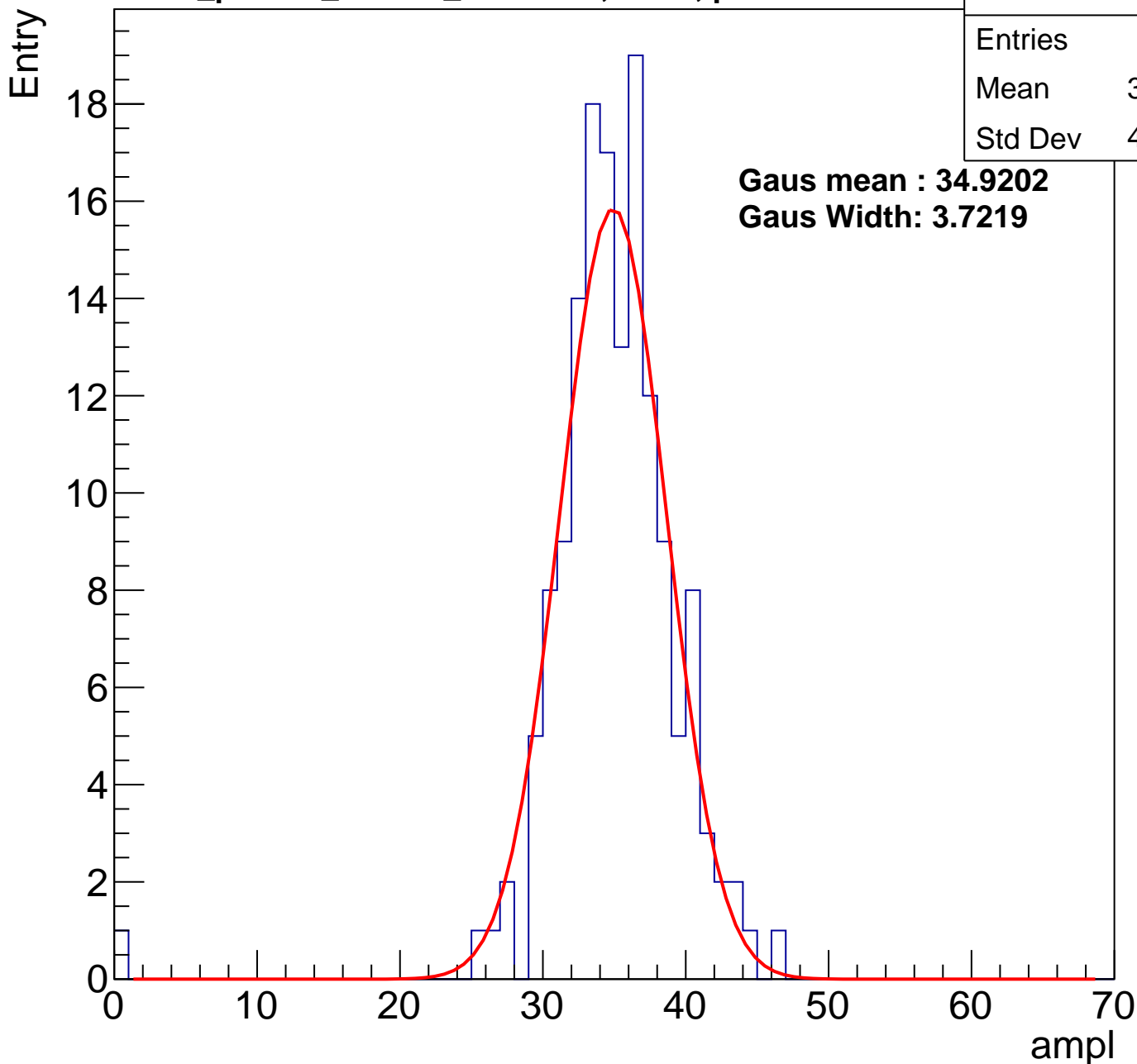
# B0L101S, U23-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	34.52
Std Dev	4.605

**Gaus mean : 34.9202**

**Gaus Width: 3.7219**



# B0L101S, U23-ch93, adc2

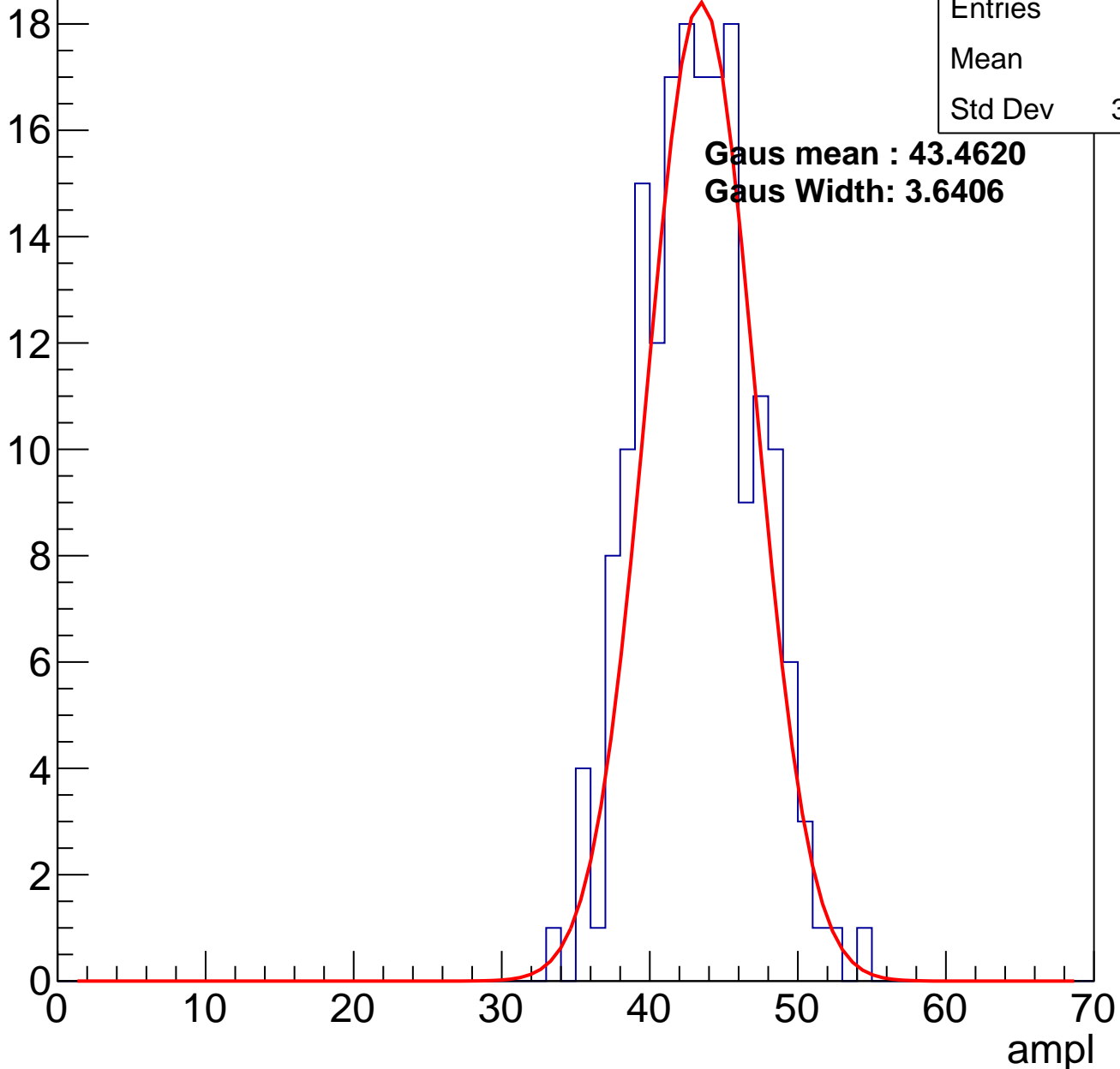
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	180
Mean	42.8
Std Dev	3.797

**Gaus mean : 43.4620**

**Gaus Width: 3.6406**

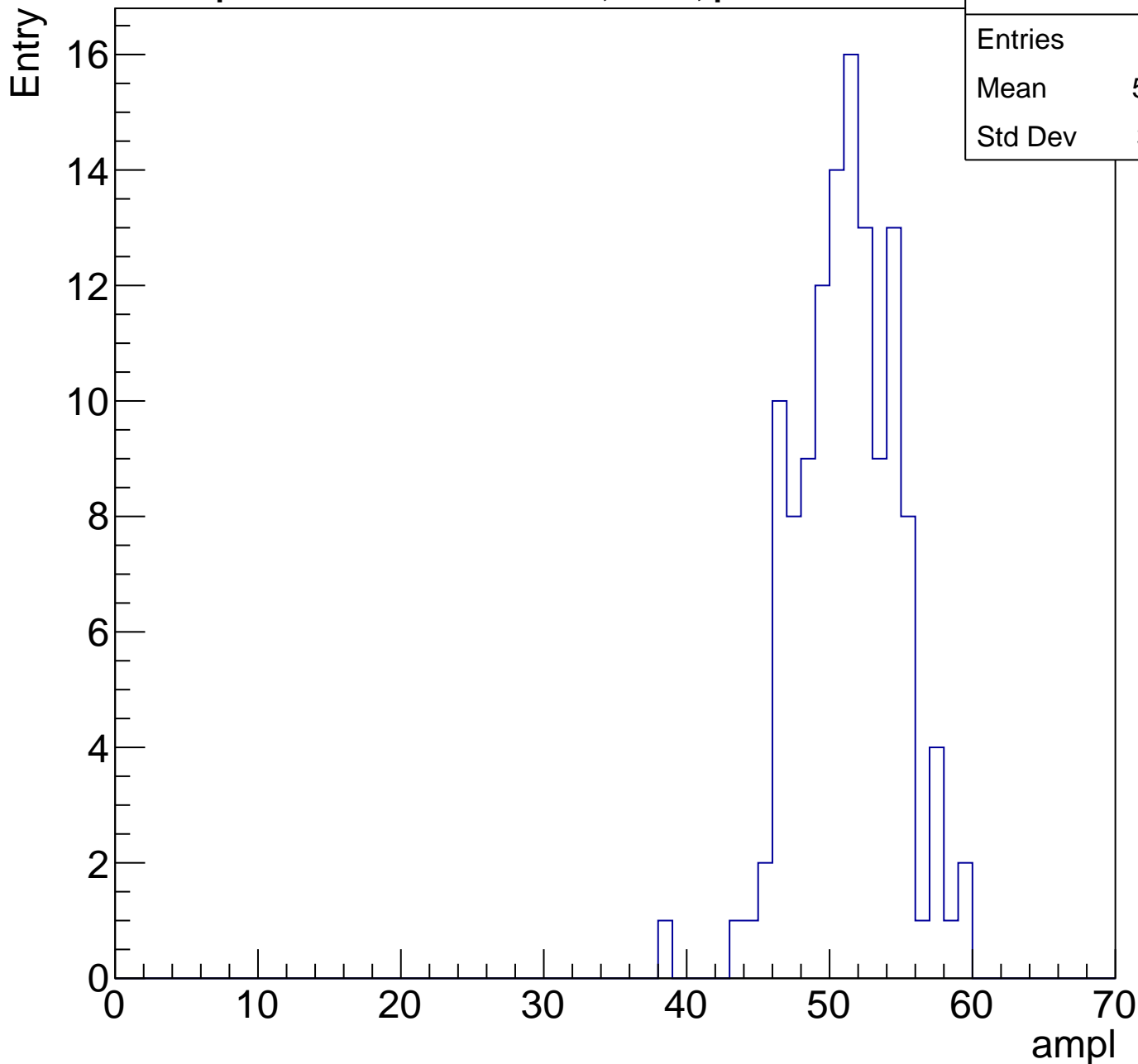
Entry



# B0L101S, U23-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	50.74
Std Dev	3.471



# B0L101S, U23-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

118

Mean

55.77

Std Dev

6.309

ampl

0

10

20

30

40

50

60

70

# B0L101S, U23-ch93, adc5

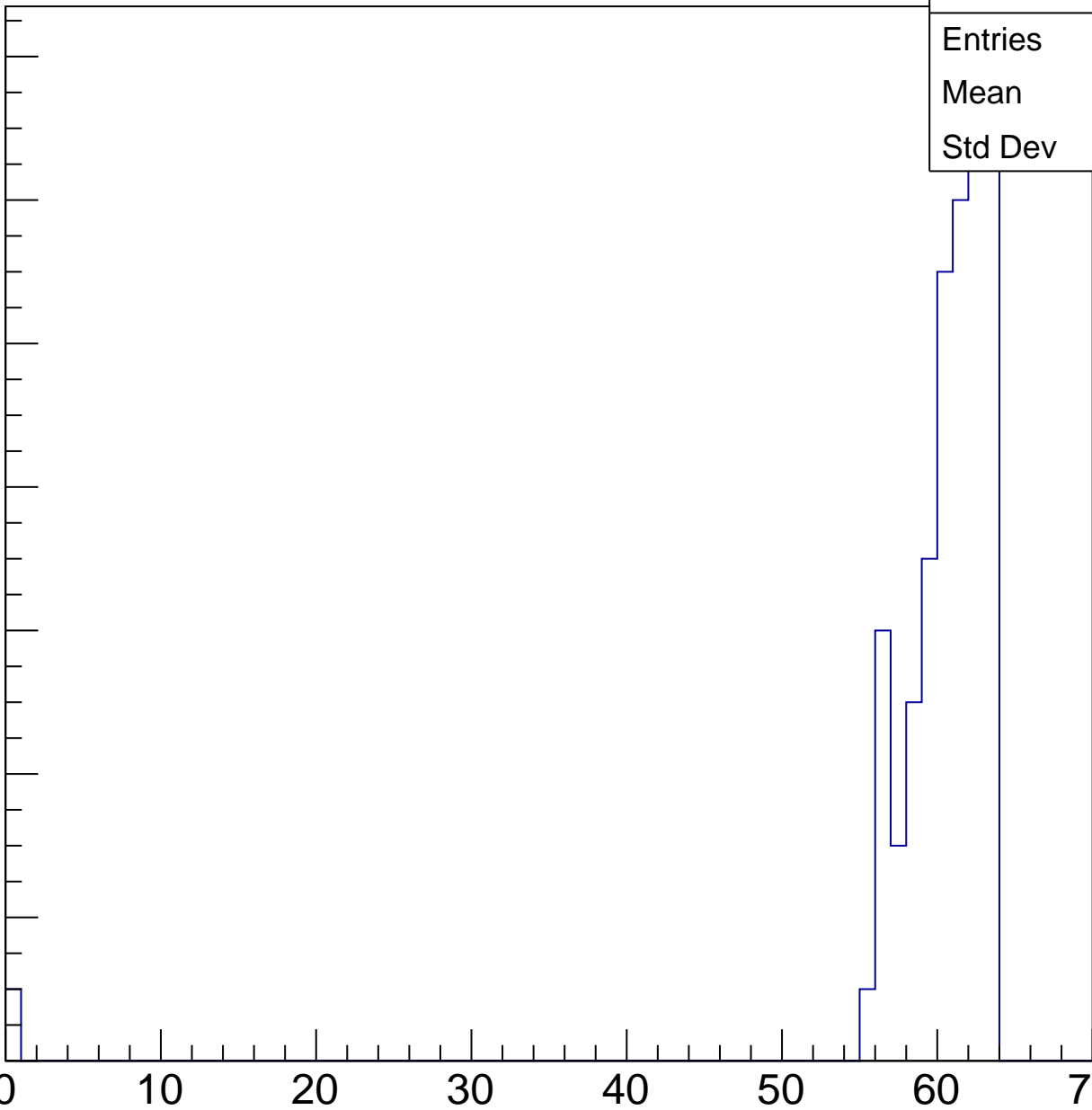
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	59.51
Std Dev	7.349

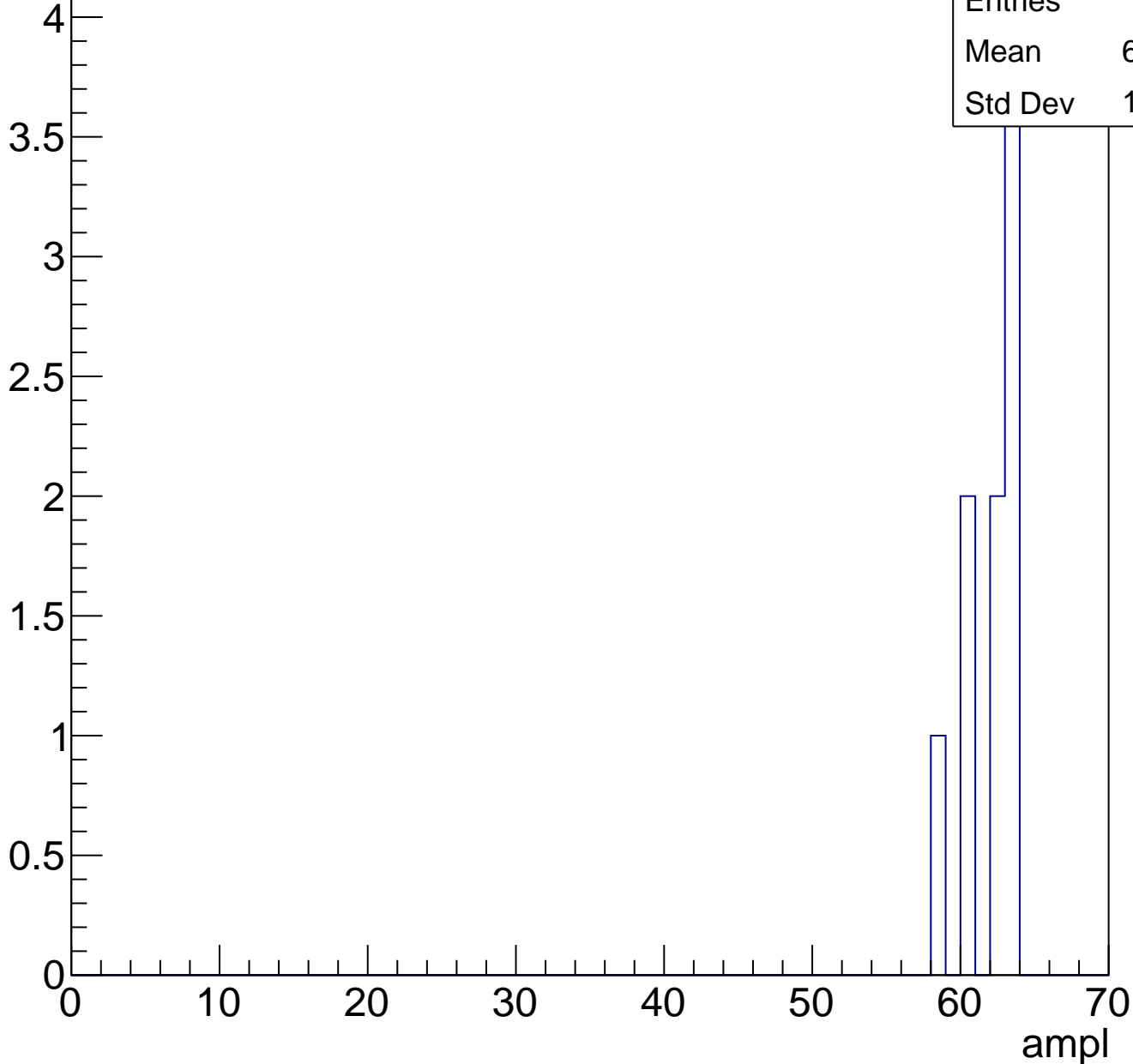
ampl



# B0L101S, U23-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L101S, U23-ch94, adc0

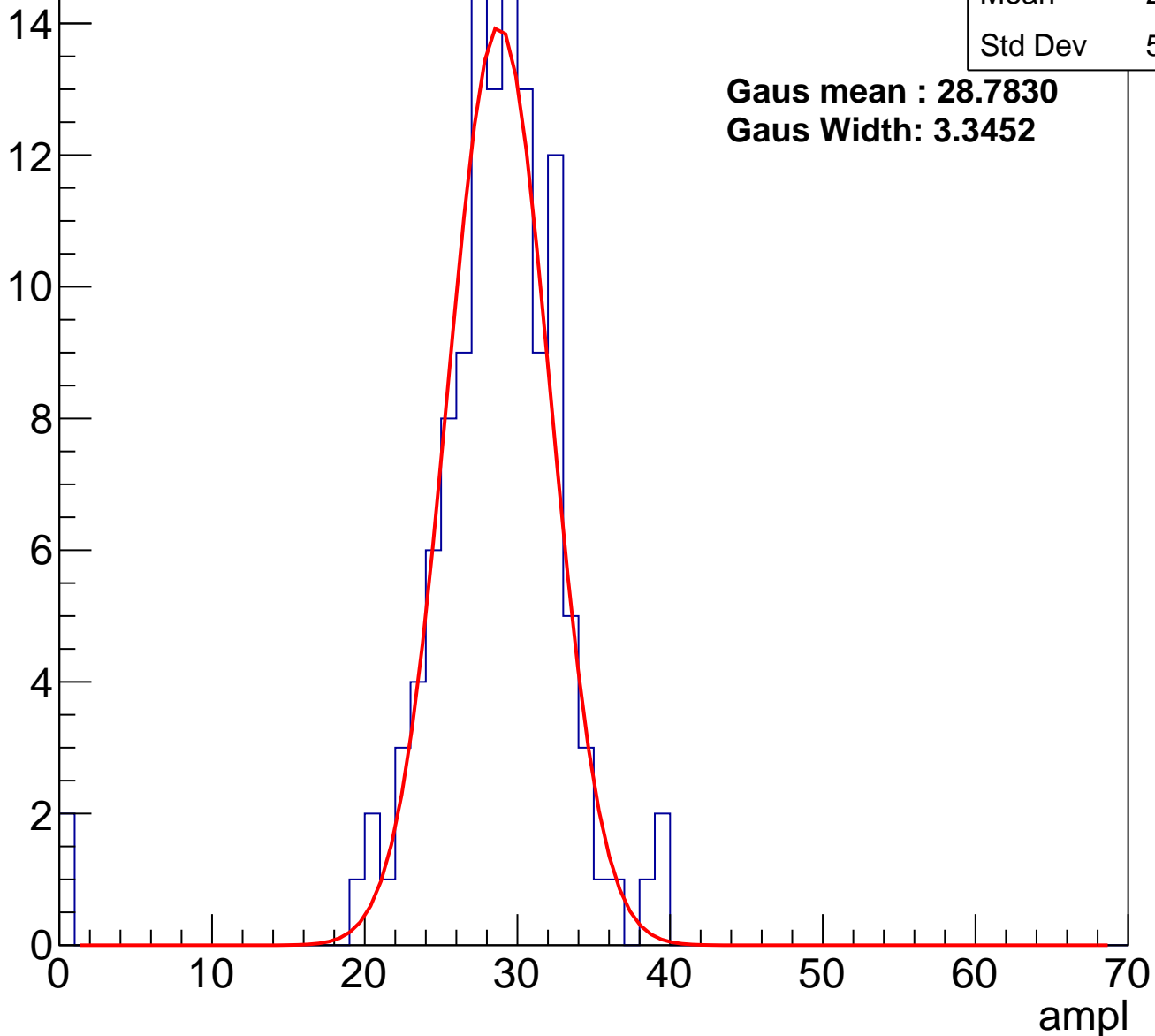
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	27.99
Std Dev	5.103

**Gaus mean : 28.7830**

**Gaus Width: 3.3452**

Entry

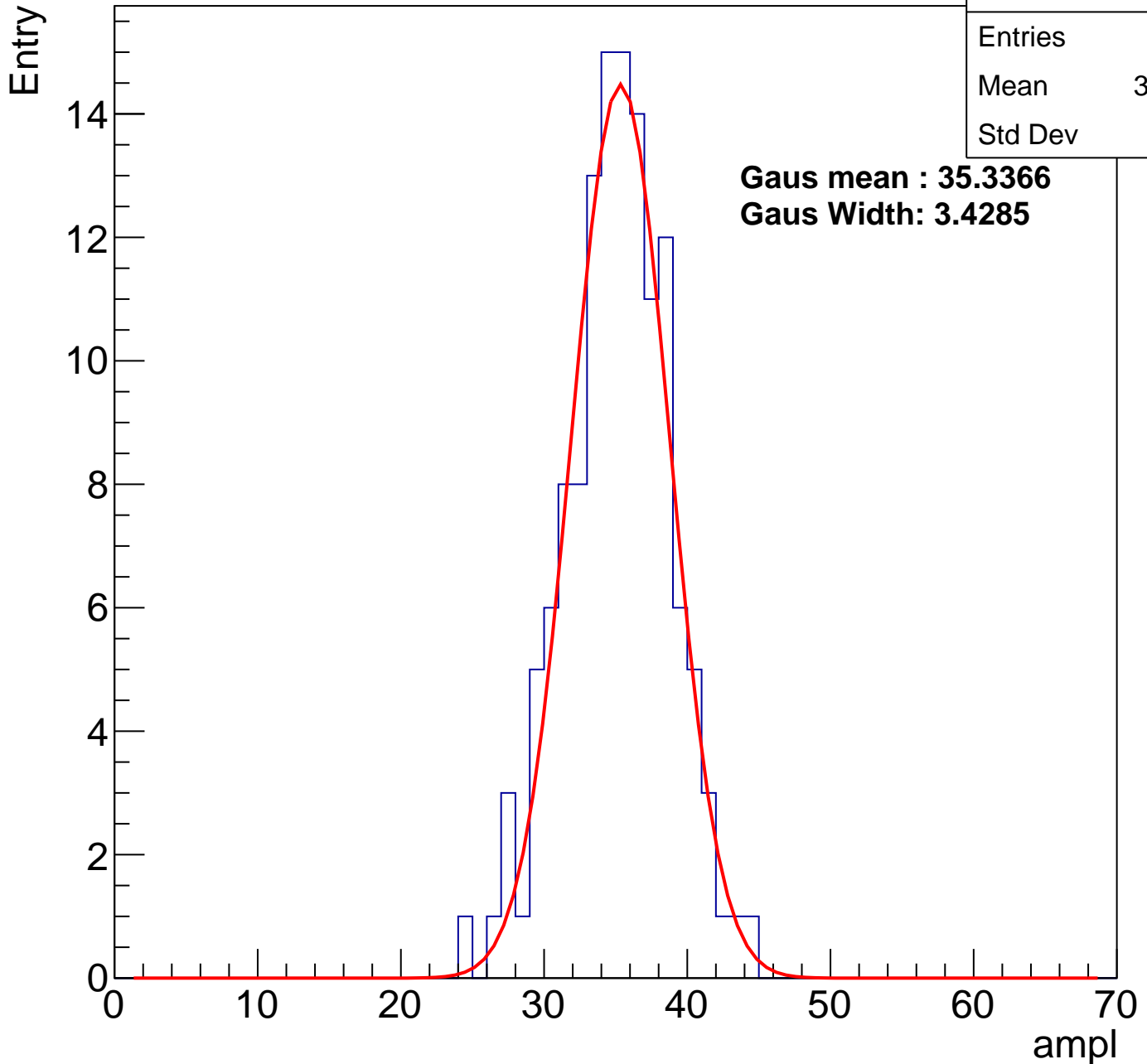


# B0L101S, U23-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	34.65
Std Dev	3.63

**Gaus mean : 35.3366**  
**Gaus Width: 3.4285**



# B0L101S, U23-ch94, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	41.42
Std Dev	3.76

**Gaus mean : 41.7309**

**Gaus Width: 3.7277**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

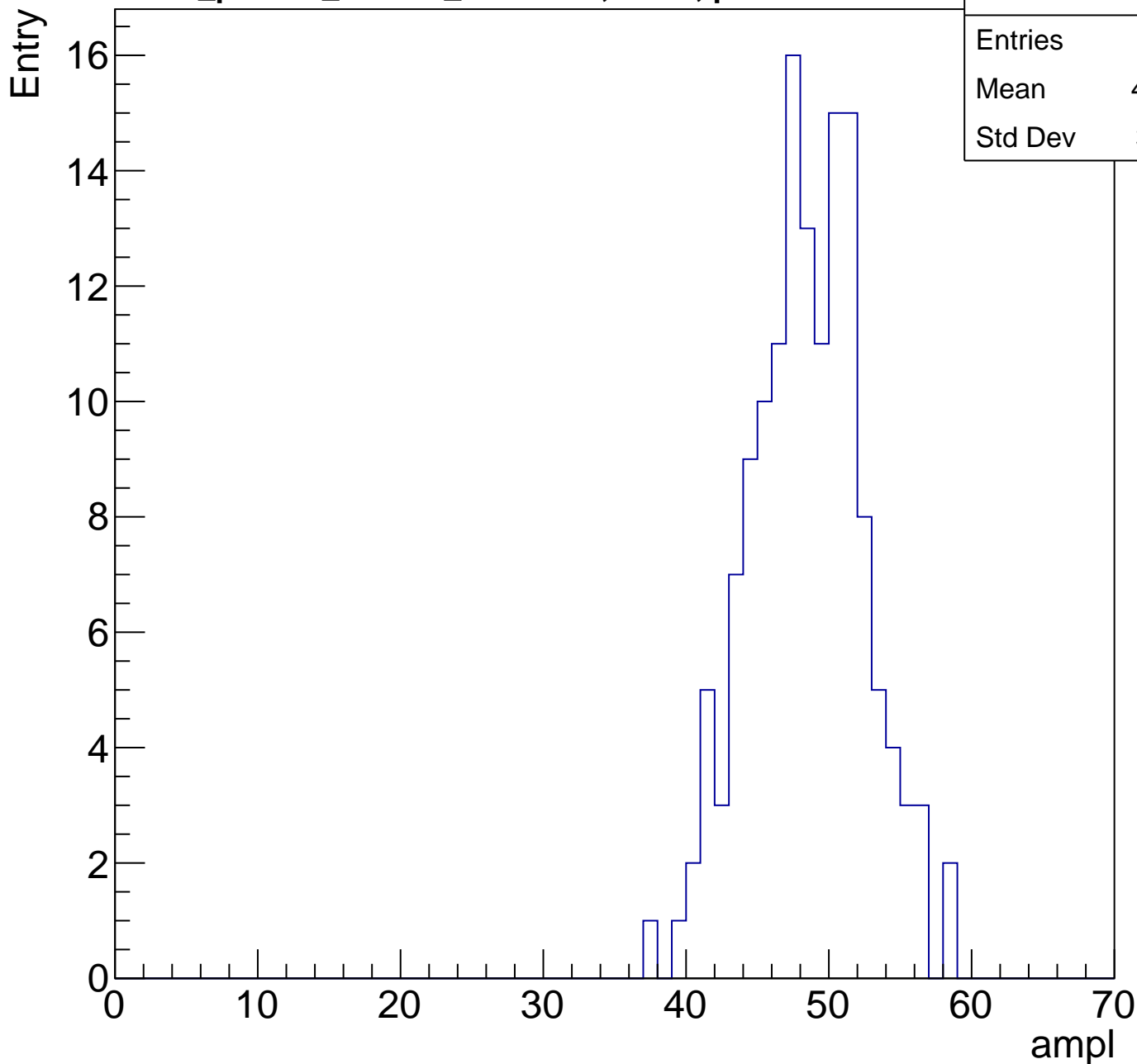
60

70

# B0L101S, U23-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	48.03
Std Dev	3.991

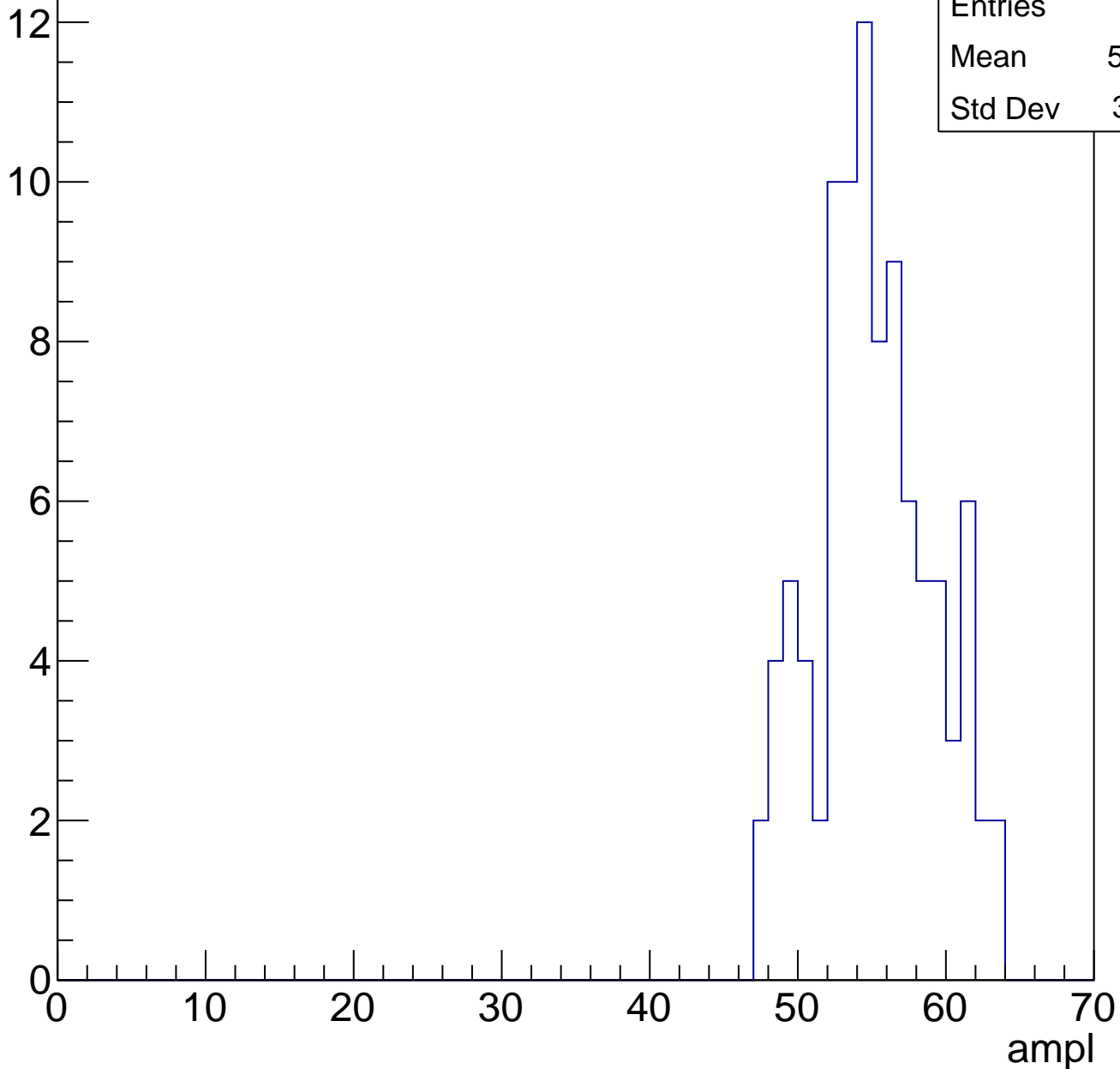


# B0L101S, U23-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	95
Mean	54.72
Std Dev	3.901

Entry



# B0L101S, U23-ch94, adc5

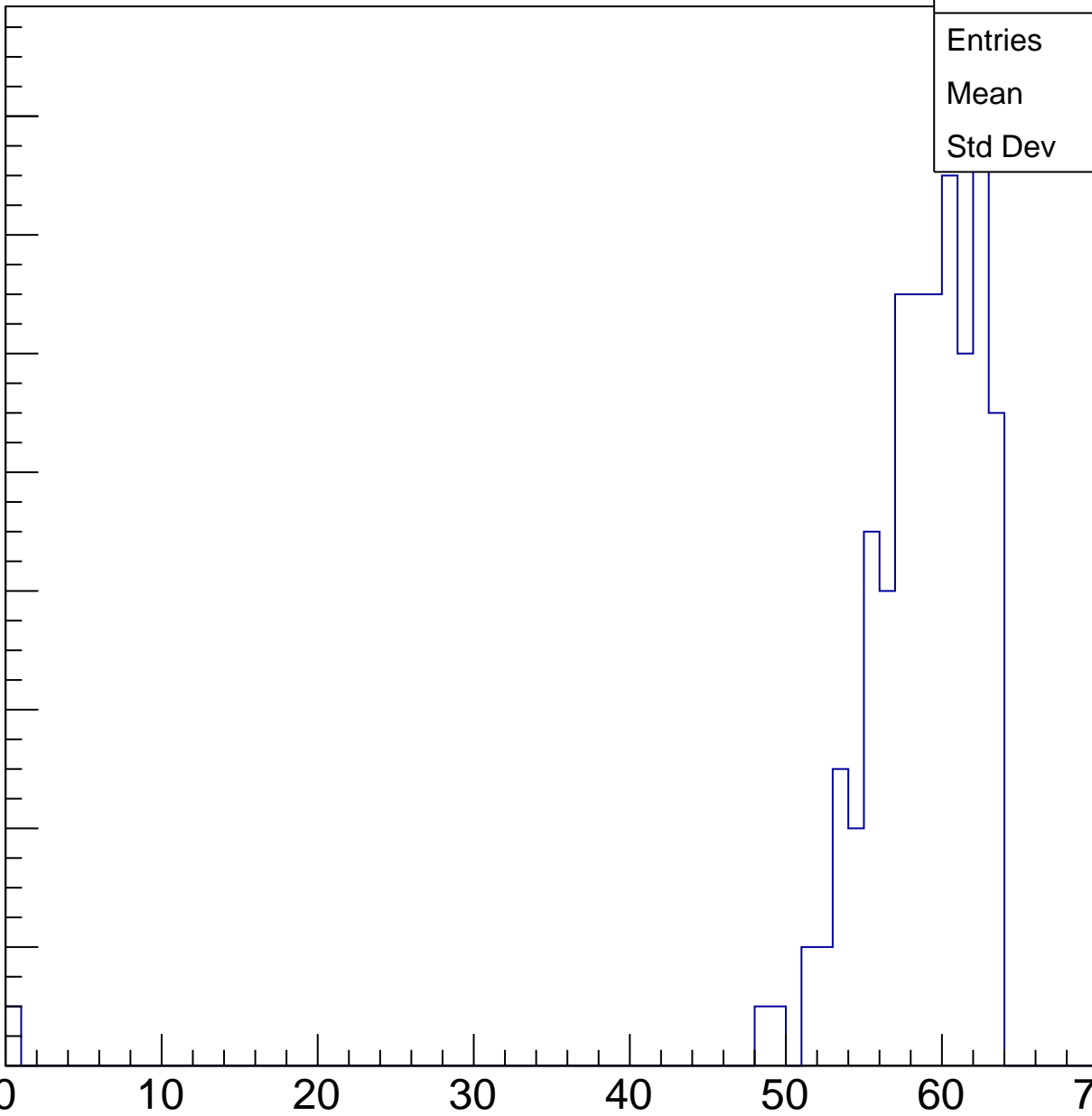
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	127
Mean	58.02
Std Dev	6.121

ampl

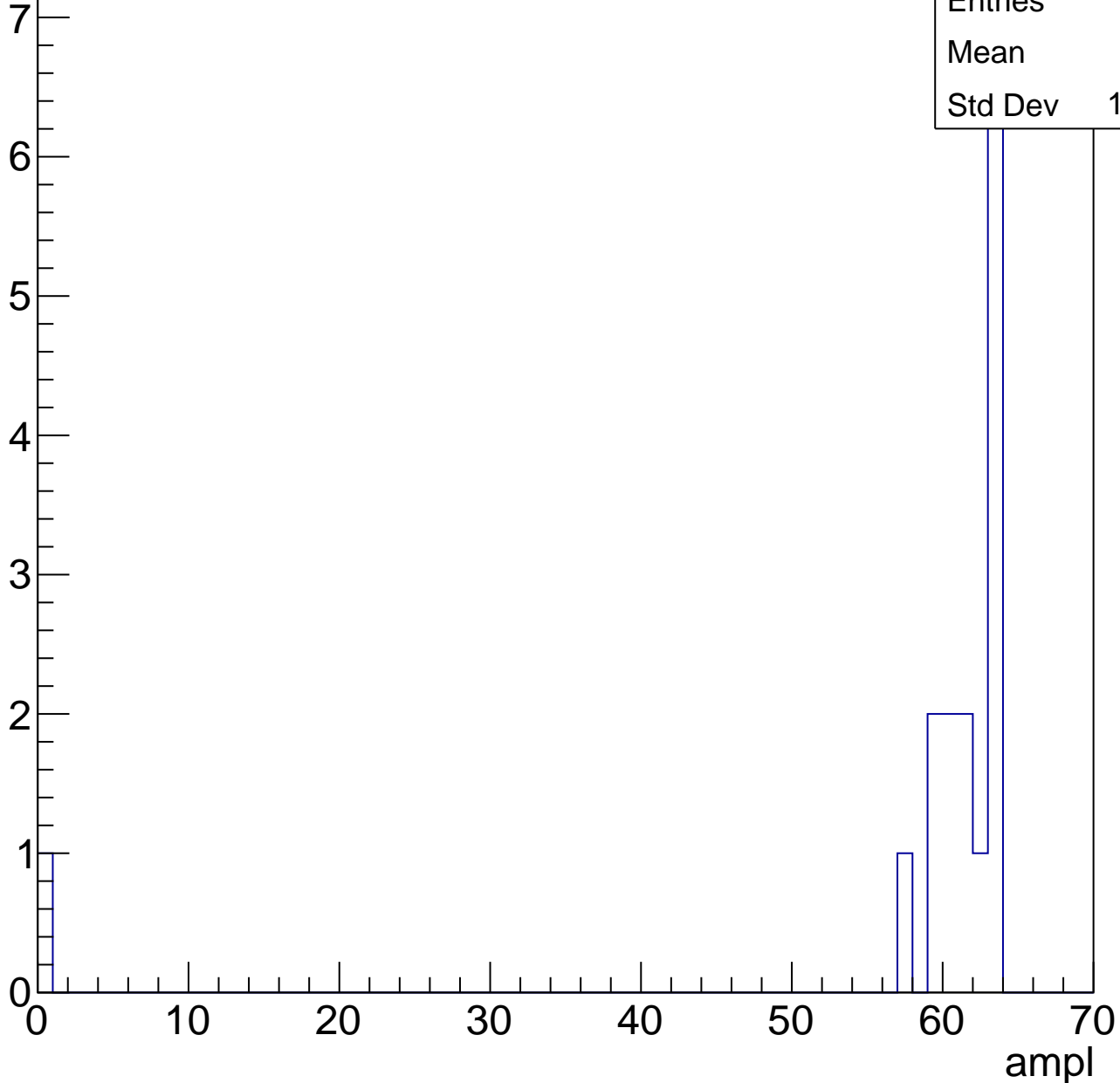


# B0L101S, U23-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	16
Mean	57.5
Std Dev	14.96

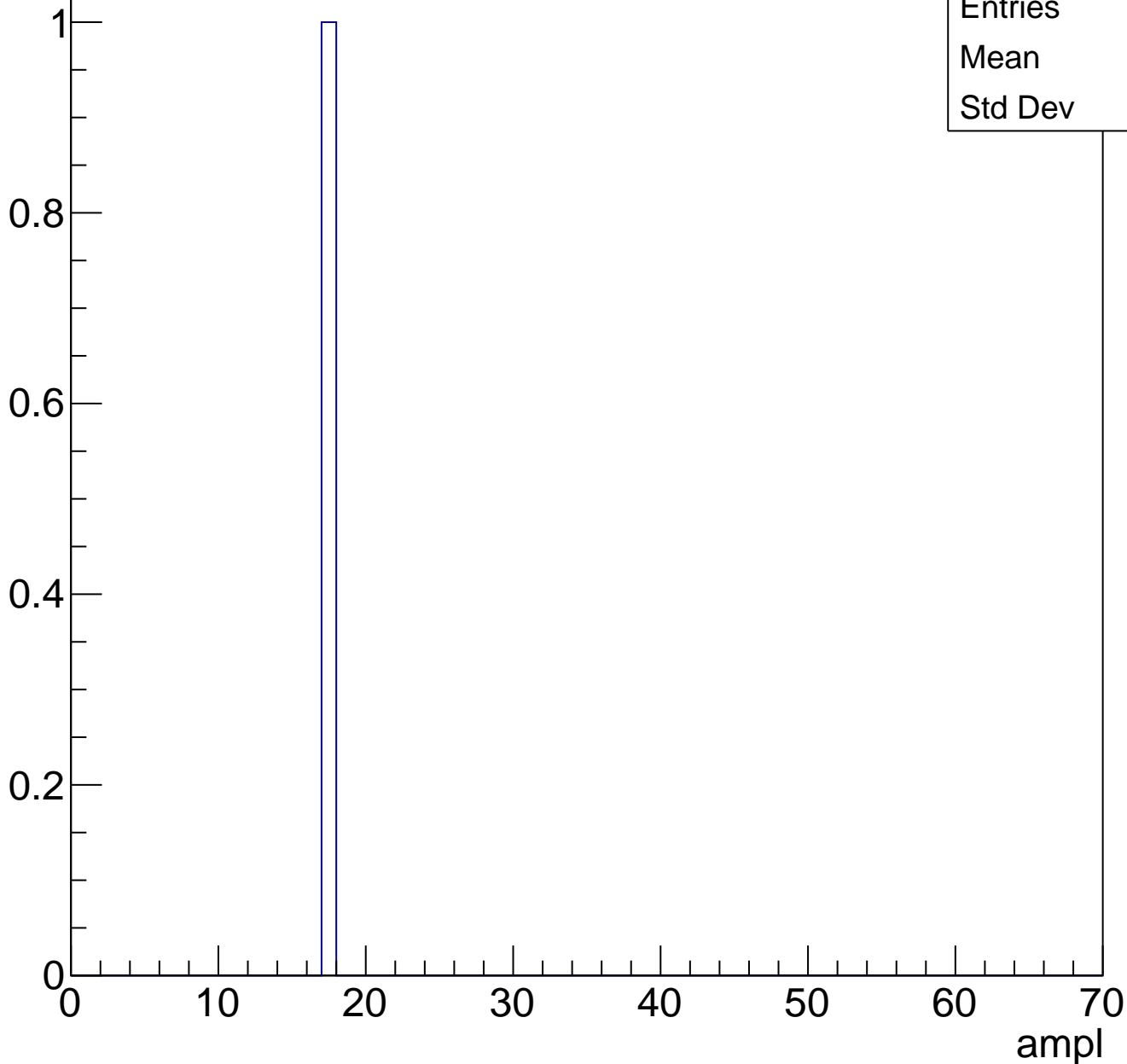




# B0L101S, U23-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch95, adc0

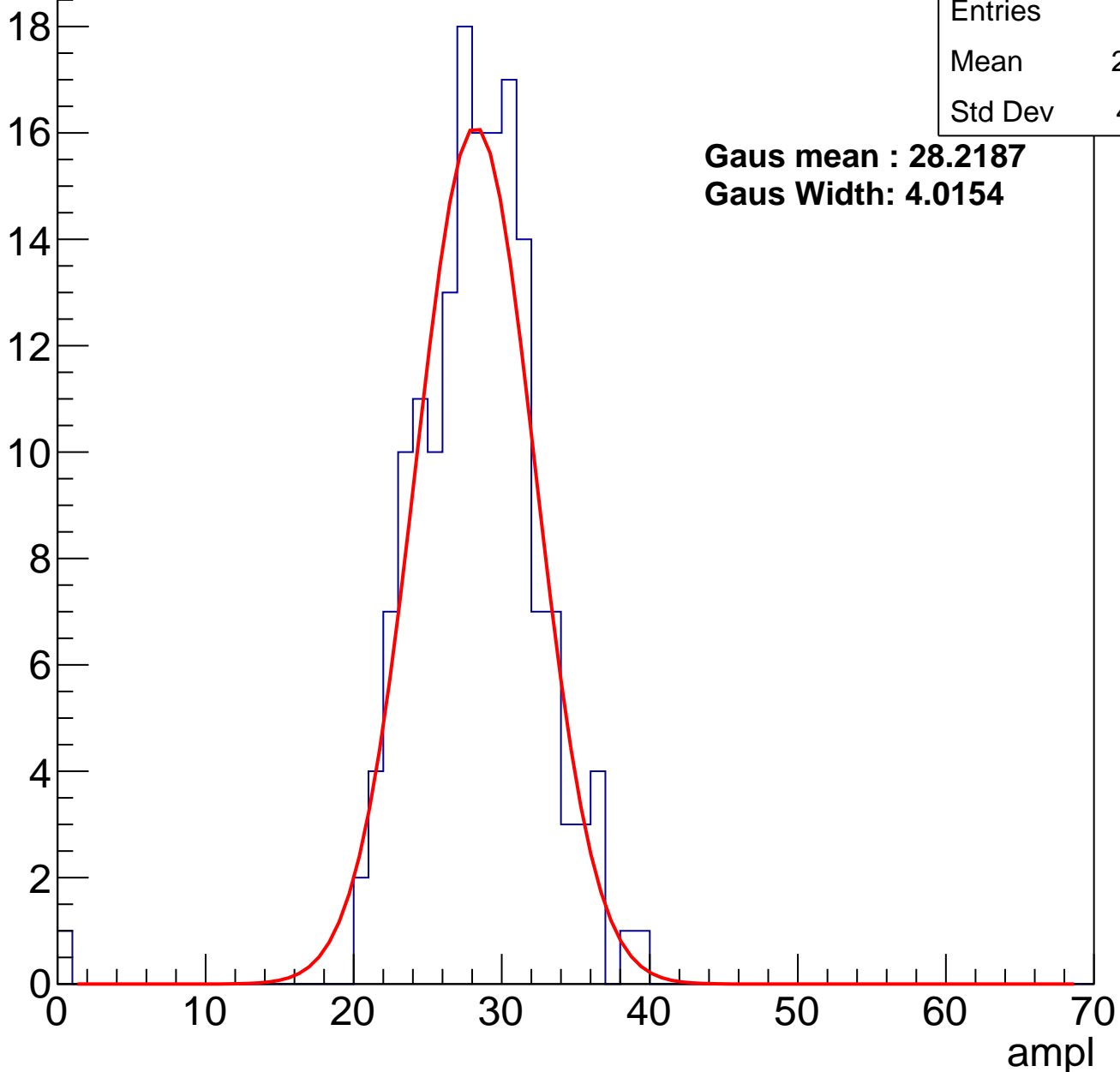
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	27.79
Std Dev	4.381

**Gaus mean : 28.2187**

**Gaus Width: 4.0154**

Entry



# B0L101S, U23-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

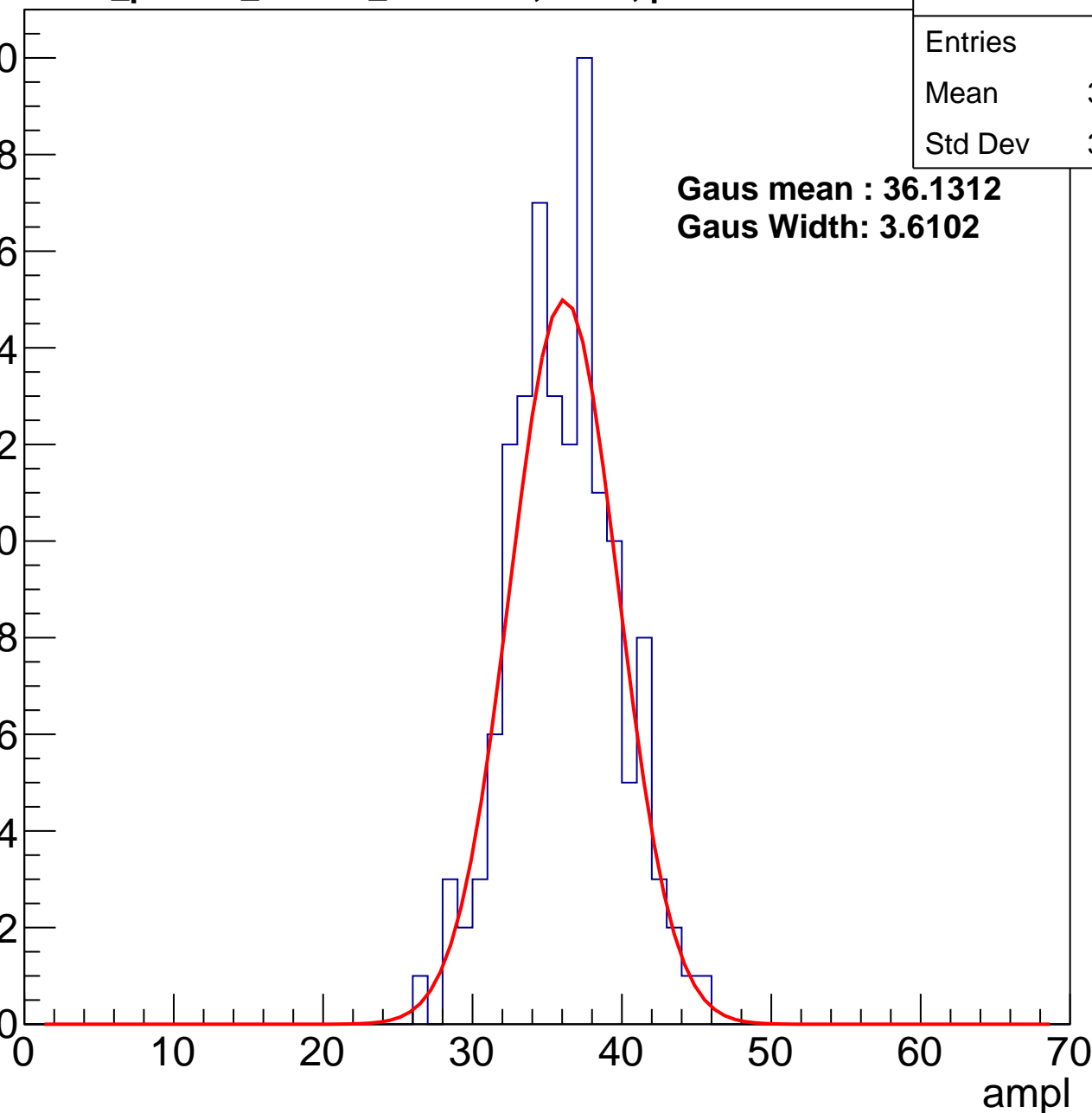
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	143
Mean	35.66
Std Dev	3.554

**Gaus mean : 36.1312**

**Gaus Width: 3.6102**



# B0L101S, U23-ch95, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

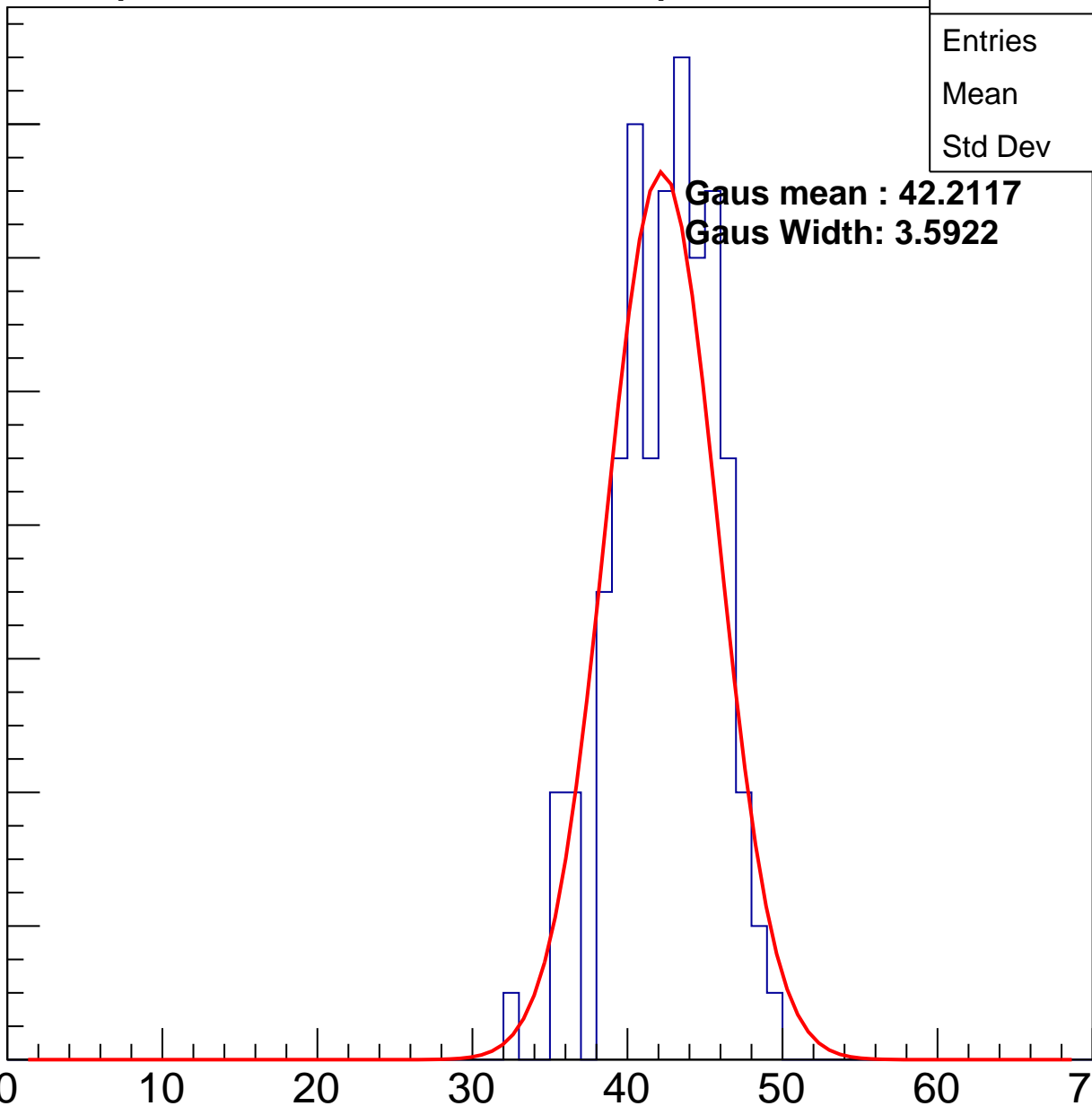
14  
12  
10  
8  
6  
4  
2  
0

Entries	117
Mean	41.99
Std Dev	3.259

**Gaus mean : 42.2117**

**Gaus Width: 3.5922**

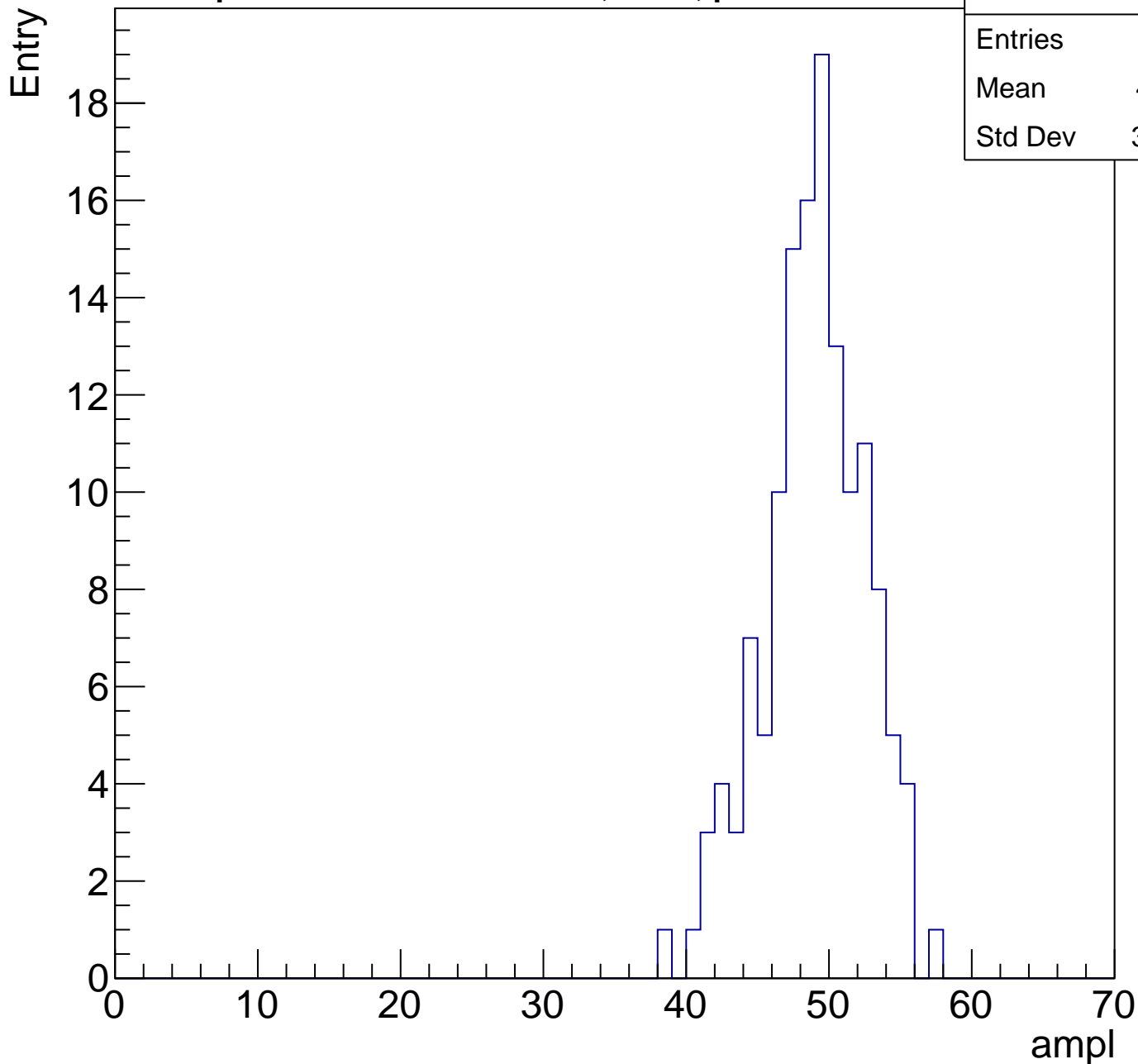
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	48.51
Std Dev	3.536



# B0L101S, U23-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	55.05
Std Dev	3.393

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

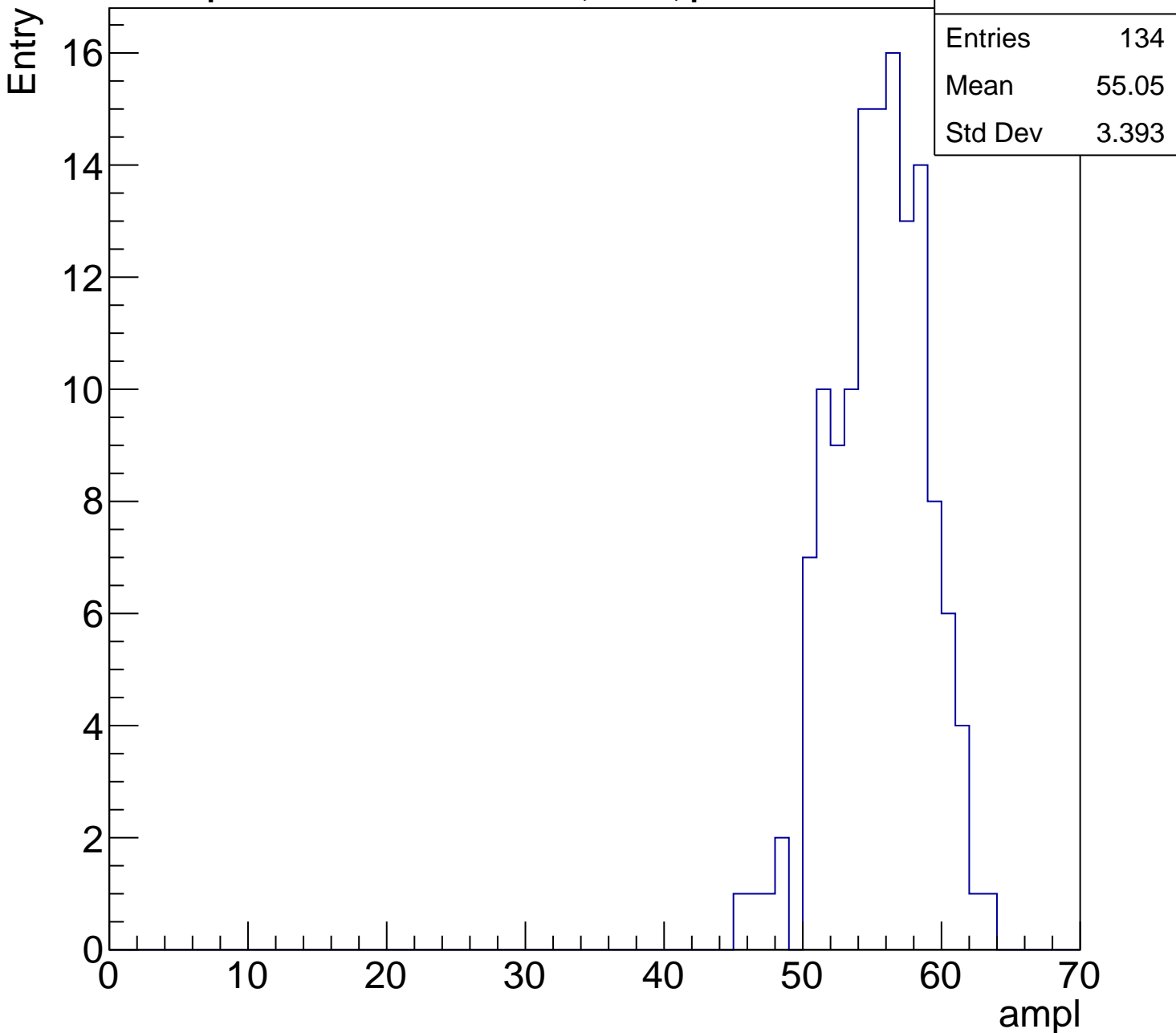
40

50

60

70

ampl



# B0L101S, U23-ch95, adc5

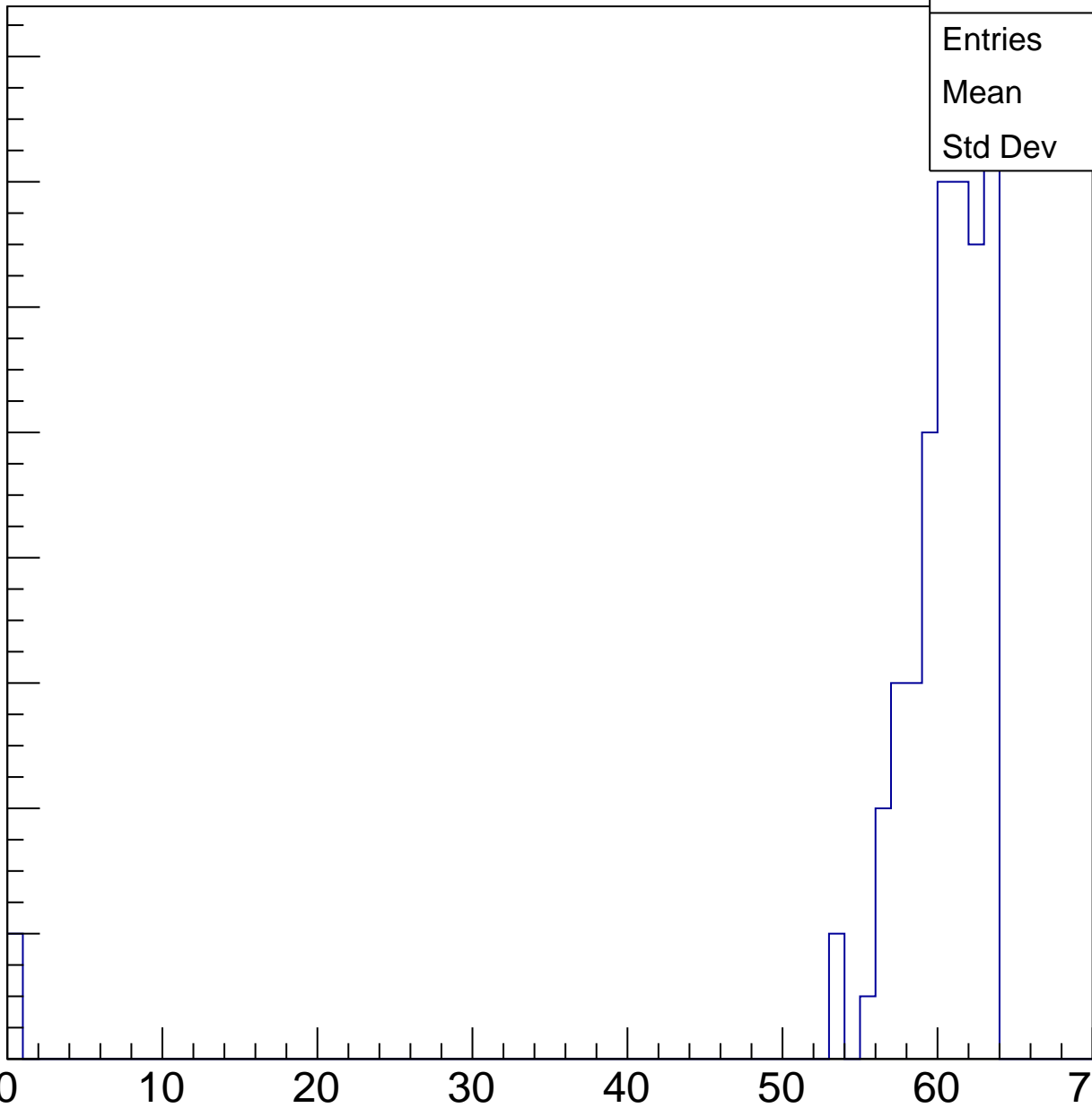
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	58.78
Std Dev	9.266

ampl

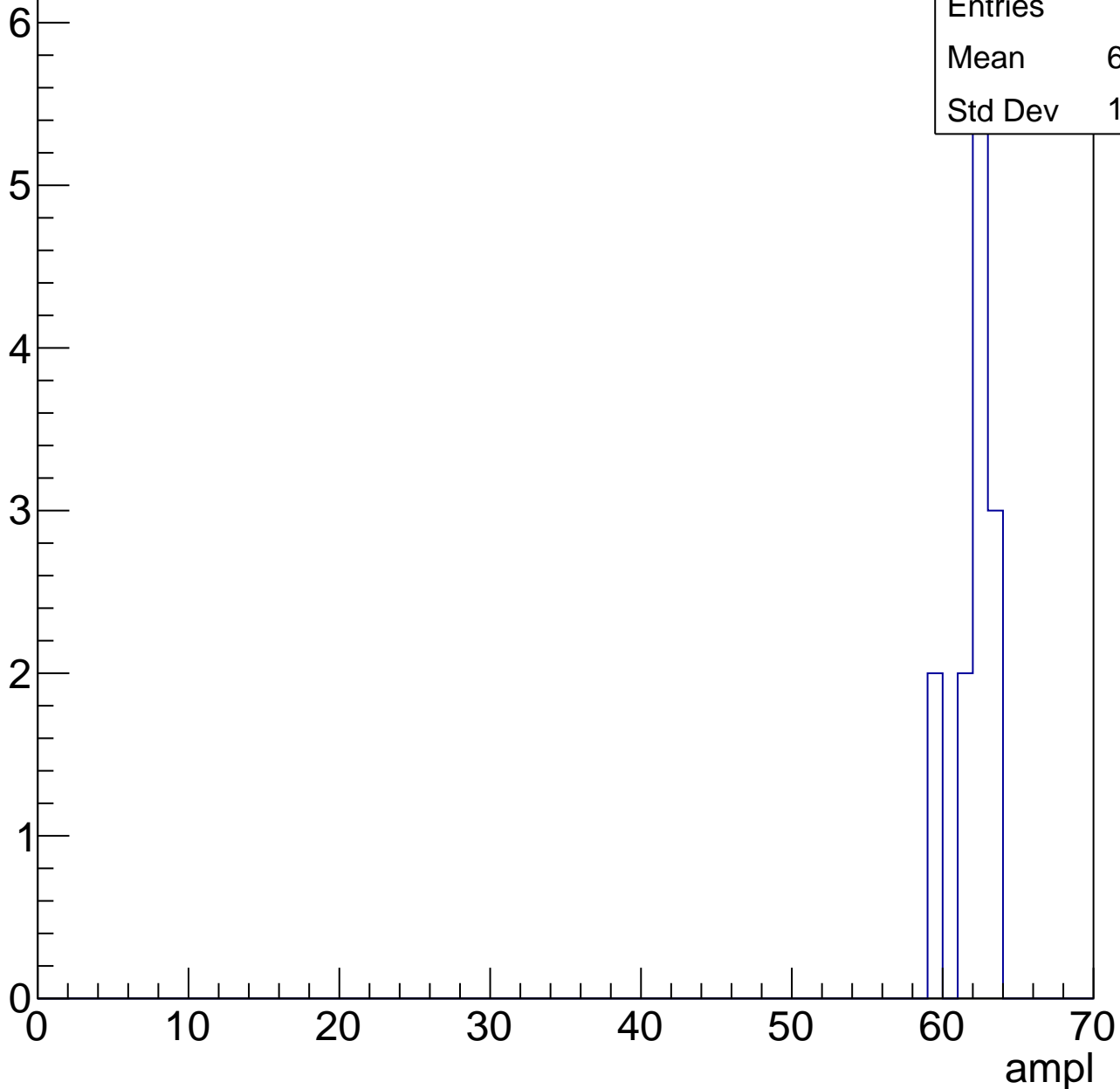


# B0L101S, U23-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.62
Std Dev	1.273





# B0L101S, U23-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

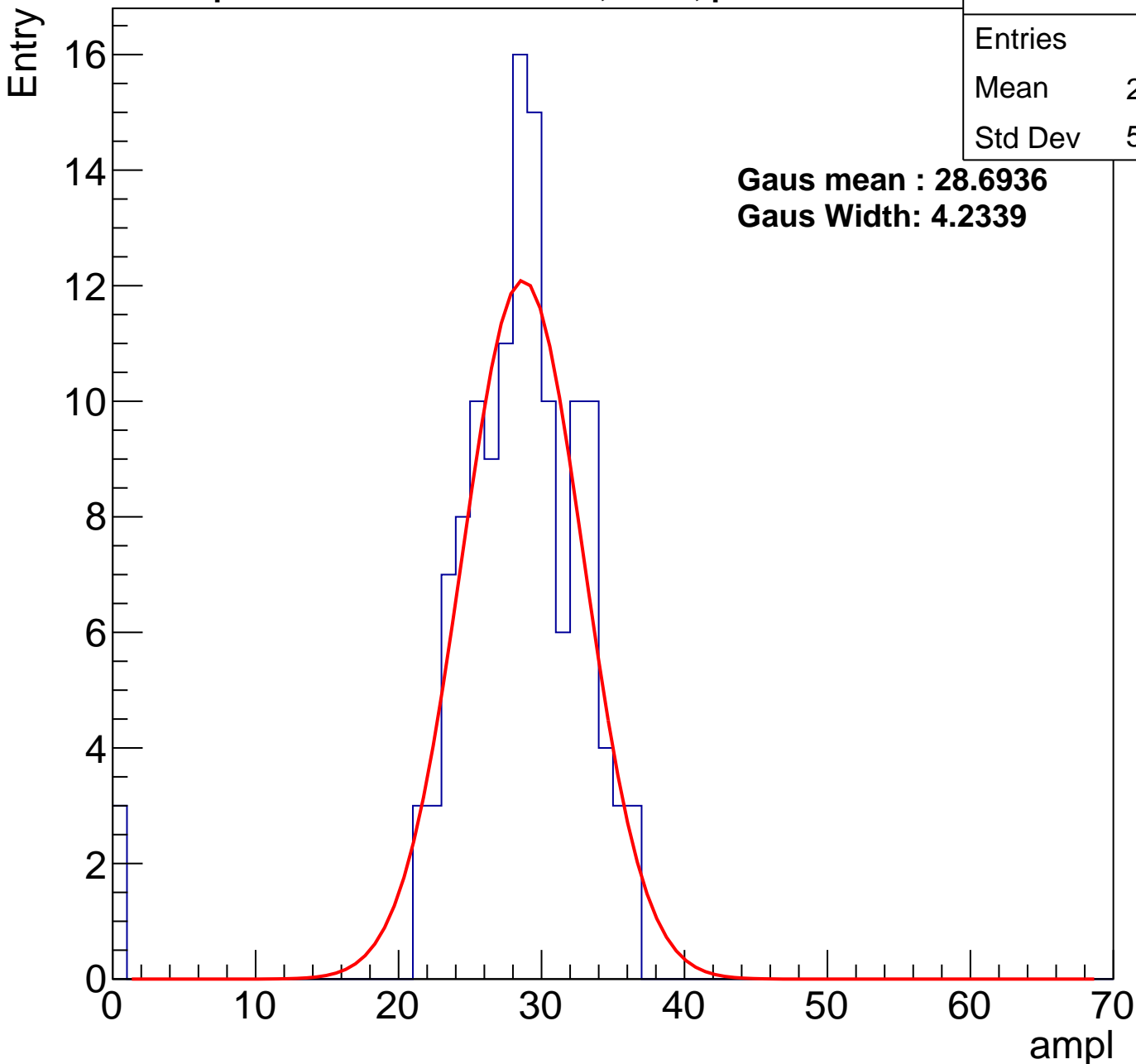
# B0L101S, U23-ch96, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	27.72
Std Dev	5.552

**Gaus mean : 28.6936**

**Gaus Width: 4.2339**



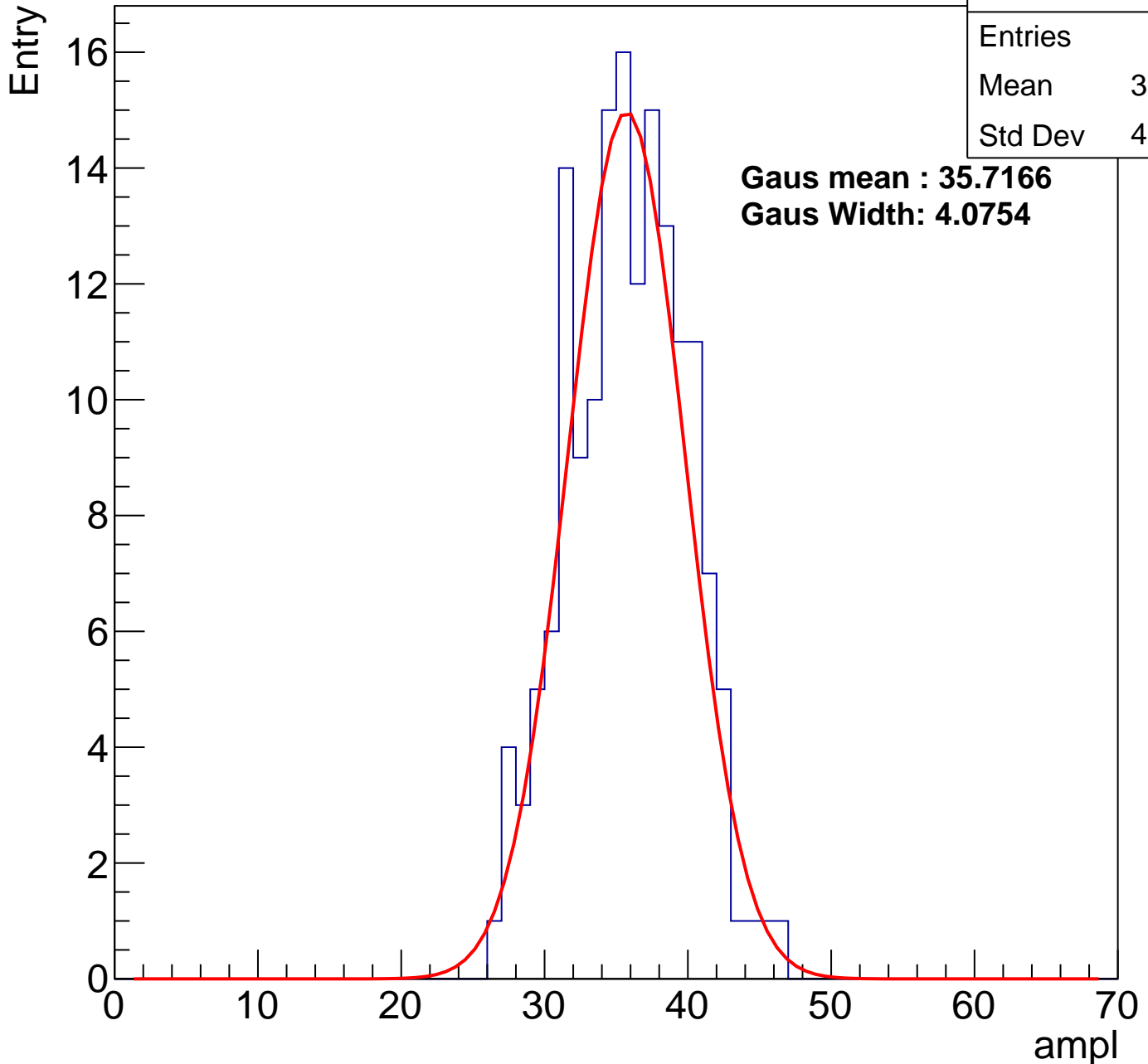
# B0L101S, U23-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	35.34
Std Dev	4.063

**Gaus mean : 35.7166**

**Gaus Width: 4.0754**



# B0L101S, U23-ch96, adc2

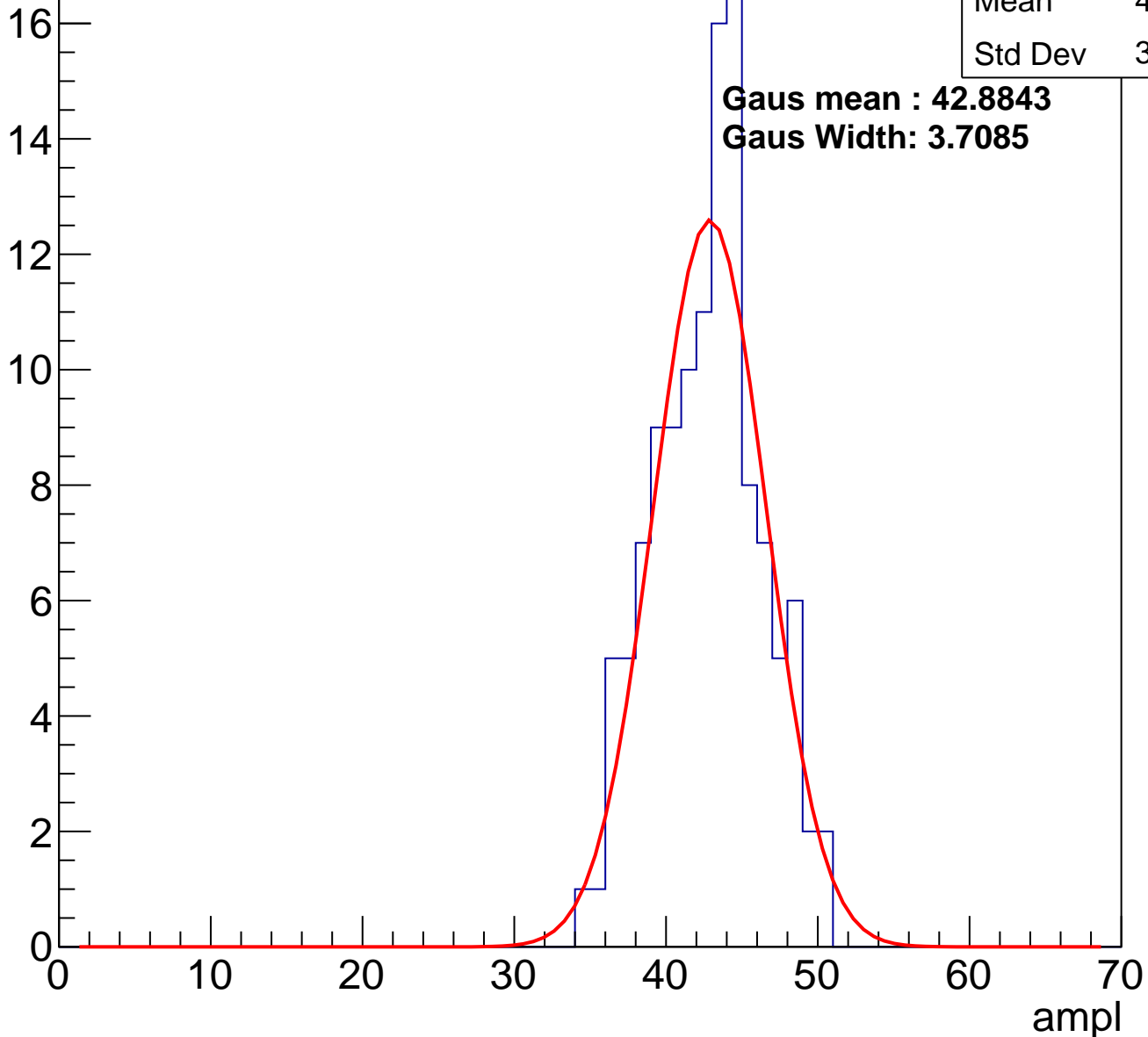
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	121
Mean	42.33
Std Dev	3.496

**Gaus mean : 42.8843**

**Gaus Width: 3.7085**



# B0L101S, U23-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	48.96
Std Dev	3.645

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

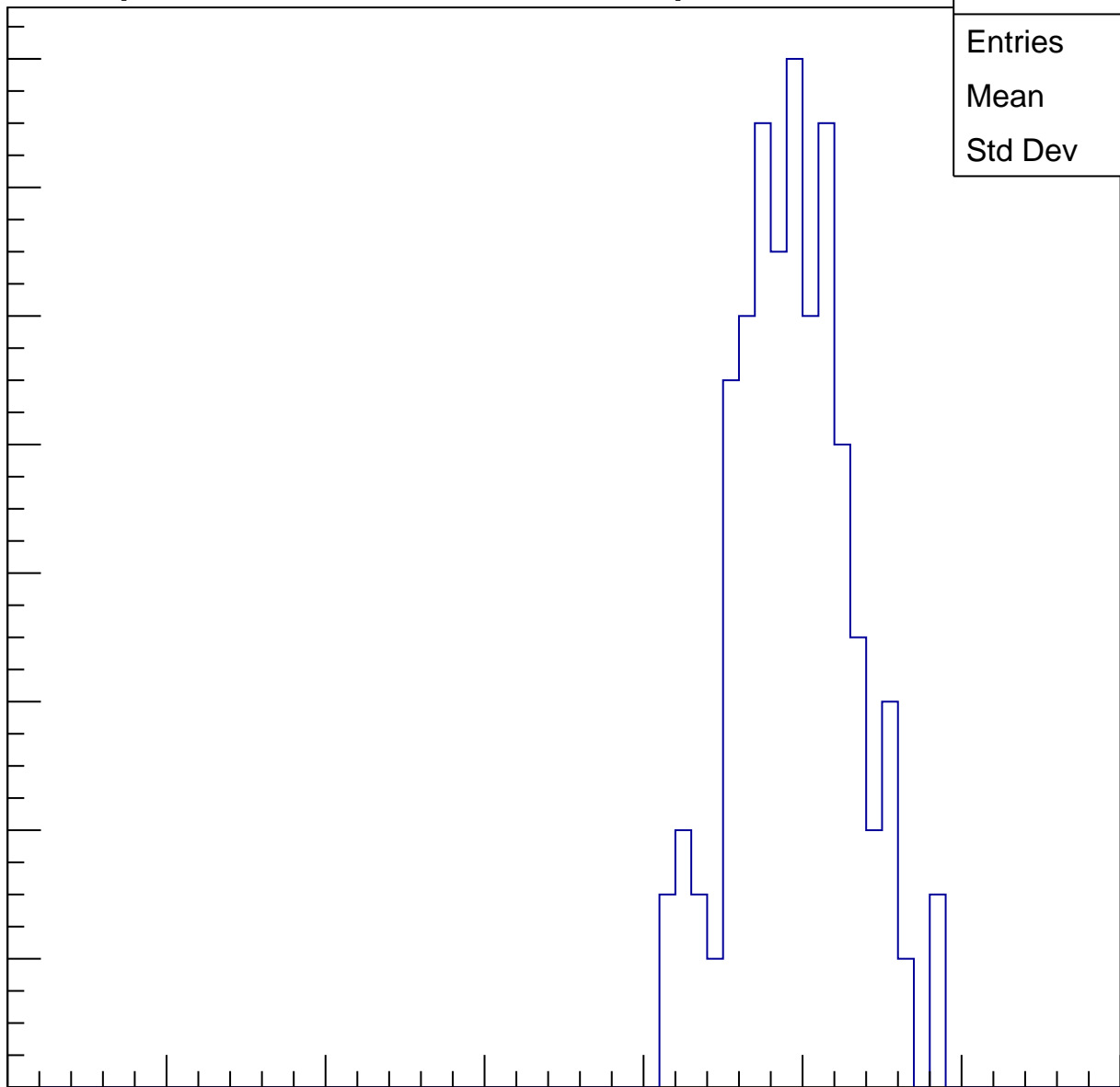
40

50

60

70

ampl



# B0L101S, U23-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

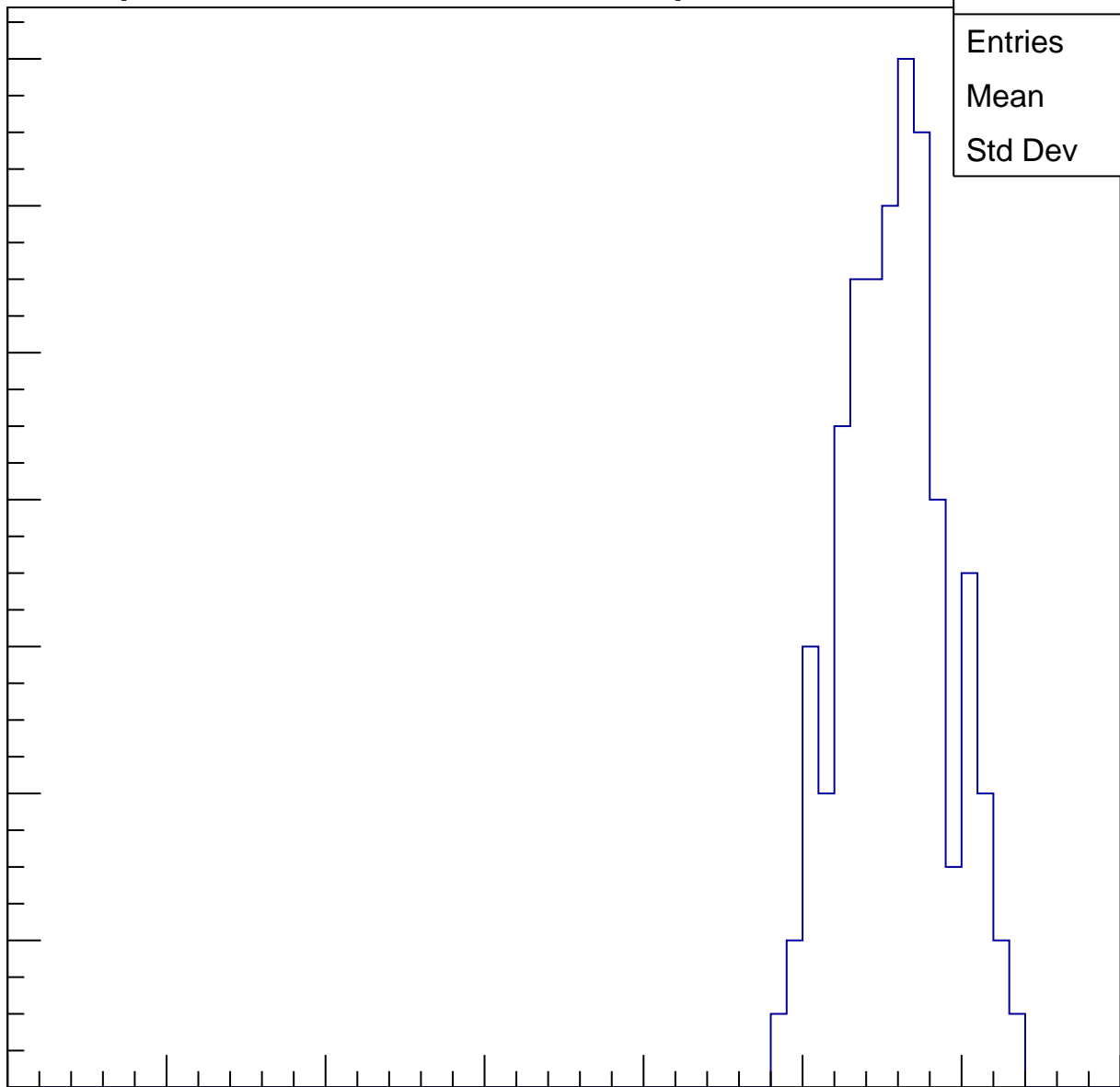
Entries	108
Mean	55.3
Std Dev	3.233

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch96, adc5

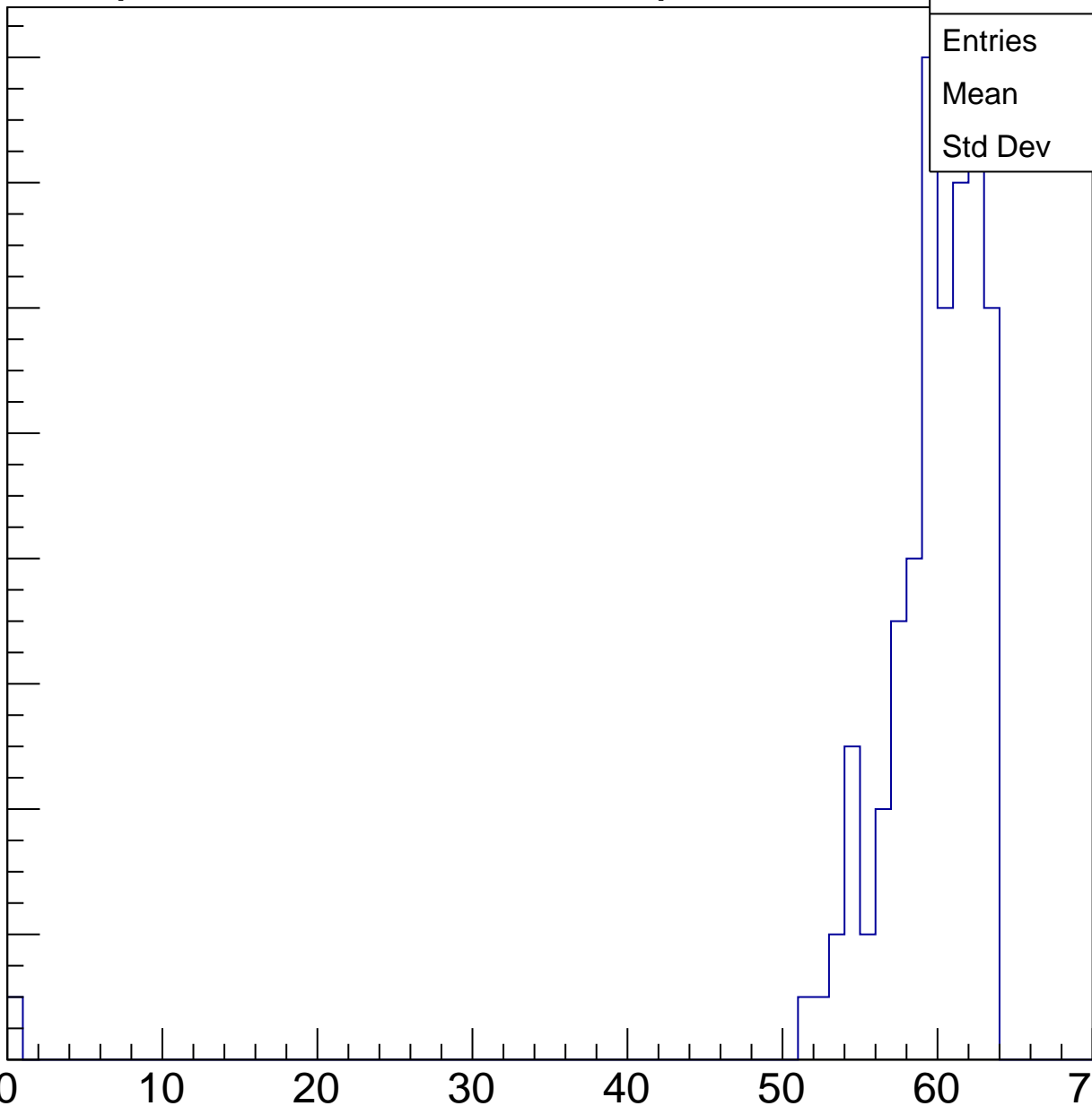
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	100
Mean	58.8
Std Dev	6.547

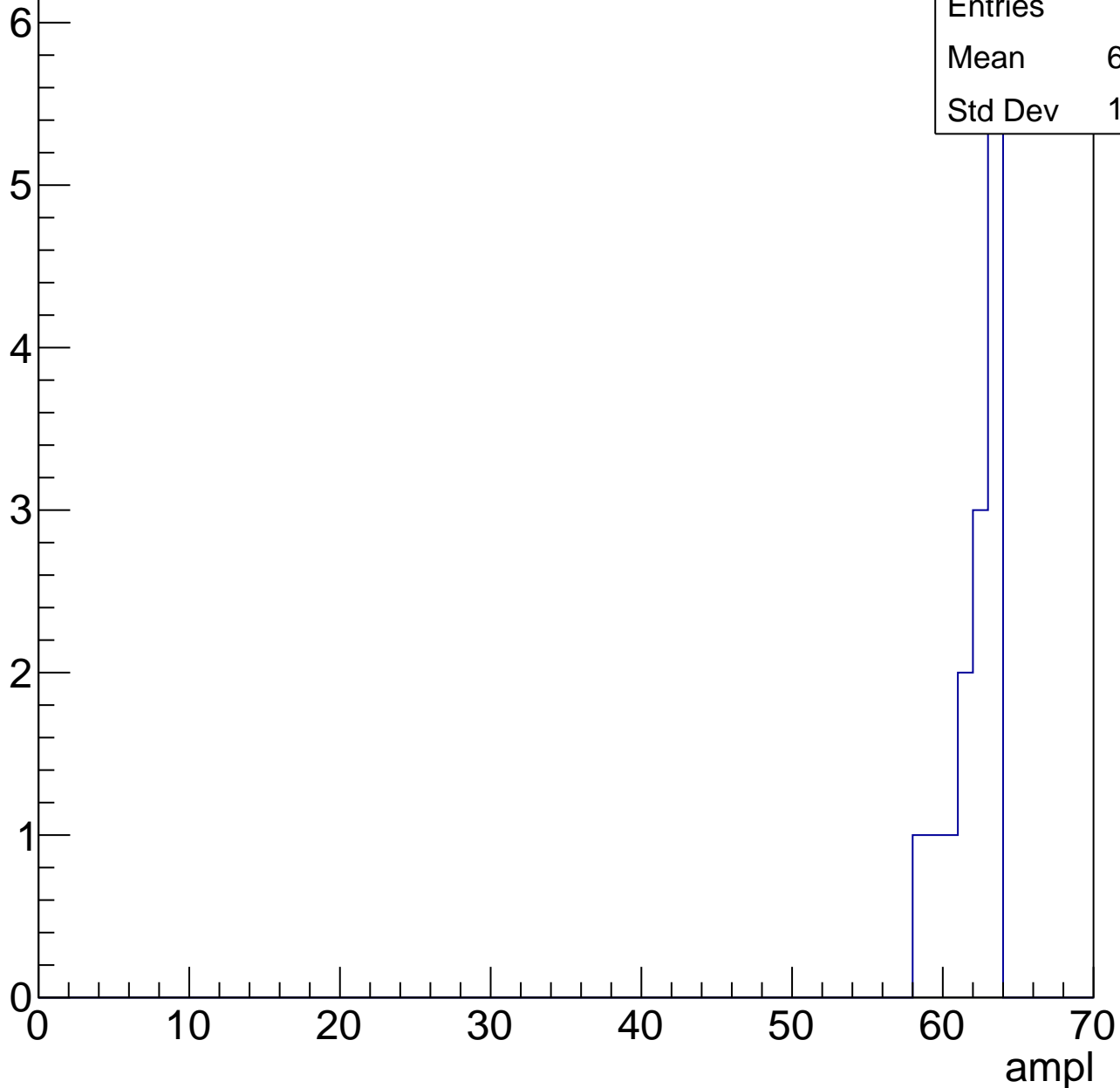
ampl



# B0L101S, U23-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	29.8
Std Dev	4.17

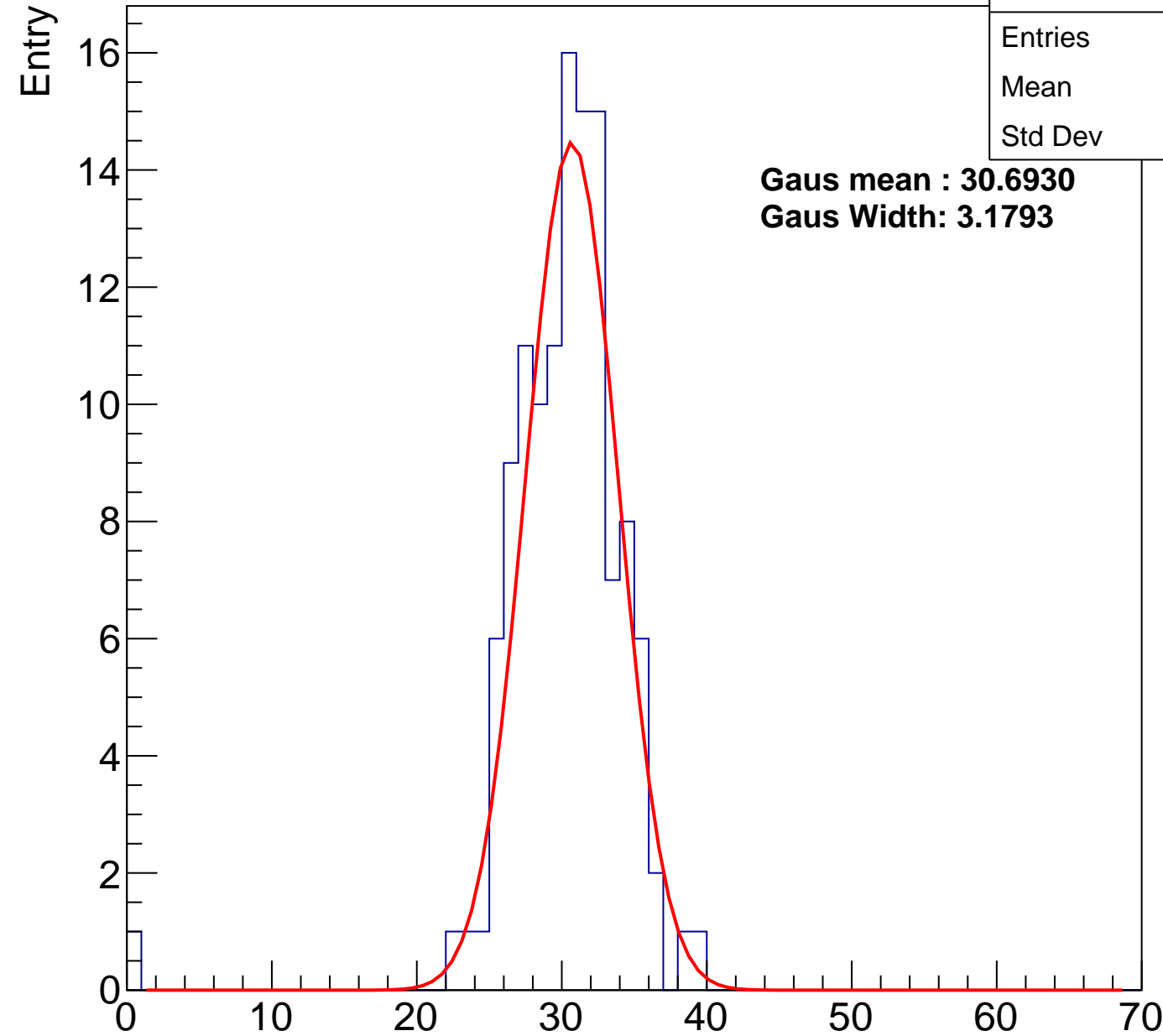
**Gaus mean : 30.6930**

**Gaus Width: 3.1793**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch97, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	36.27
Std Dev	5.609

**Gaus mean : 37.3440**

**Gaus Width: 3.6572**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

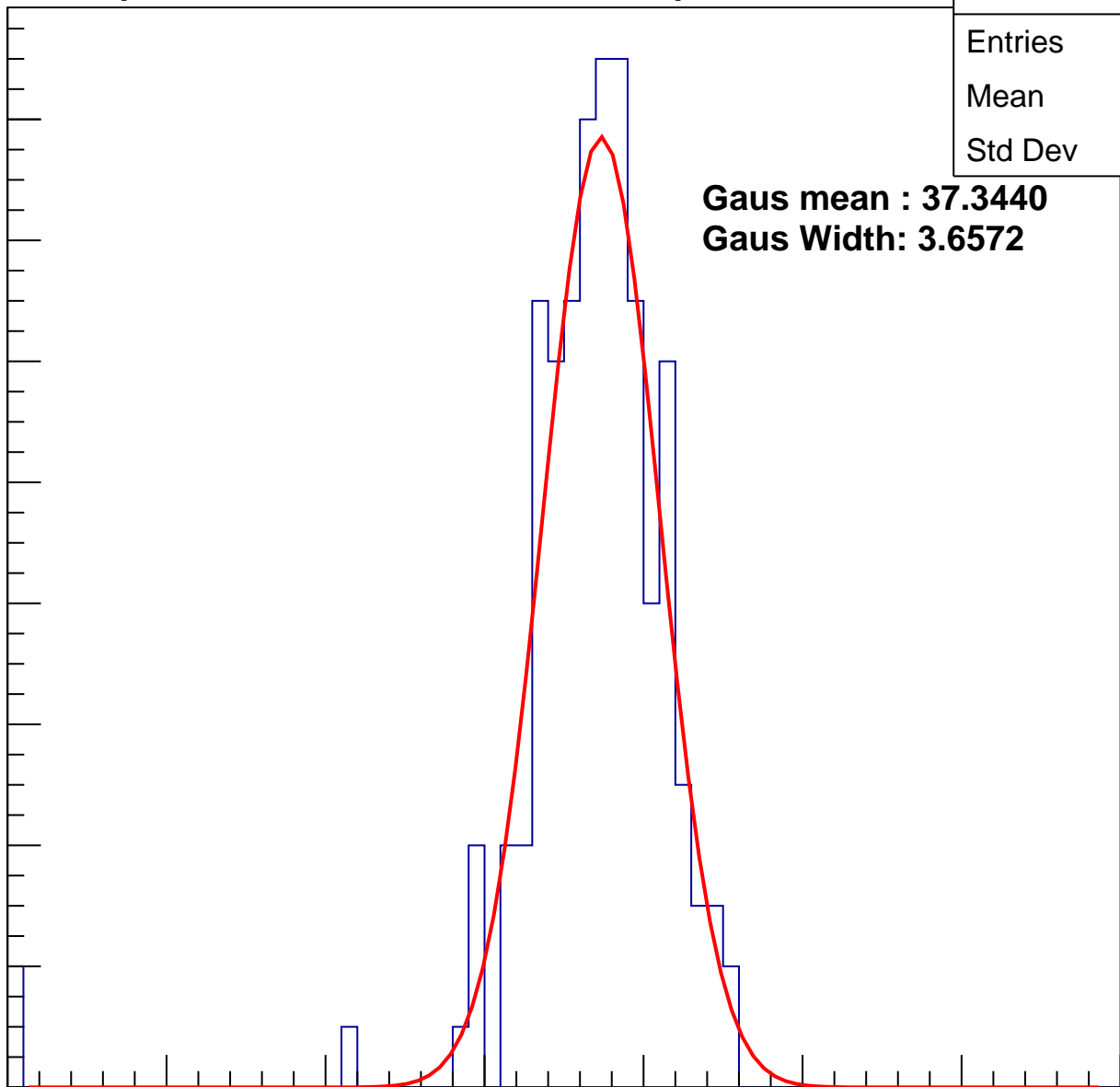
40

50

60

70

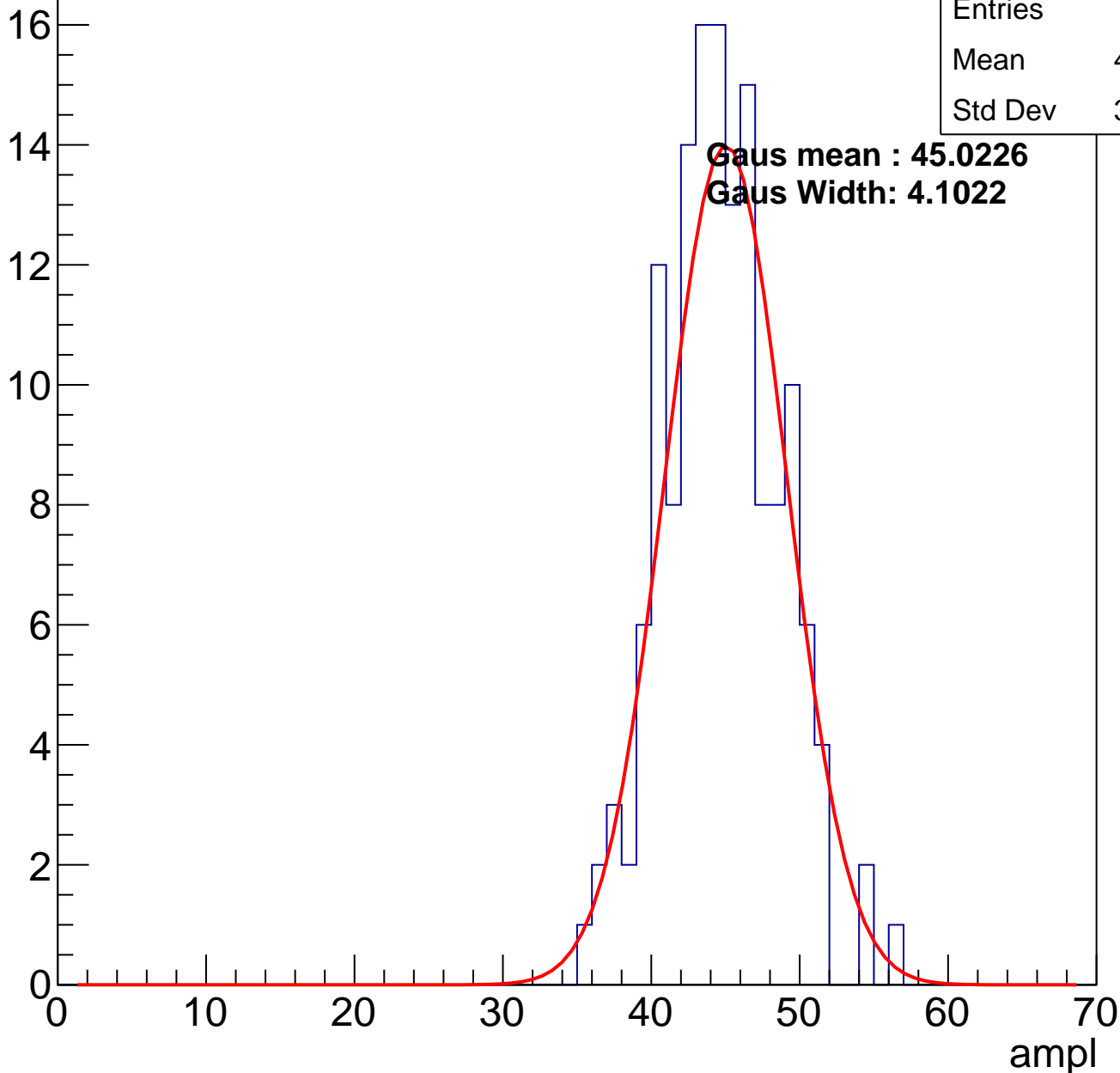
ampl



# B0L101S, U23-ch97, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

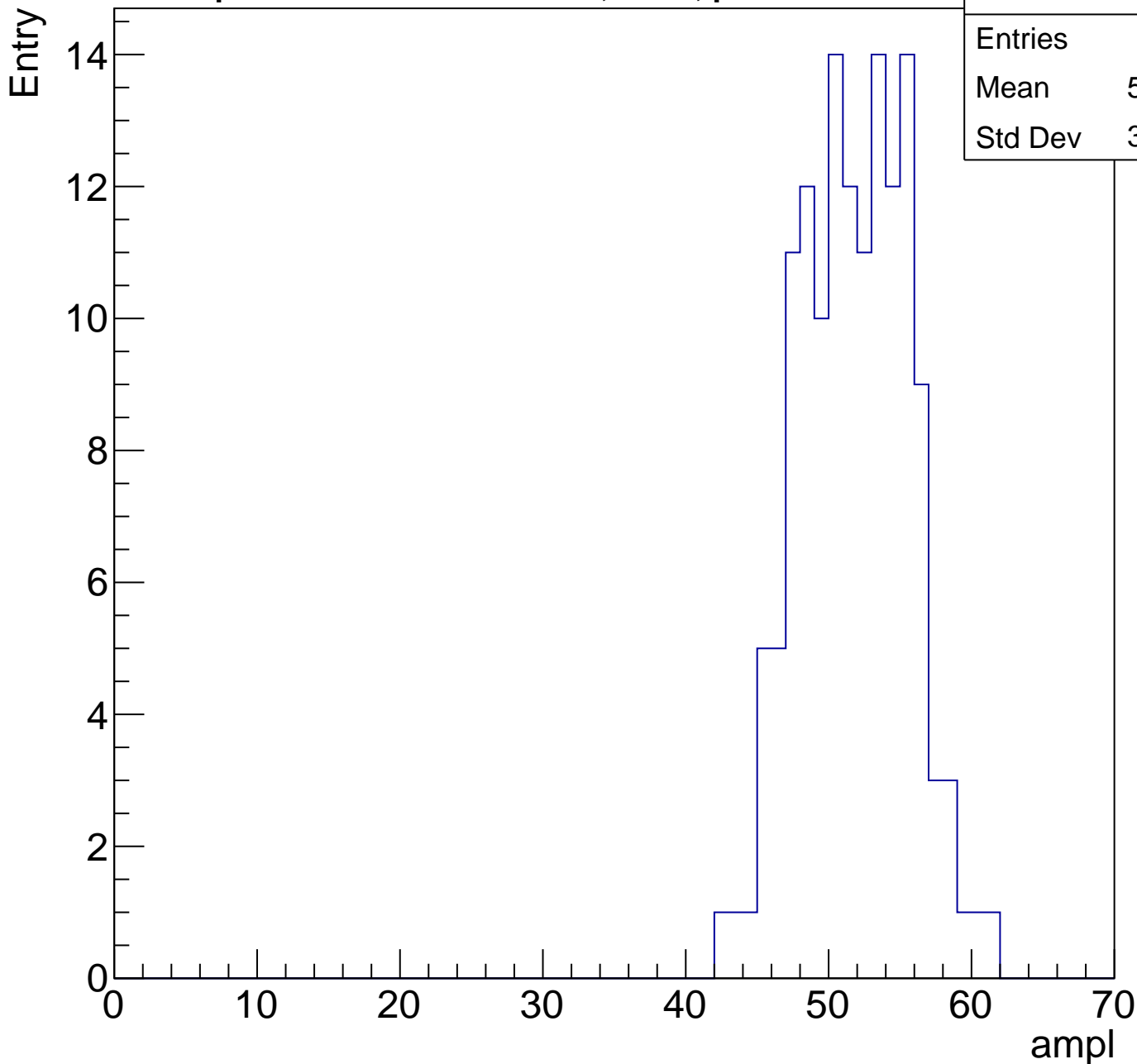
Entry



# B0L101S, U23-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	51.35
Std Dev	3.713



# B0L101S, U23-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

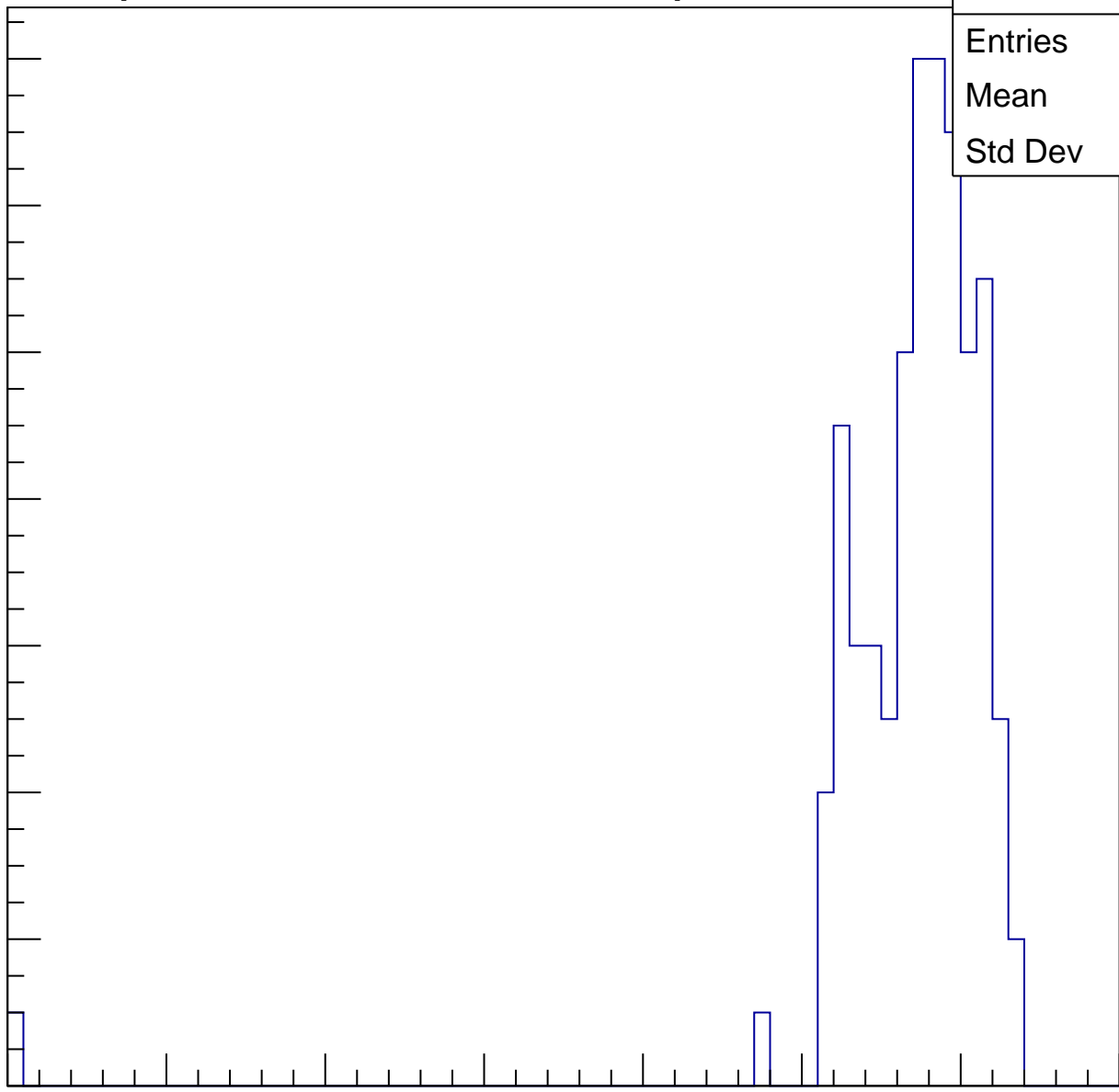
Entries	111
Mean	56.58
Std Dev	6.301

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch97, adc5

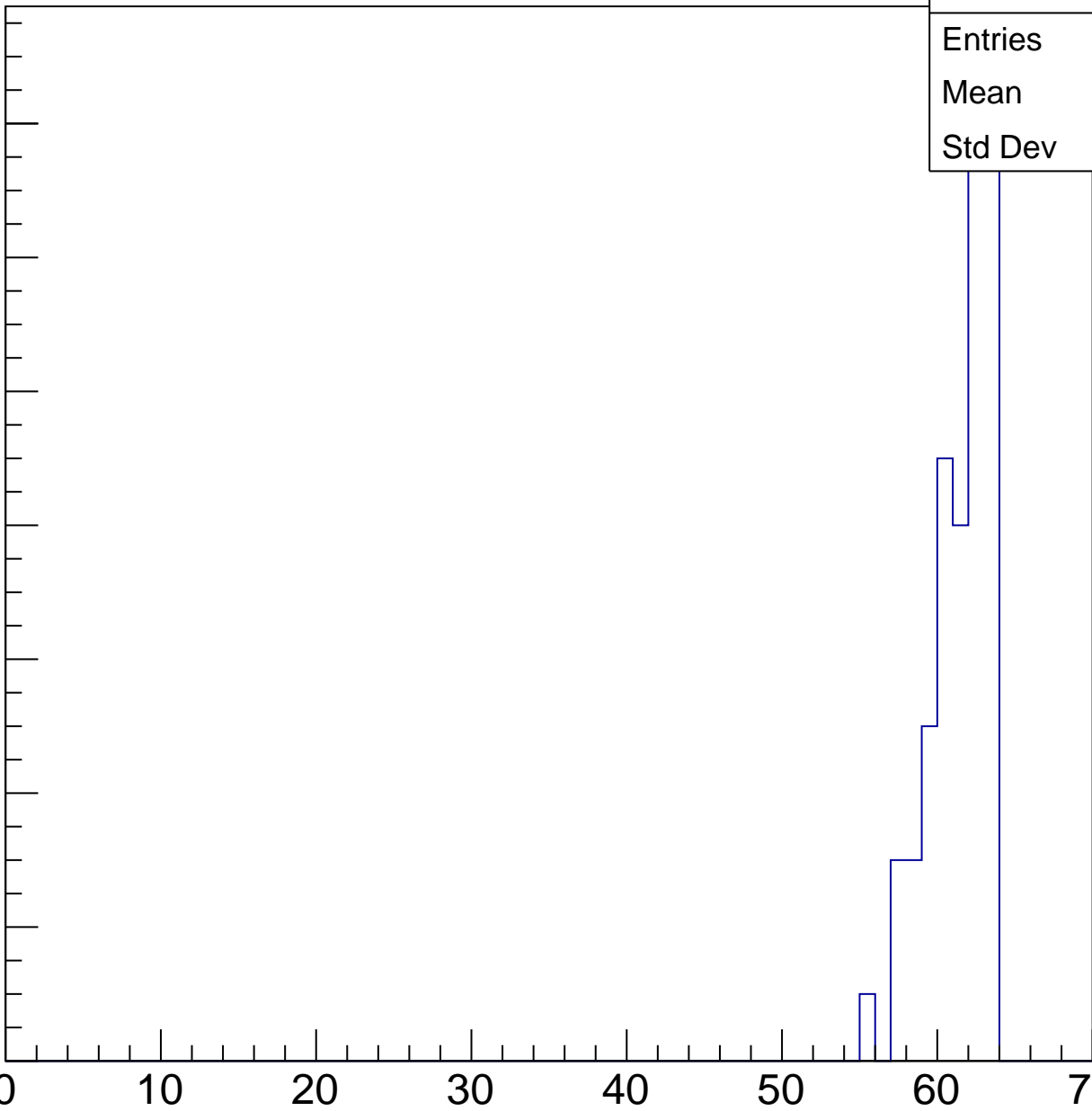
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	58
Mean	60.95
Std Dev	1.907

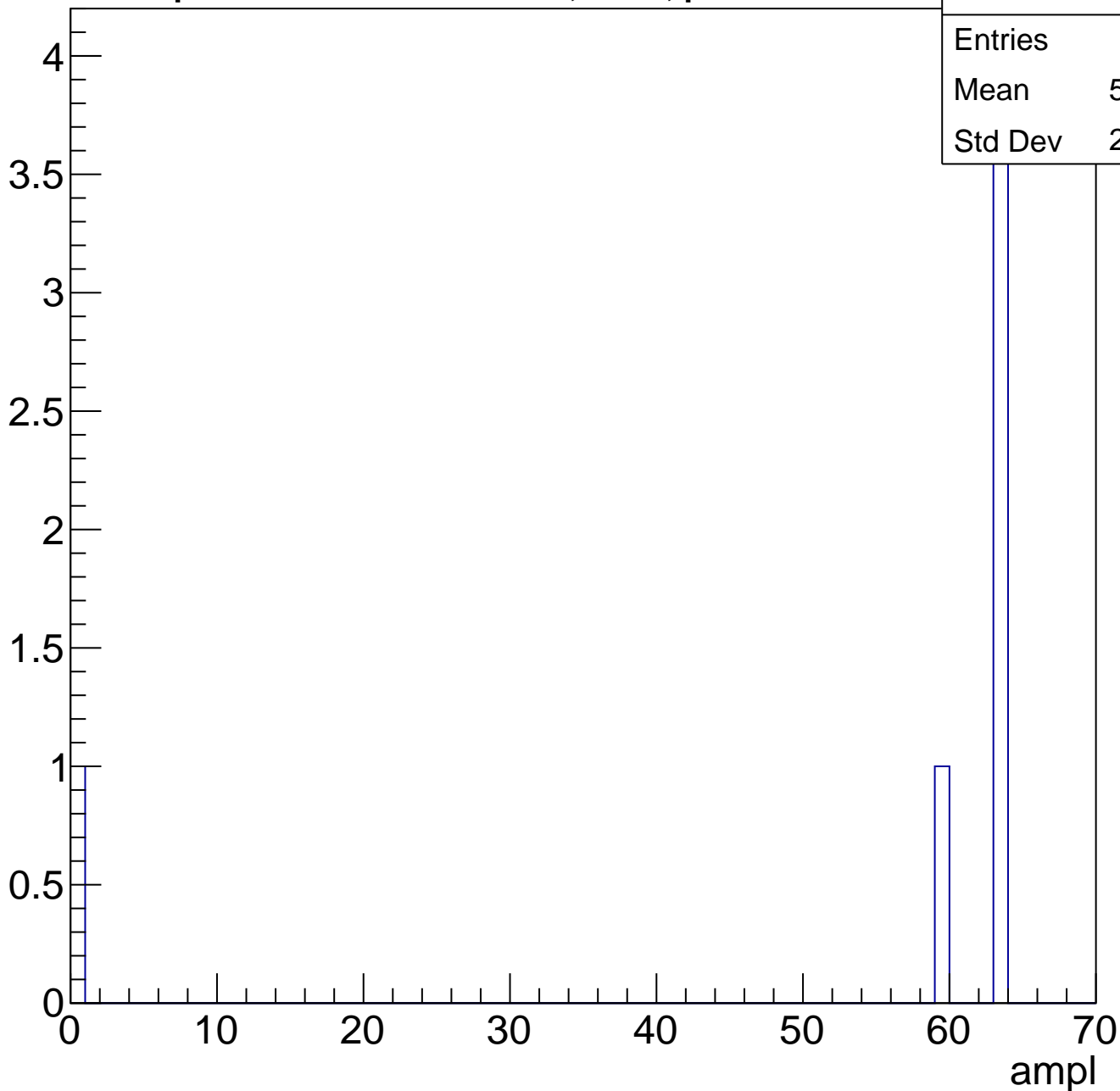
ampl



# B0L101S, U23-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

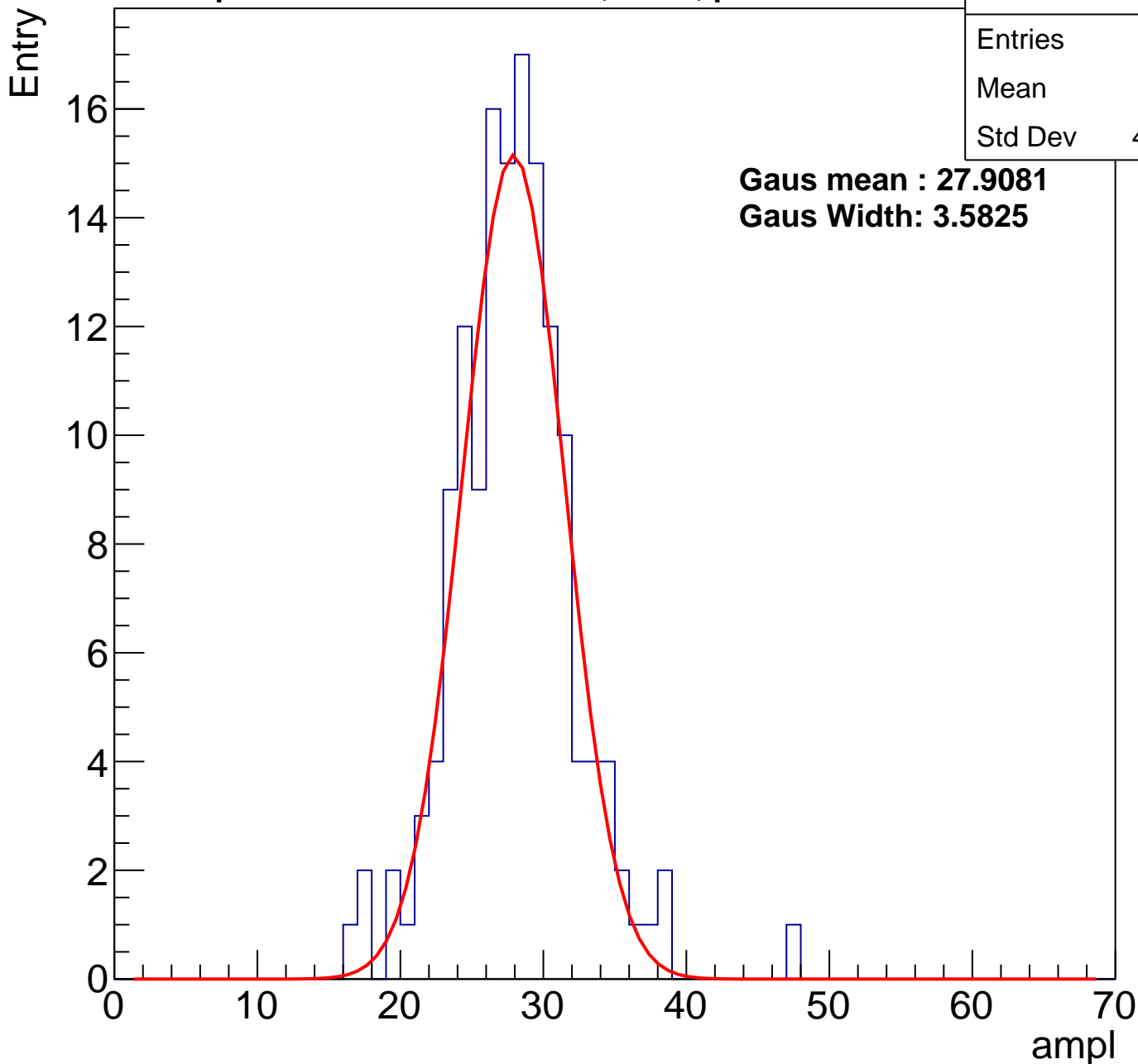
# B0L101S, U23-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	27.5
Std Dev	4.293

**Gaus mean : 27.9081**

**Gaus Width: 3.5825**

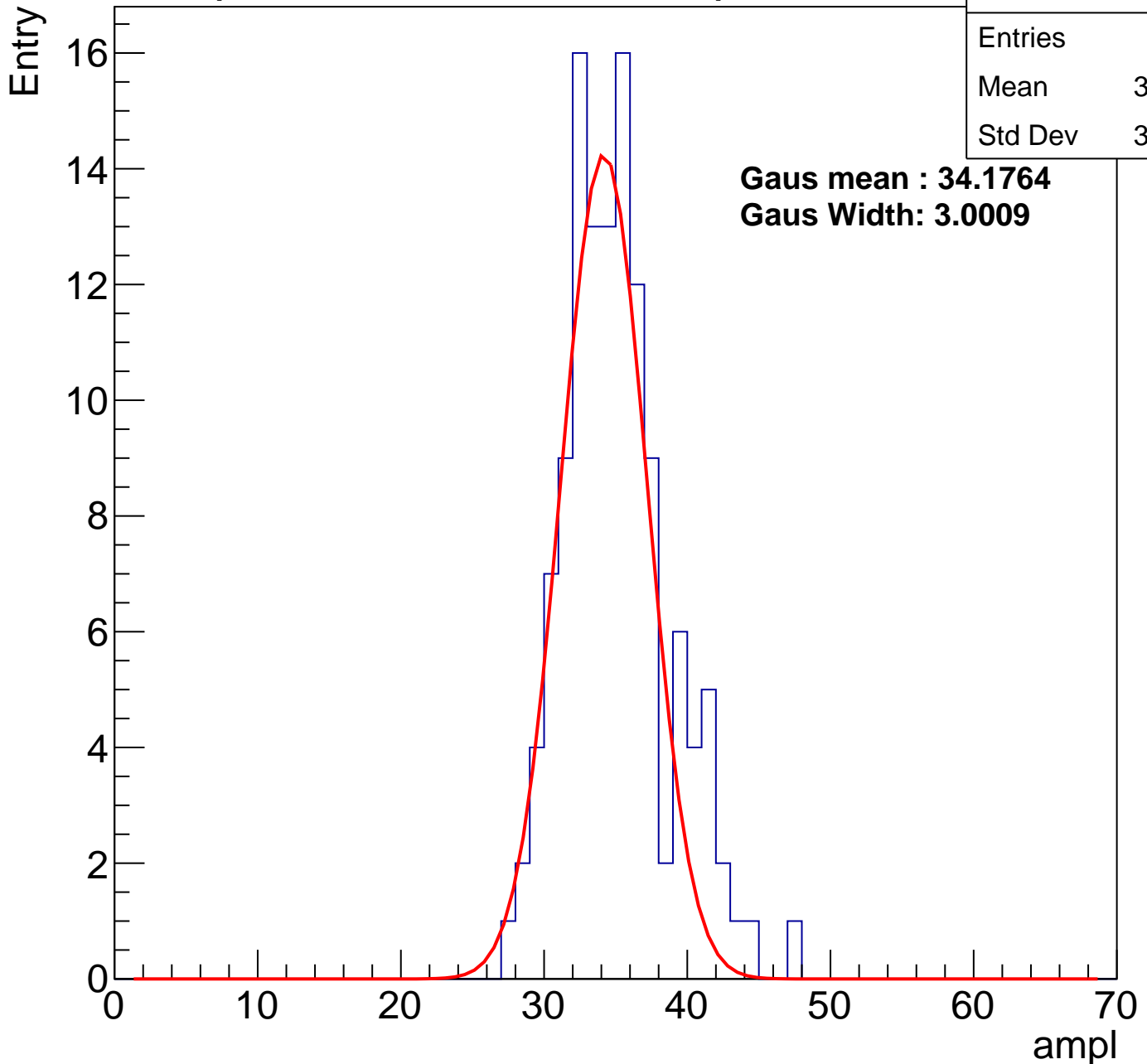


# B0L101S, U23-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	34.59
Std Dev	3.639

**Gaus mean : 34.1764**  
**Gaus Width: 3.0009**



# B0L101S, U23-ch98, adc2

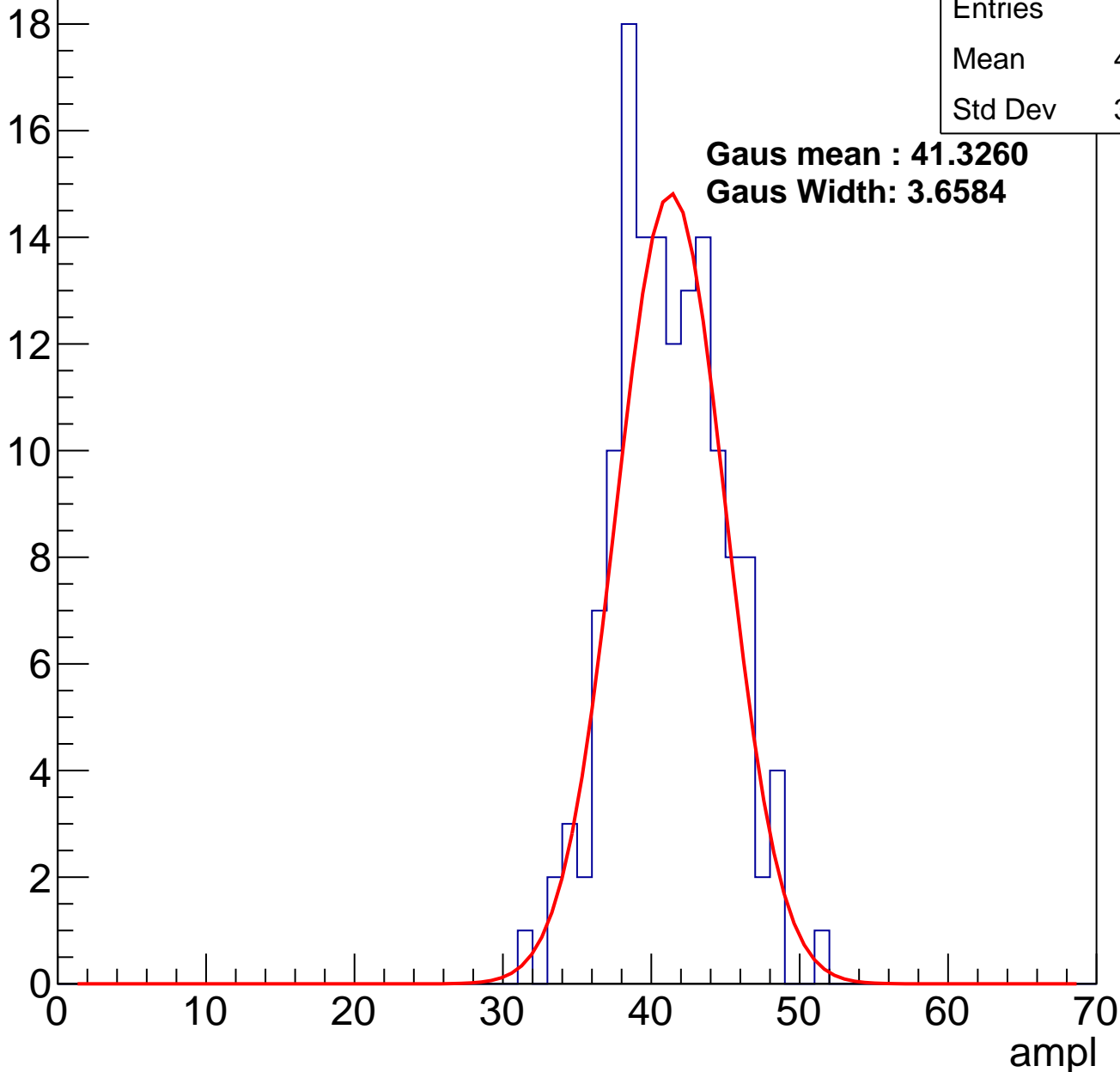
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	143
Mean	40.74
Std Dev	3.606

**Gaus mean : 41.3260**

**Gaus Width: 3.6584**

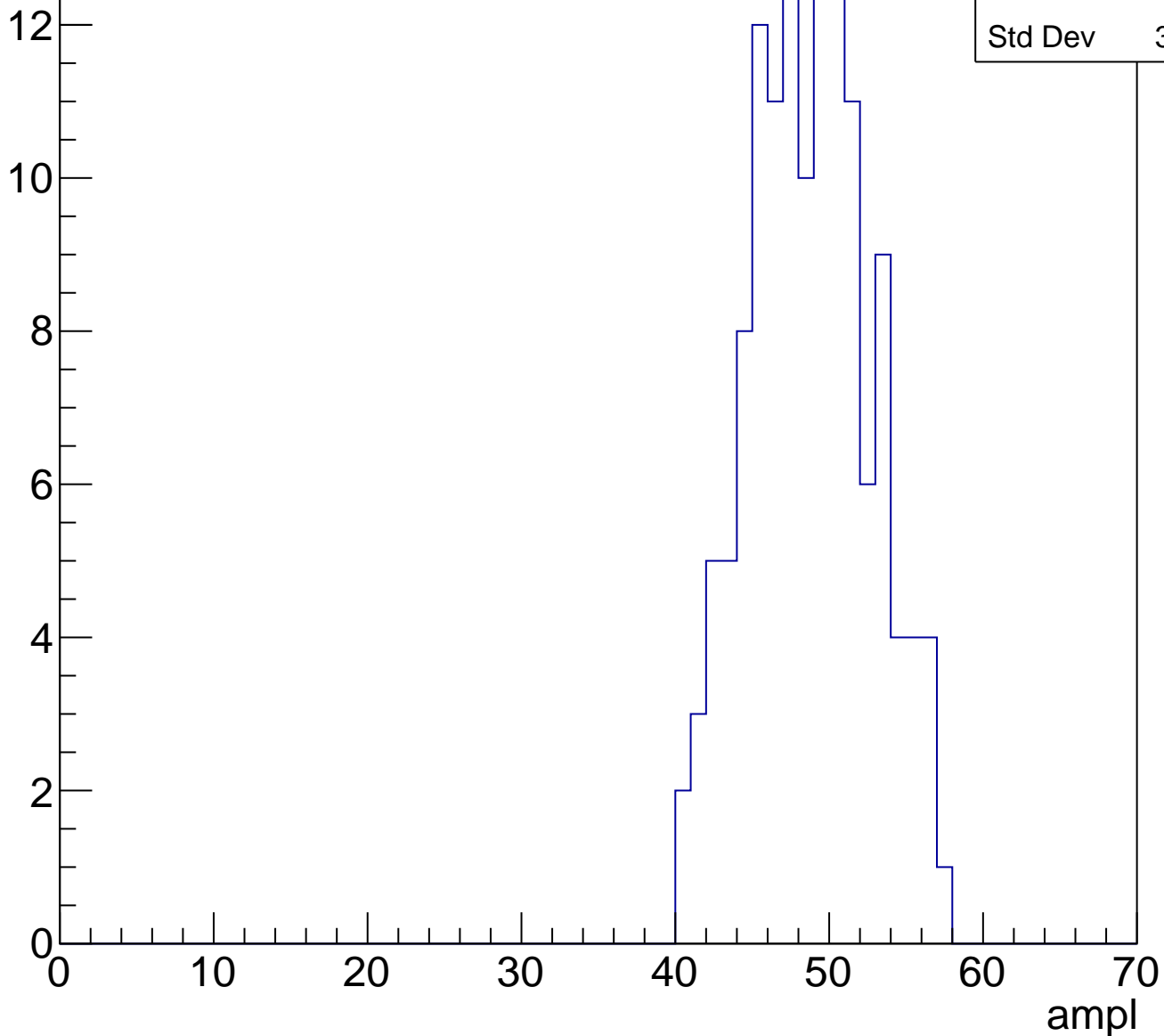


# B0L101S, U23-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	48.29
Std Dev	3.886

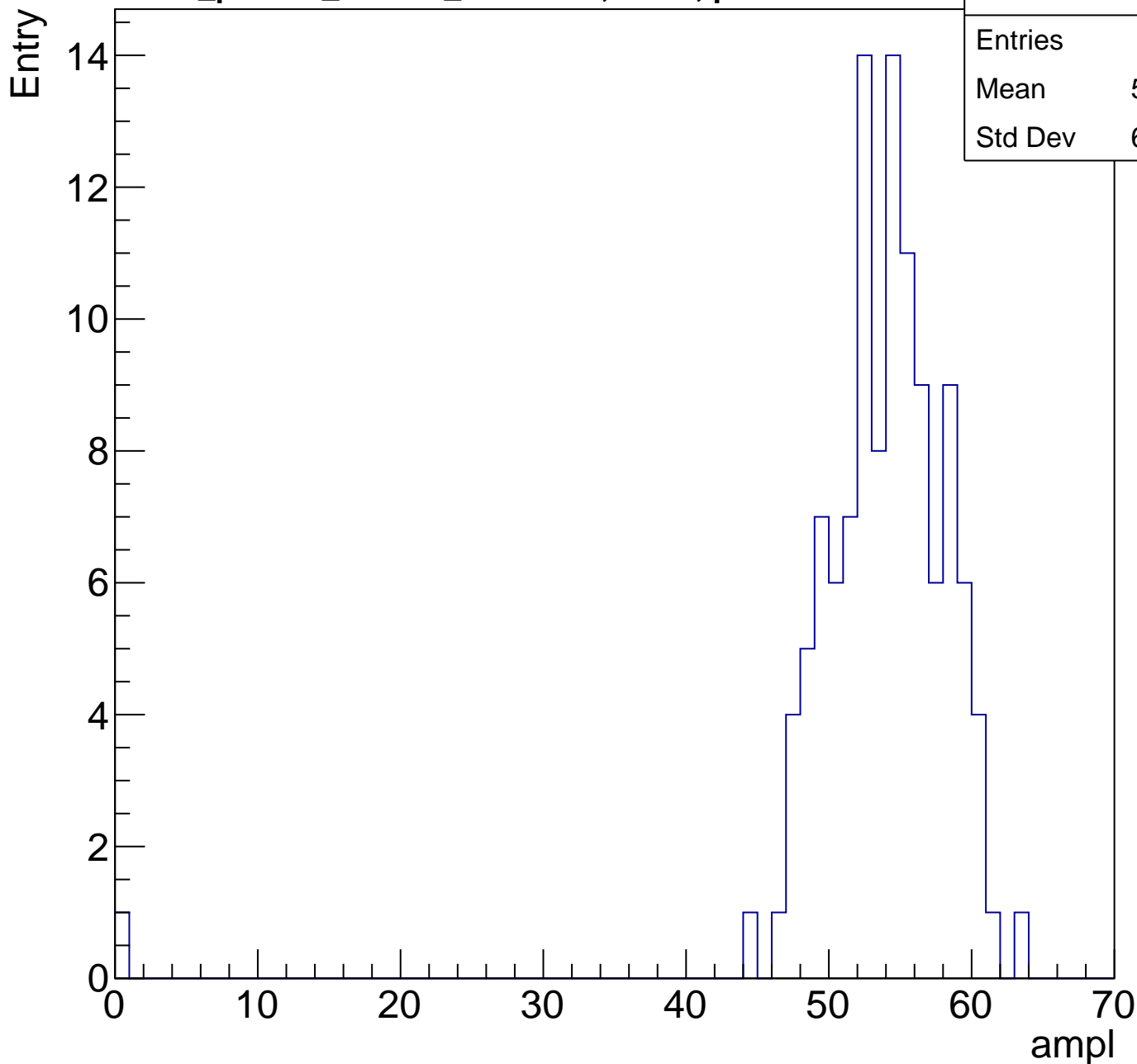
Entry



# B0L101S, U23-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	53.19
Std Dev	6.219



# B0L101S, U23-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

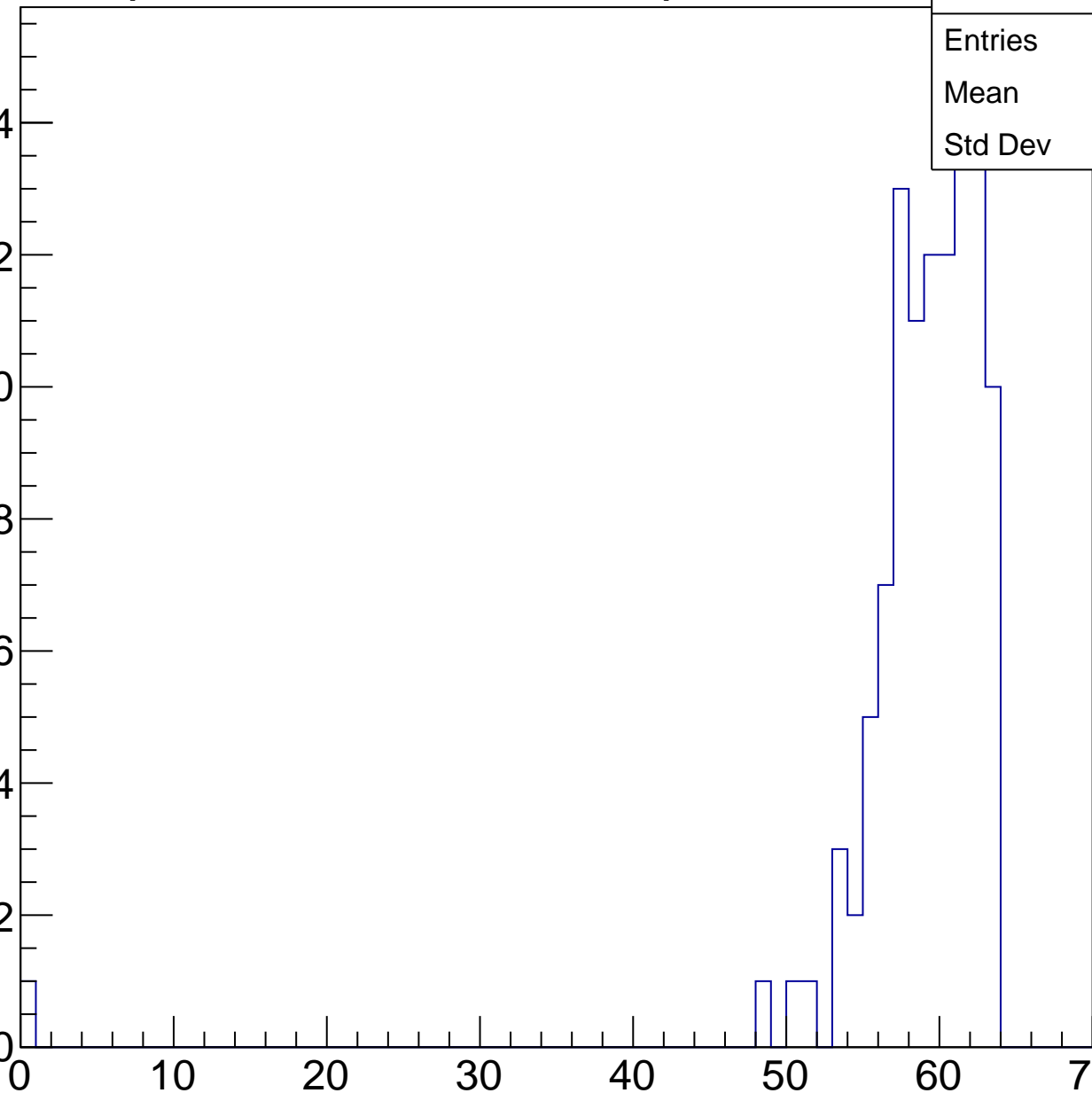
0

Entries 108

Mean 58.36

Std Dev 6.401

ampl

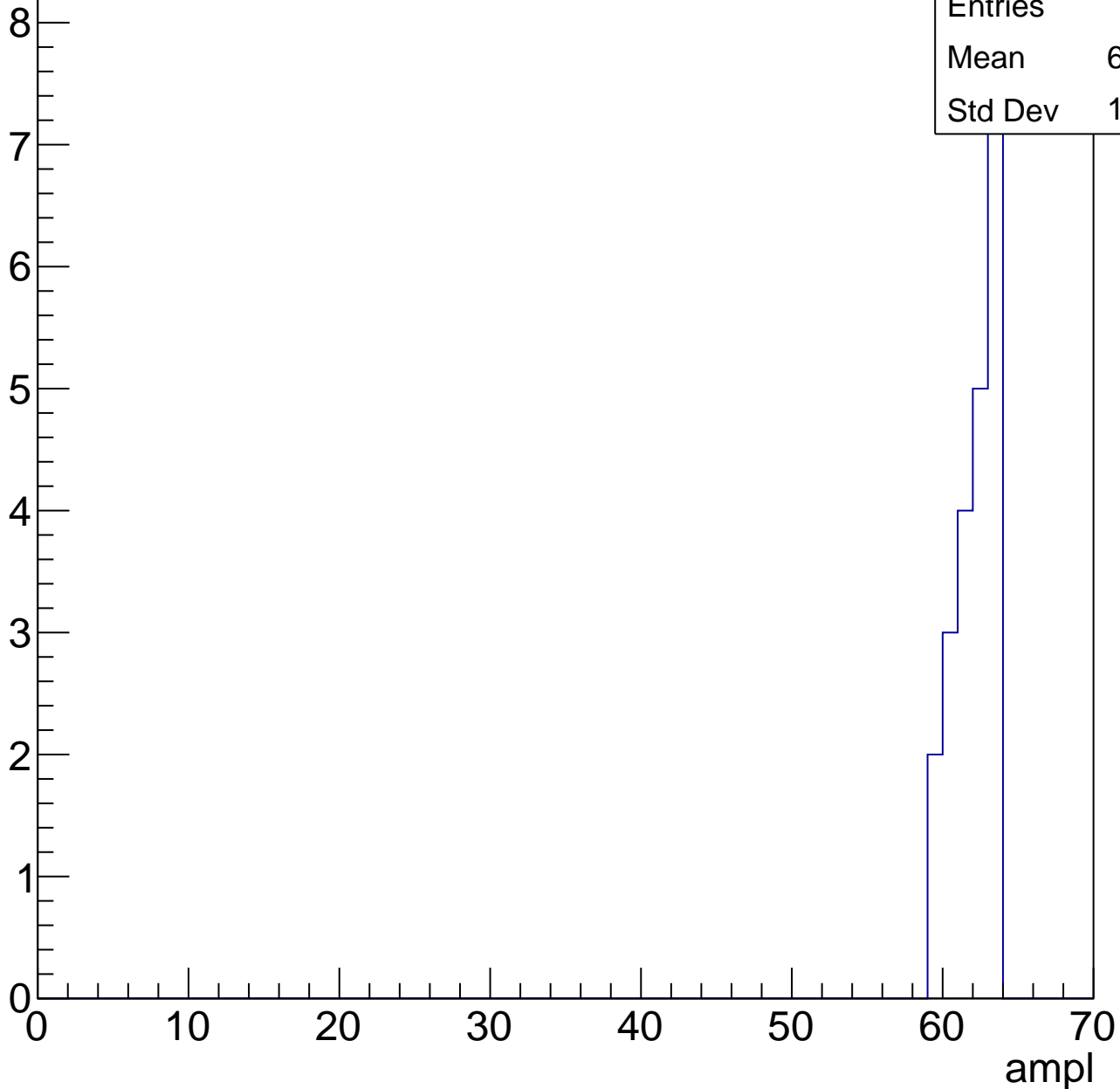


# B0L101S, U23-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	22
Mean	61.64
Std Dev	1.333

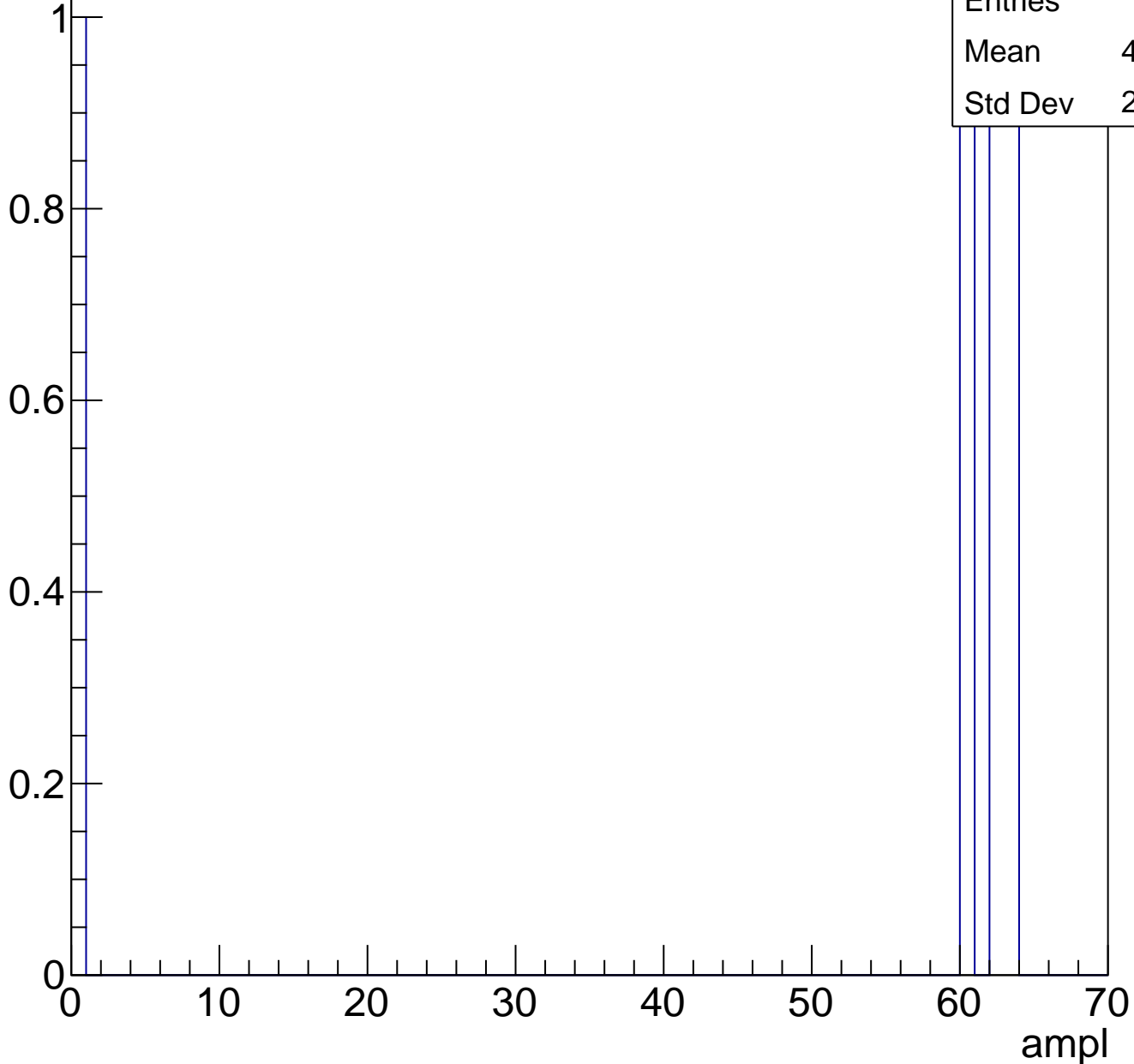




# B0L101S, U23-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

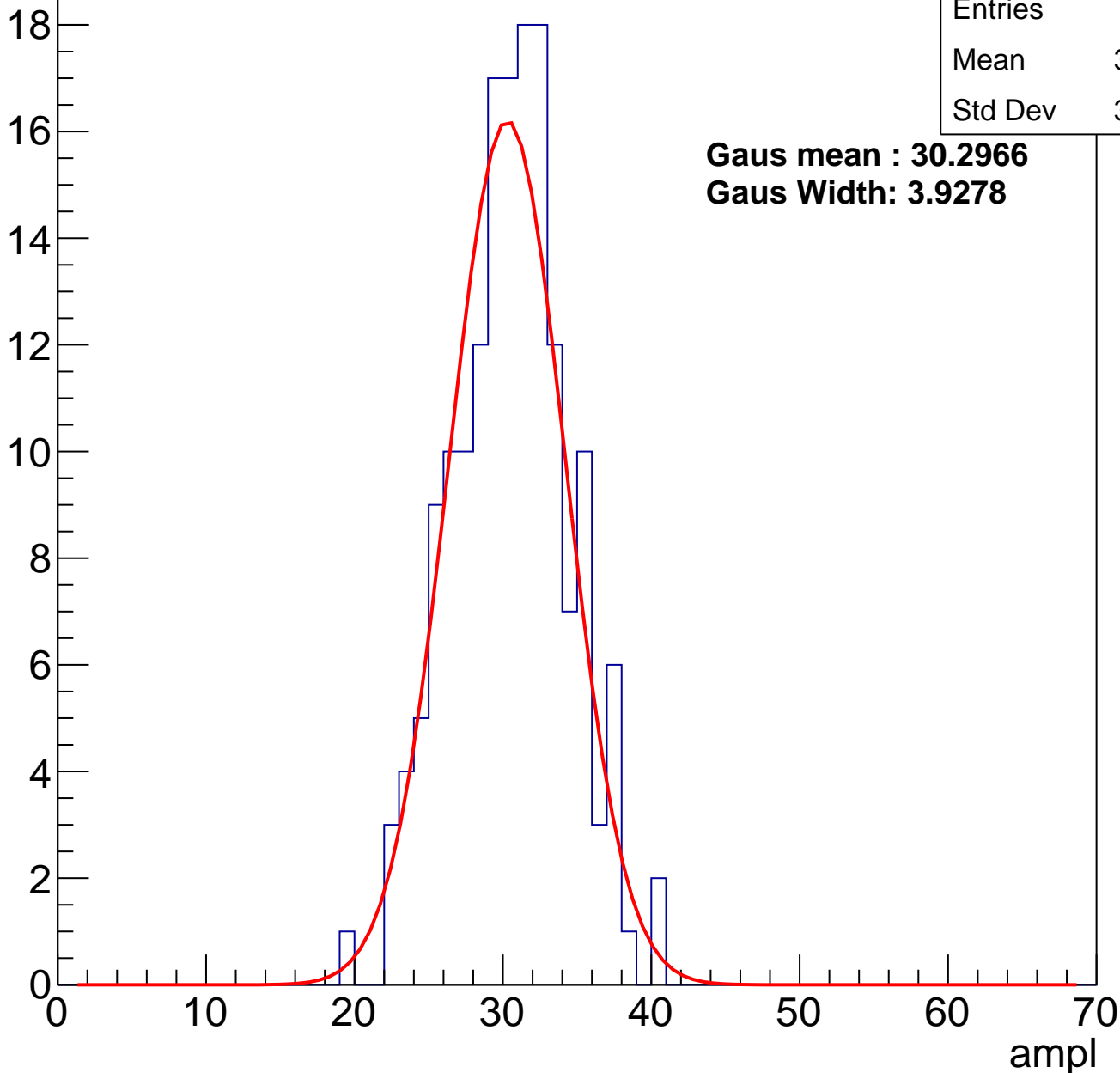
Entry



# B0L101S, U23-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	101
Mean	37.42
Std Dev	3.087

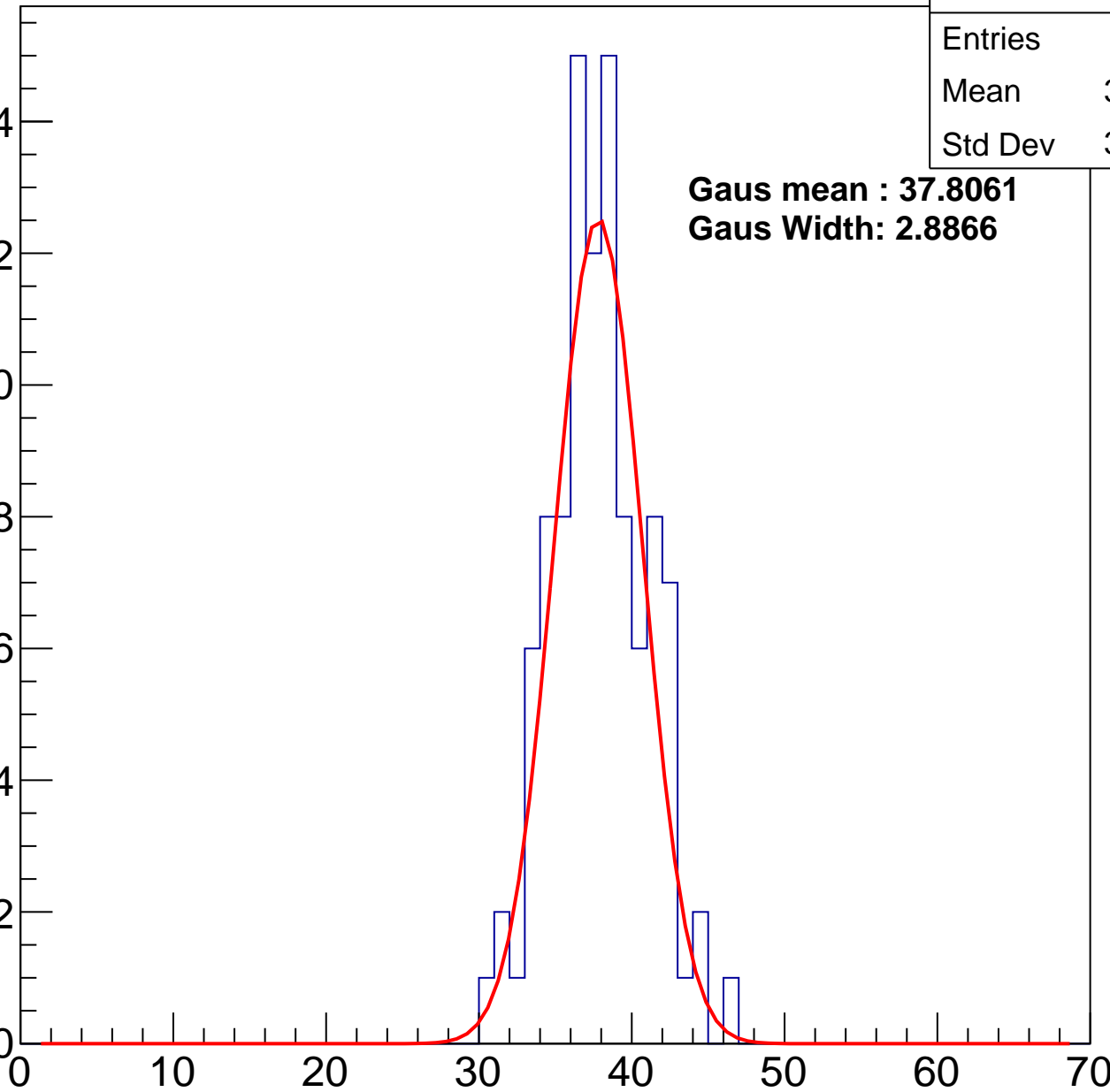
**Gaus mean : 37.8061**

**Gaus Width: 2.8866**

Entry

14  
12  
10  
8  
6  
4  
2  
0

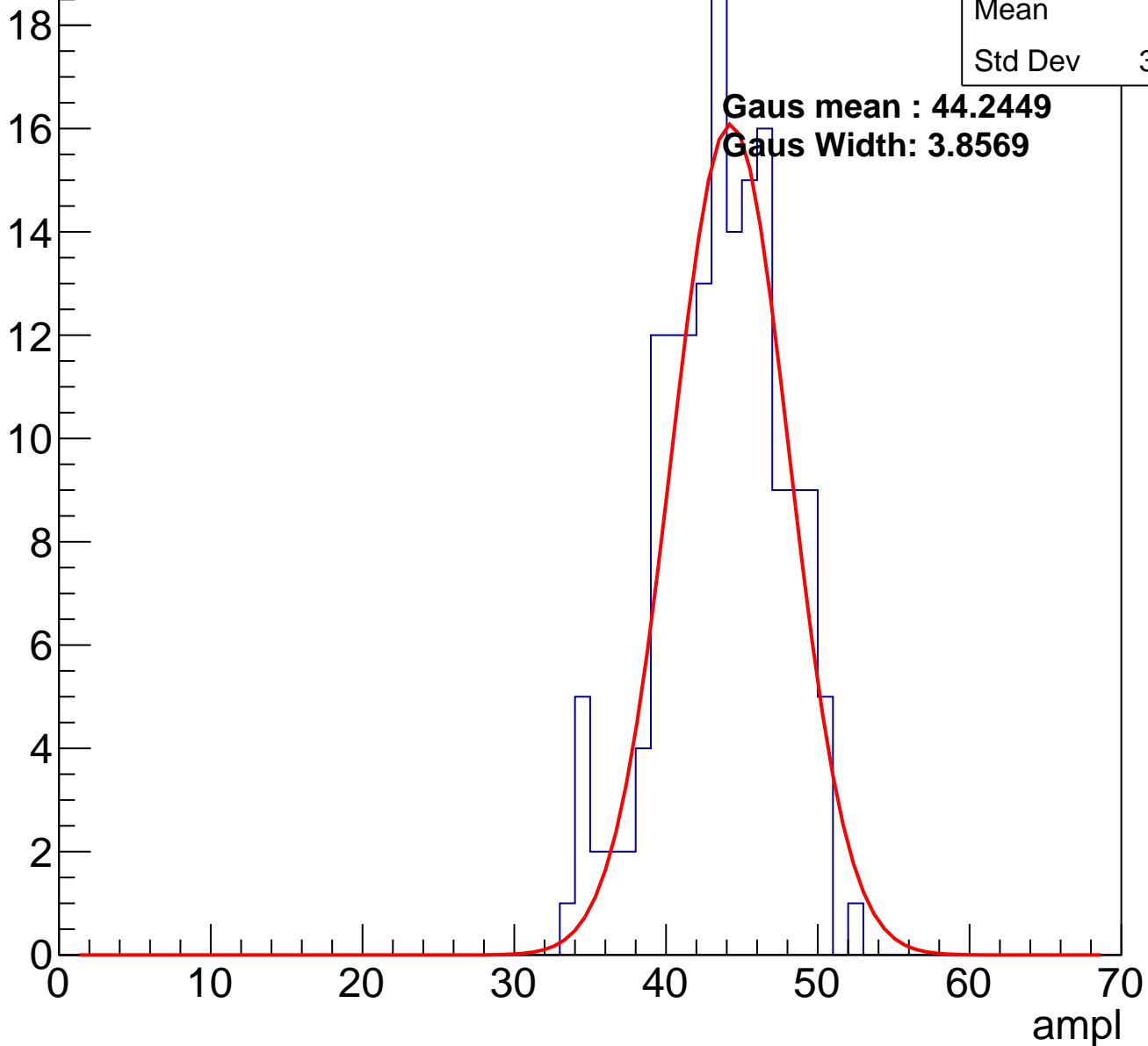
ampl



# B0L101S, U23-ch99, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	51.74
Std Dev	3.957

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

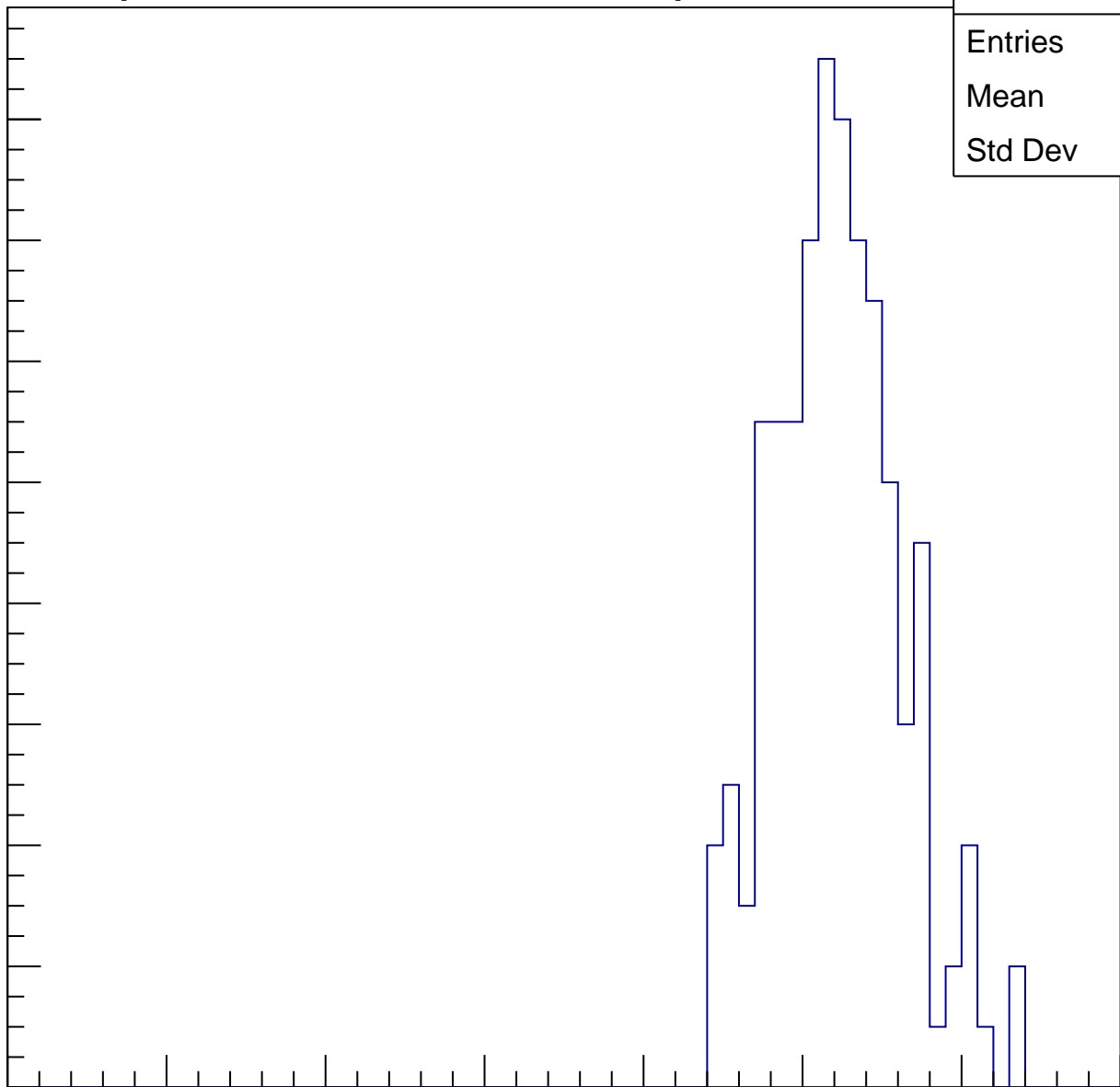
40

50

60

70

ampl



# B0L101S, U23-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

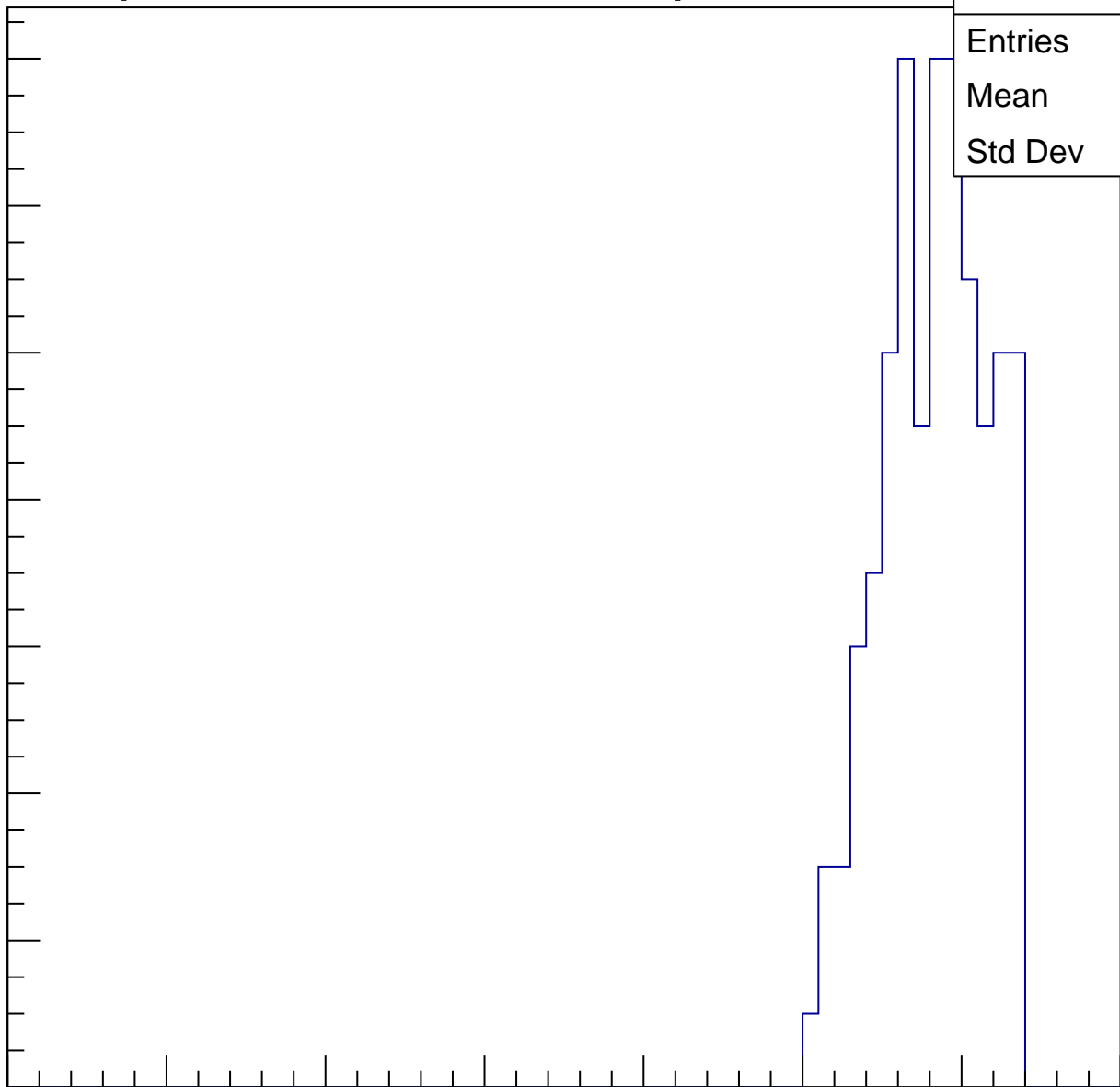
Entries	121
Mean	57.84
Std Dev	3.255

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

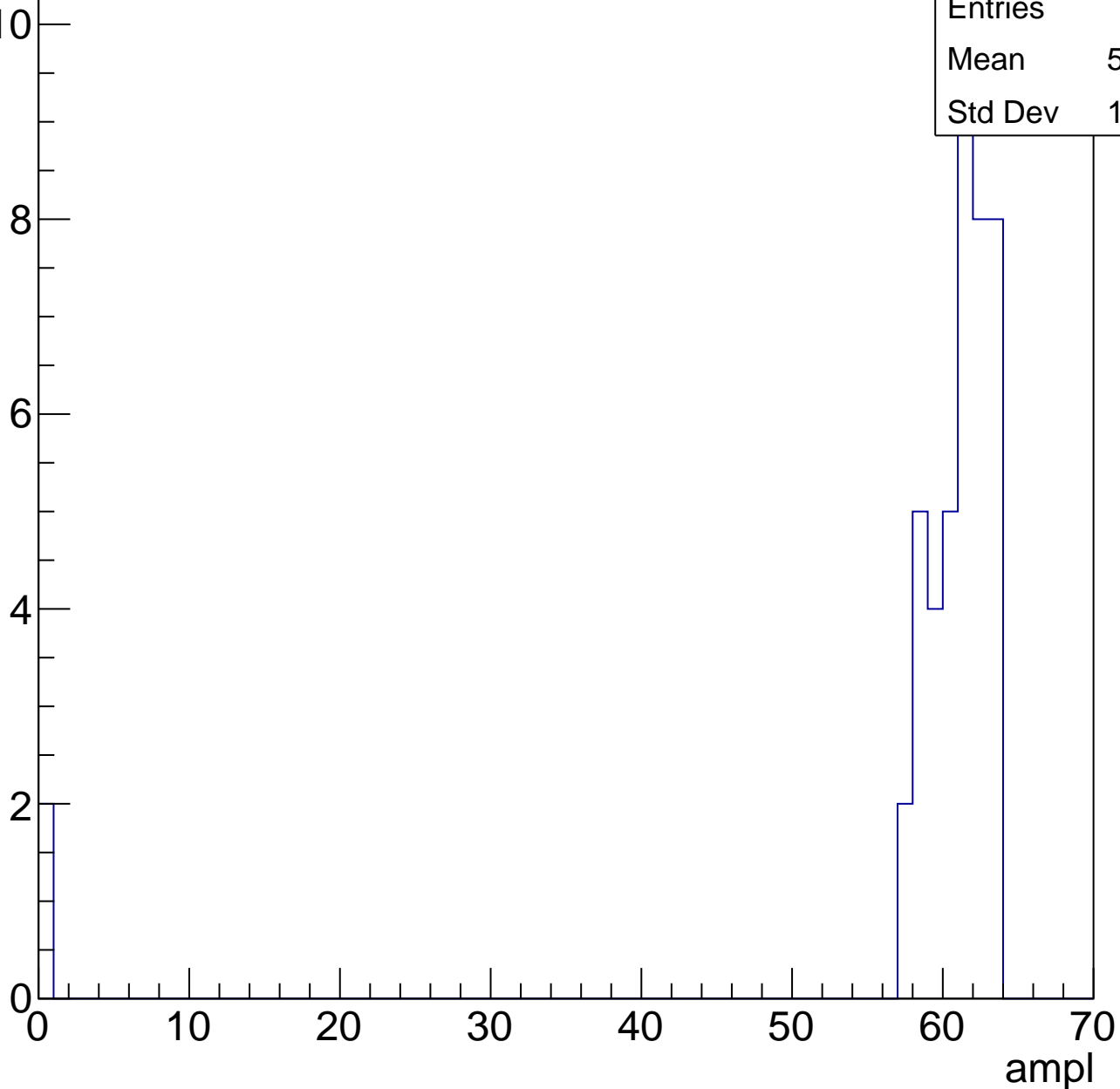


# B0L101S, U23-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	44
Mean	57.95
Std Dev	12.77



# B0L101S, U23-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	62
Std Dev	0

ampl



# B0L101S, U23-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	27.98
Std Dev	3.722

**Gaus mean : 28.5346**

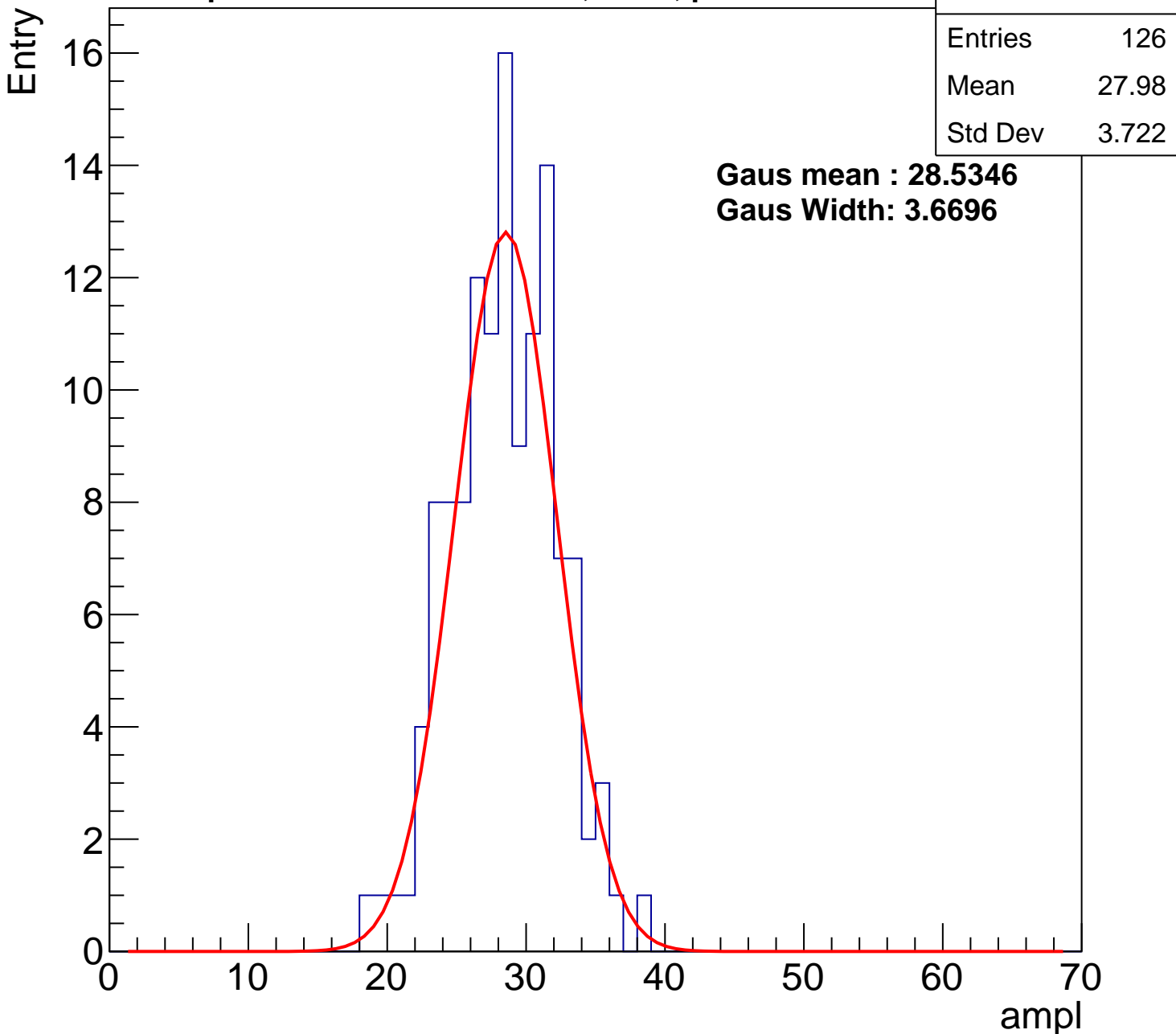
**Gaus Width: 3.6696**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



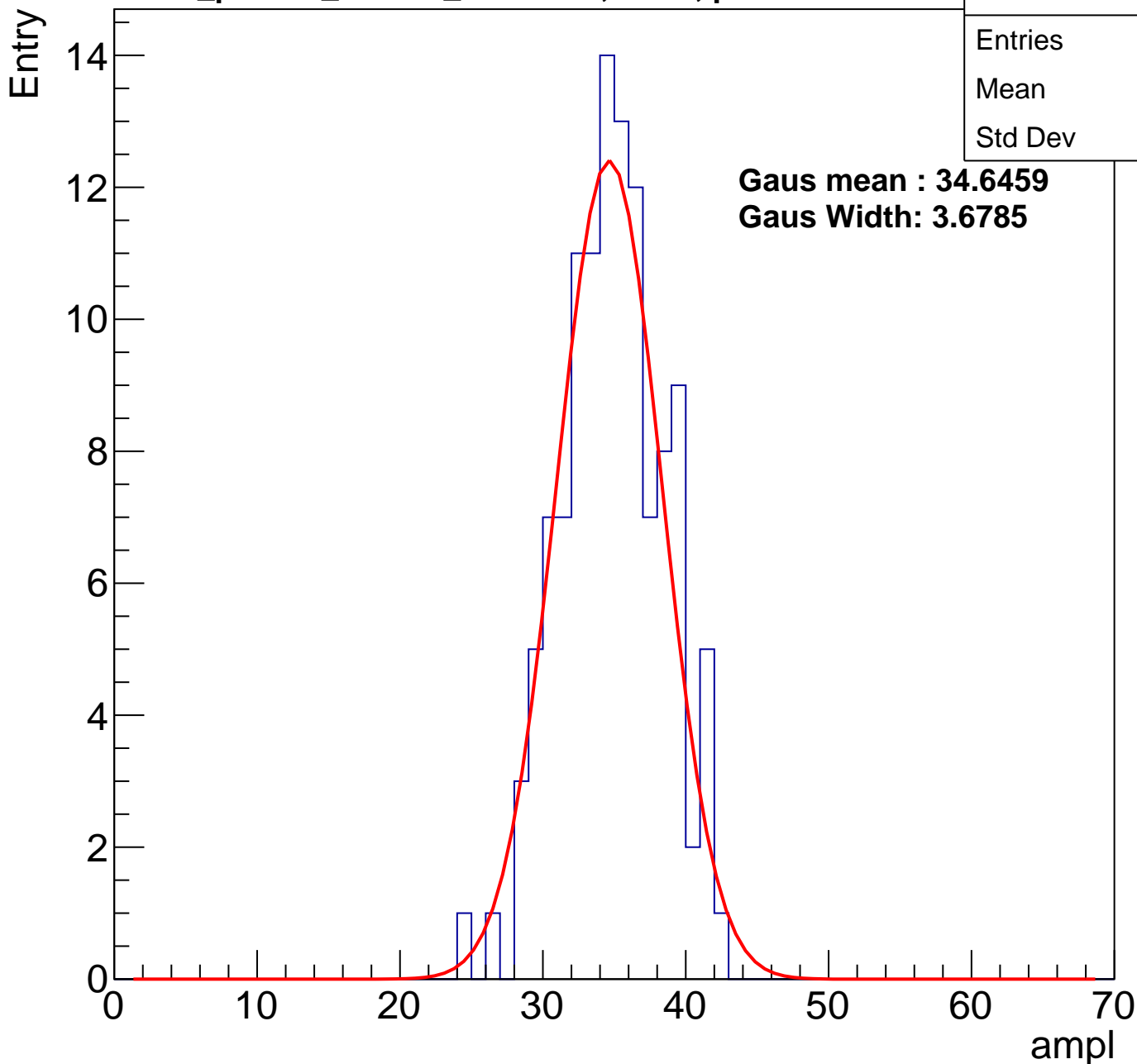
# B0L101S, U23-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	34.4
Std Dev	3.54

**Gaus mean : 34.6459**

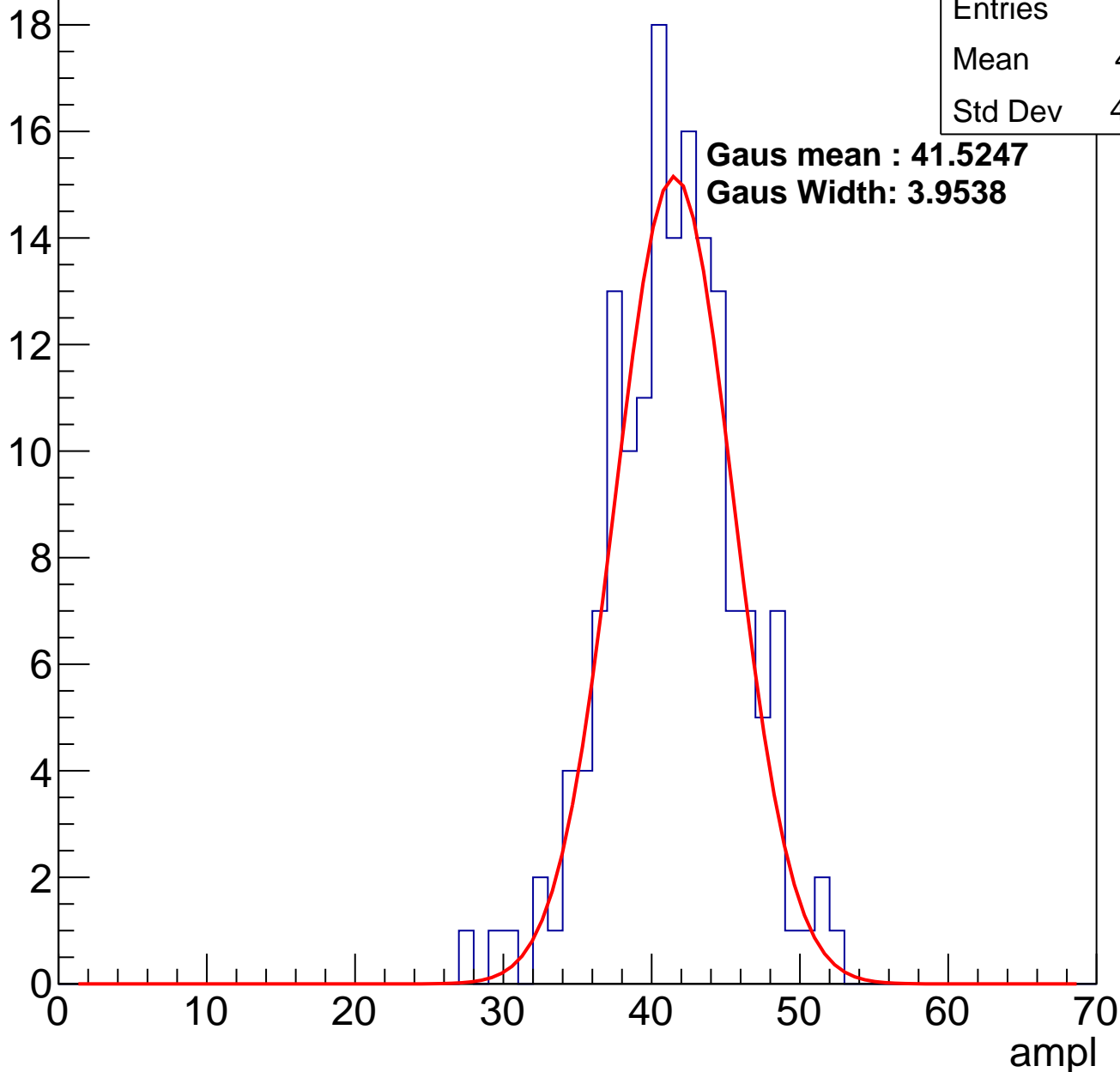
**Gaus Width: 3.6785**



# B0L101S, U23-ch100, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

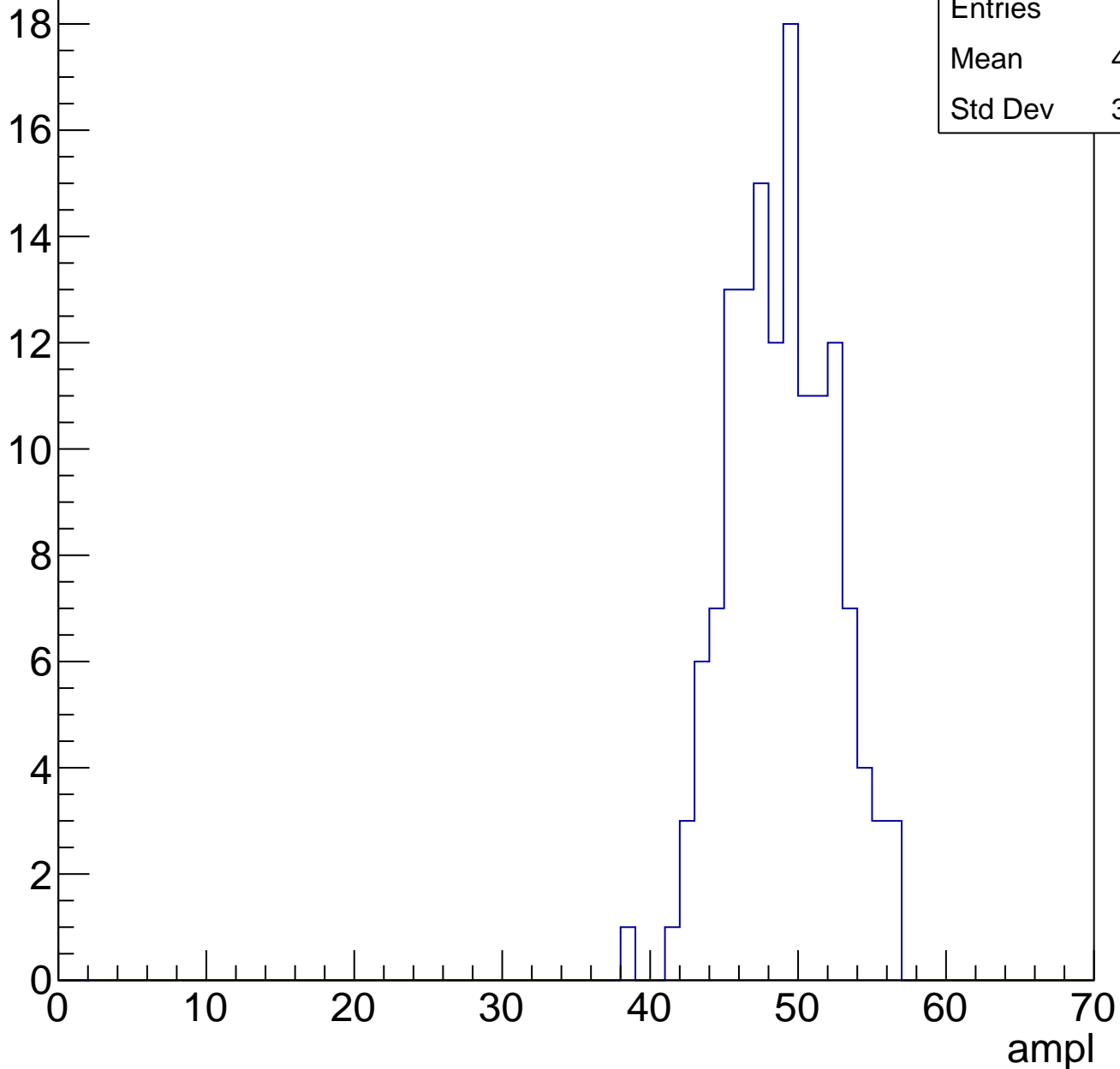


# B0L101S, U23-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	48.37
Std Dev	3.479

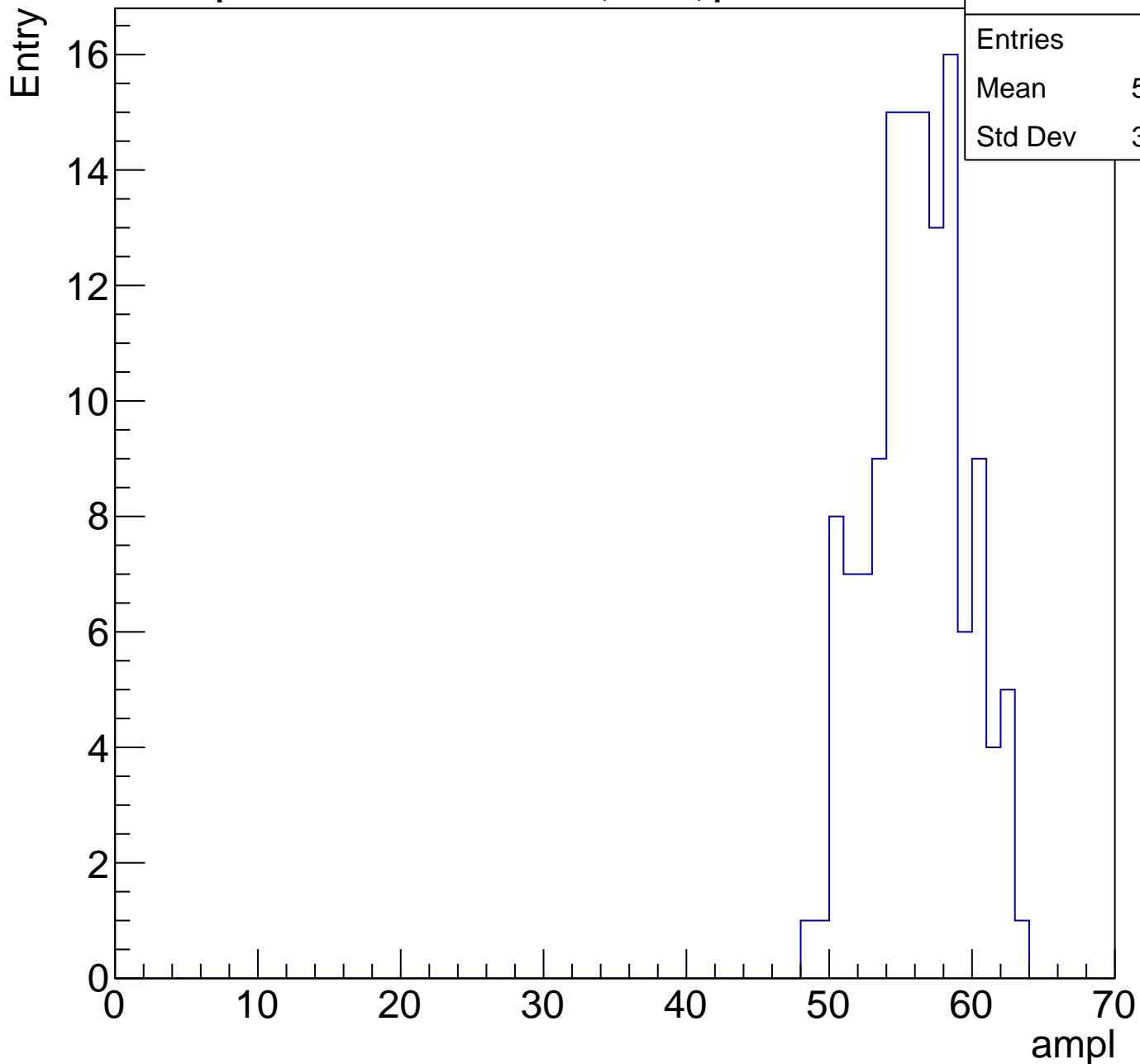
Entry



# B0L101S, U23-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	55.68
Std Dev	3.297



# B0L101S, U23-ch100, adc5

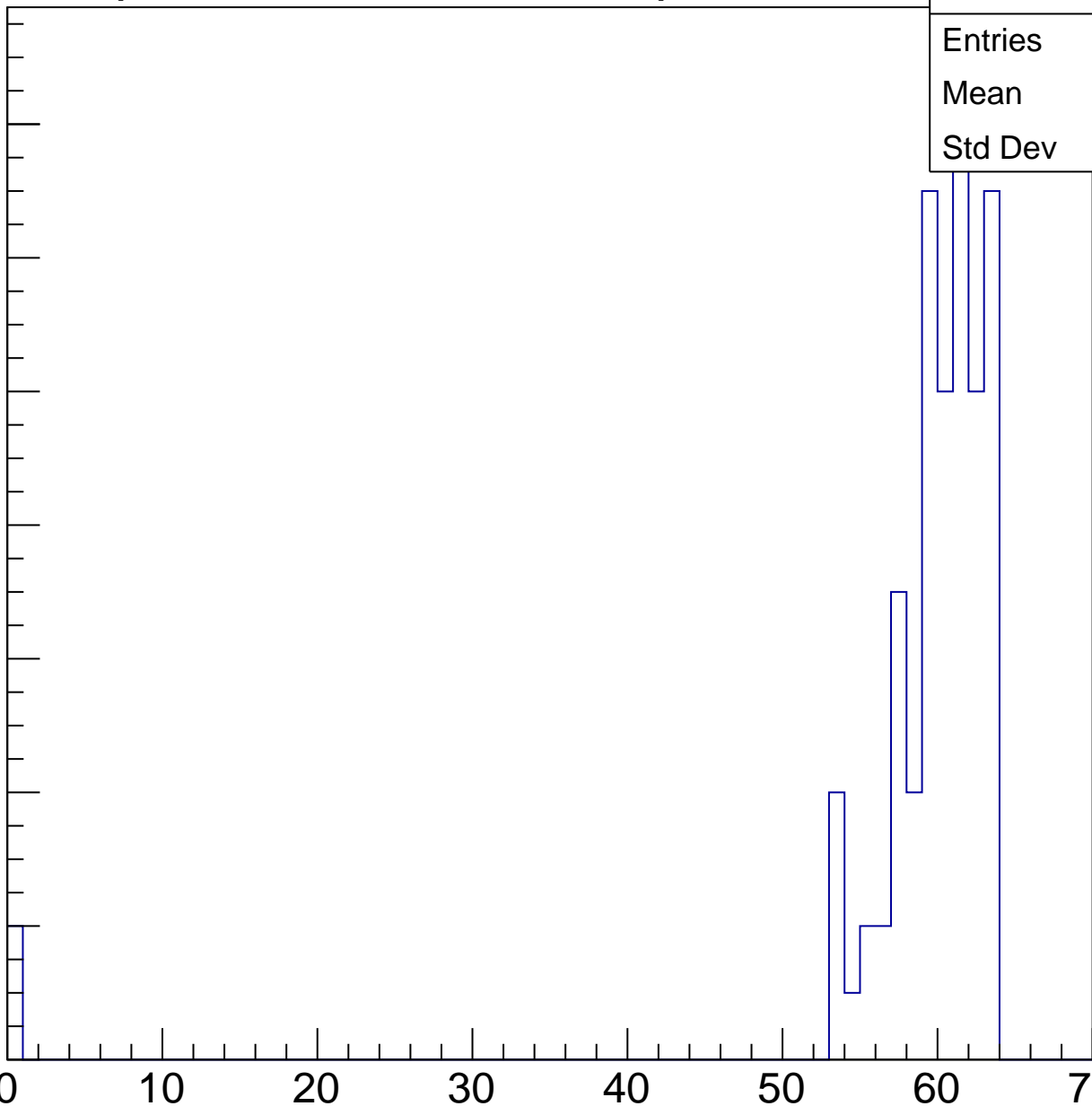
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	58.31
Std Dev	9.532

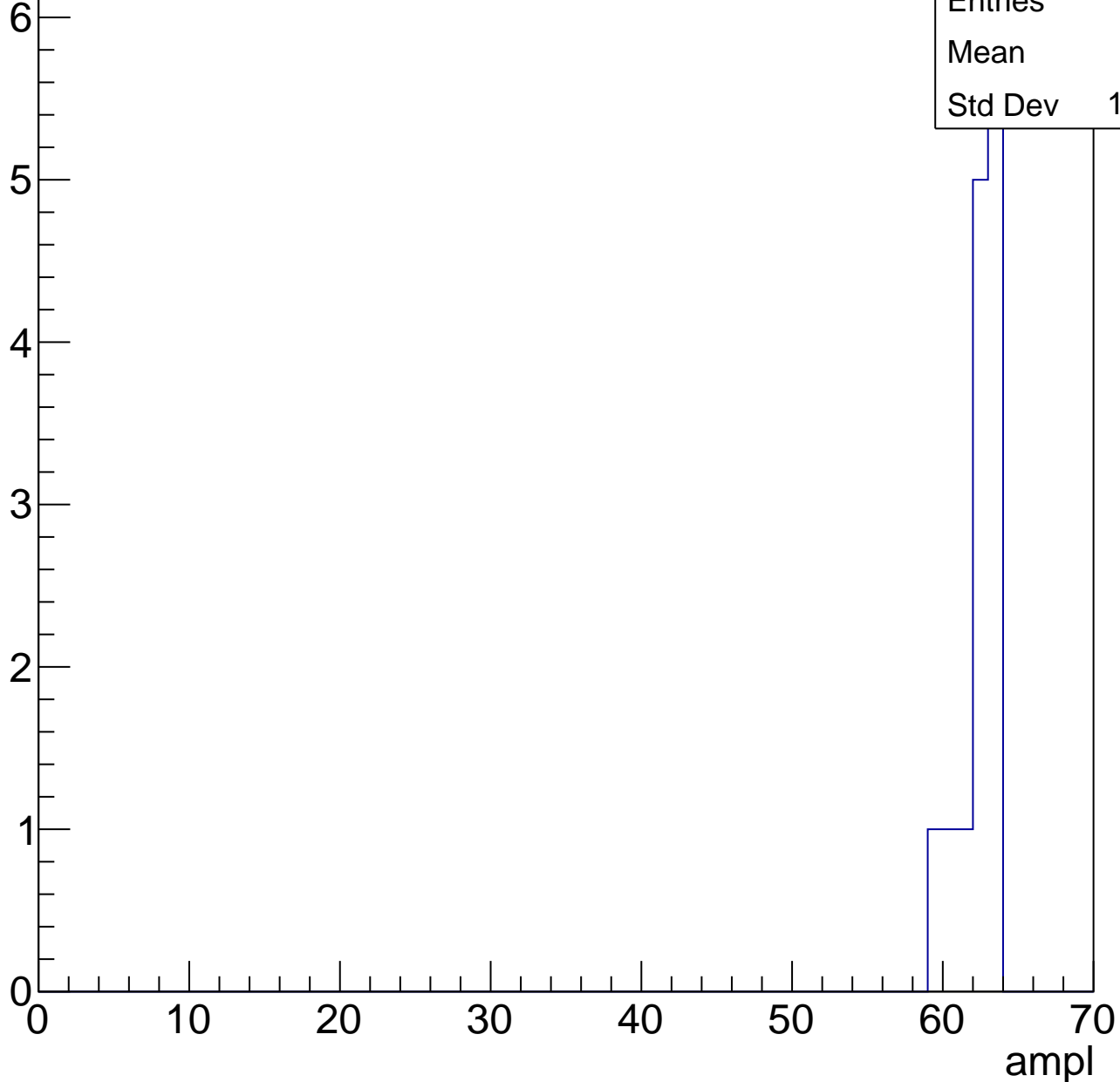
ampl



# B0L101S, U23-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



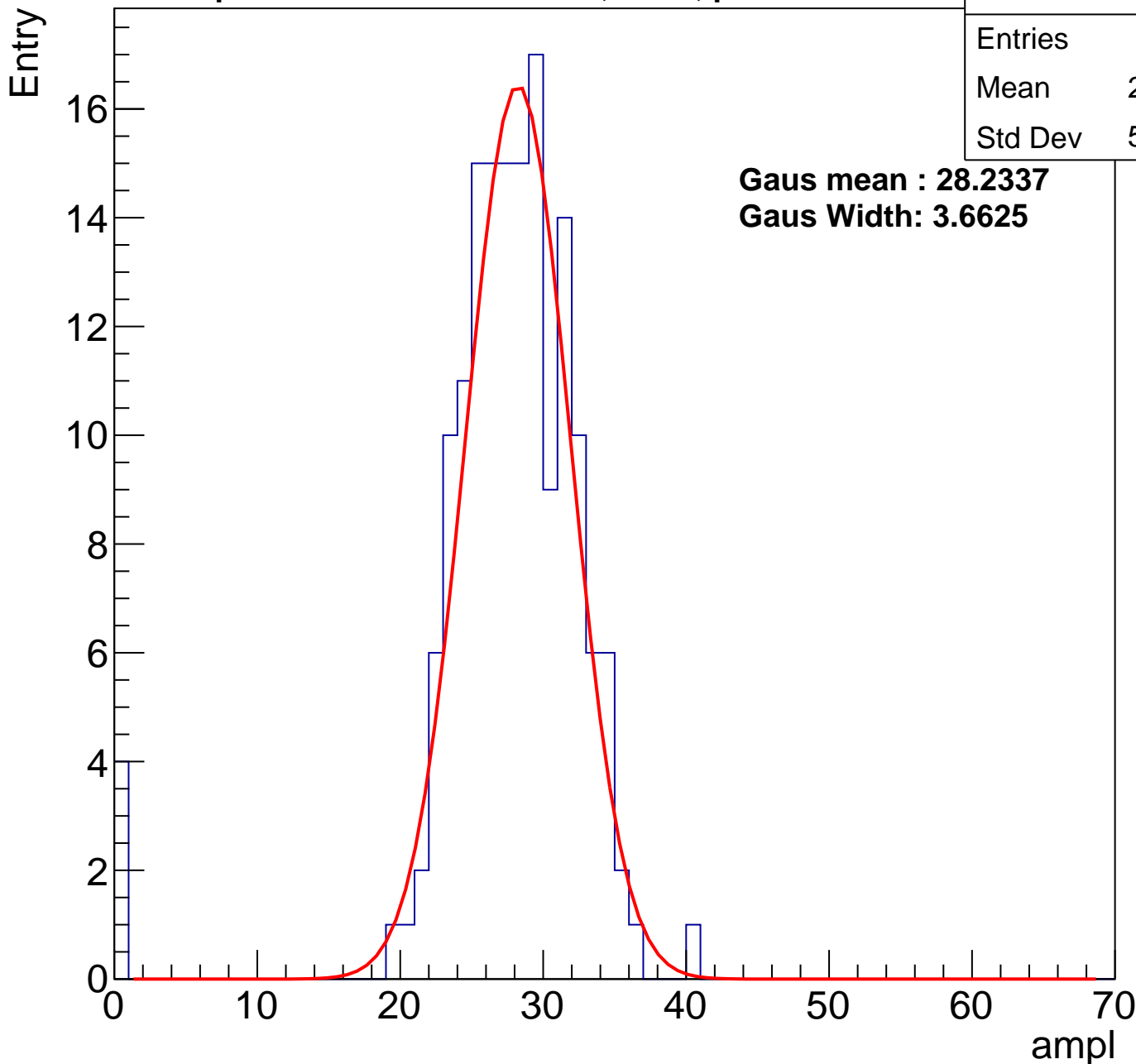
# B0L101S, U23-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	27.09
Std Dev	5.634

**Gaus mean : 28.2337**

**Gaus Width: 3.6625**

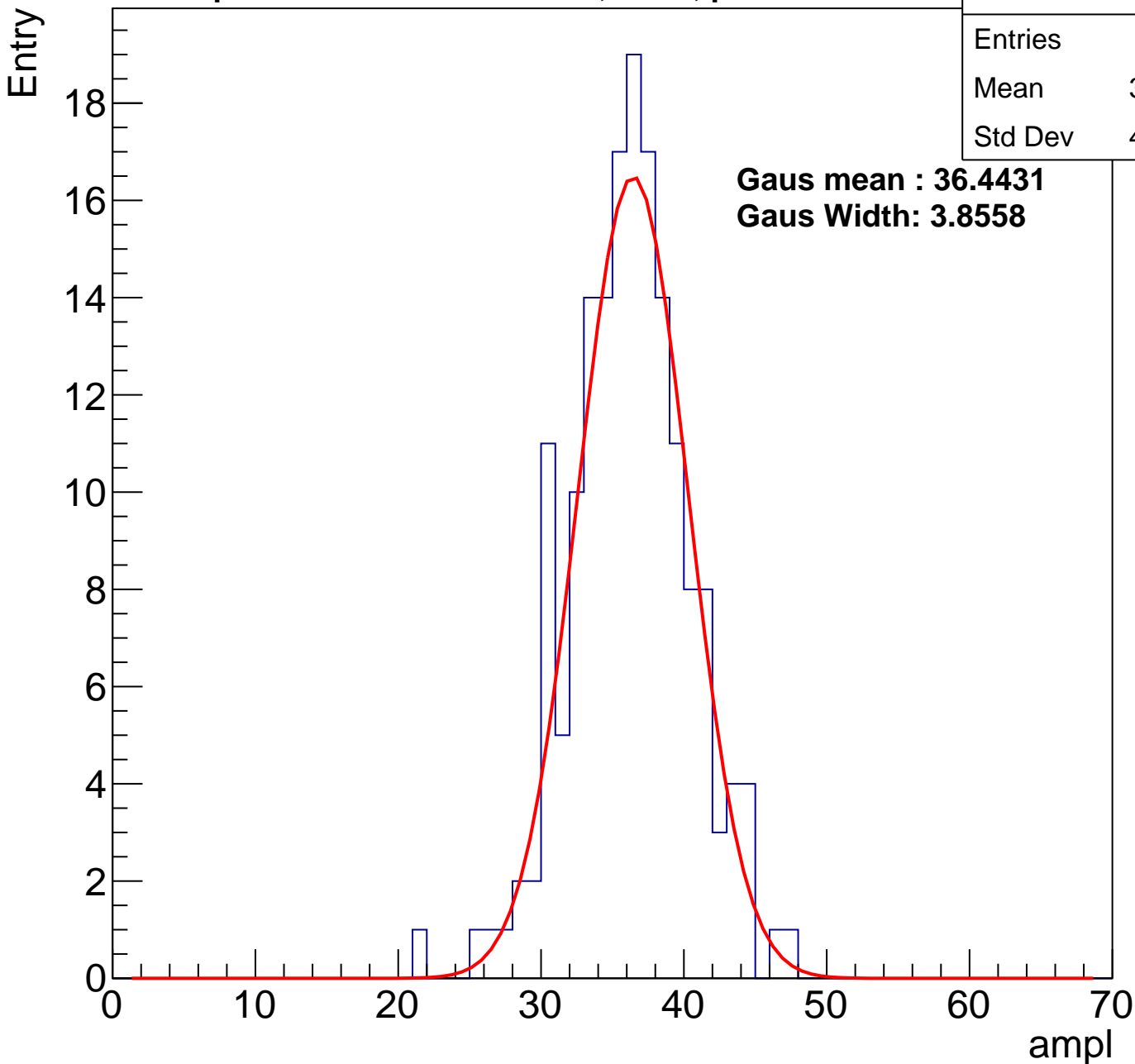


# B0L101S, U23-ch101, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	35.74
Std Dev	4.144

**Gaus mean : 36.4431**  
**Gaus Width: 3.8558**



# B0L101S, U23-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

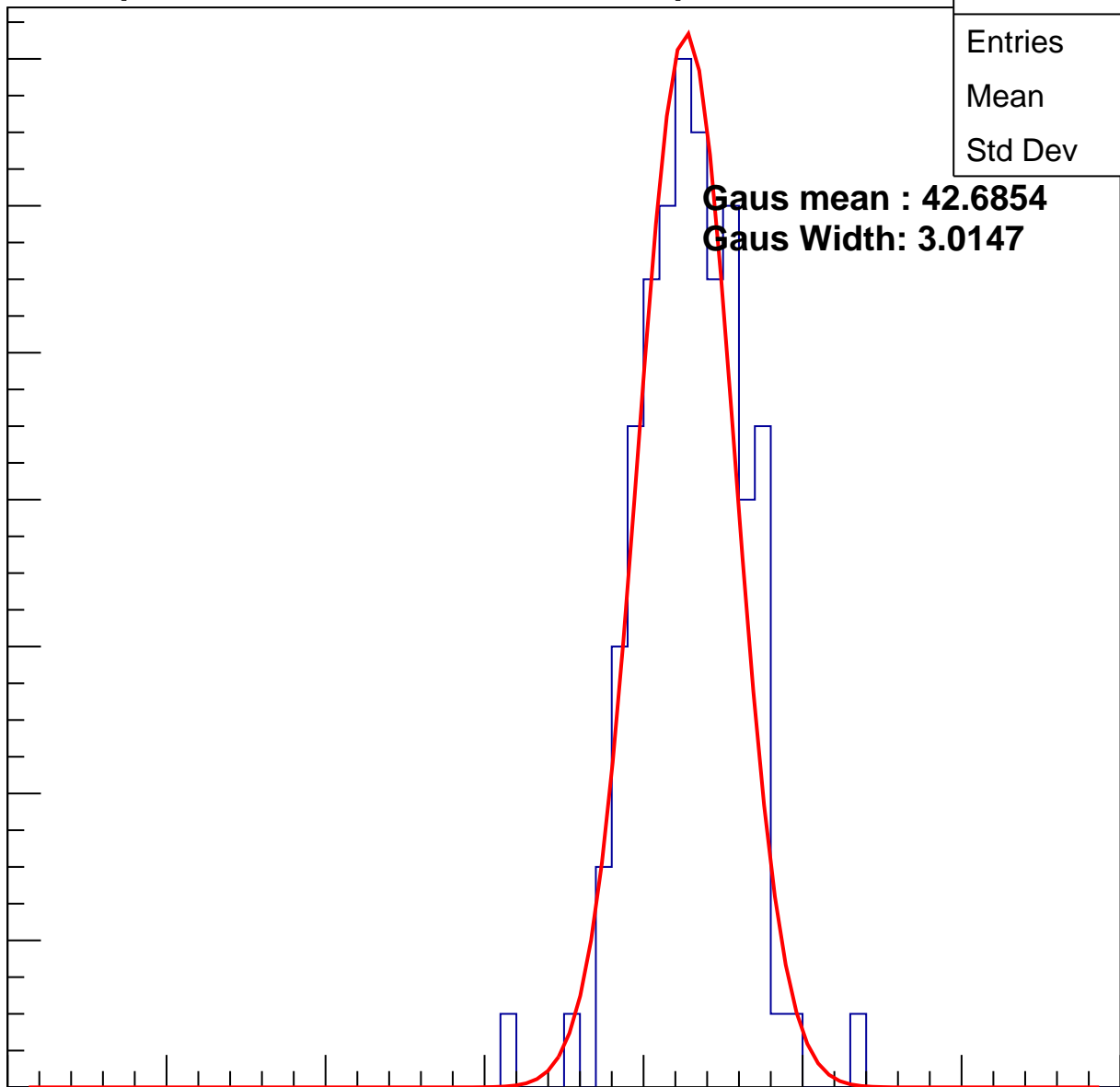
14  
12  
10  
8  
6  
4  
2  
0

Entries	113
Mean	42.48
Std Dev	3.226

**Gaus mean : 42.6854**  
**Gaus Width: 3.0147**

ampl

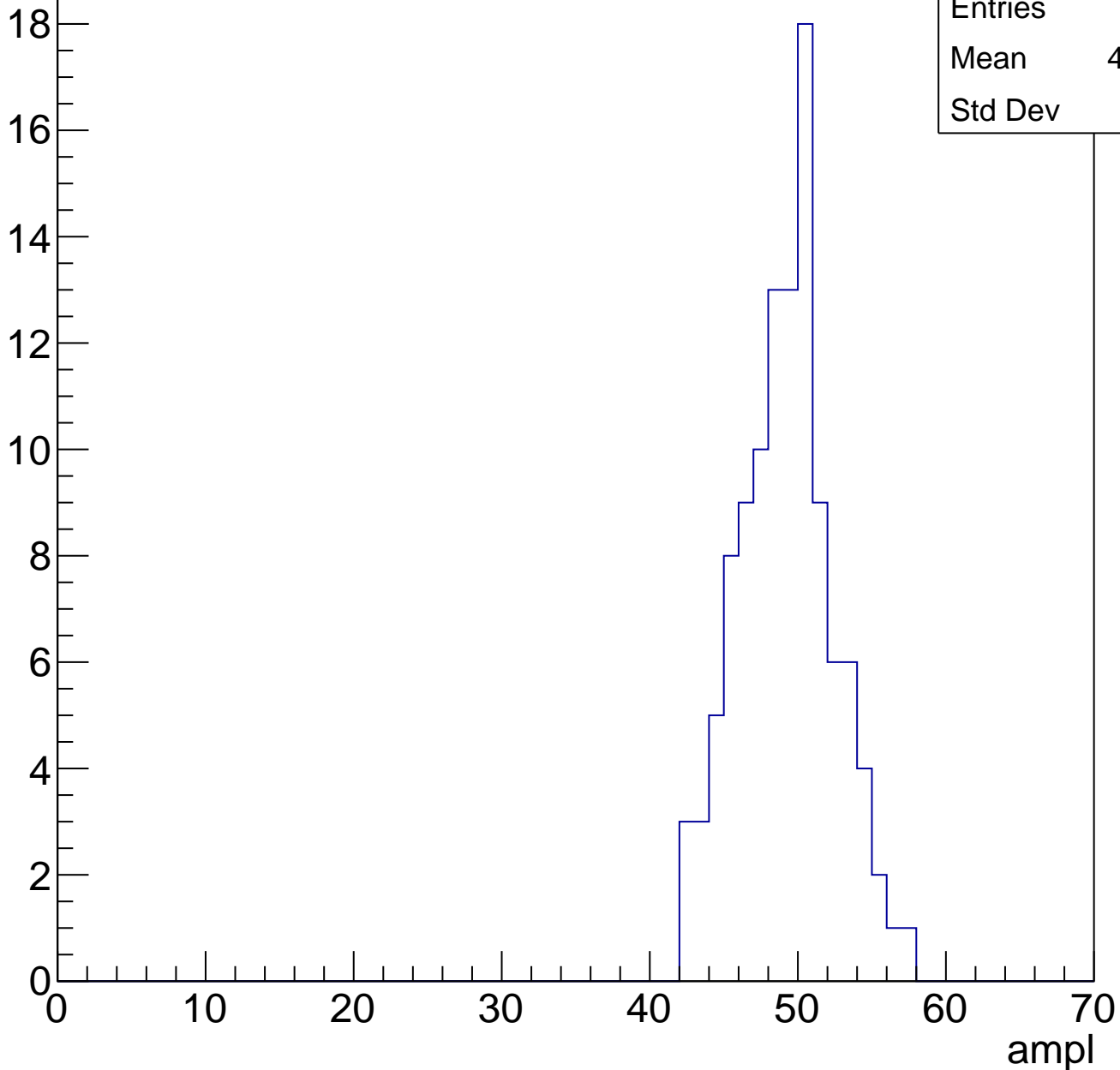
0 10 20 30 40 50 60 70



# B0L101S, U23-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

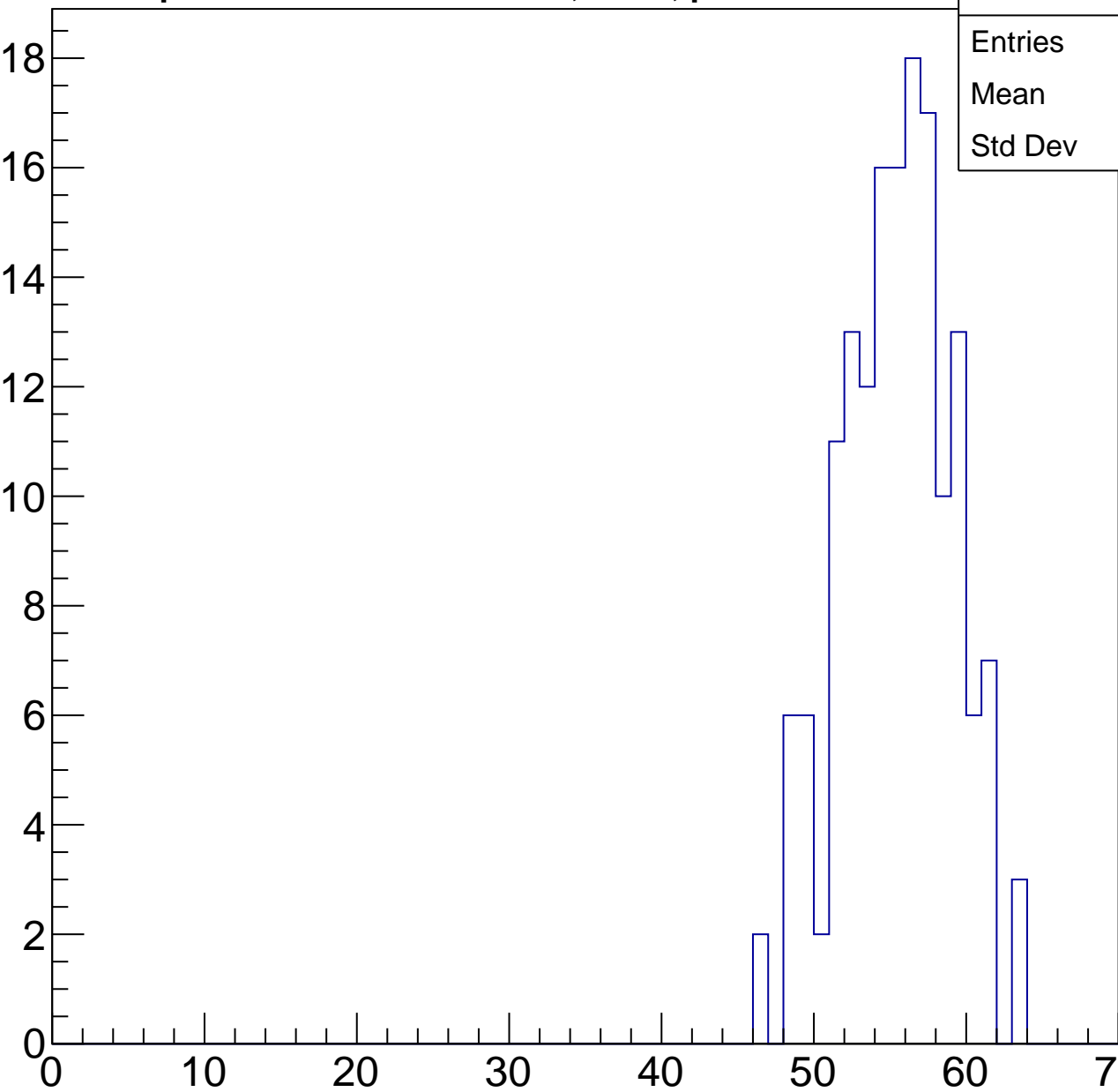
Entries	158
Mean	55.01
Std Dev	3.608

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	73
Mean	59.32
Std Dev	7.378

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

40

50

60

70

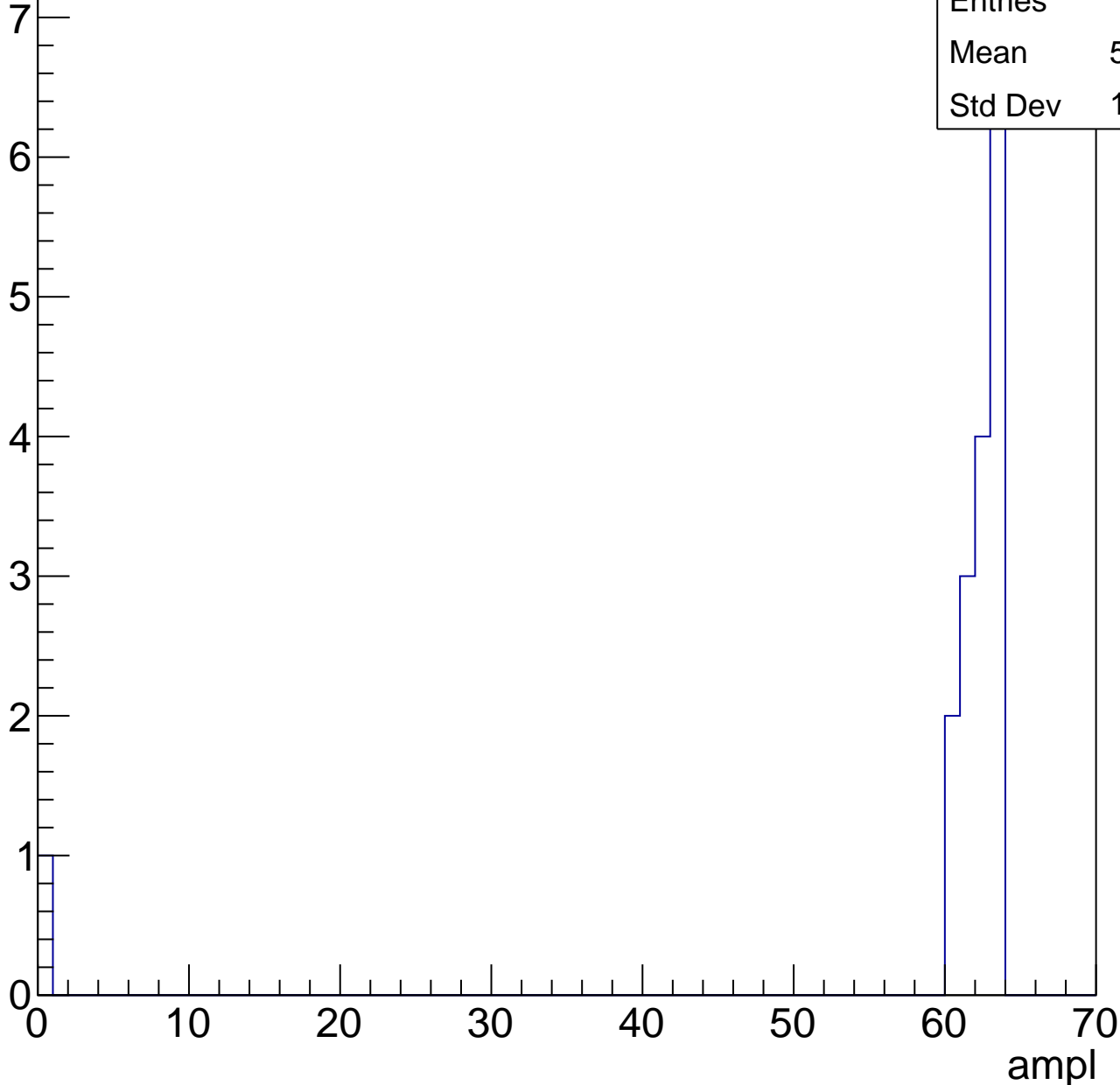
ampl

# B0L101S, U23-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.35
Std Dev	14.62





# B0L101S, U23-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



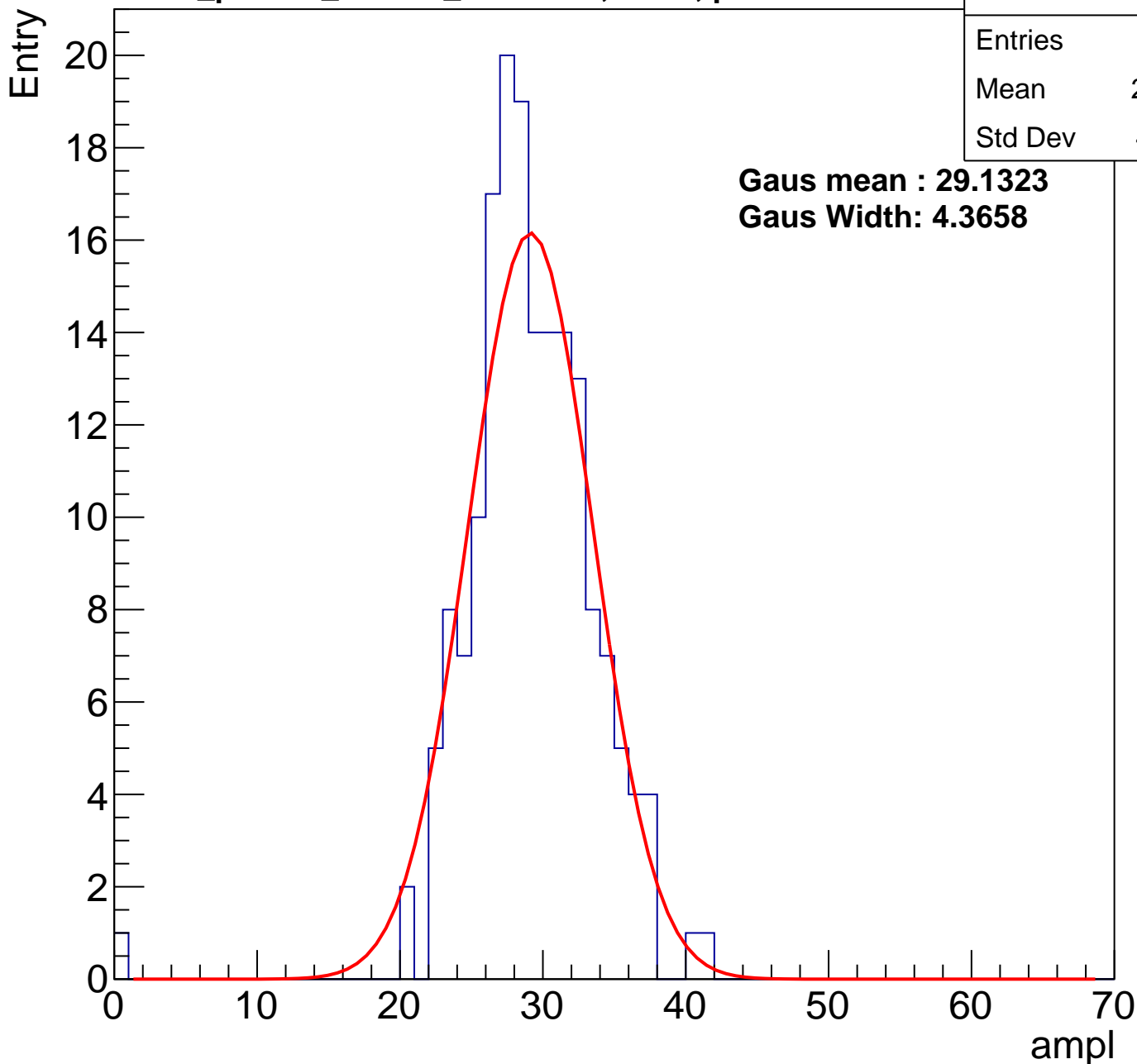
# B0L101S, U23-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	174
Mean	28.69
Std Dev	4.481

**Gaus mean : 29.1323**

**Gaus Width: 4.3658**



# B0L101S, U23-ch102, adc1

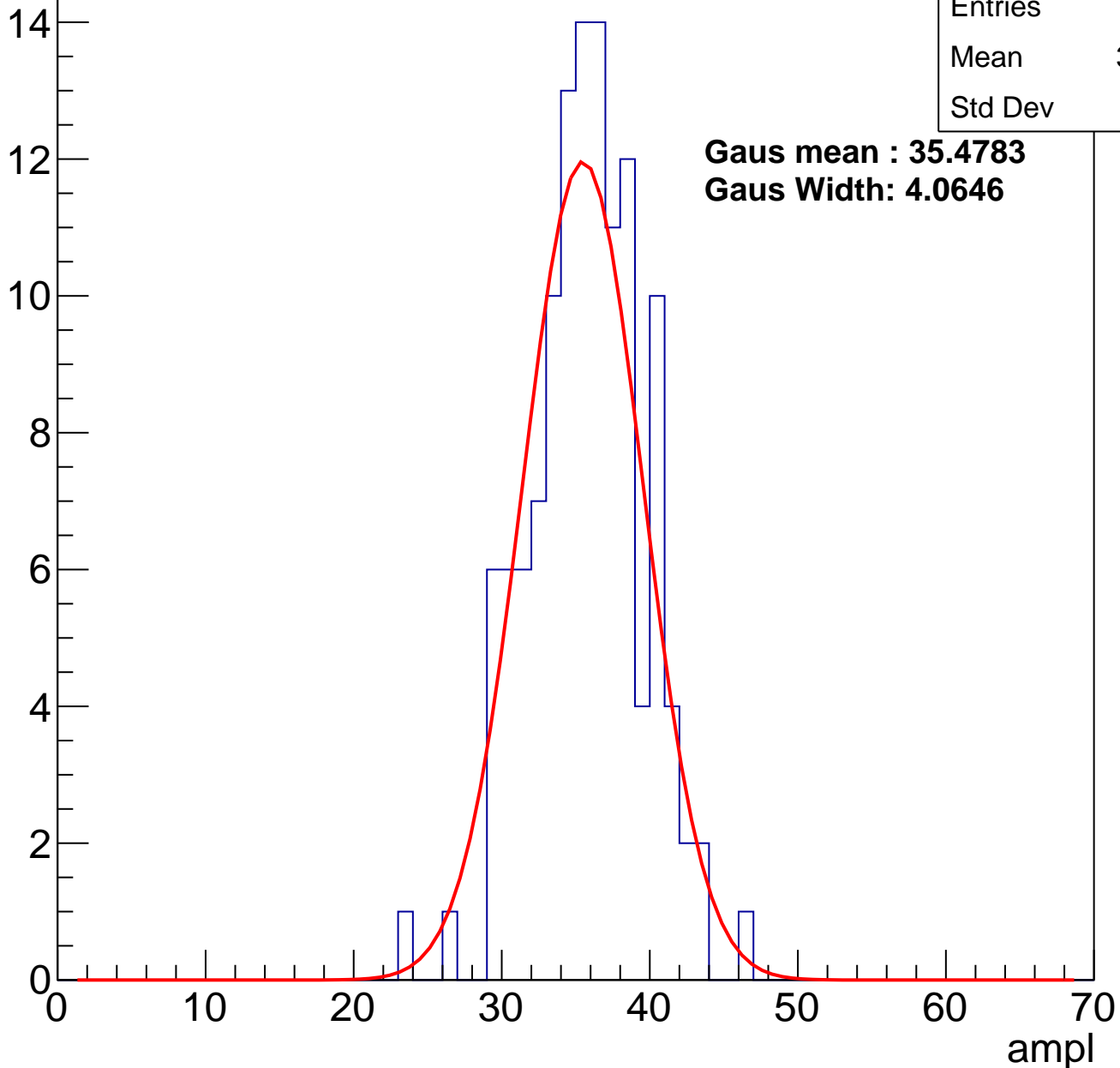
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	35.31
Std Dev	3.77

**Gaus mean : 35.4783**

**Gaus Width: 4.0646**

Entry



# B0L101S, U23-ch102, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

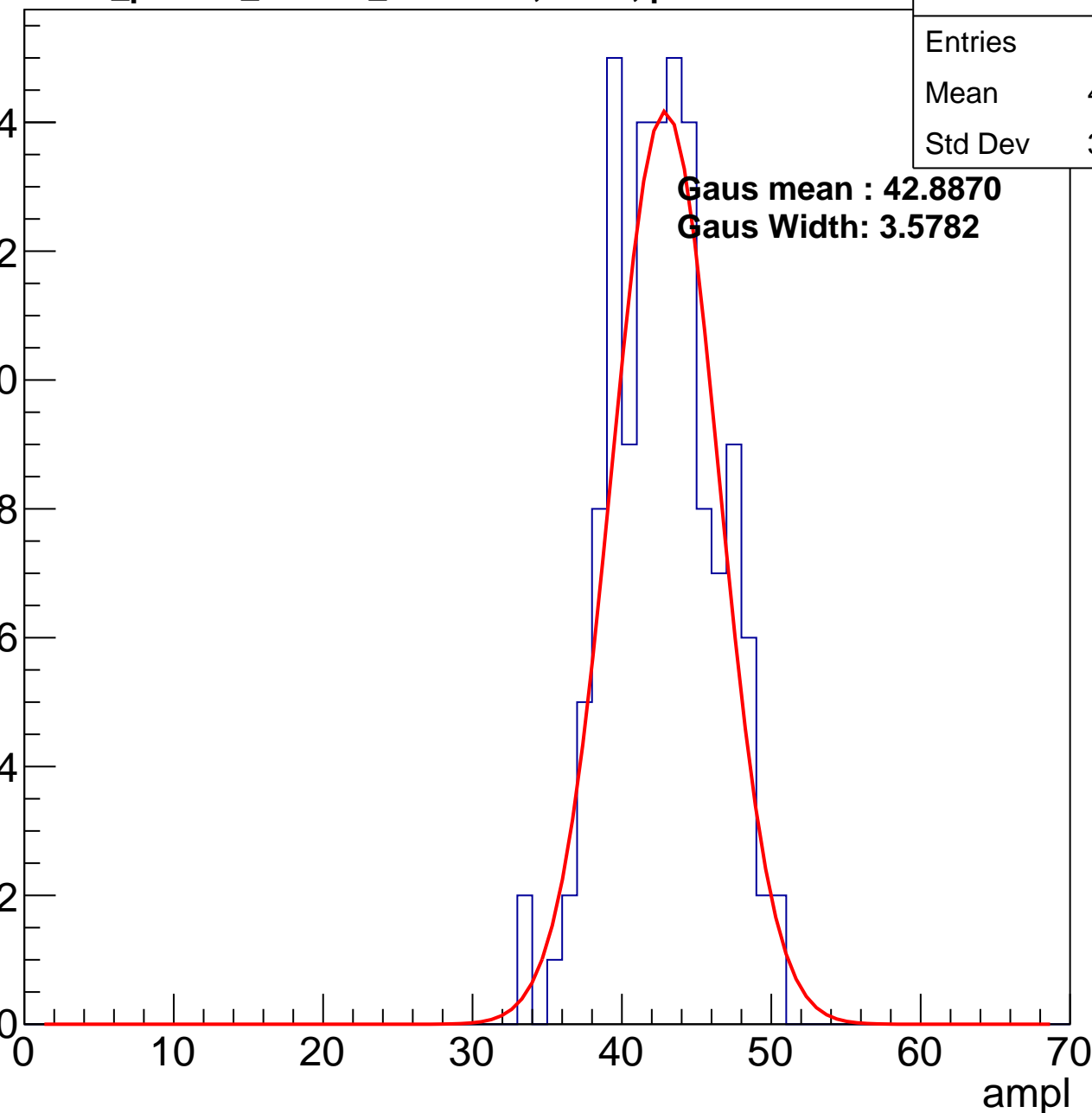
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	133
Mean	42.26
Std Dev	3.505

**Gaus mean : 42.8870**

**Gaus Width: 3.5782**



# B0L101S, U23-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	48.75
Std Dev	3.76

Entry

12

10

8

6

4

2

0

0

10

20

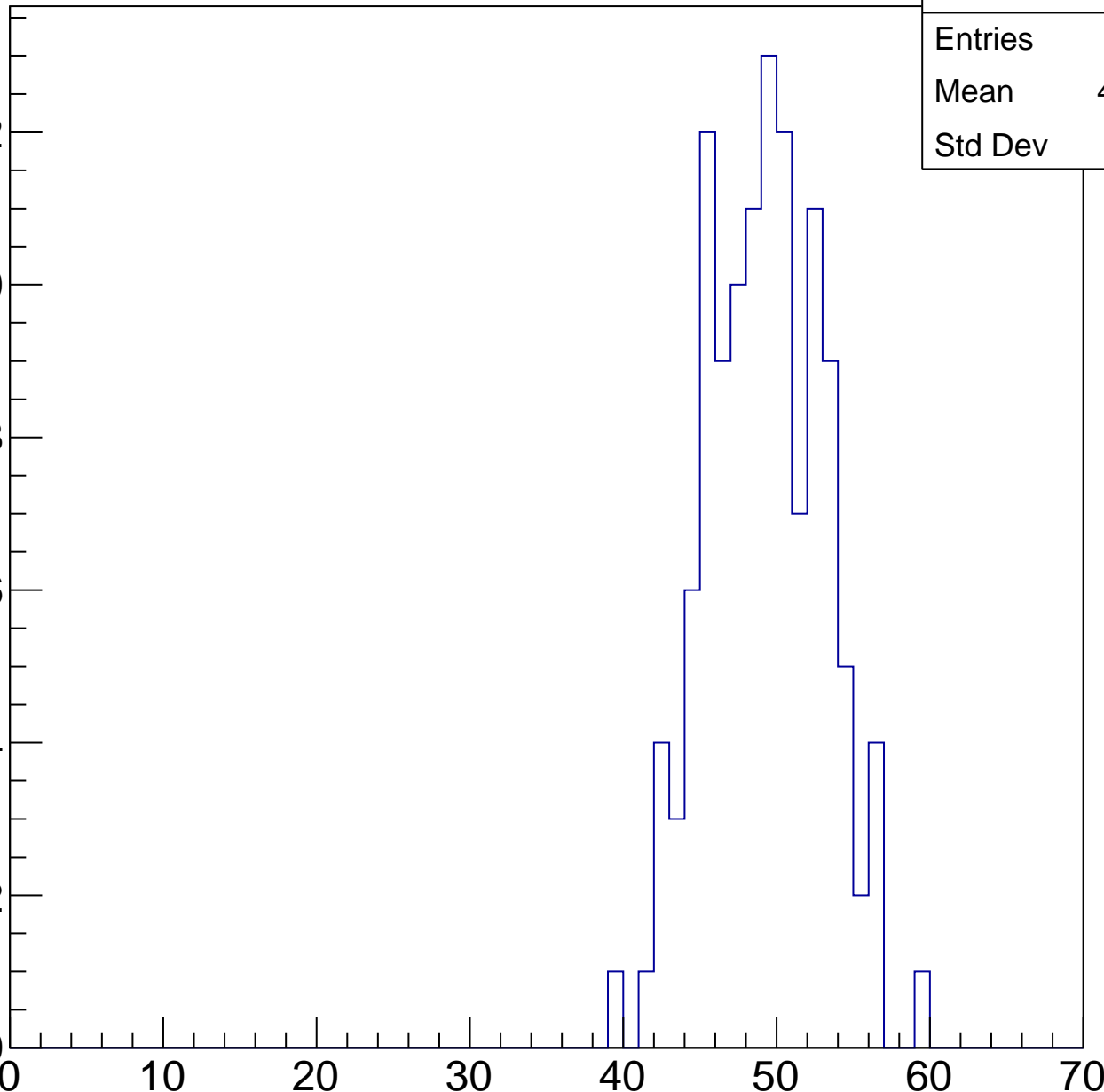
30

40

50

60

ampl



# B0L101S, U23-ch102, adc4

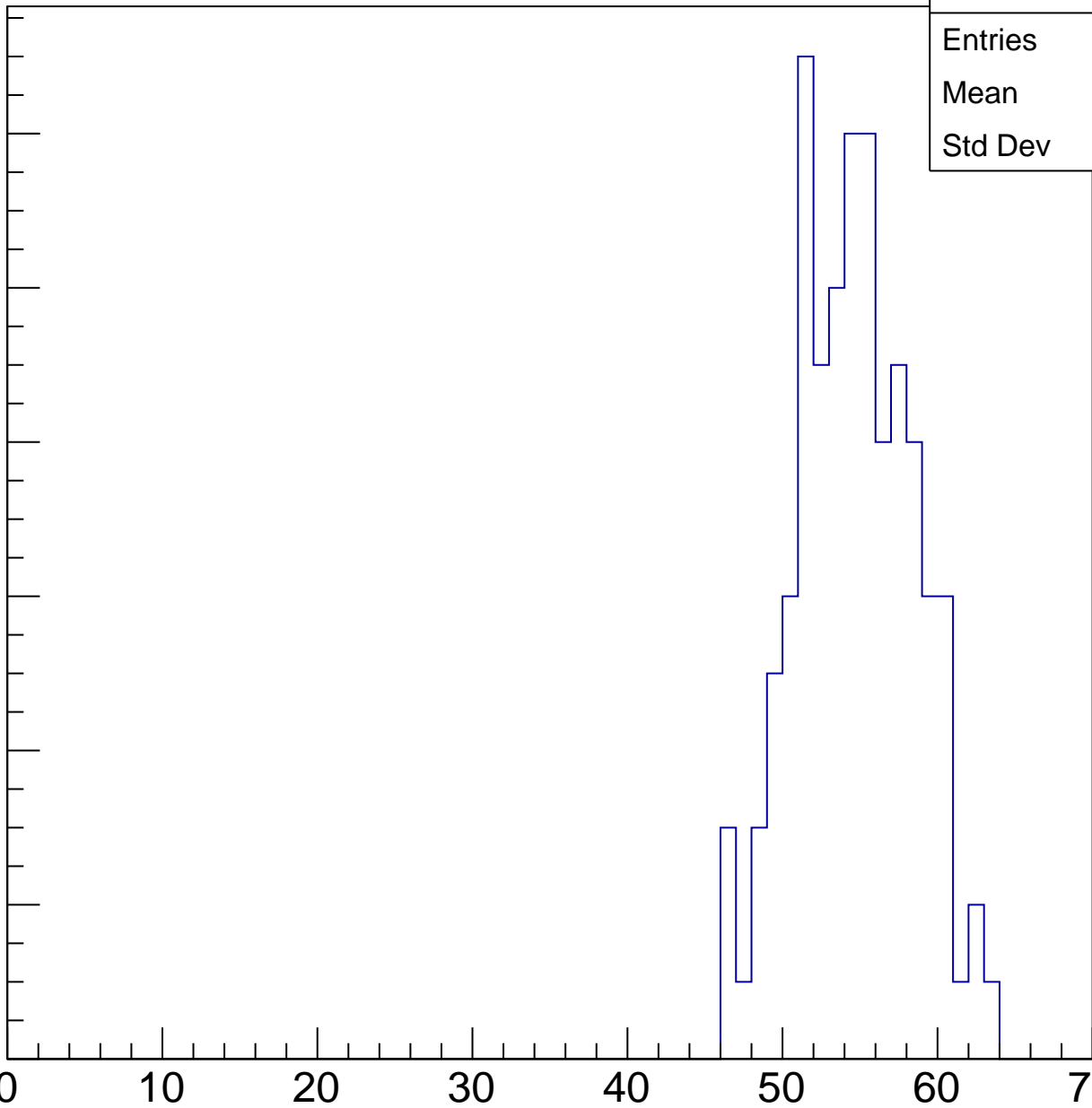
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	54.17
Std Dev	3.738

Entry

12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch102, adc5

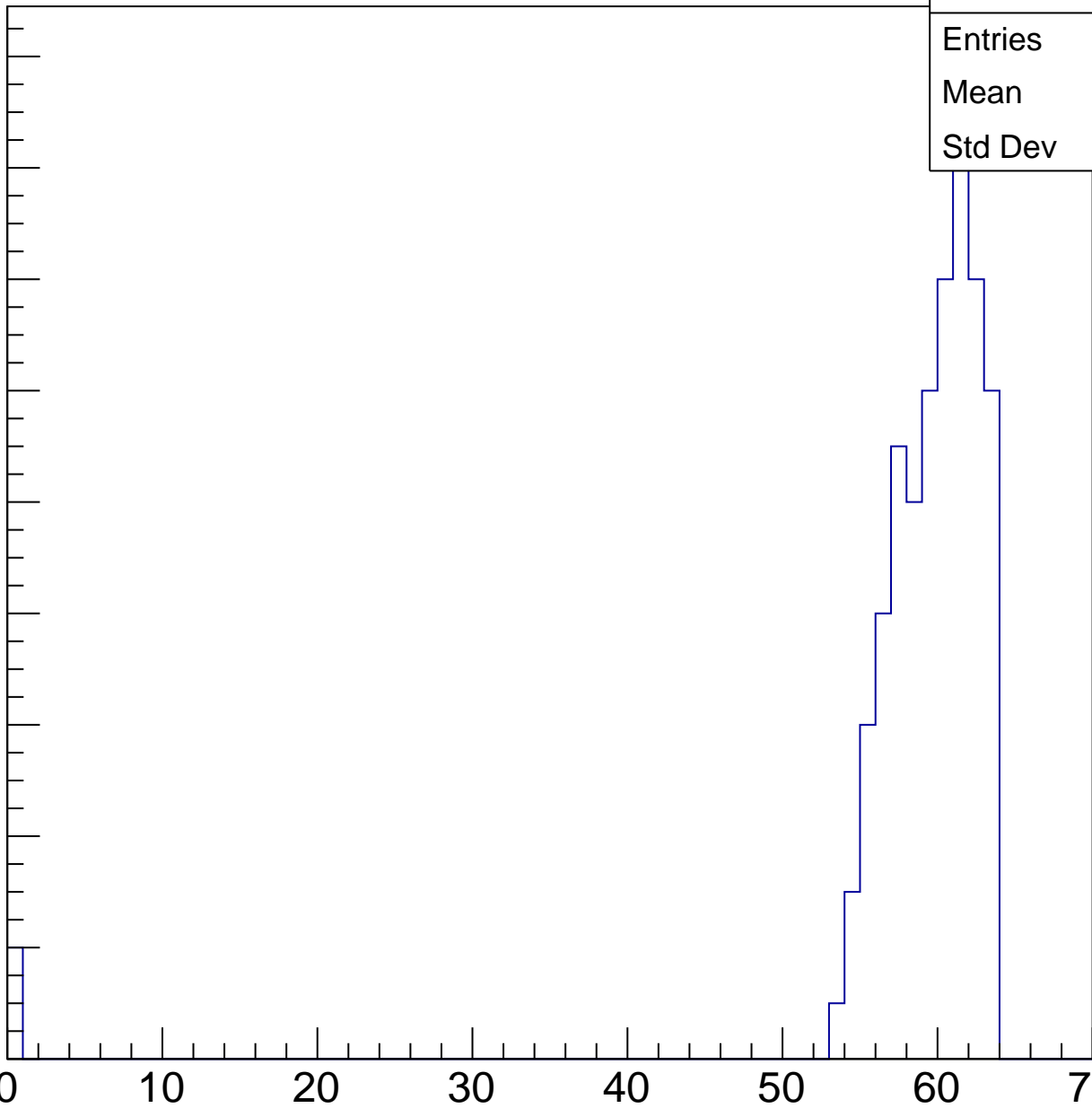
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	111
Mean	58.29
Std Dev	8.298

ampl

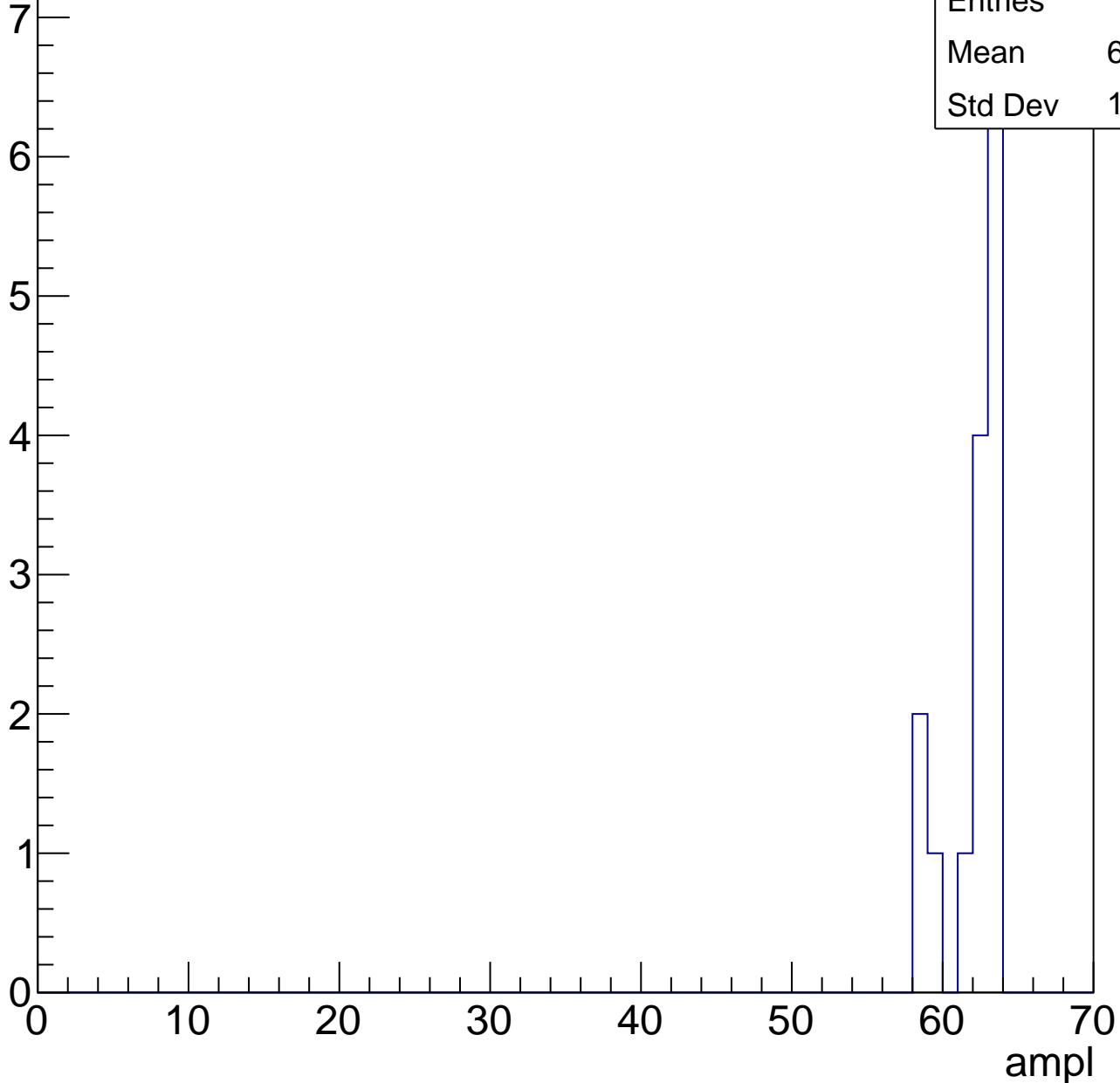


# B0L101S, U23-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.67
Std Dev	1.776





# B0L101S, U23-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch103, adc0

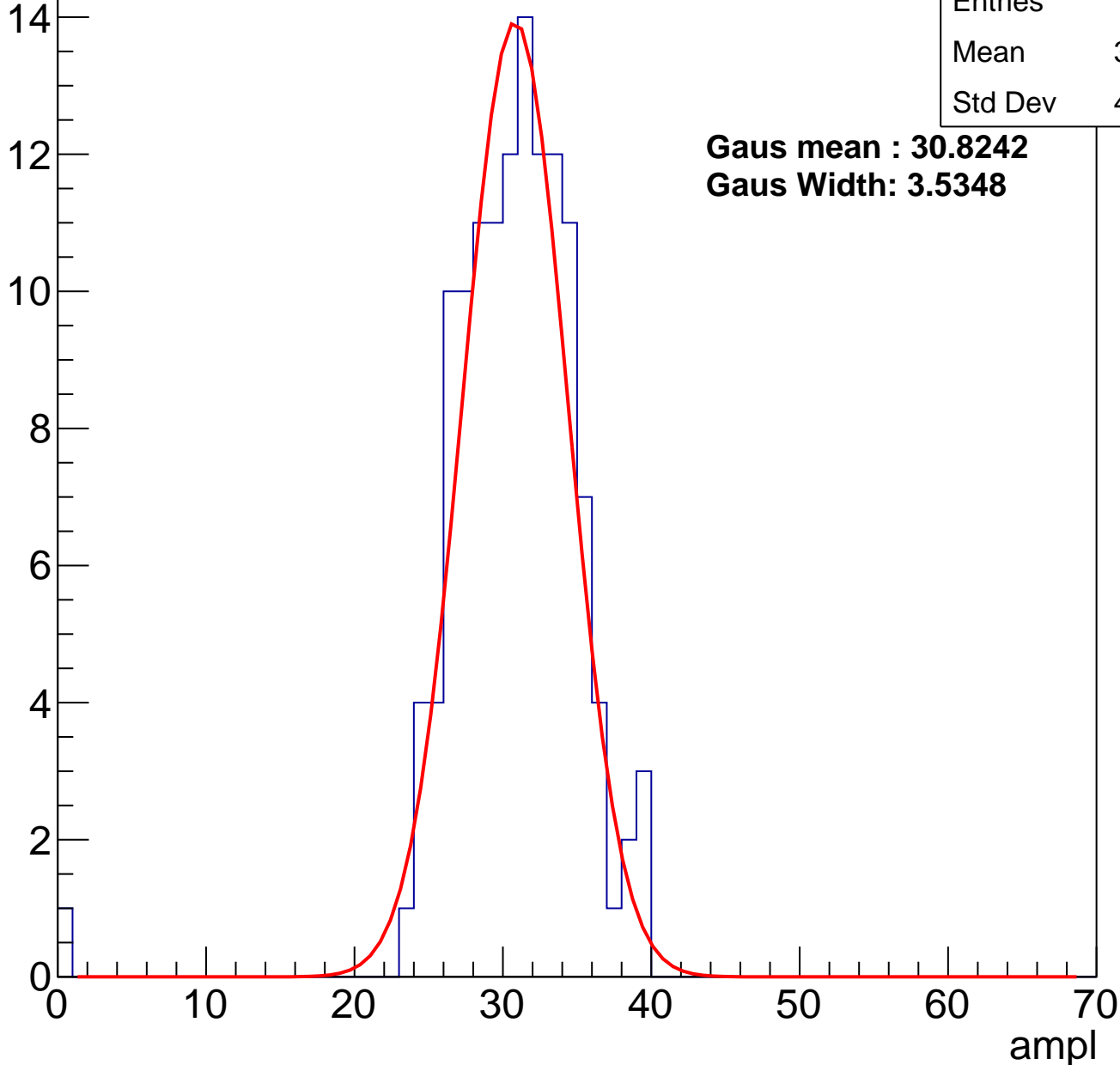
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	30.33
Std Dev	4.445

**Gaus mean : 30.8242**

**Gaus Width: 3.5348**

Entry



# B0L101S, U23-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	37.24
Std Dev	3.328

**Gaus mean : 37.7302**

**Gaus Width: 3.4796**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

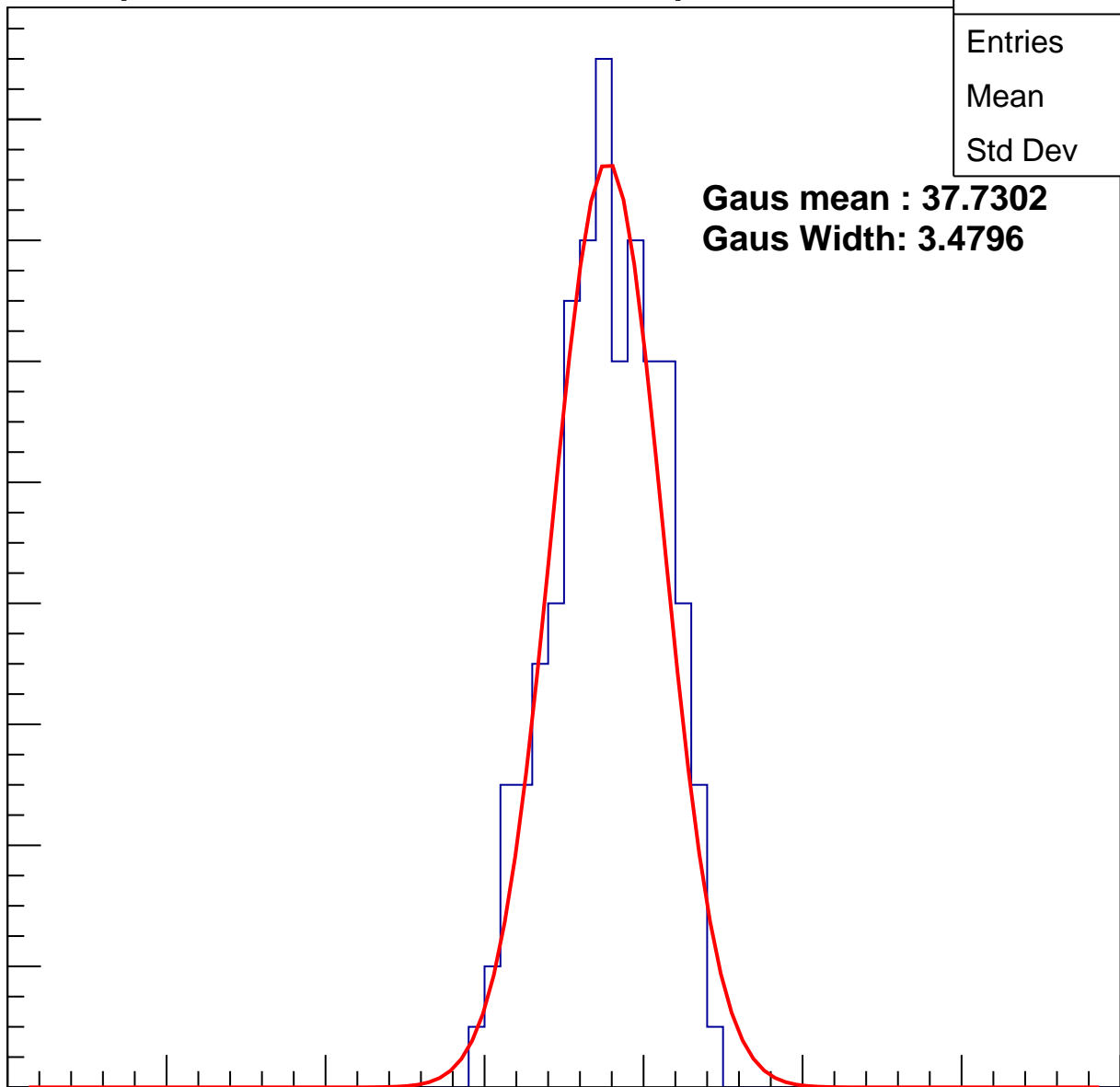
40

50

60

70

ampl



# B0L101S, U23-ch103, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

122

Mean

43.9

Std Dev

3.263

**Gaus mean : 44.5060**

**Gaus Width: 3.0401**

0

10

20

30

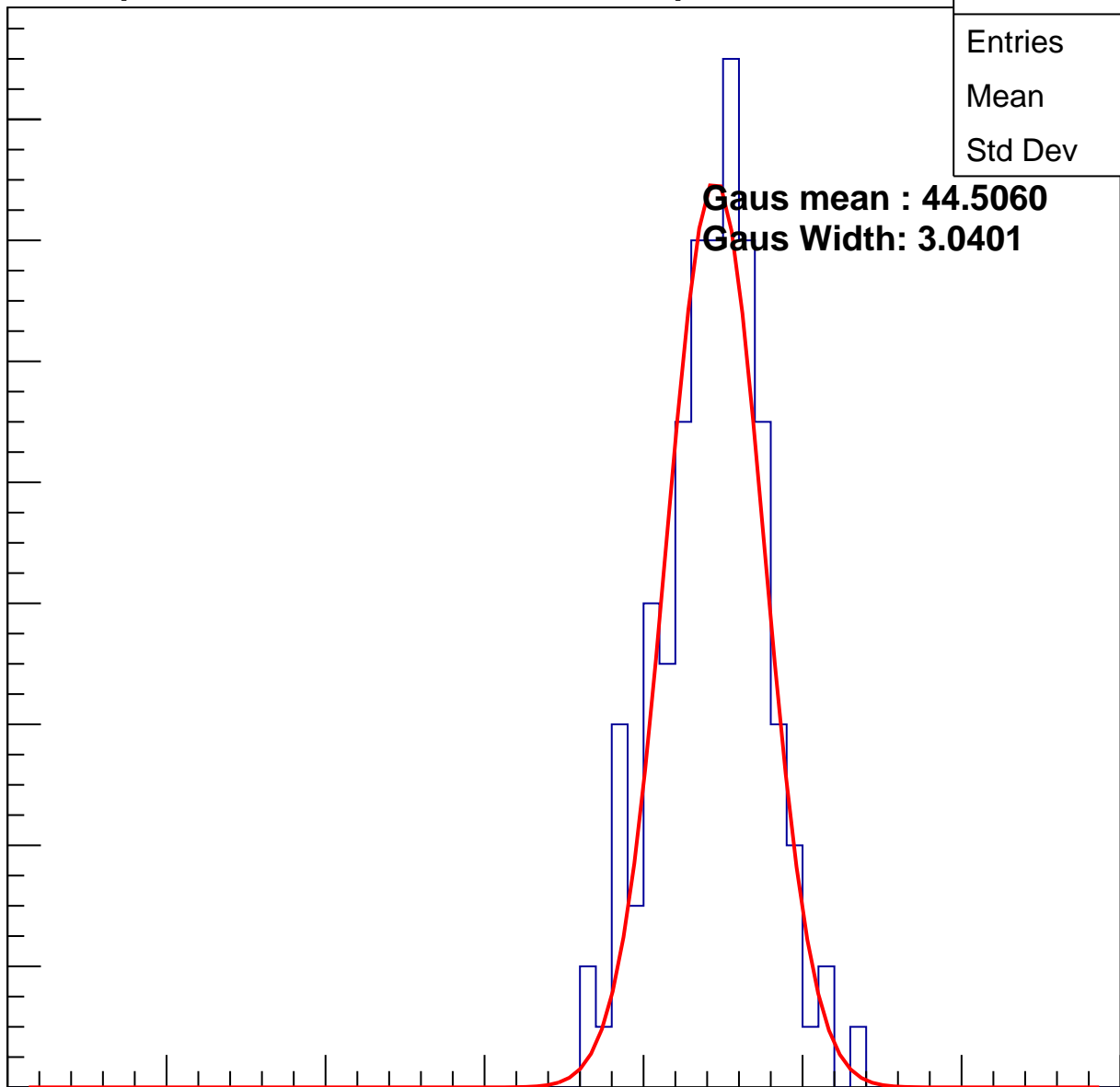
40

50

60

70

ampl



# B0L101S, U23-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	50.41
Std Dev	3.443

Entry

16

14

12

10

8

6

4

2

0

0

10

20

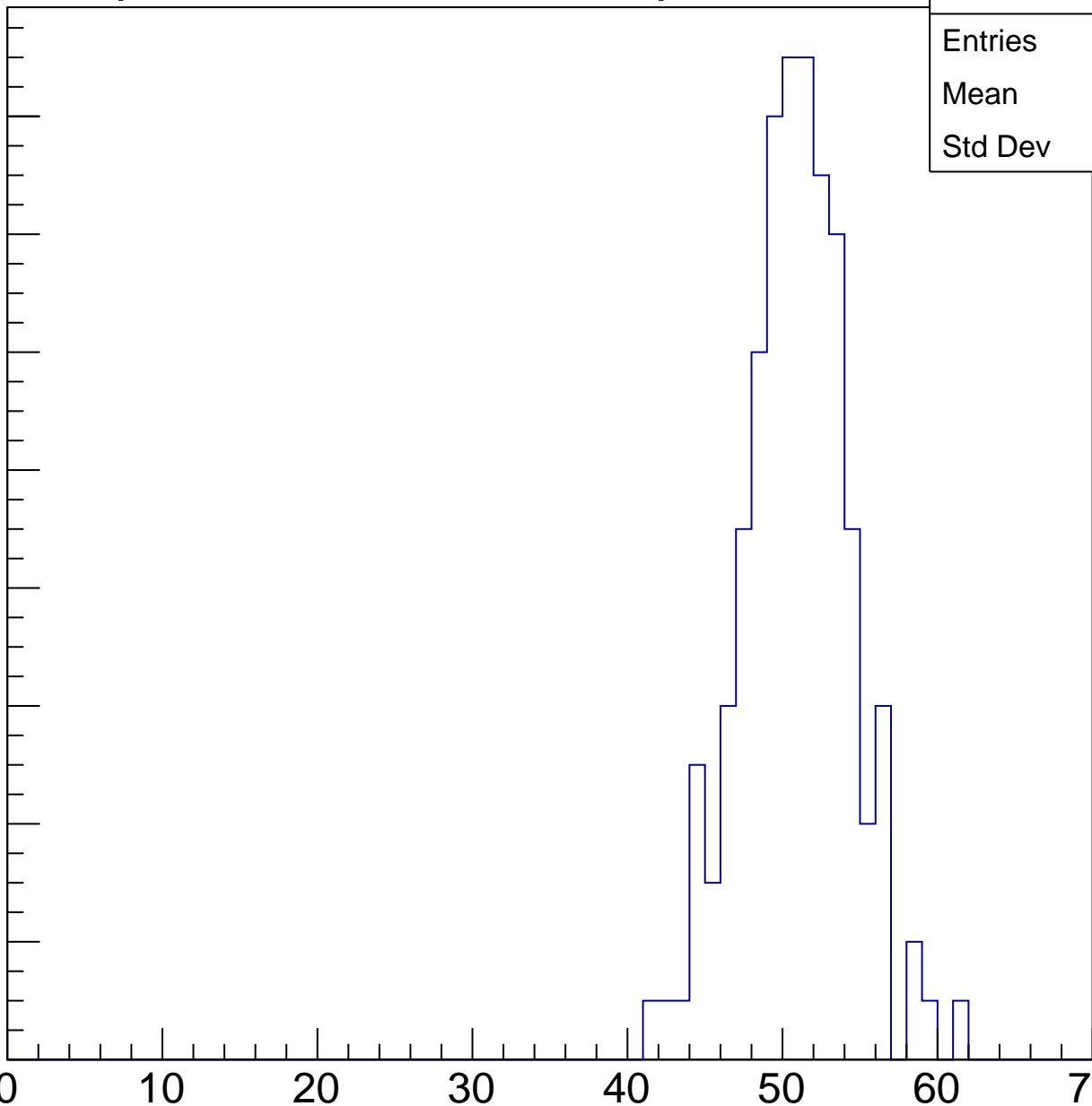
30

40

50

60

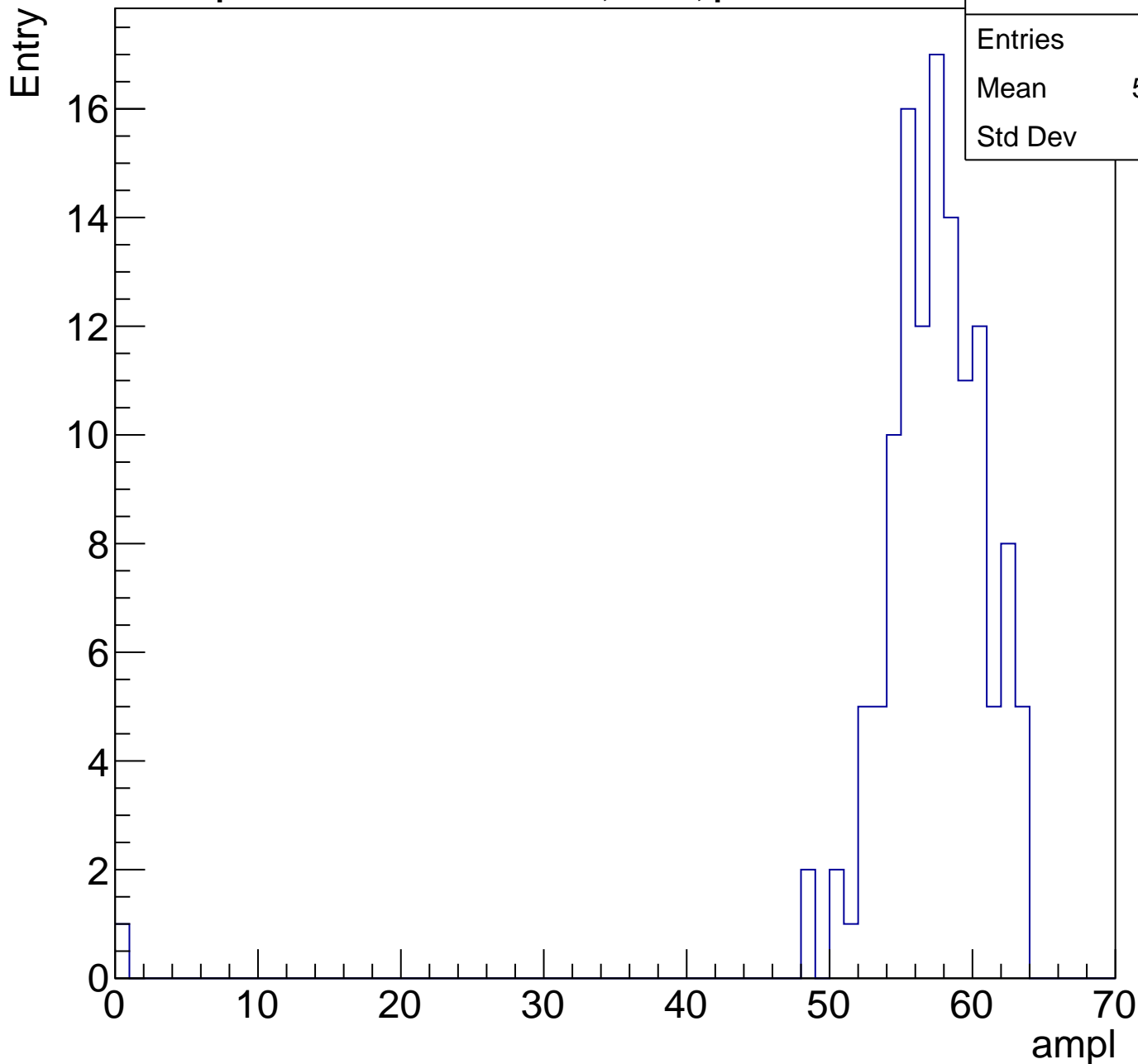
ampl



# B0L101S, U23-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	56.59
Std Dev	5.99



# B0L101S, U23-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

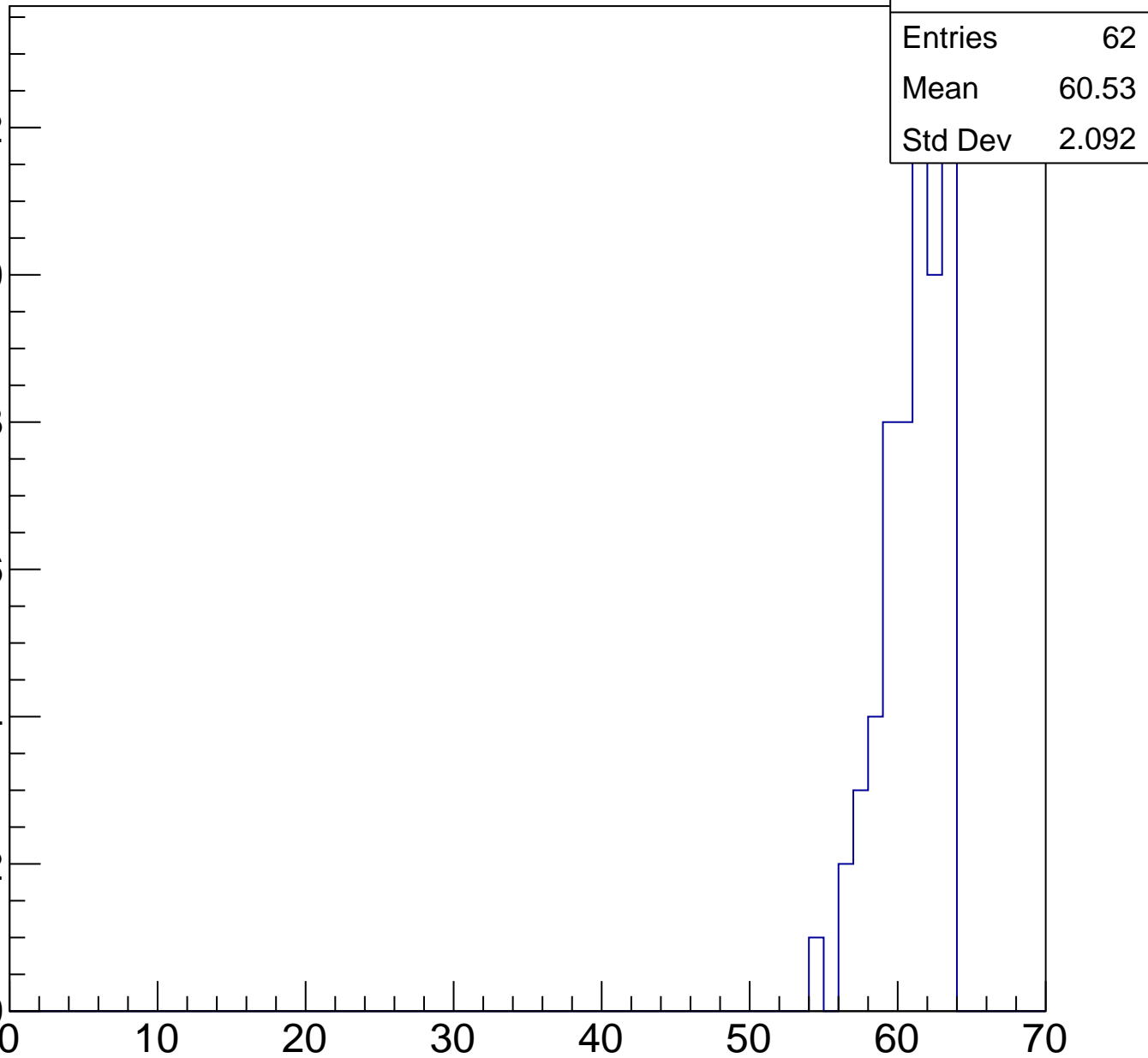
0

Entries	62
---------	----

Mean	60.53
------	-------

Std Dev	2.092
---------	-------

ampl



# B0L101S, U23-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

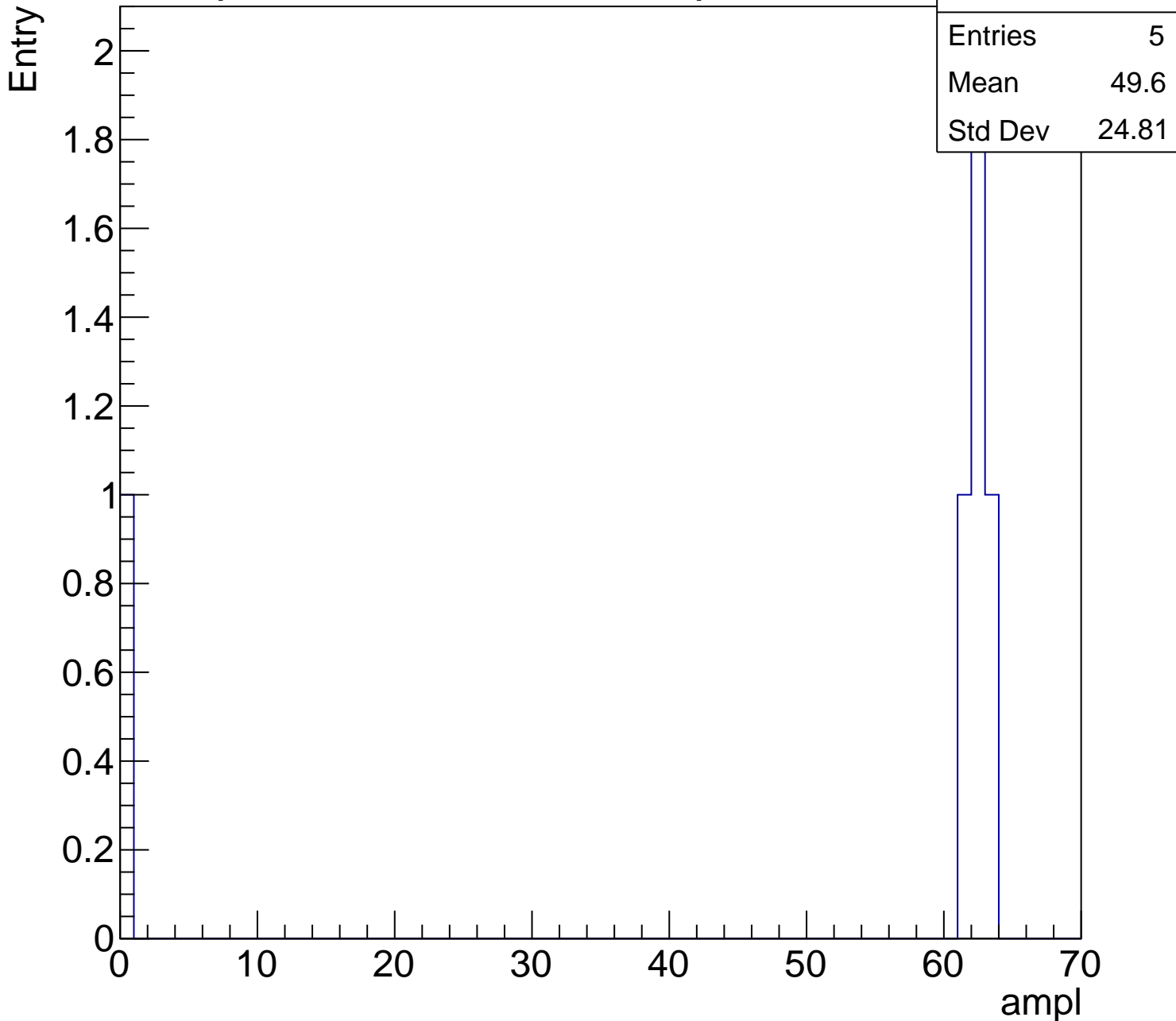
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.6
Std Dev	24.81

ampl

0 10 20 30 40 50 60 70

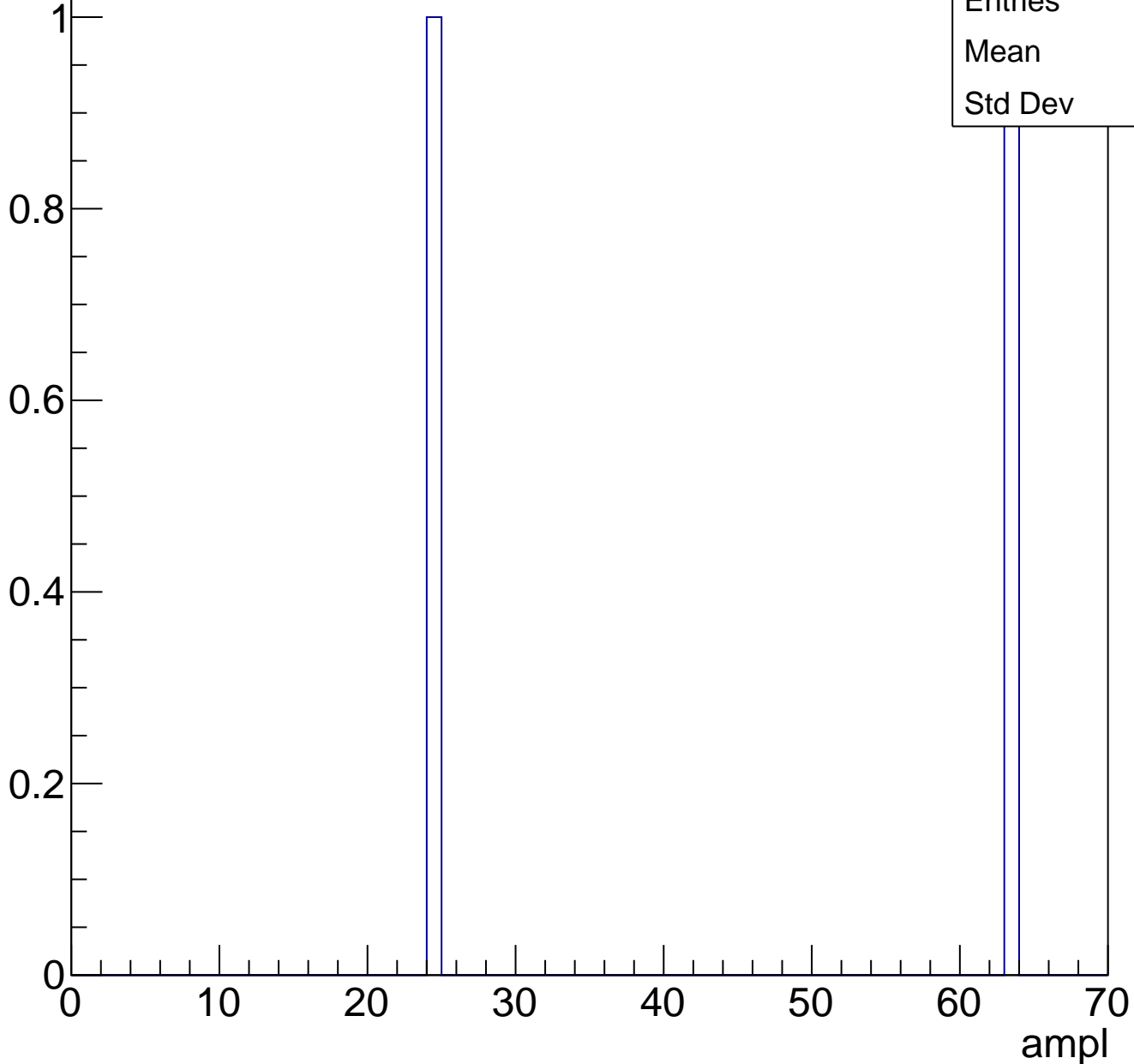




# B0L101S, U23-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch104, adc0

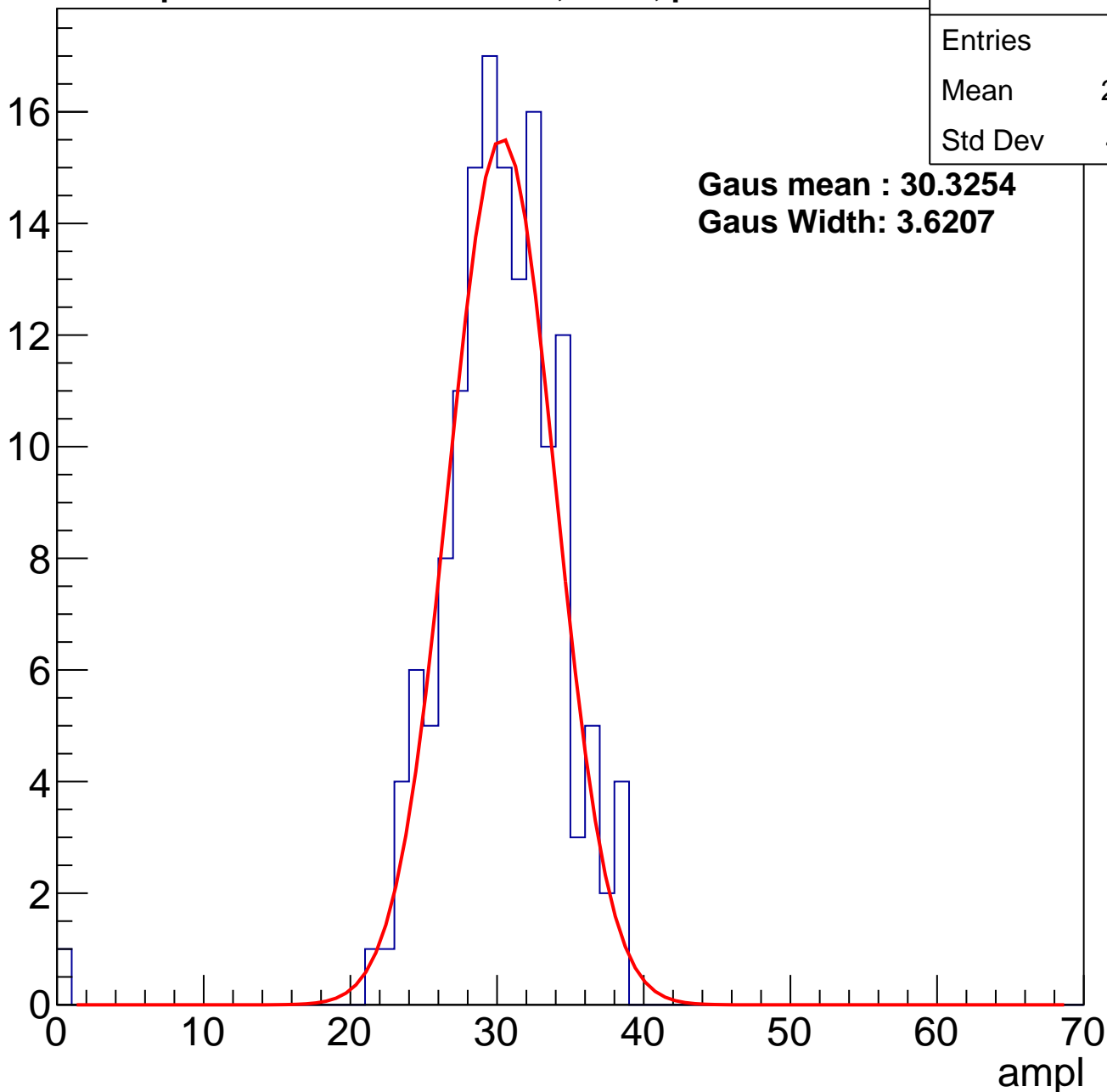
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	29.77
Std Dev	4.371

**Gaus mean : 30.3254**

**Gaus Width: 3.6207**

Entry



# B0L101S, U23-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	37.01
Std Dev	3.595

**Gaus mean : 37.7958**

**Gaus Width: 3.5641**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U23-ch104, adc2

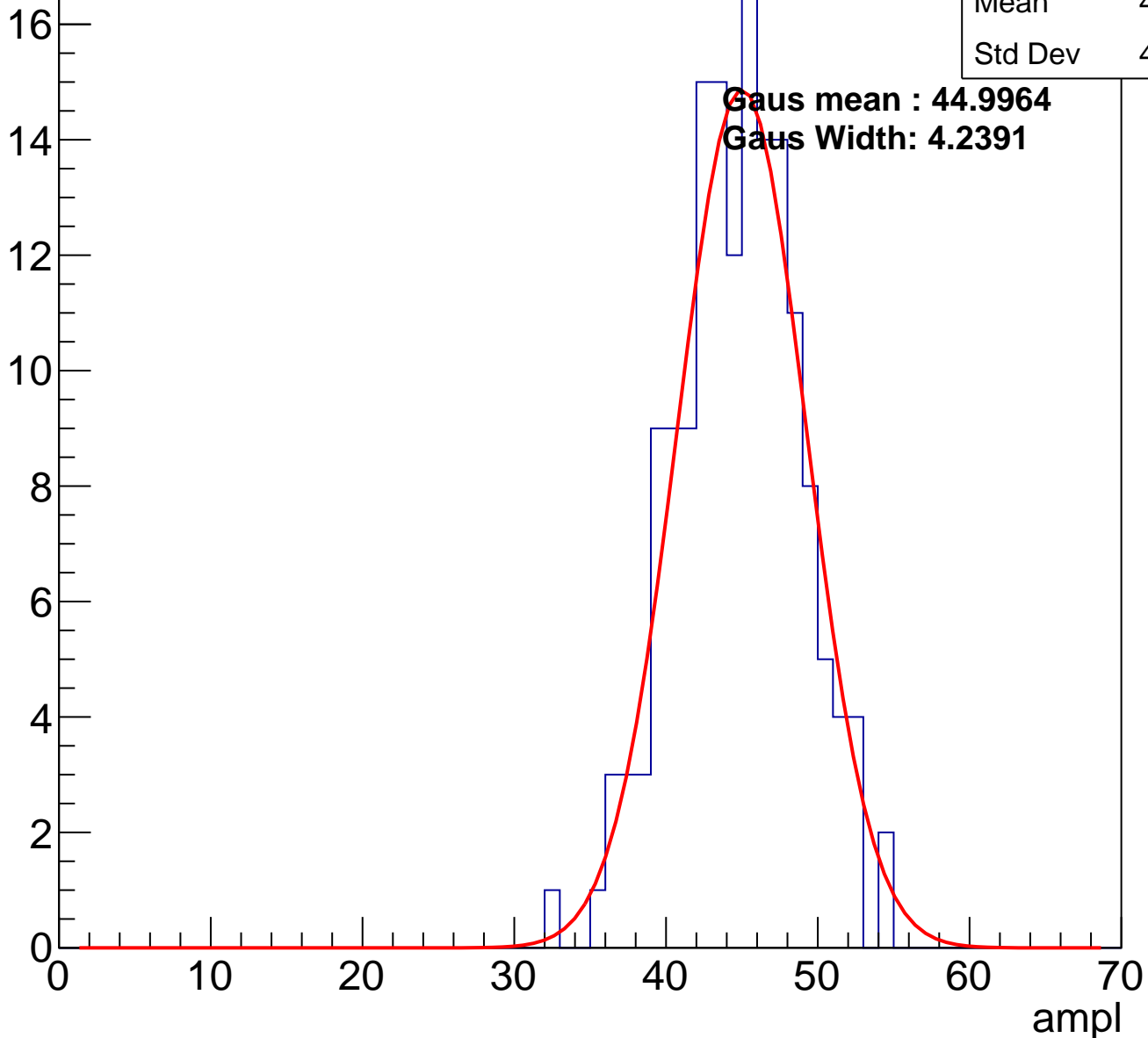
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	159
Mean	44.28
Std Dev	4.037

**Gaus mean : 44.9964**

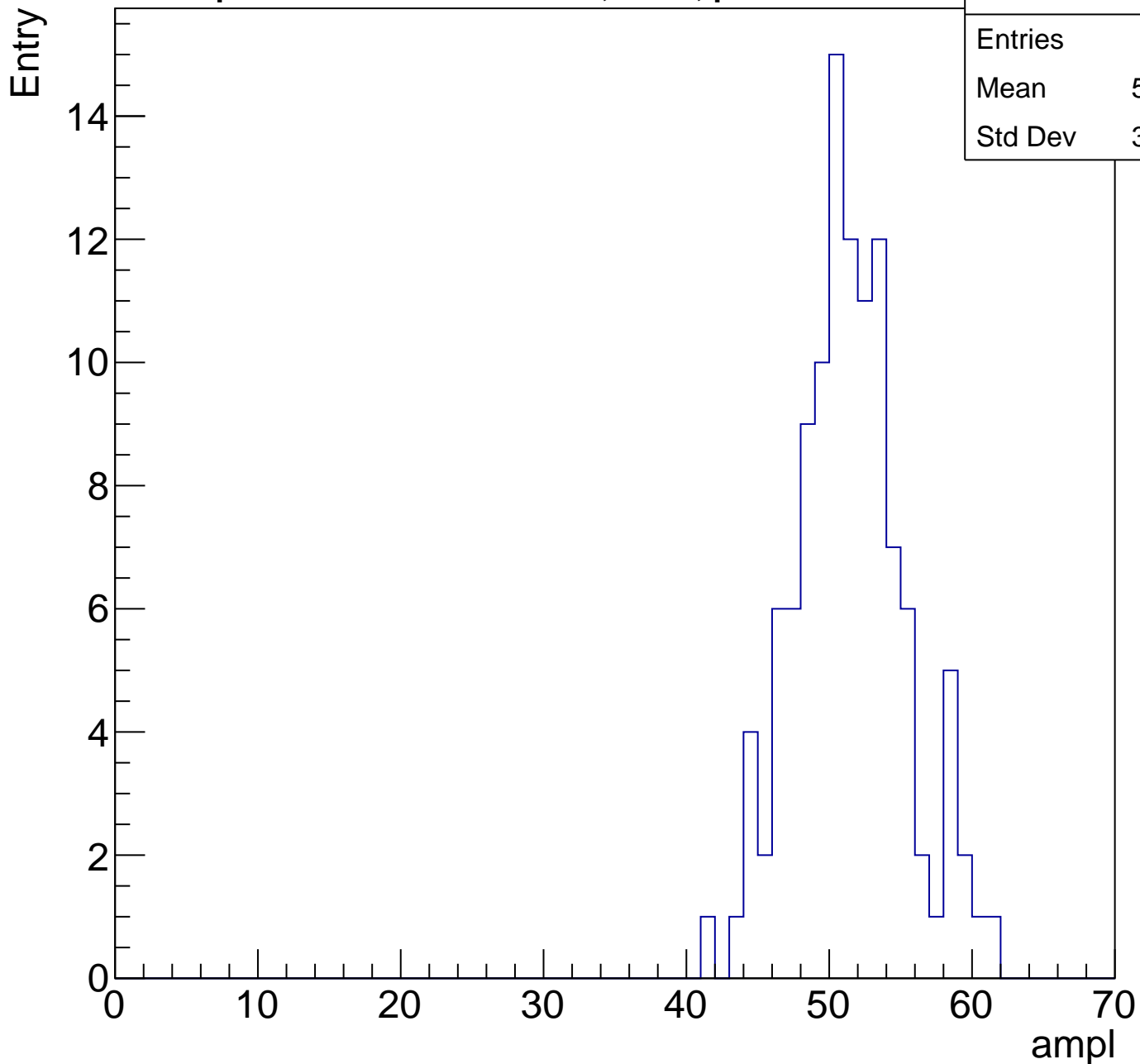
**Gaus Width: 4.2391**



# B0L101S, U23-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	50.93
Std Dev	3.829



# B0L101S, U23-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

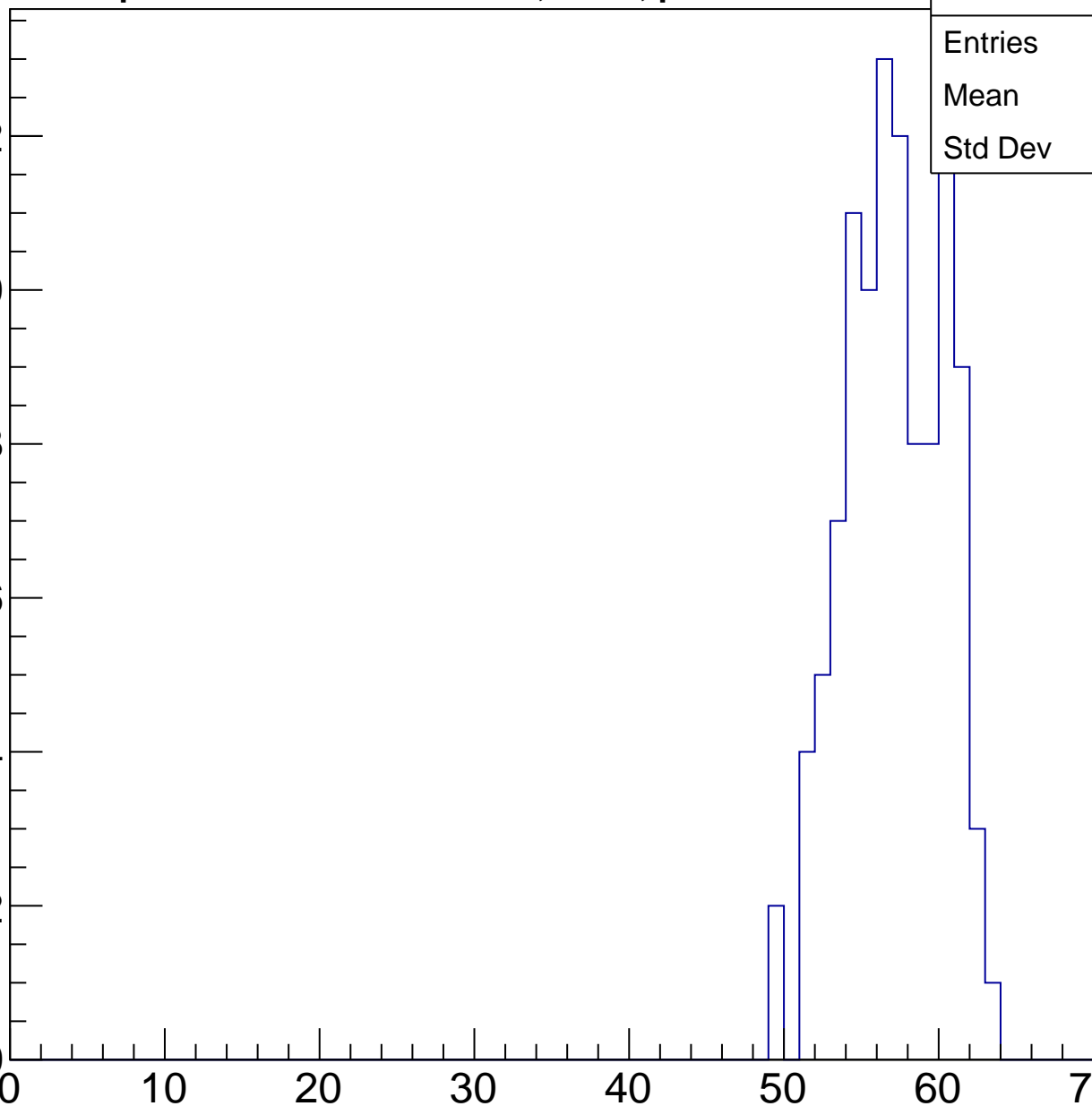
50

60

70

ampl

Entries	105
Mean	56.6
Std Dev	3.158



# B0L101S, U23-ch104, adc5

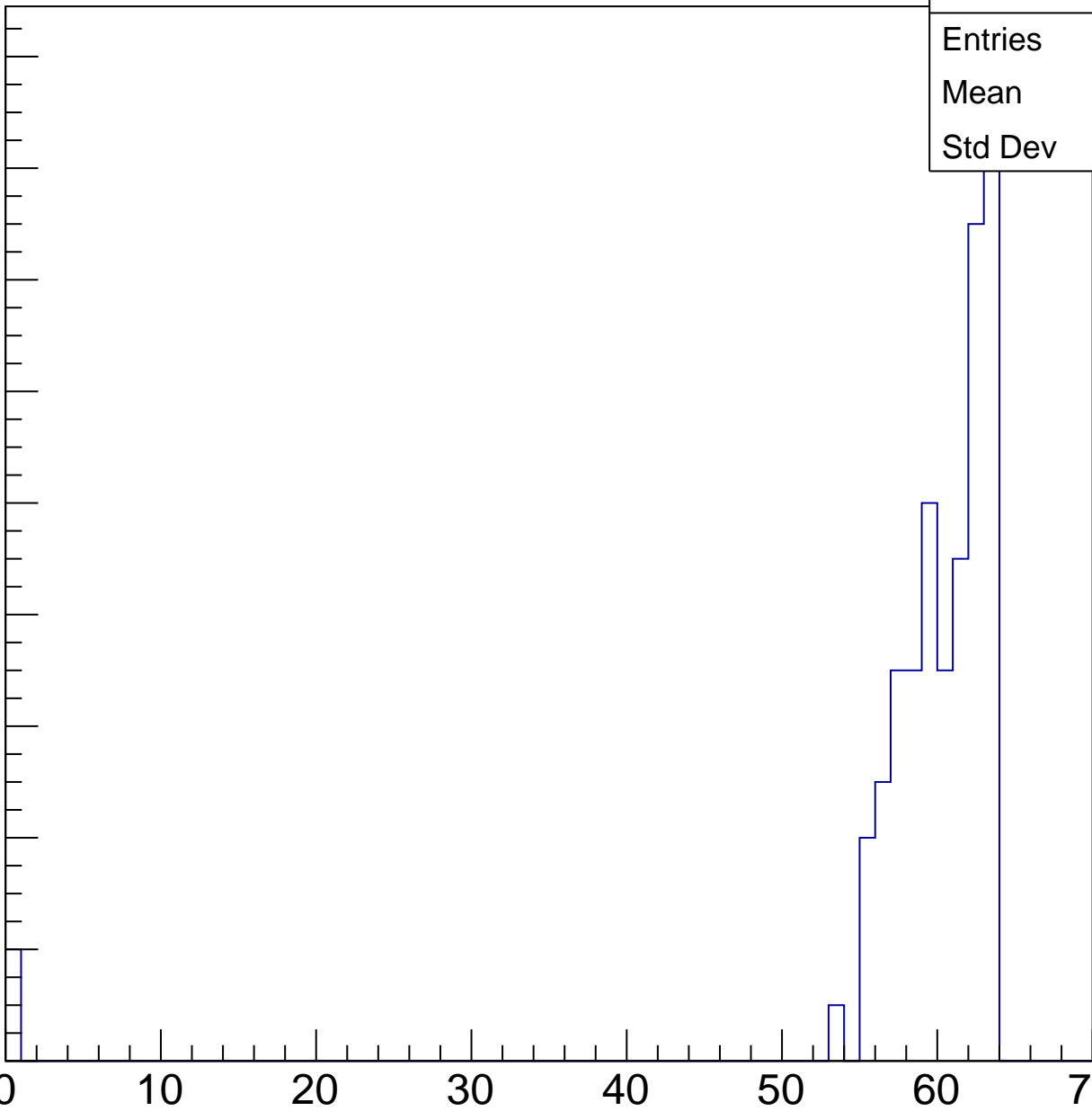
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	58.6
Std Dev	9.452

ampl



# B0L101S, U23-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	62
Std Dev	0.7071



# B0L101S, U23-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



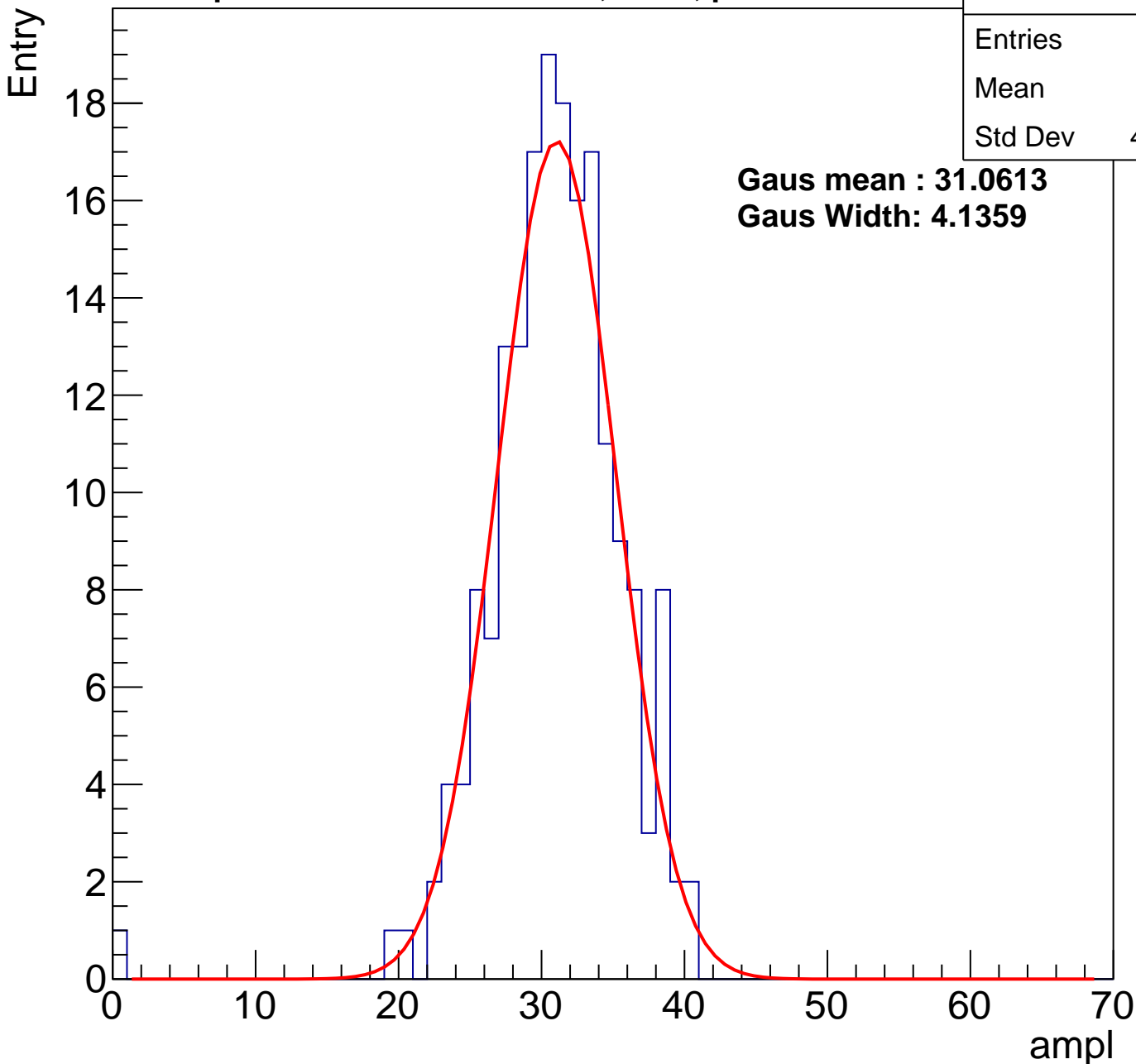
Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	184
Mean	30.5
Std Dev	4.654

**Gaus mean : 31.0613**  
**Gaus Width: 4.1359**



# B0L101S, U23-ch105, adc1

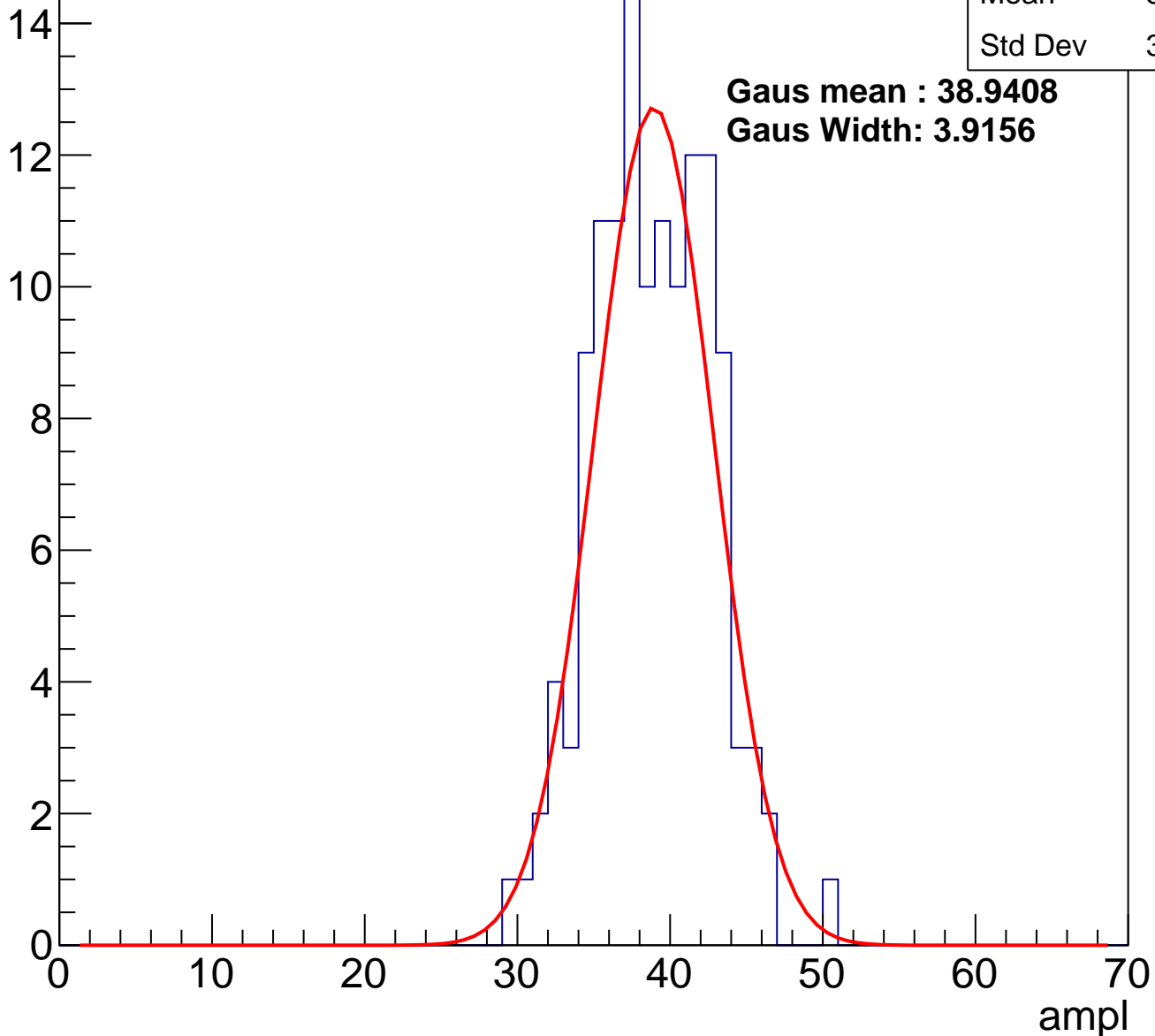
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	38.39
Std Dev	3.769

**Gaus mean : 38.9408**

**Gaus Width: 3.9156**

Entry



# B0L101S, U23-ch105, adc2

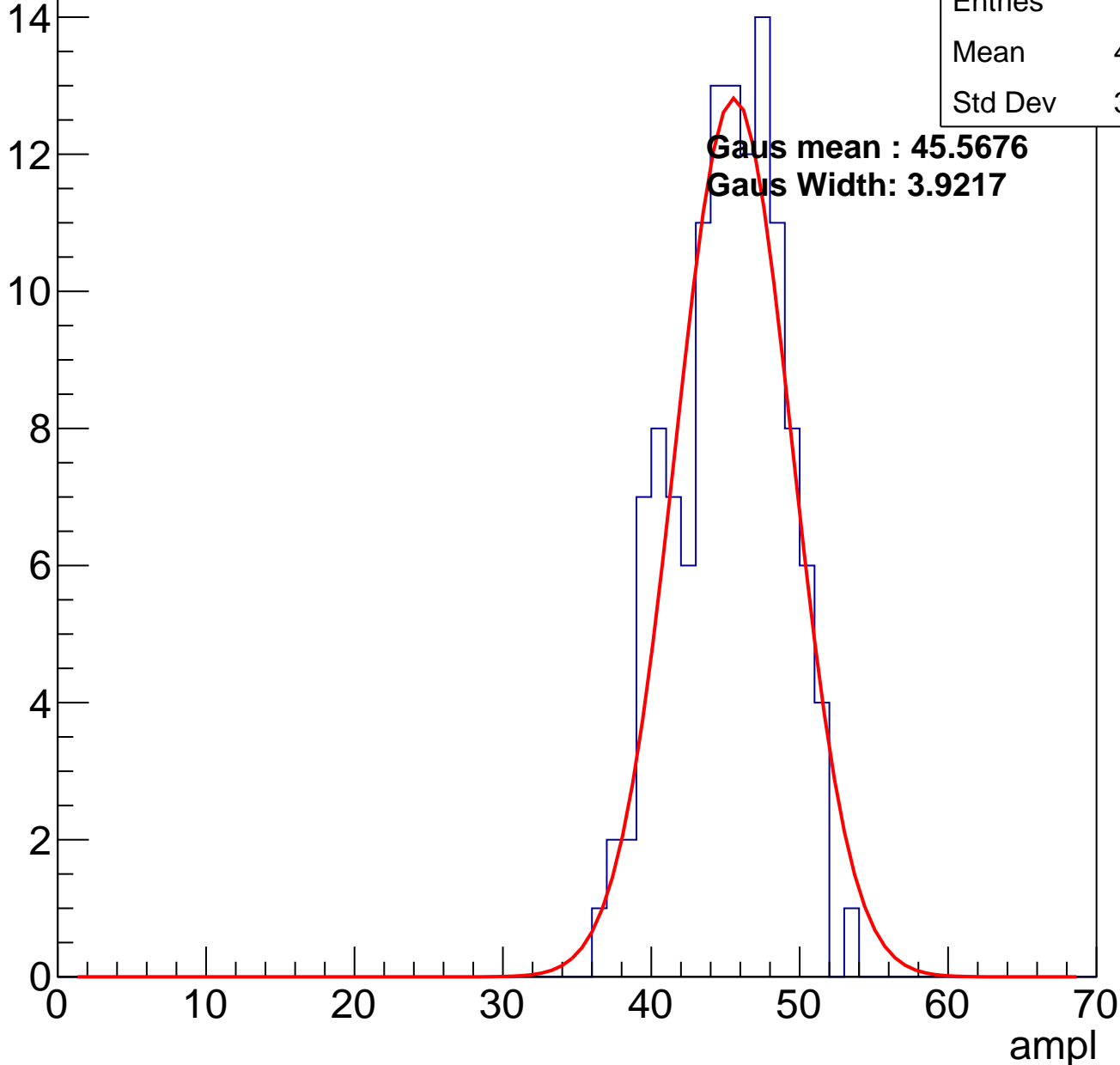
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	126
Mean	44.72
Std Dev	3.592

**Gaus mean : 45.5676**

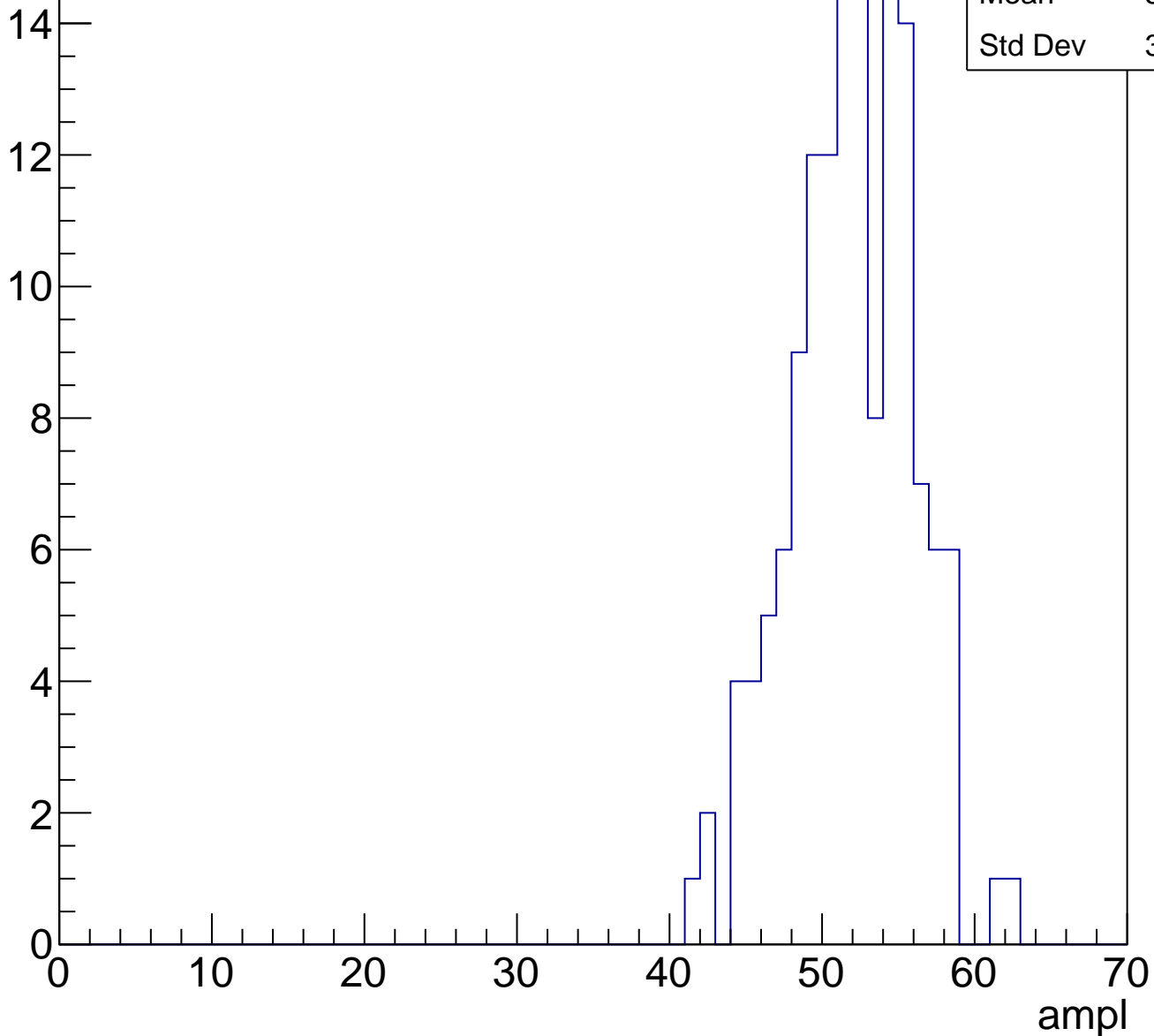
**Gaus Width: 3.9217**



# B0L101S, U23-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch105, adc4

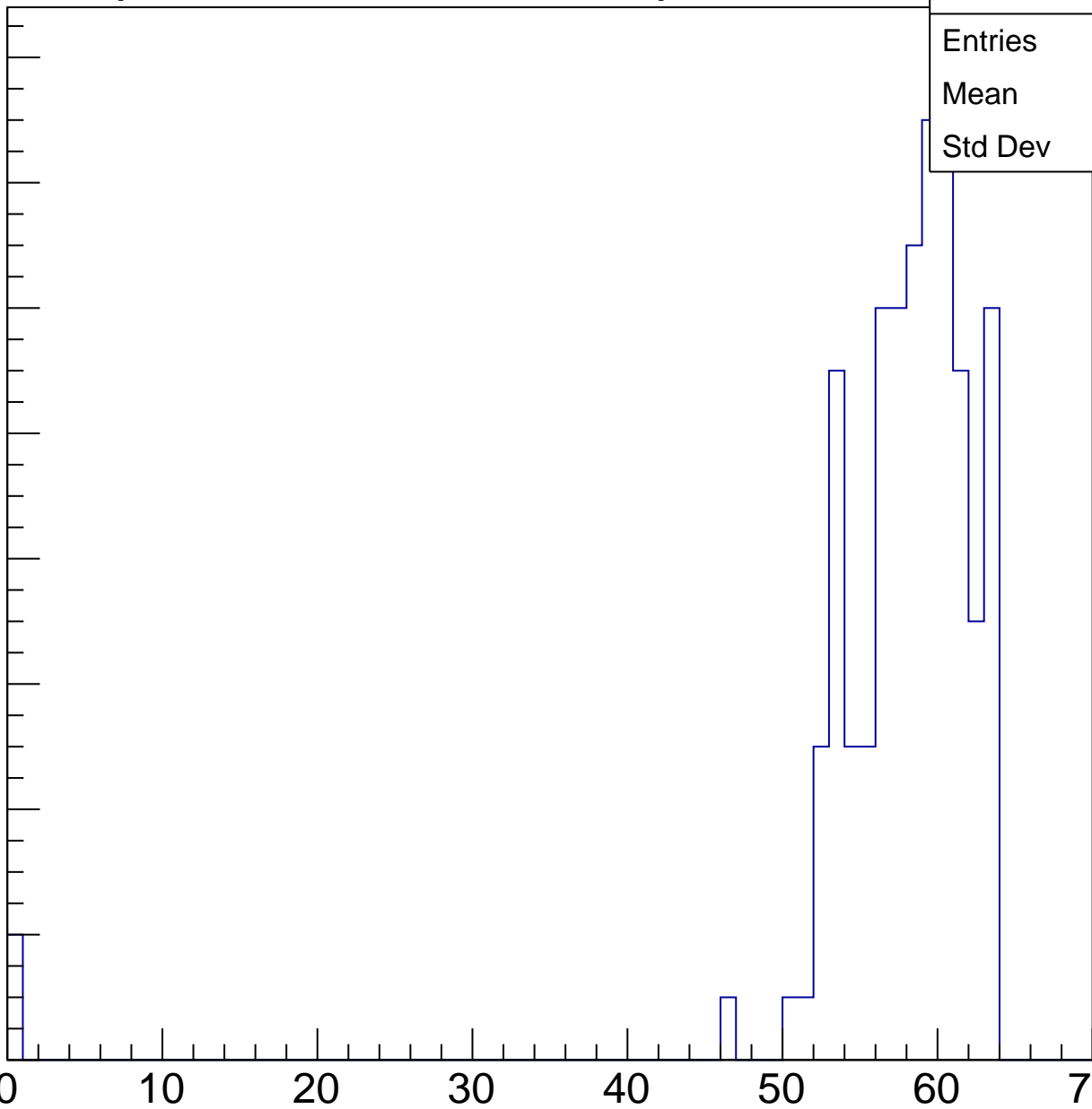
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	129
Mean	56.98
Std Dev	7.915

ampl

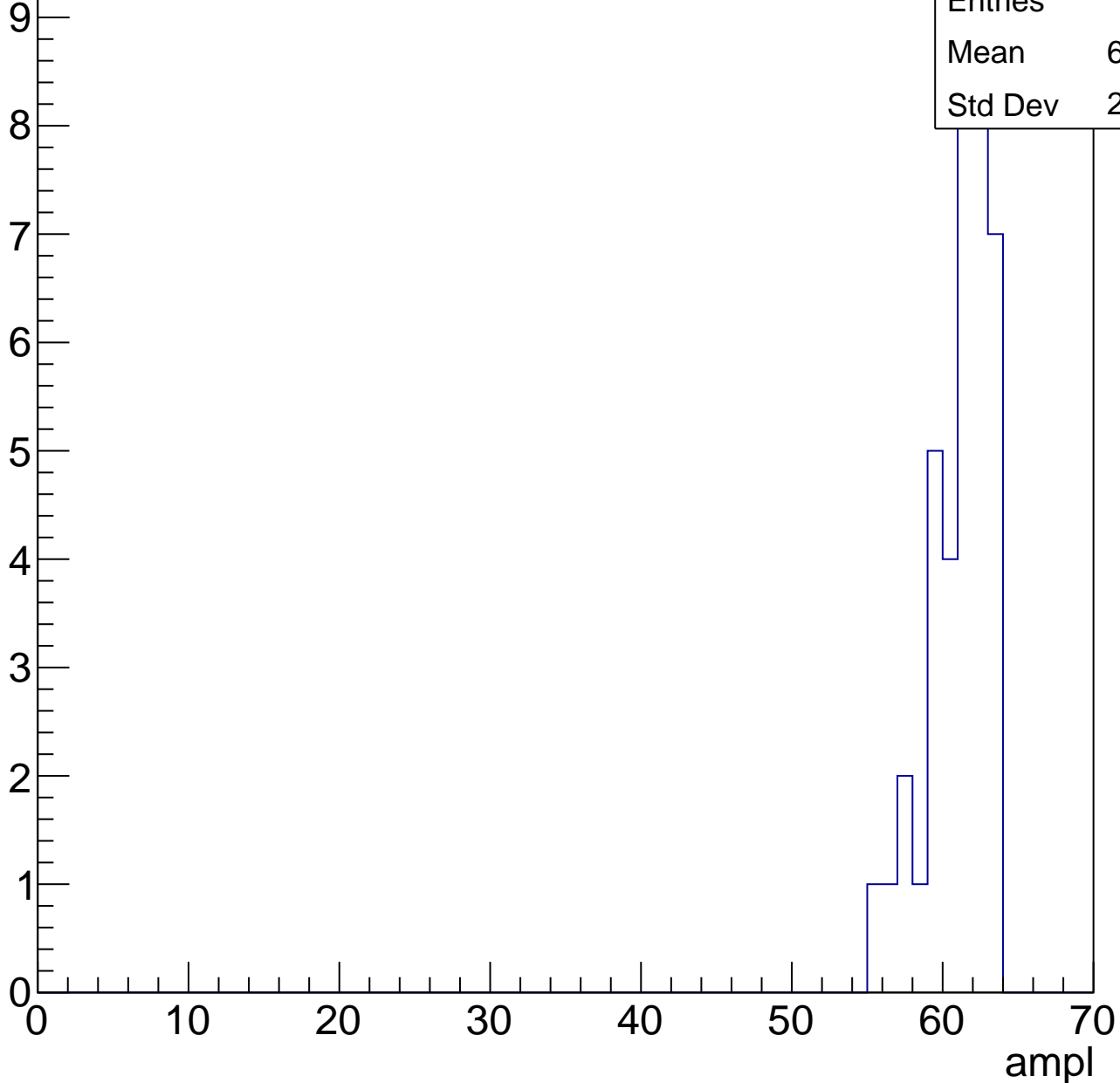


# B0L101S, U23-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	38
Mean	60.66
Std Dev	2.043



# B0L101S, U23-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch106, adc0

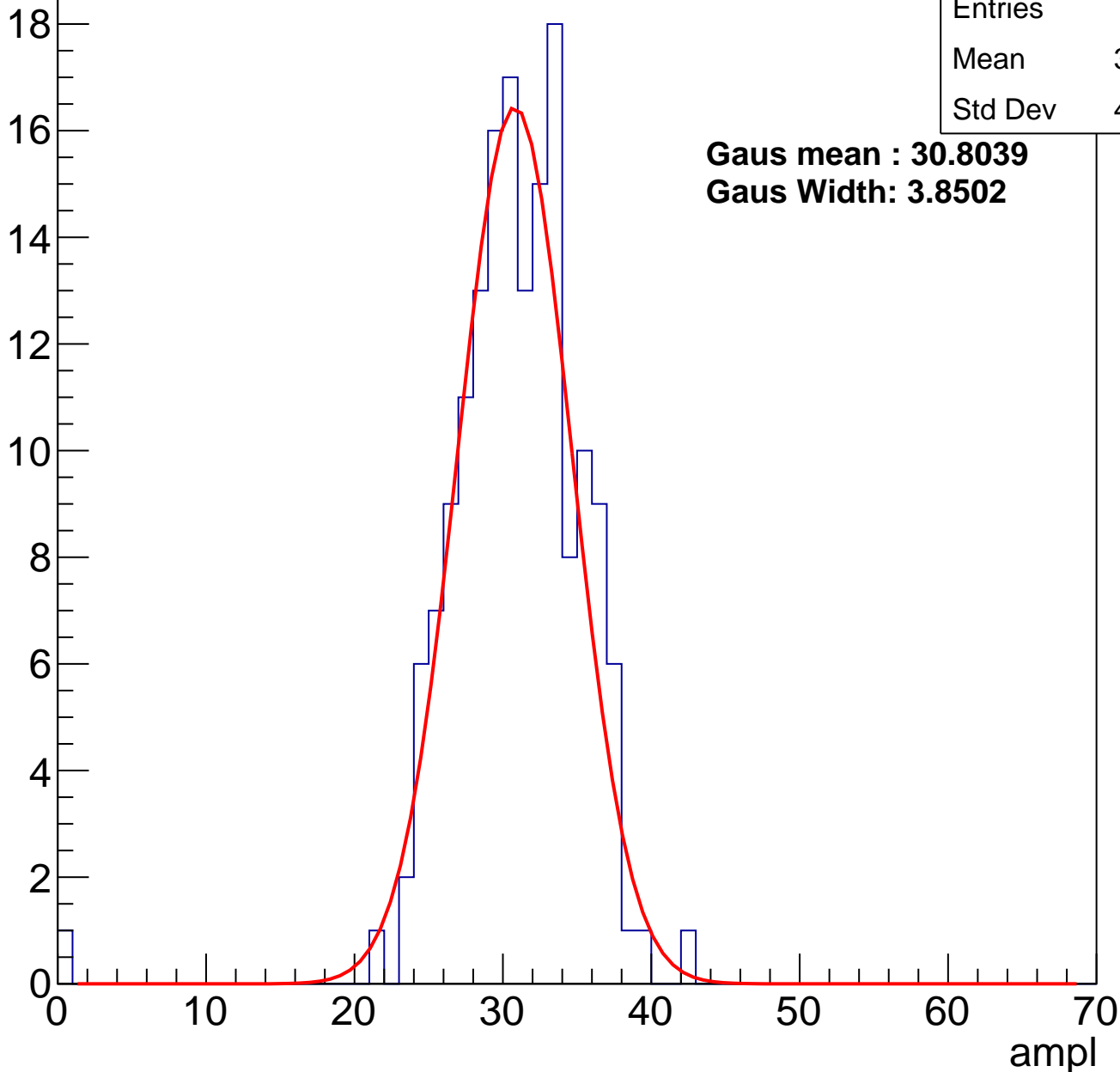
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	30.42
Std Dev	4.453

**Gaus mean : 30.8039**

**Gaus Width: 3.8502**

Entry



# B0L101S, U23-ch106, adc1

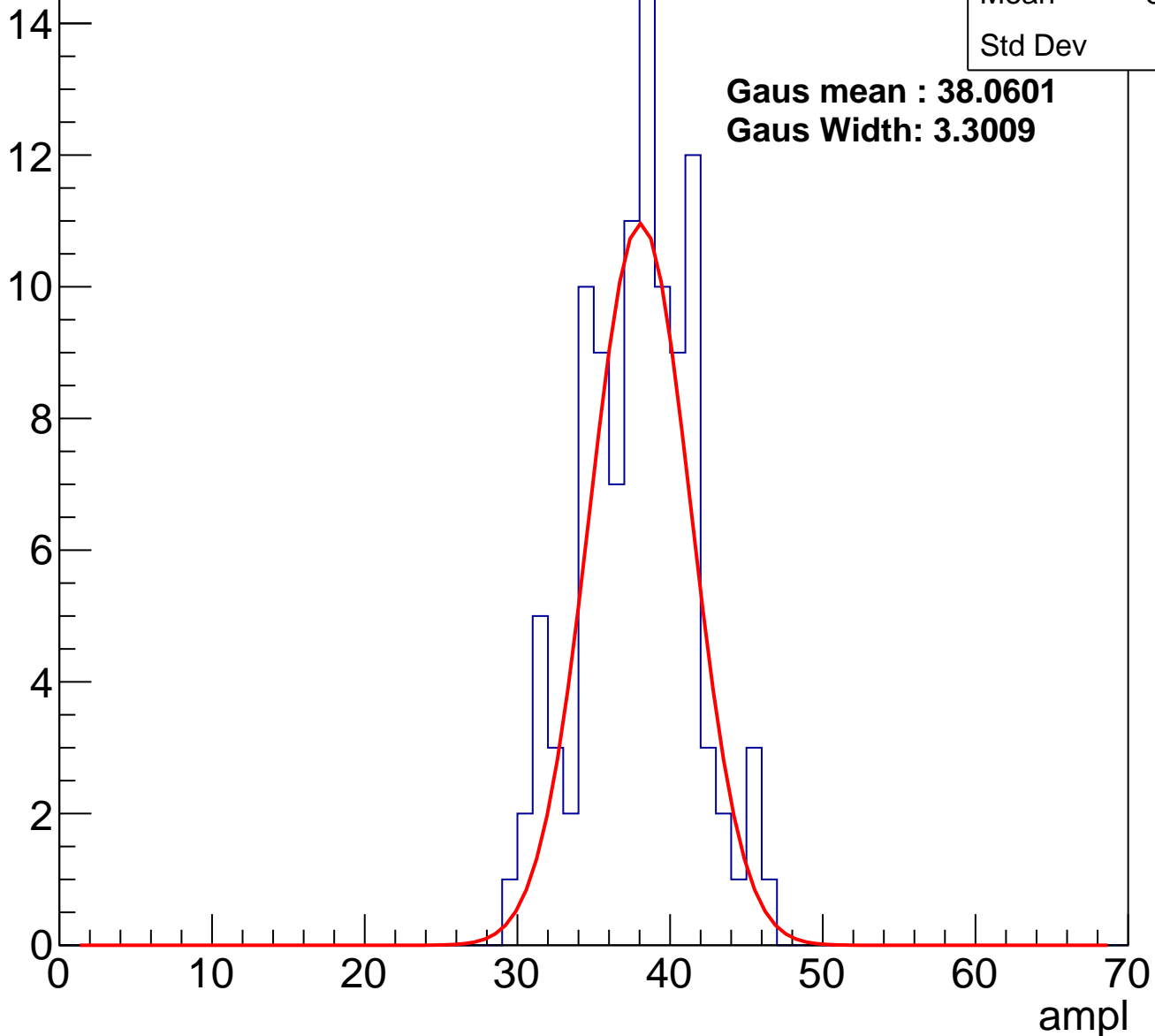
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	37.44
Std Dev	3.58

**Gaus mean : 38.0601**

**Gaus Width: 3.3009**

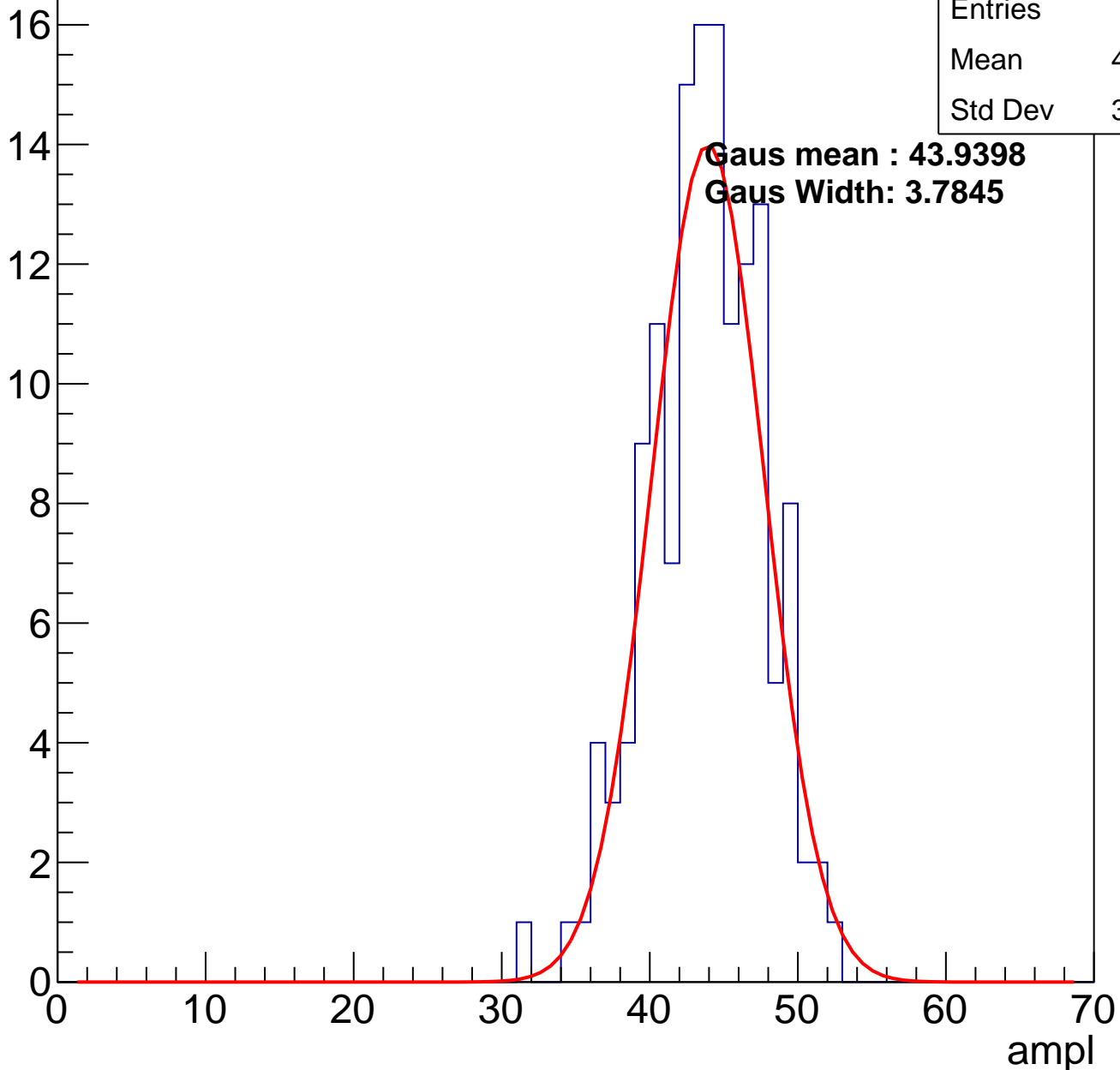
Entry



# B0L101S, U23-ch106, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

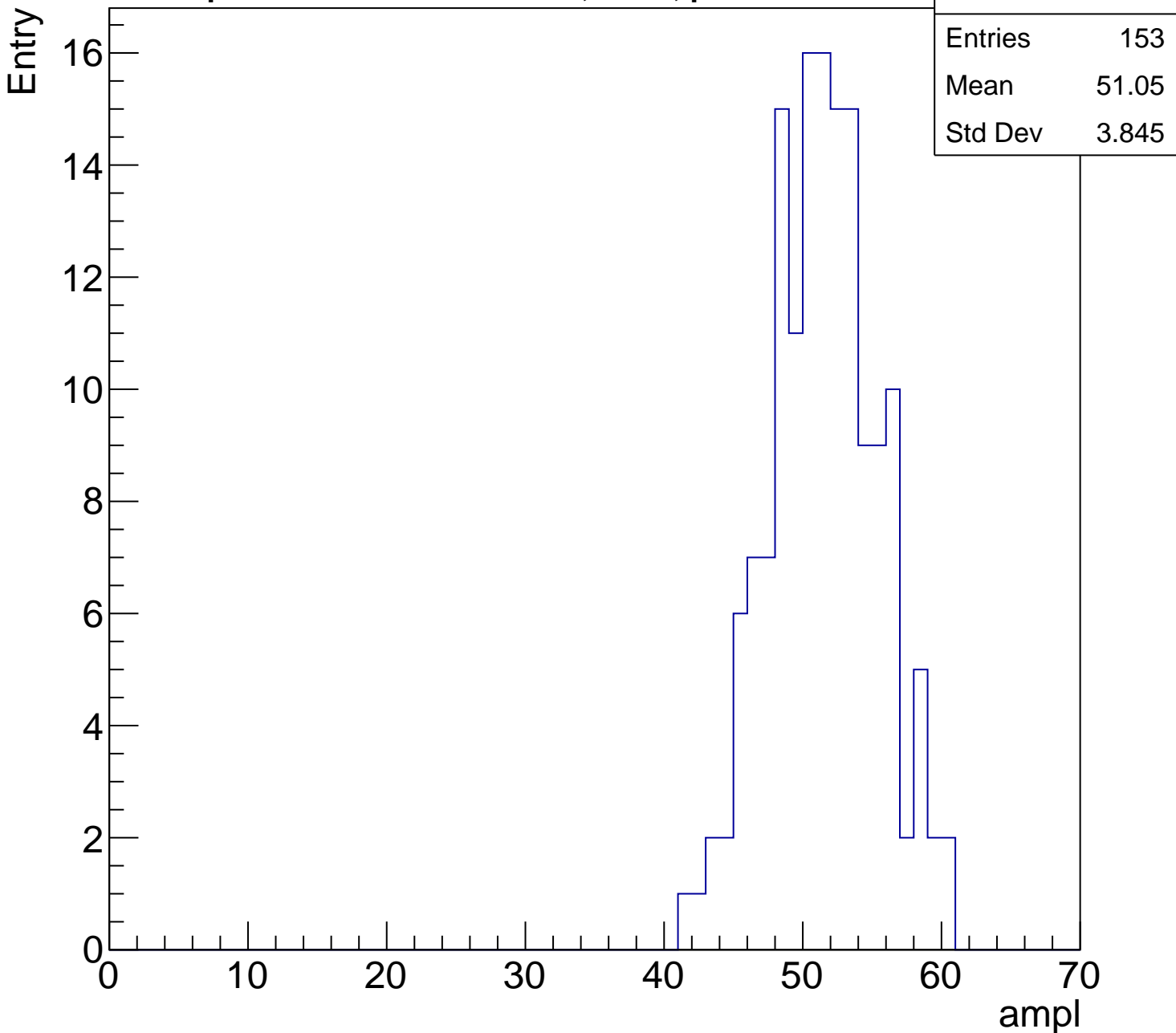
Entries	153
Mean	51.05
Std Dev	3.845

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch106, adc4

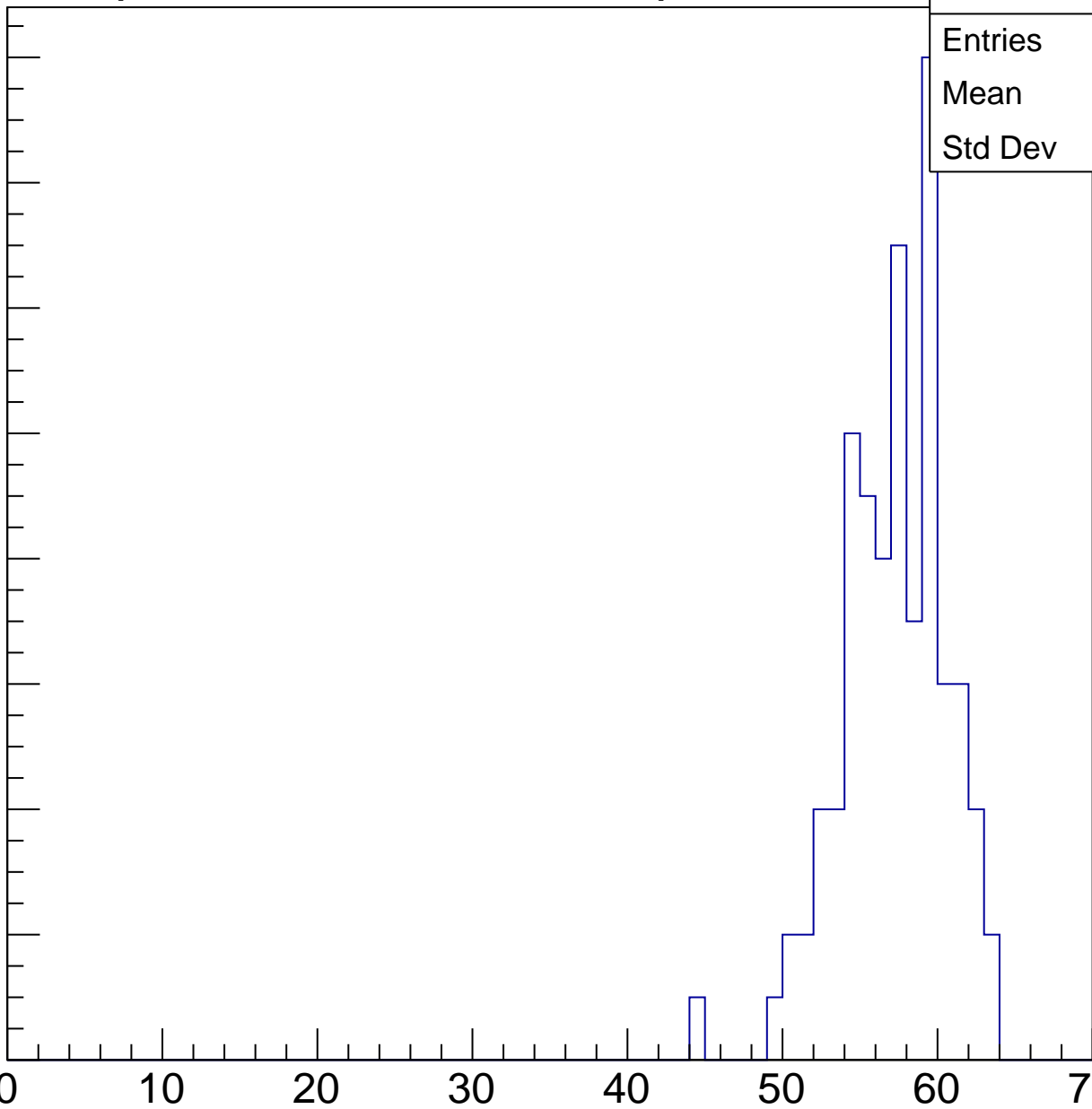
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	95
Mean	56.73
Std Dev	3.382

ampl



# B0L101S, U23-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

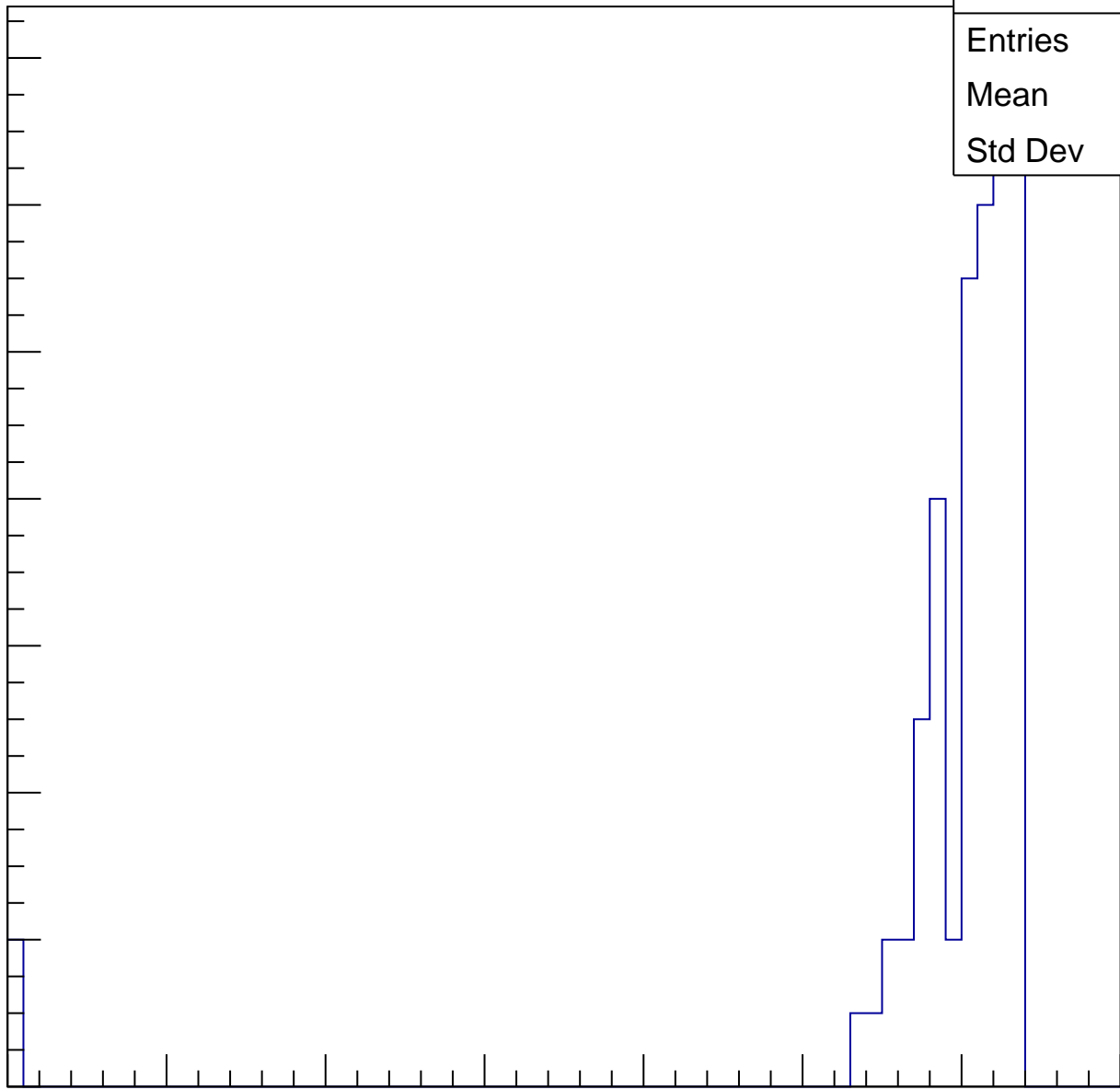
Entries	73
Mean	58.58
Std Dev	10.13

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

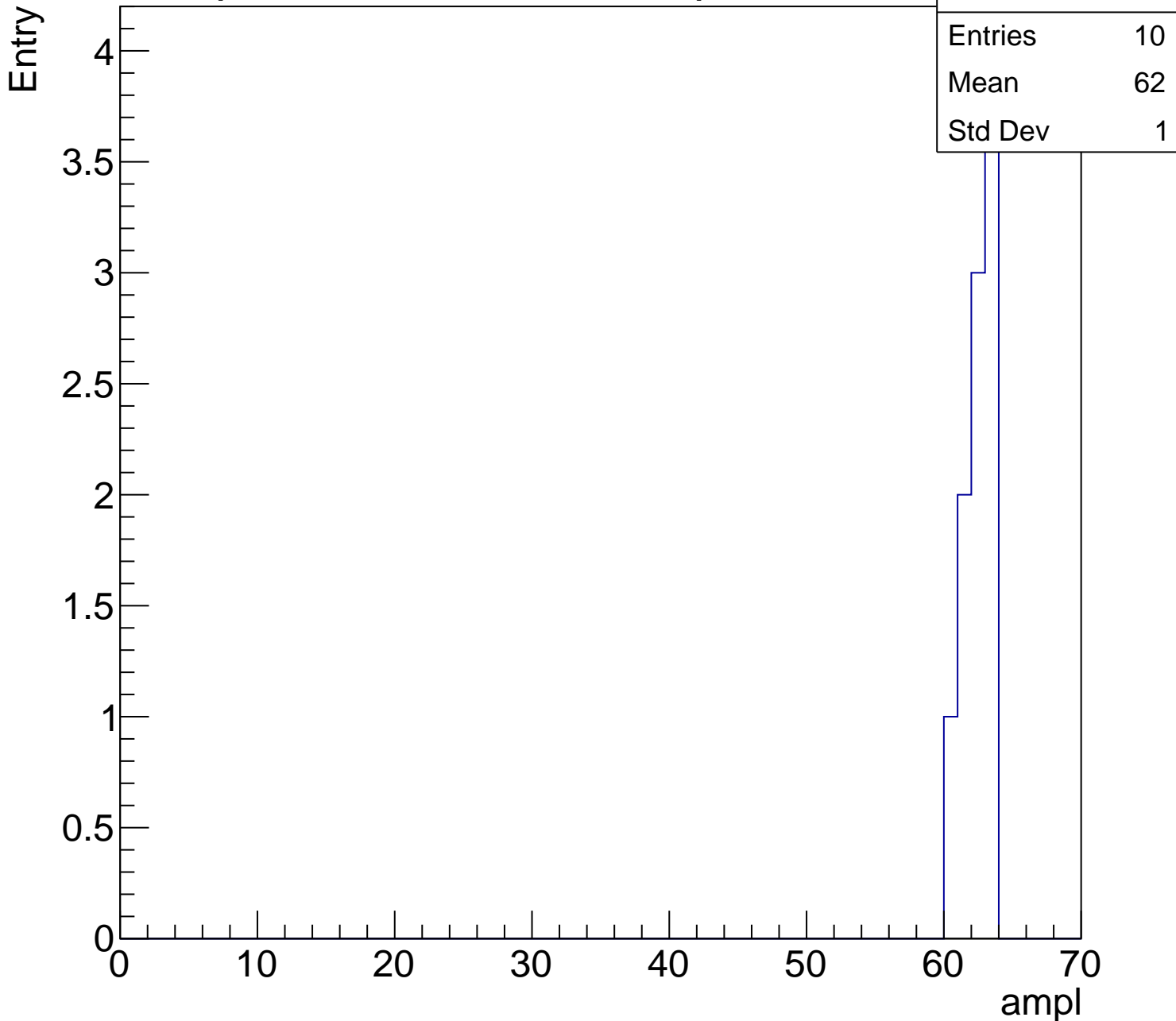
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	10
Mean	62
Std Dev	1

ampl

0 10 20 30 40 50 60 70





# B0L101S, U23-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

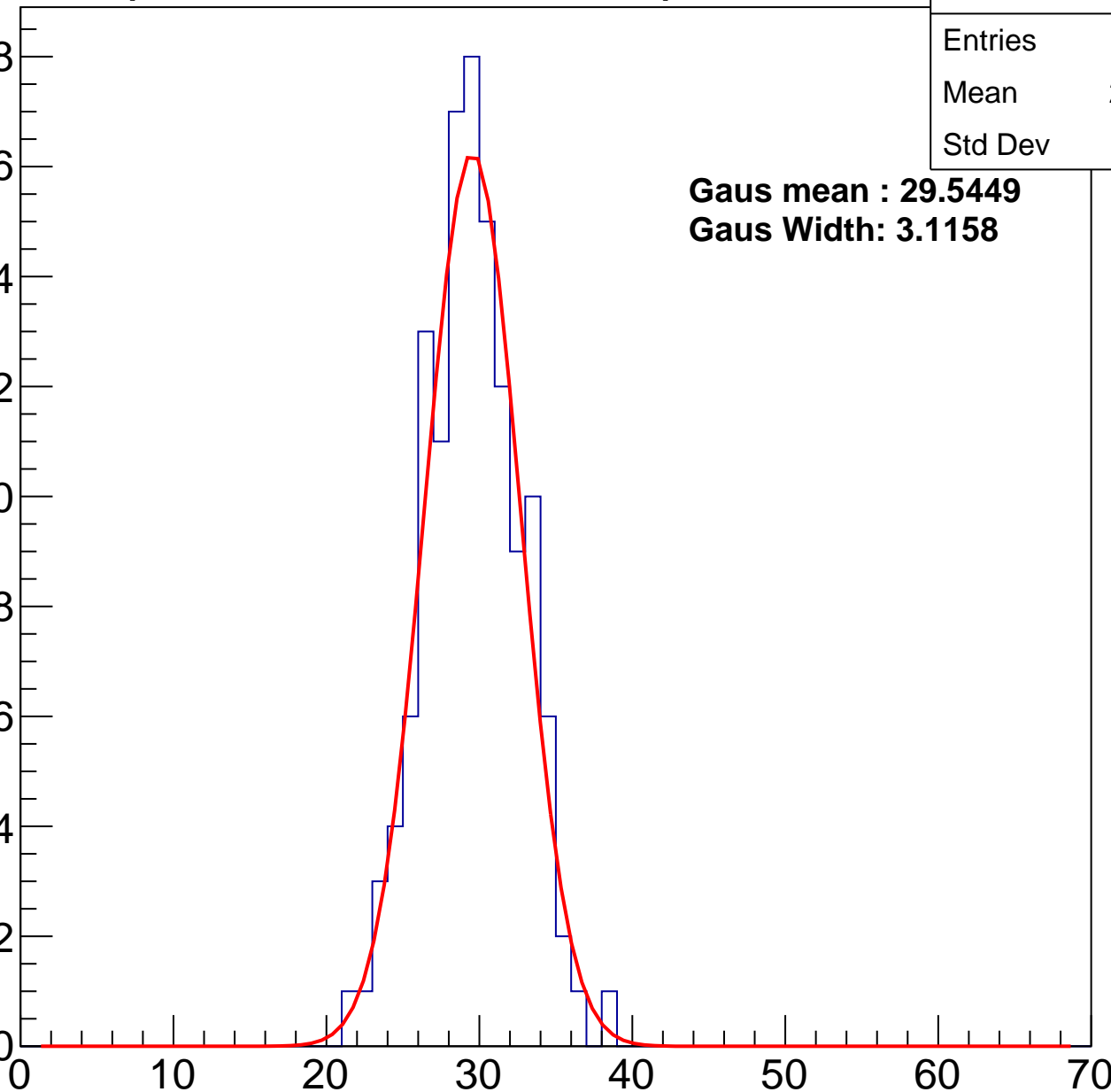
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	130
Mean	29.07
Std Dev	3.101

**Gaus mean : 29.5449**

**Gaus Width: 3.1158**

ampl



# B0L101S, U23-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	36.49
Std Dev	3.641

**Gaus mean : 36.5764**

**Gaus Width: 4.0263**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

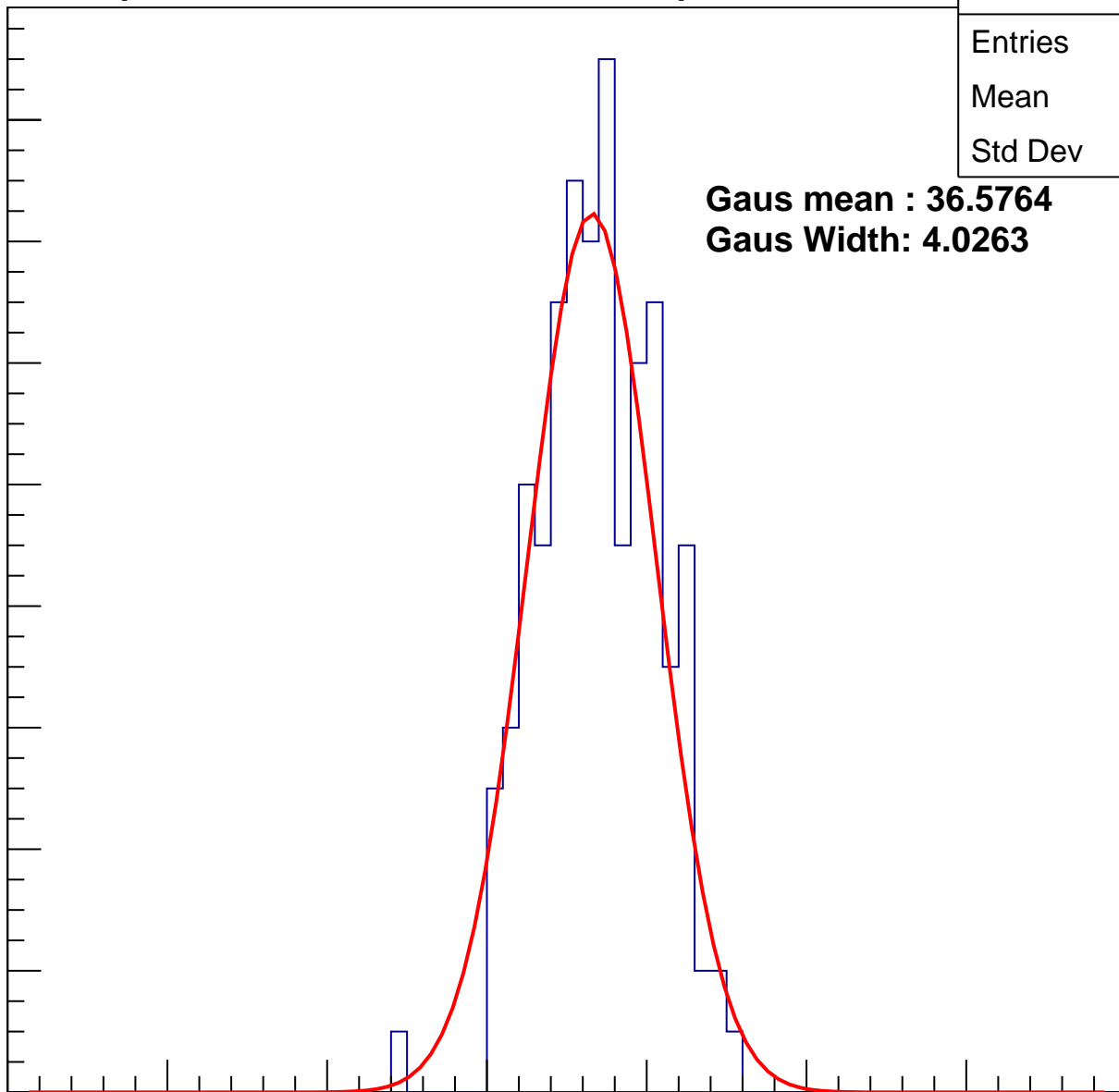
40

50

60

70

ampl



# B0L101S, U23-ch107, adc2

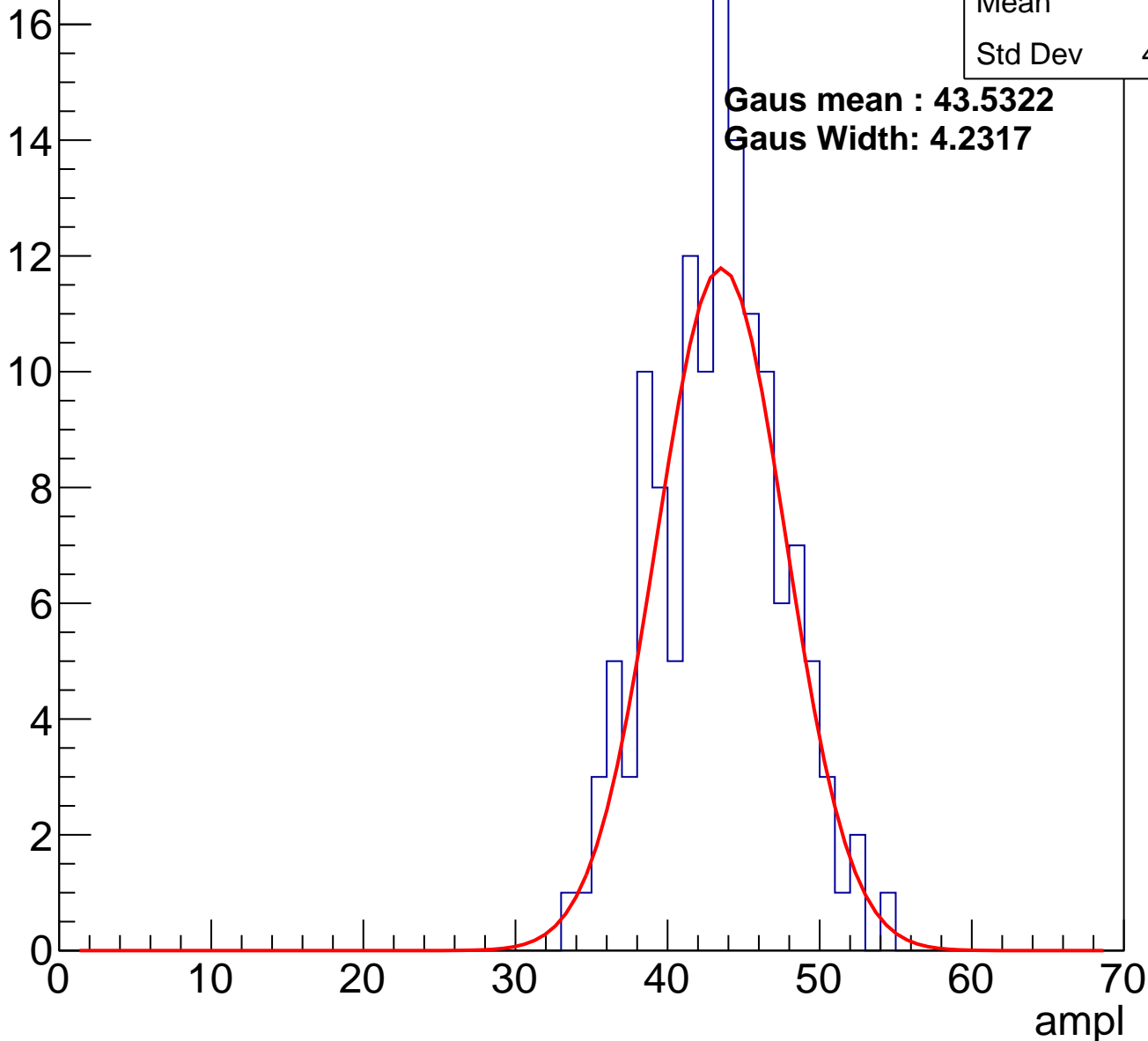
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	135
Mean	42.9
Std Dev	4.126

**Gaus mean : 43.5322**

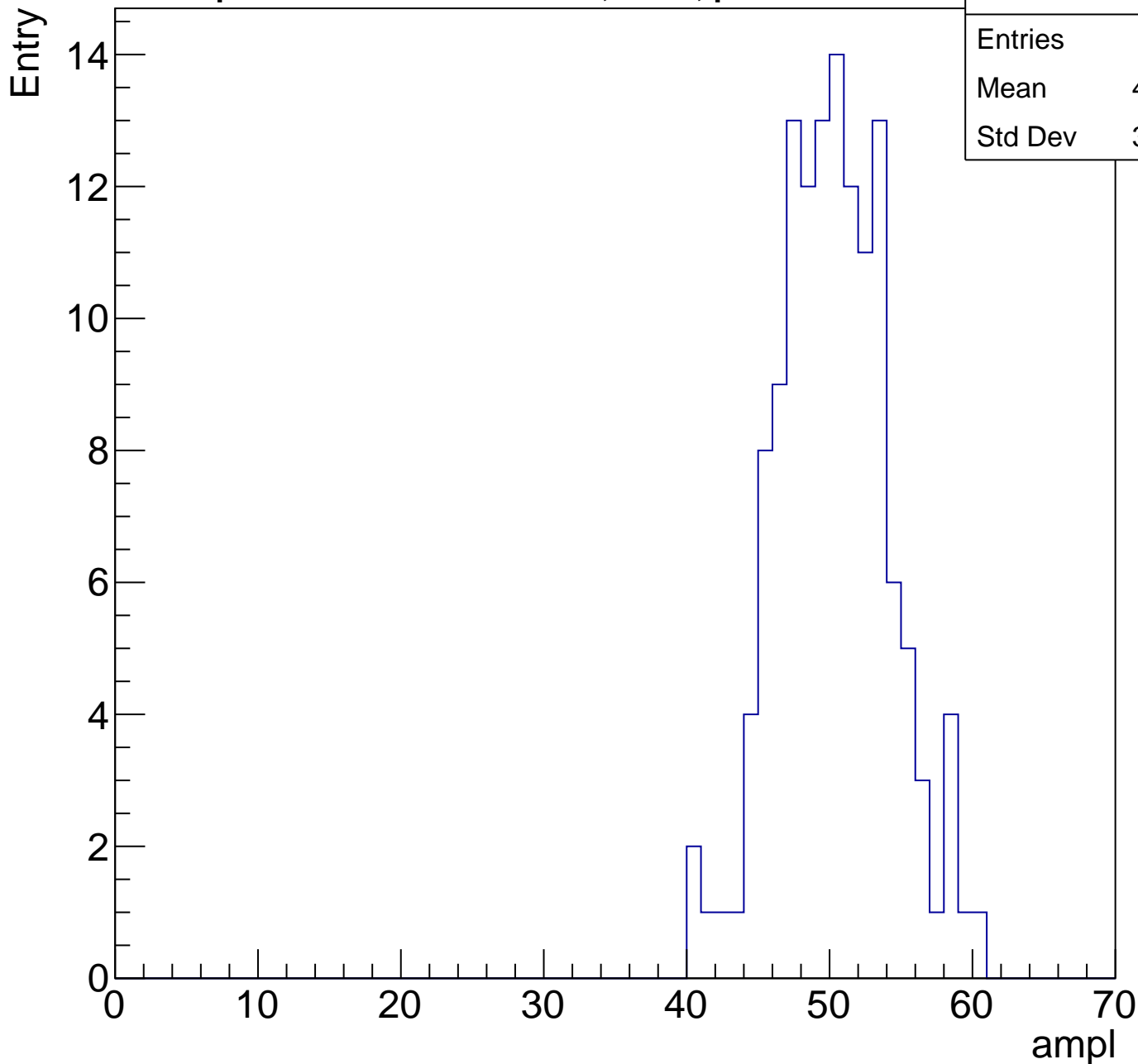
**Gaus Width: 4.2317**



# B0L101S, U23-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	49.84
Std Dev	3.892



# B0L101S, U23-ch107, adc4

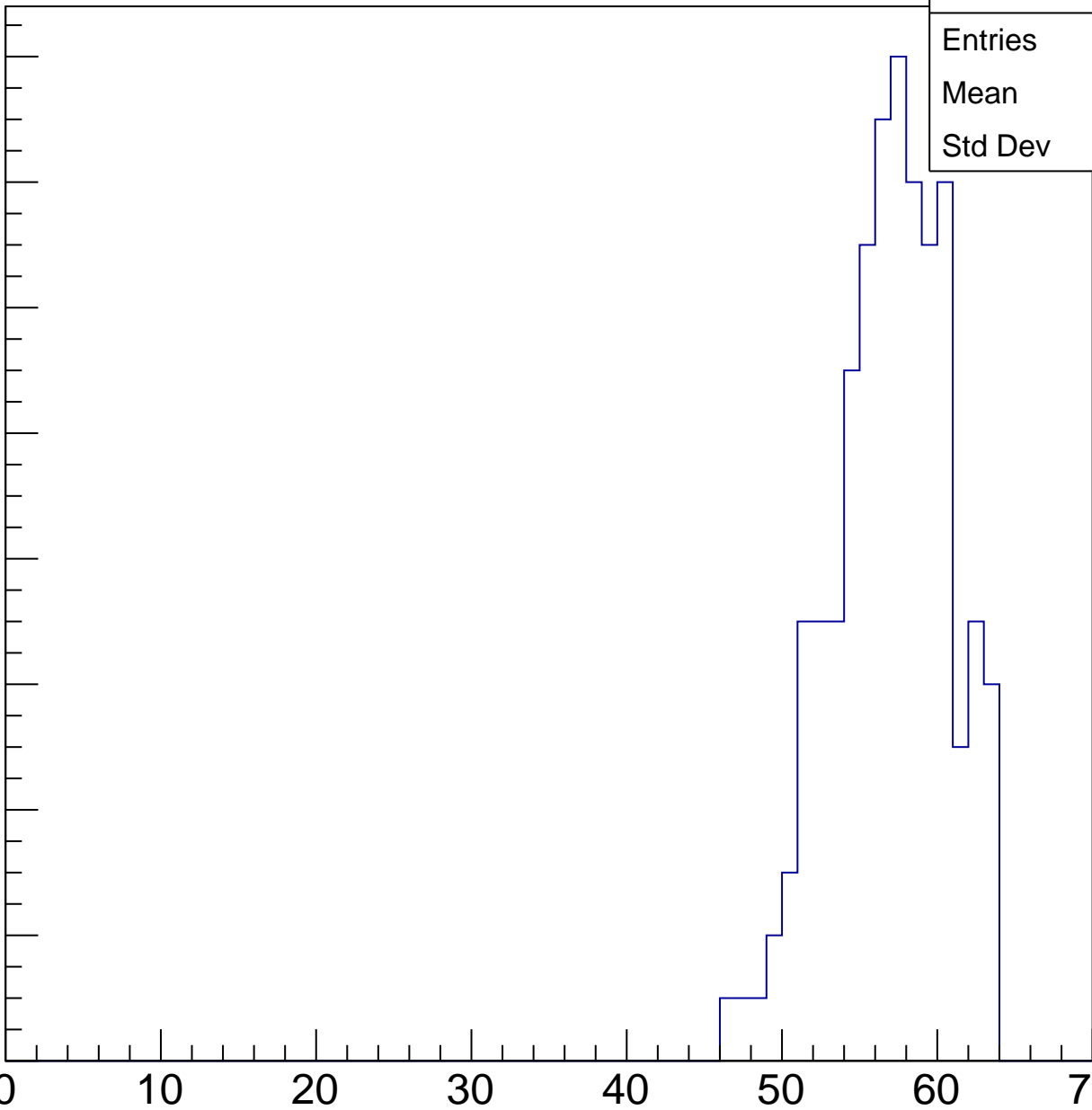
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	143
Mean	56.49
Std Dev	3.653

ampl



# B0L101S, U23-ch107, adc5

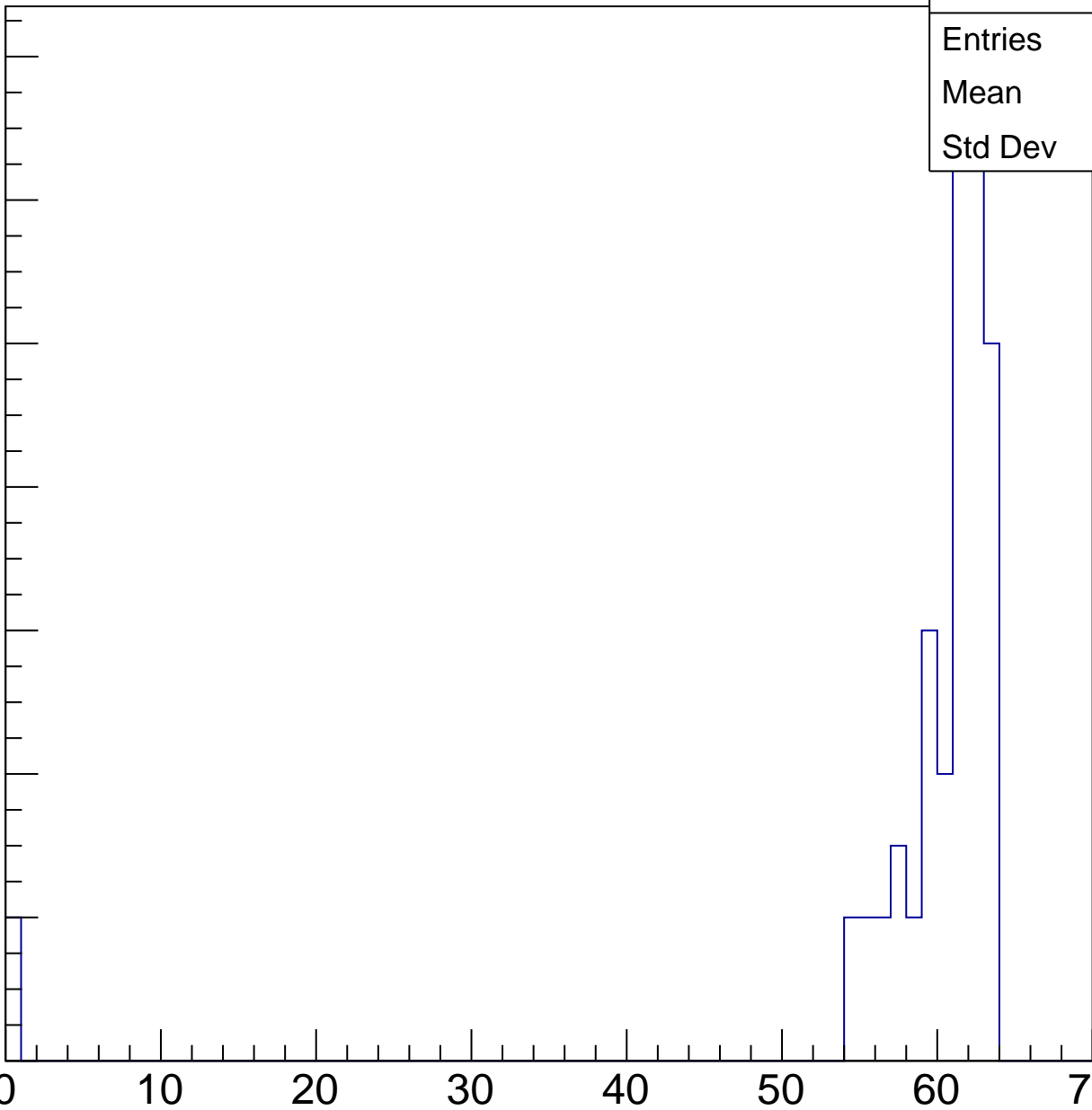
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	60
Mean	58.35
Std Dev	11.1

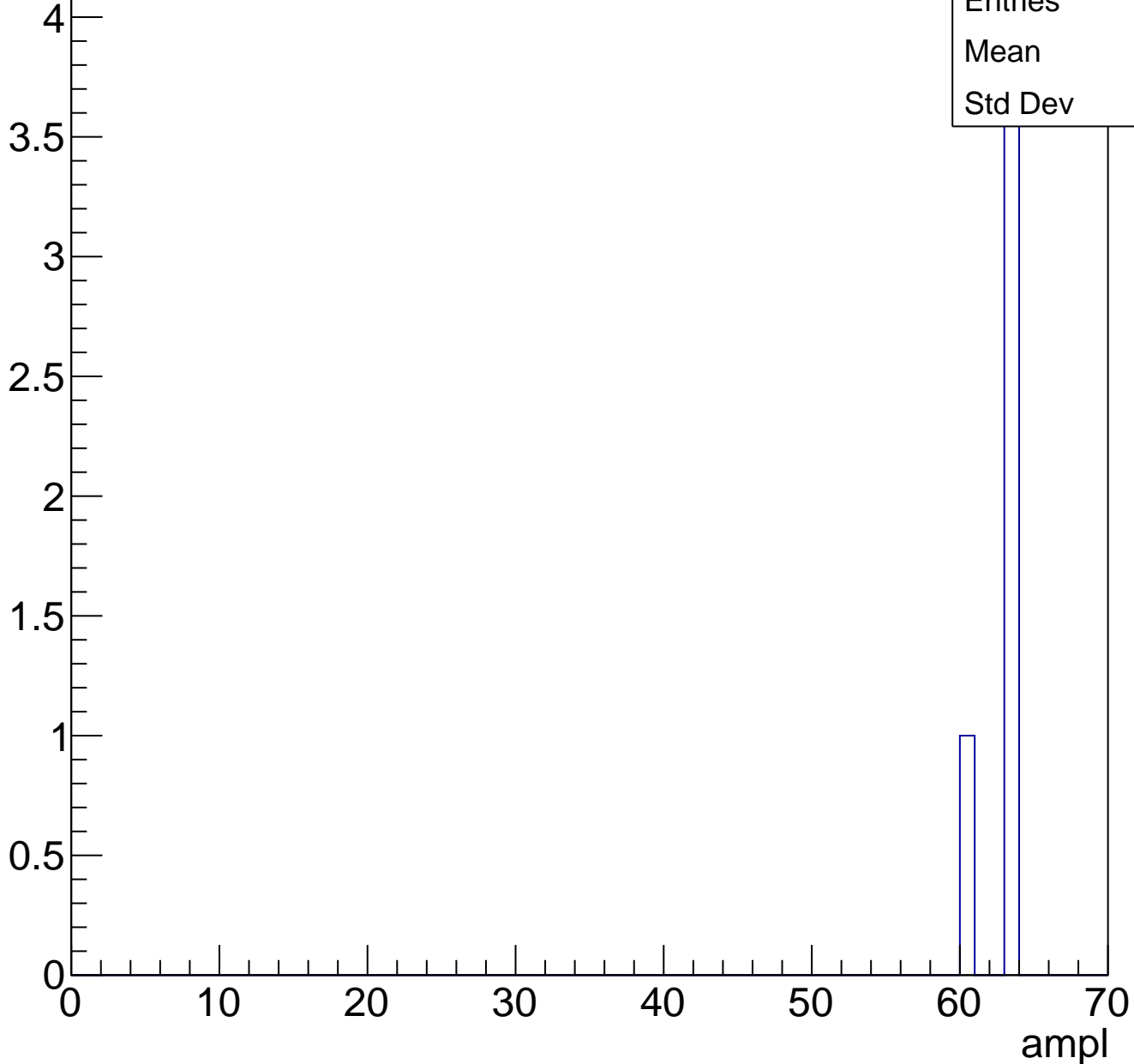
ampl



# B0L101S, U23-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

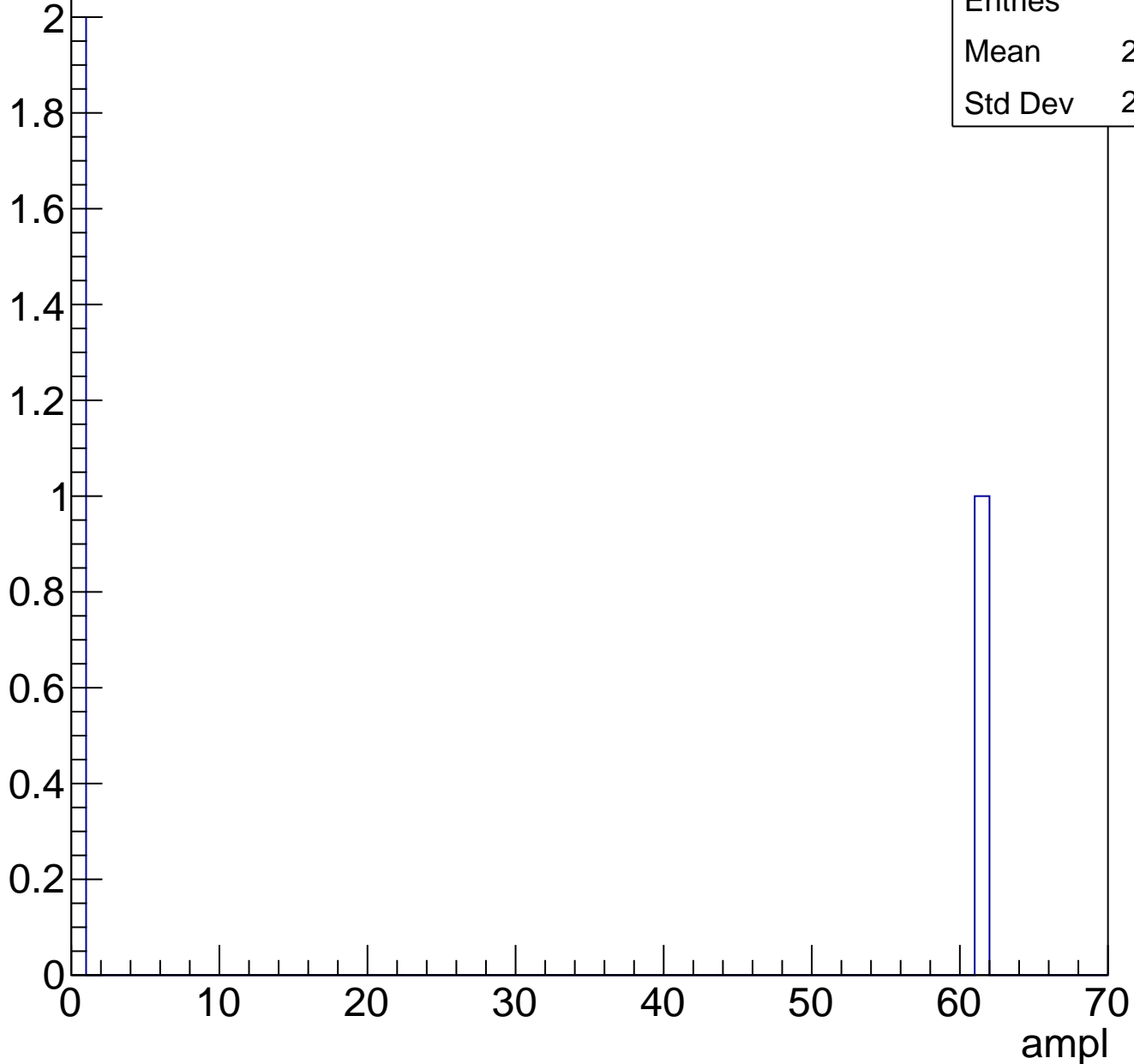




# B0L101S, U23-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch108, adc0

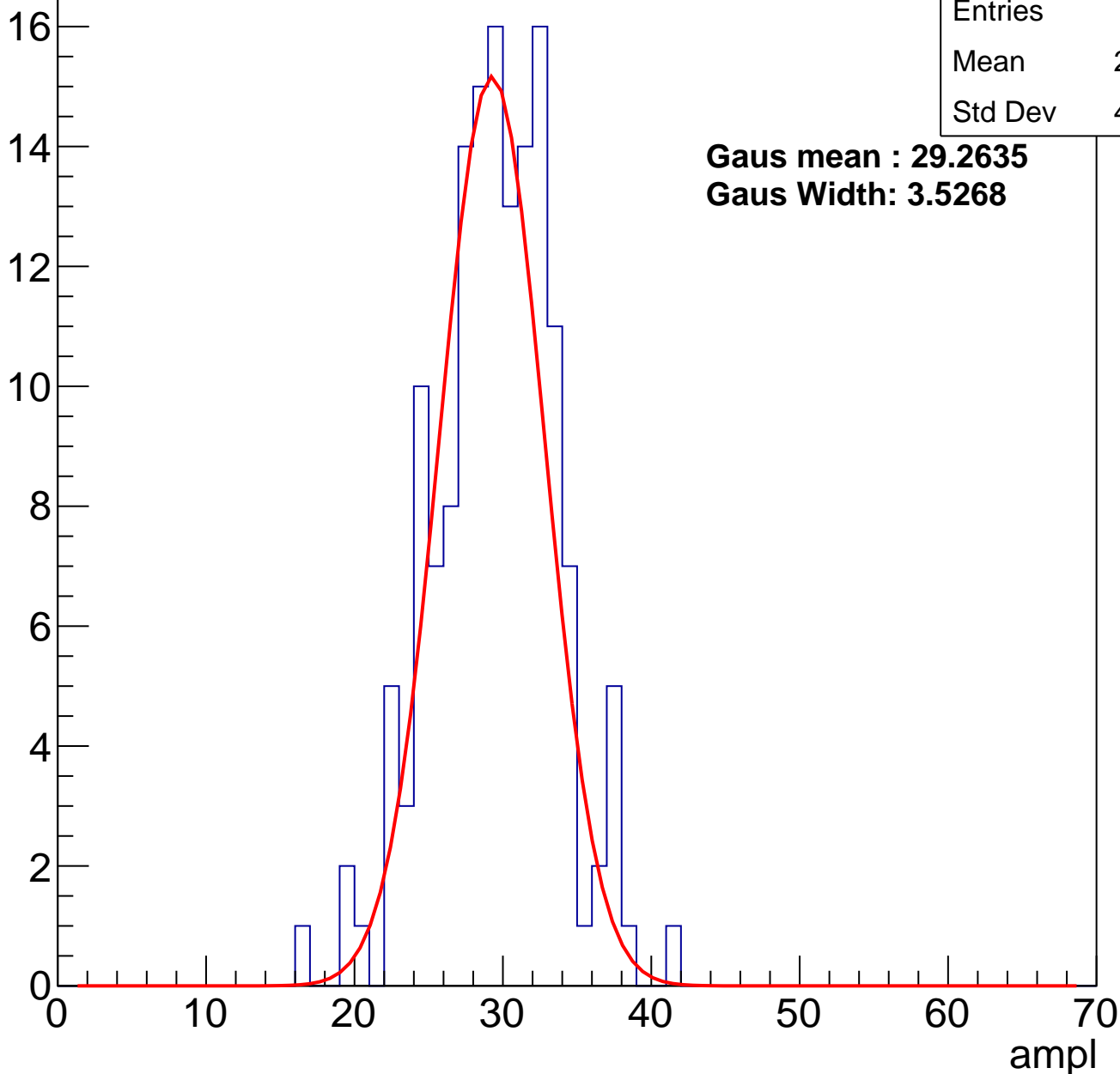
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	29.06
Std Dev	4.083

**Gaus mean : 29.2635**

**Gaus Width: 3.5268**

Entry



# B0L101S, U23-ch108, adc1

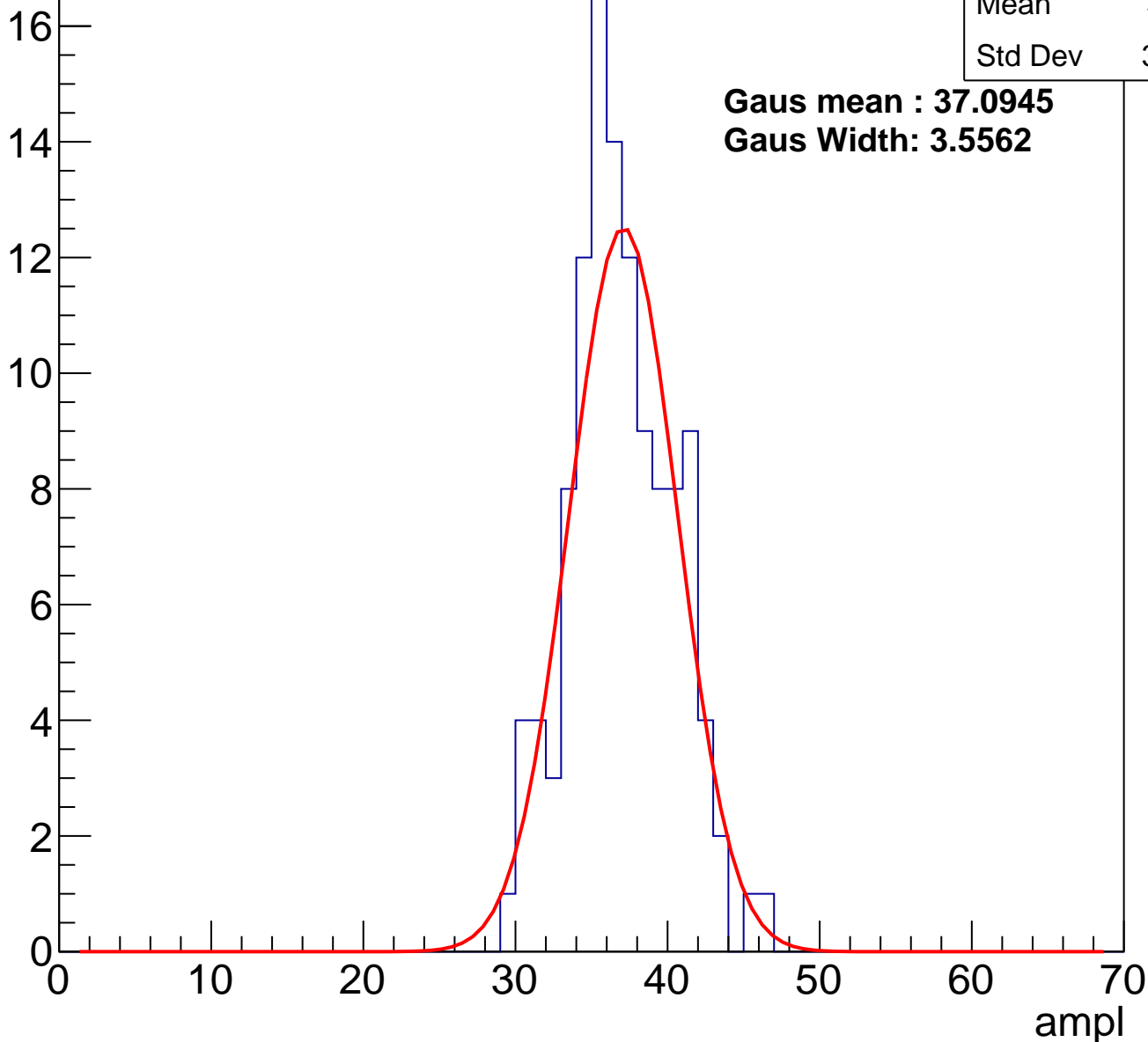
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	117
Mean	36.51
Std Dev	3.388

**Gaus mean : 37.0945**

**Gaus Width: 3.5562**



# B0L101S, U23-ch108, adc2

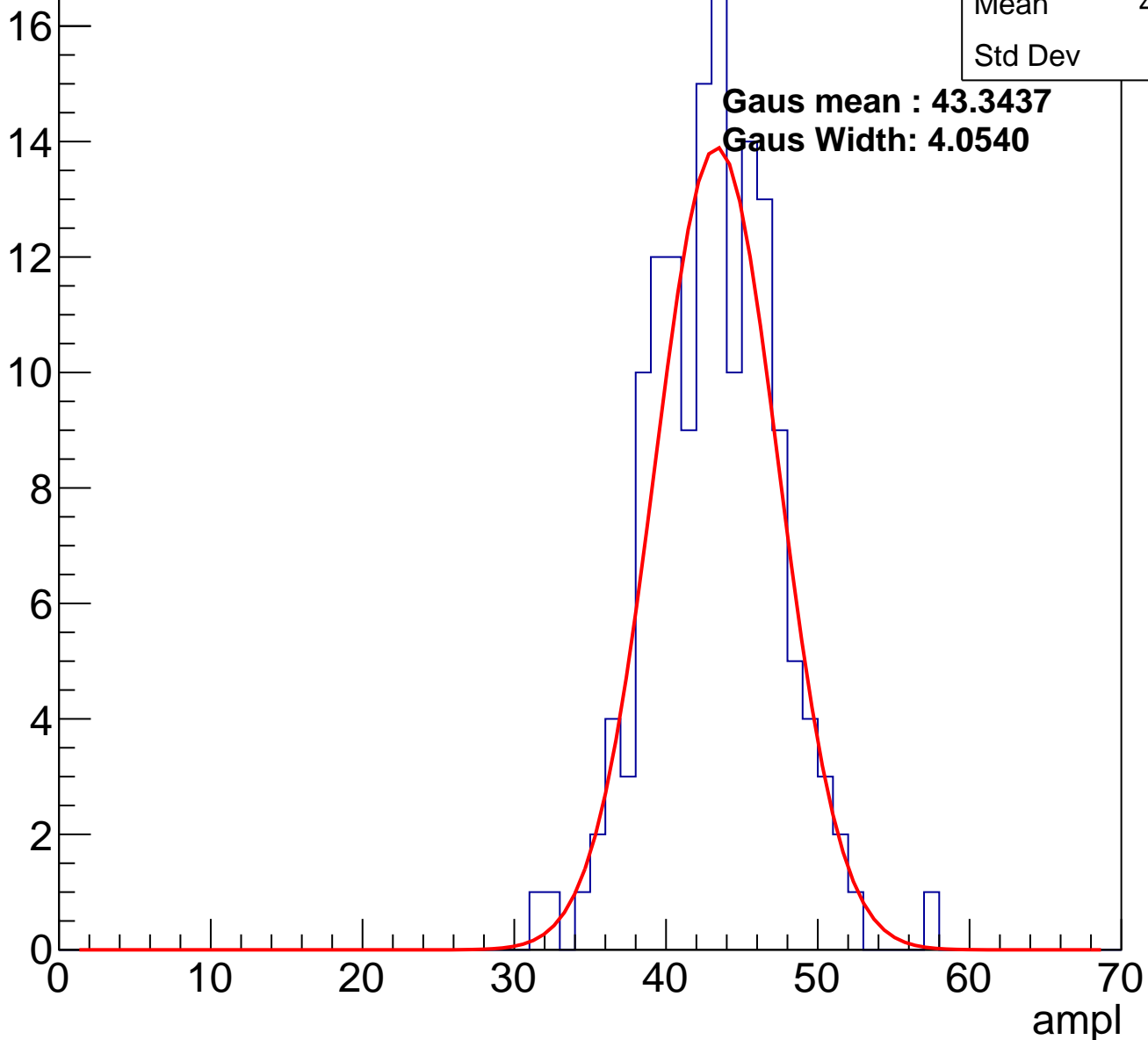
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	149
Mean	42.74
Std Dev	4.11

**Gaus mean : 43.3437**

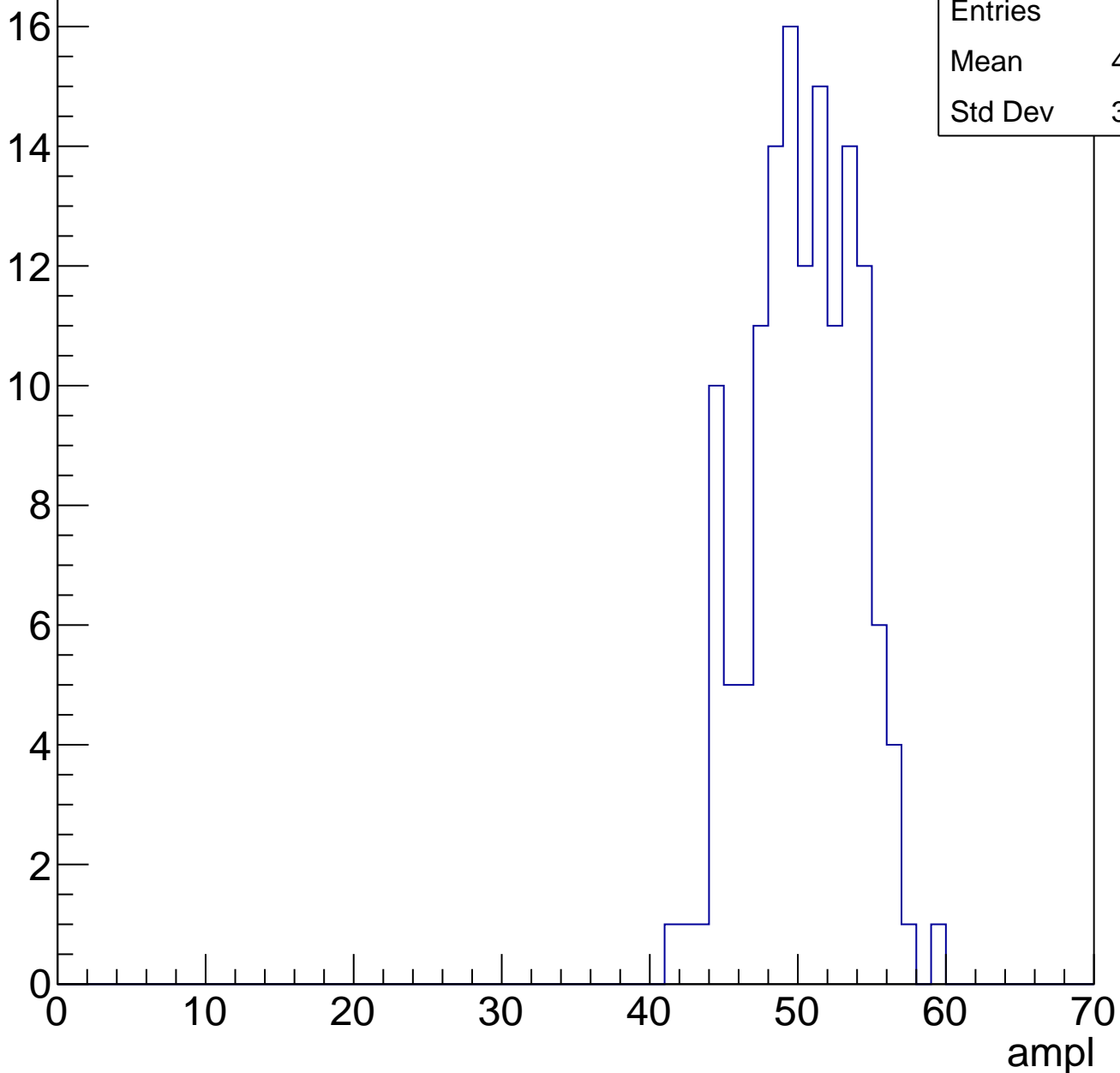
**Gaus Width: 4.0540**



# B0L101S, U23-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

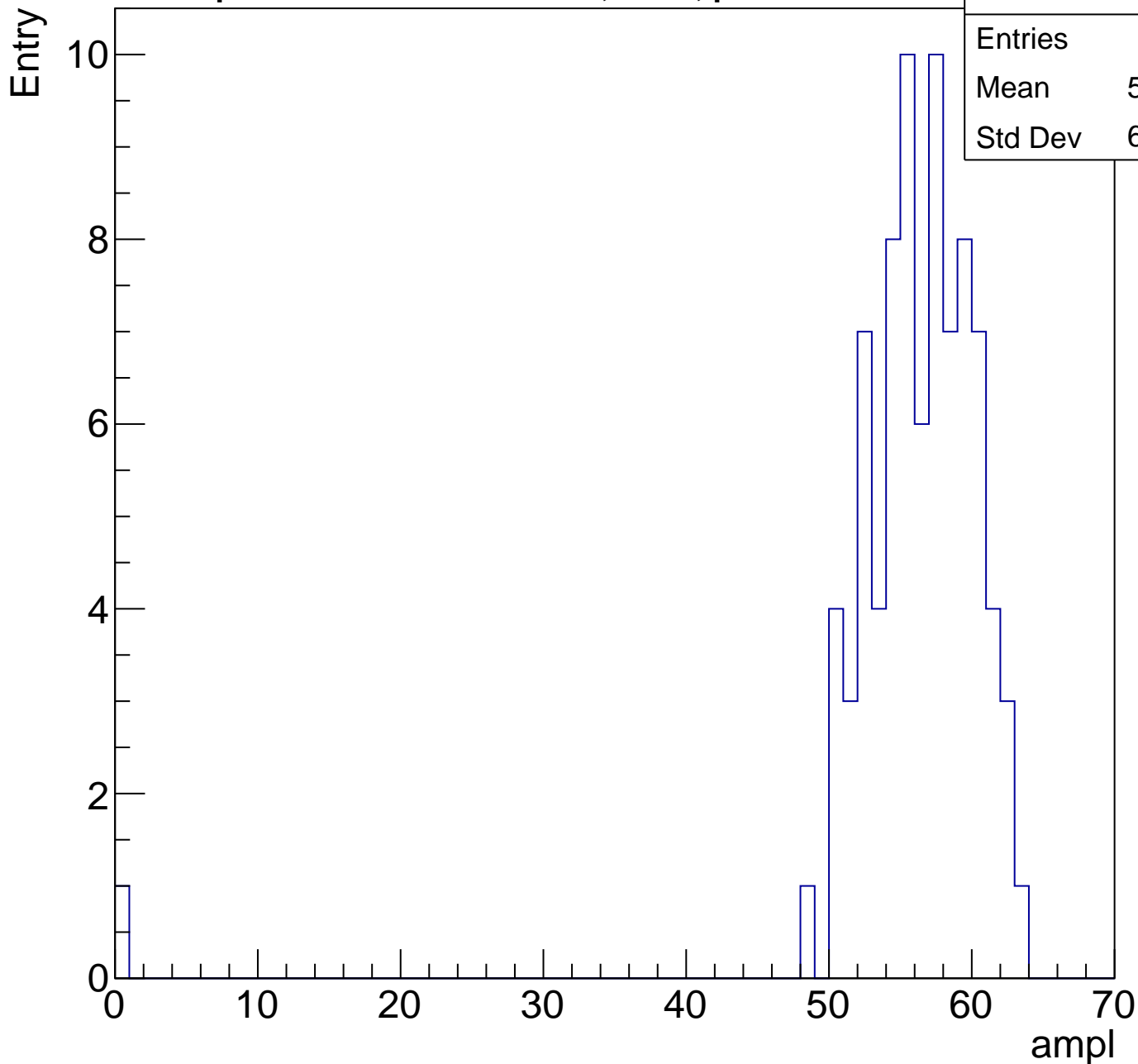
Entry



# B0L101S, U23-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	84
Mean	55.43
Std Dev	6.953



# B0L101S, U23-ch108, adc5

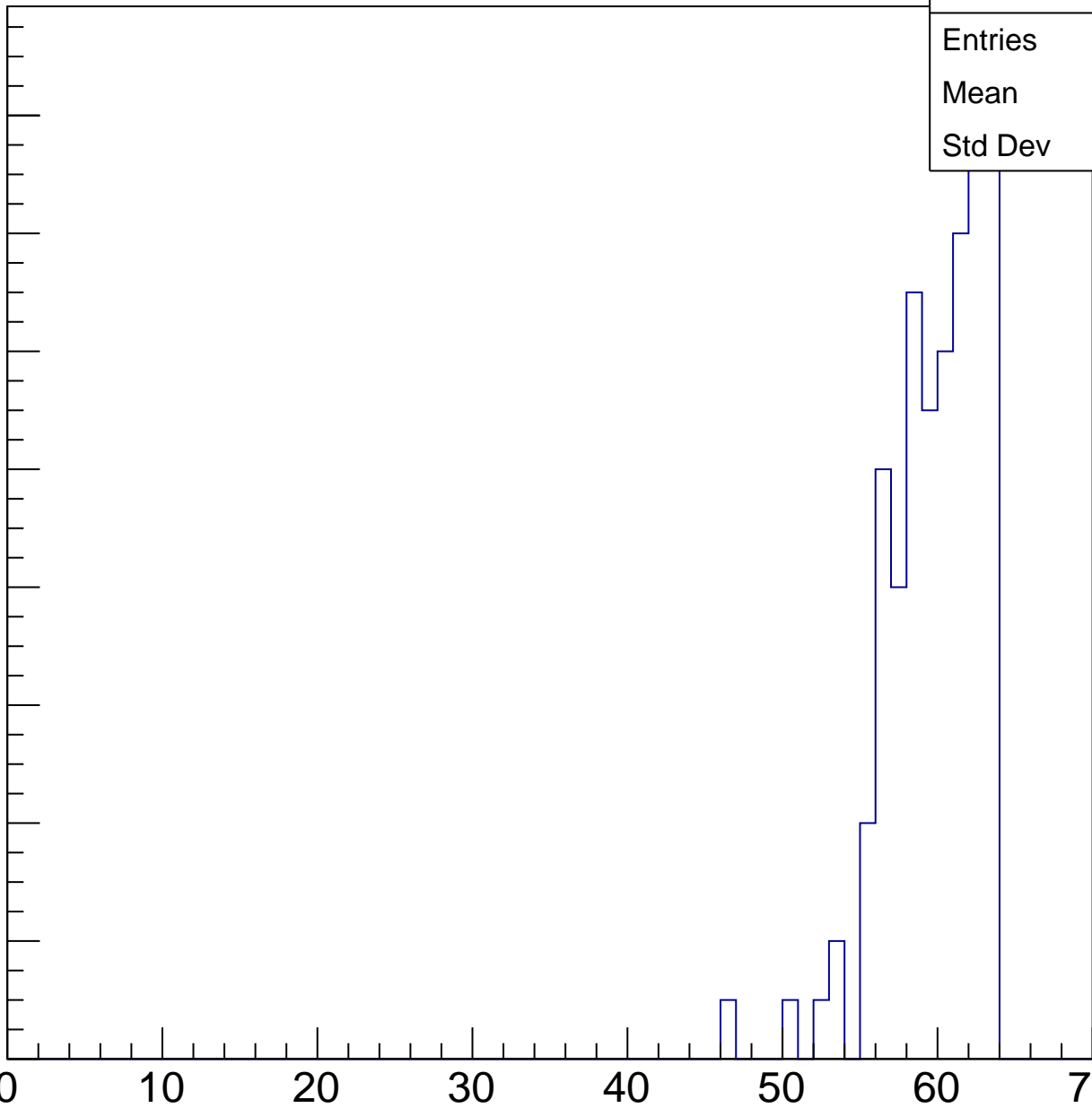
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	110
Mean	59.36
Std Dev	3.074

ampl



# B0L101S, U23-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

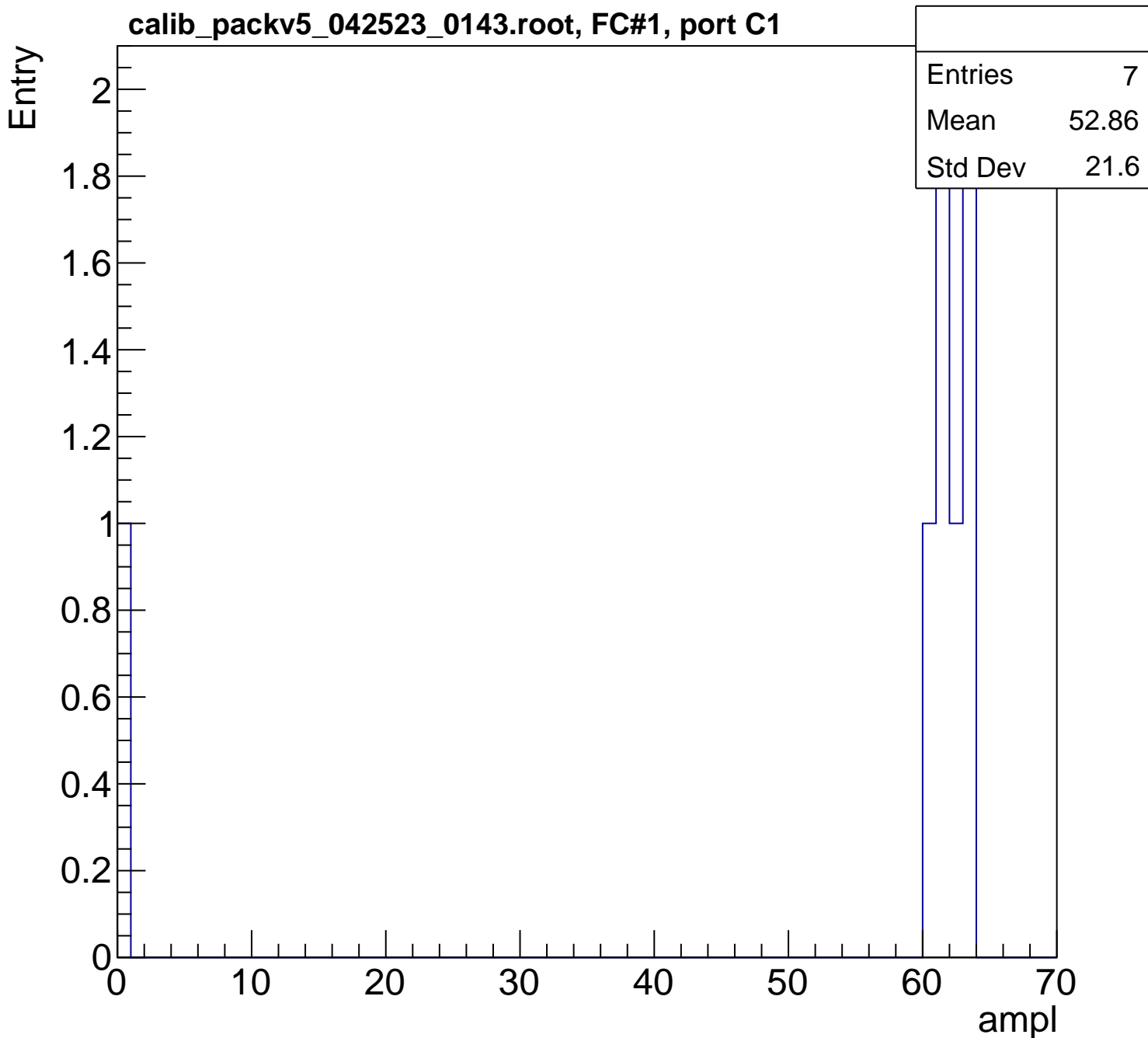
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.86
Std Dev	21.6

0 10 20 30 40 50 60 70

ampl





# B0L101S, U23-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch109, adc0

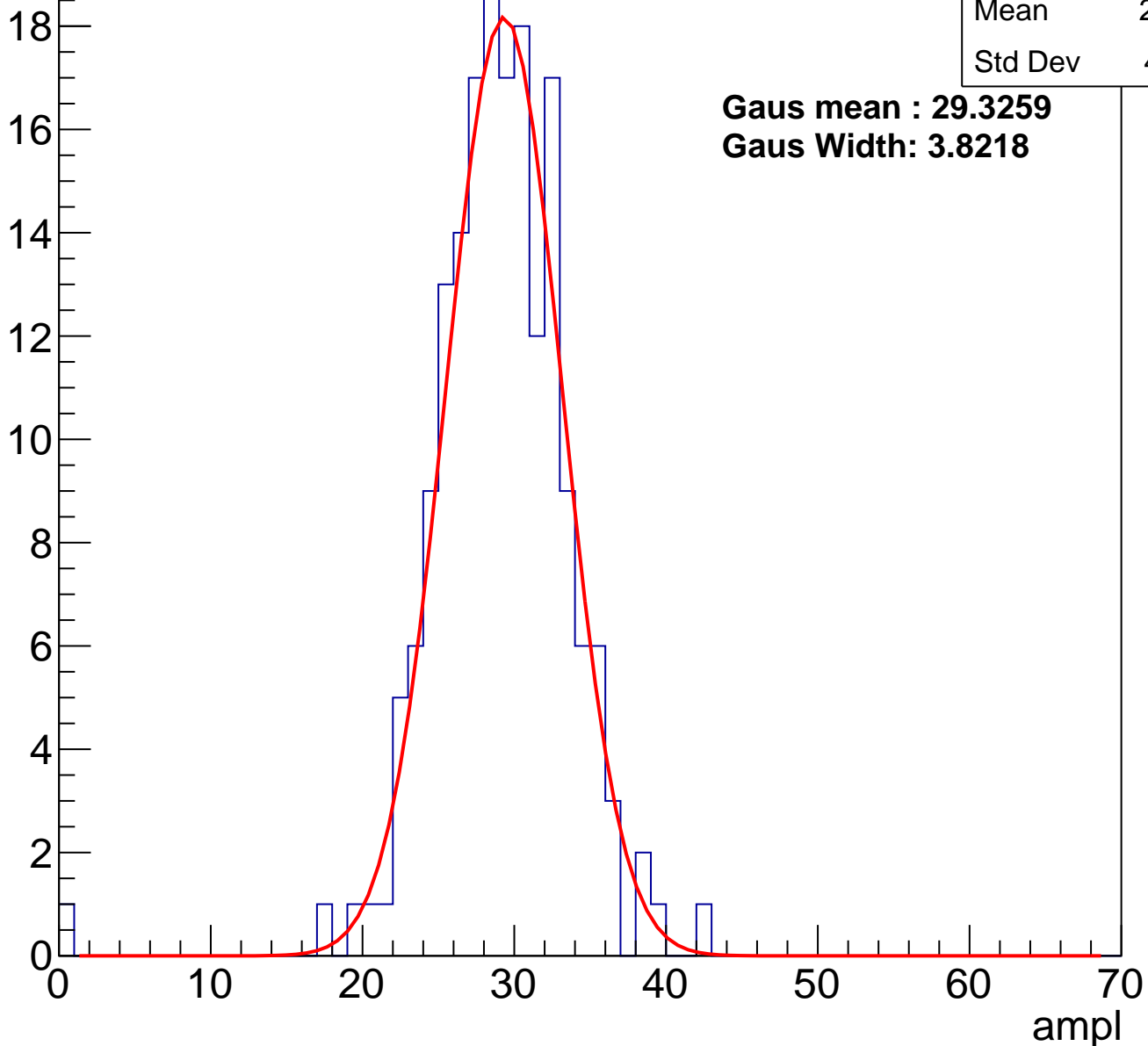
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	180
Mean	28.59
Std Dev	4.481

**Gaus mean : 29.3259**

**Gaus Width: 3.8218**

Entry



# B0L101S, U23-ch109, adc1

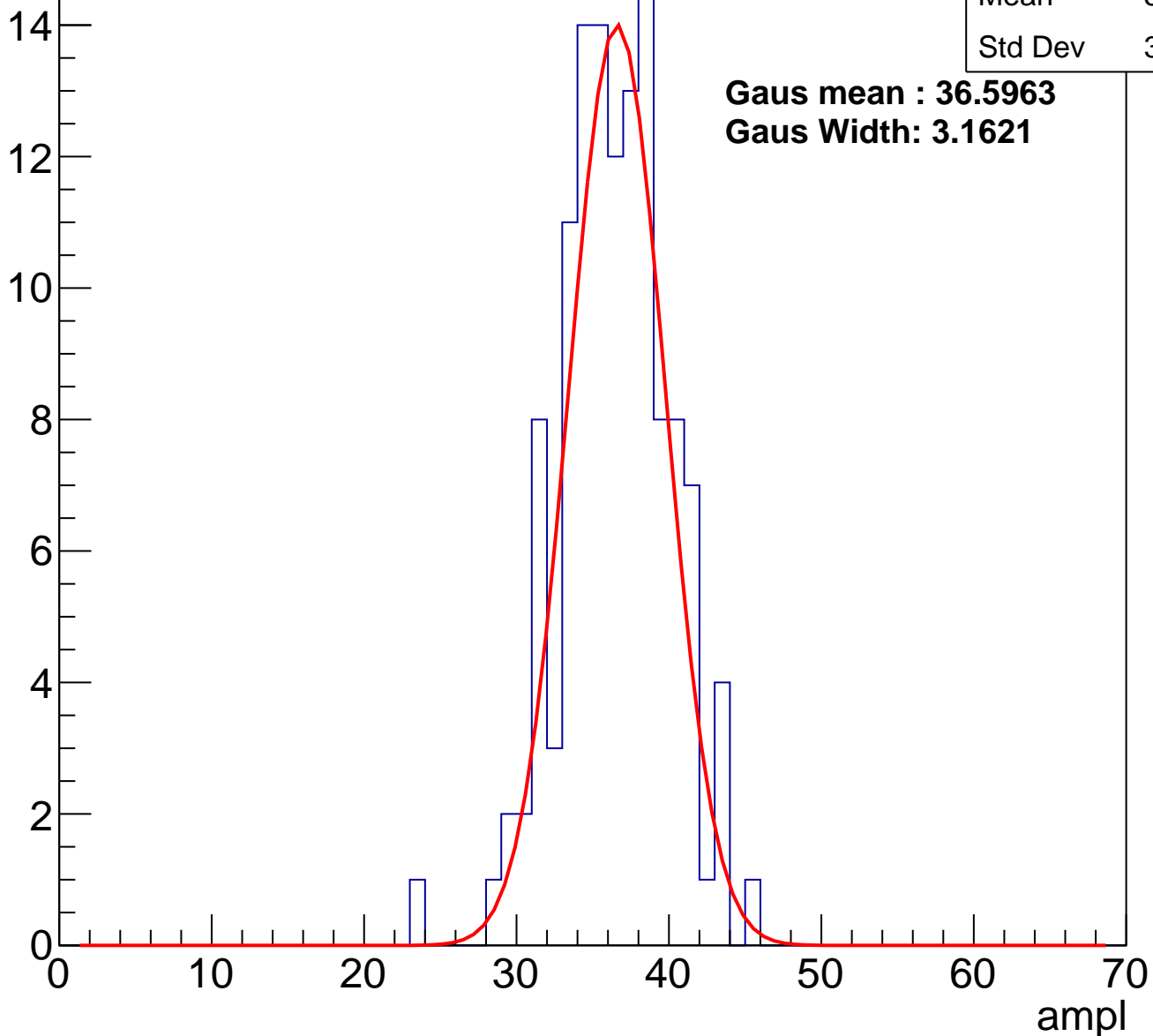
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	36.02
Std Dev	3.556

**Gaus mean : 36.5963**

**Gaus Width: 3.1621**

Entry



# B0L101S, U23-ch109, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	42.88
Std Dev	3.42

**Gaus mean : 43.3857**

**Gaus Width: 3.6272**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

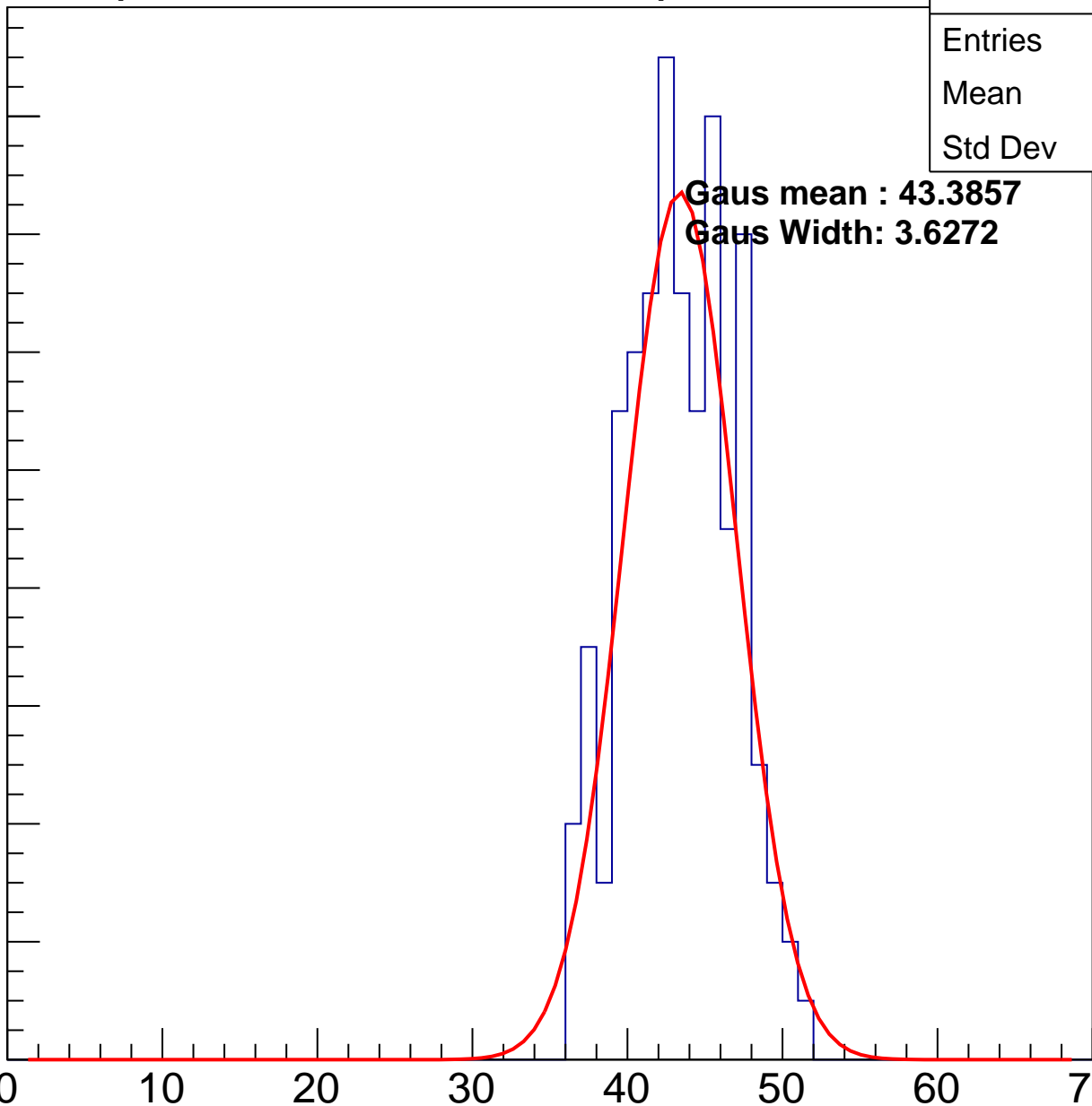
30

40

50

60

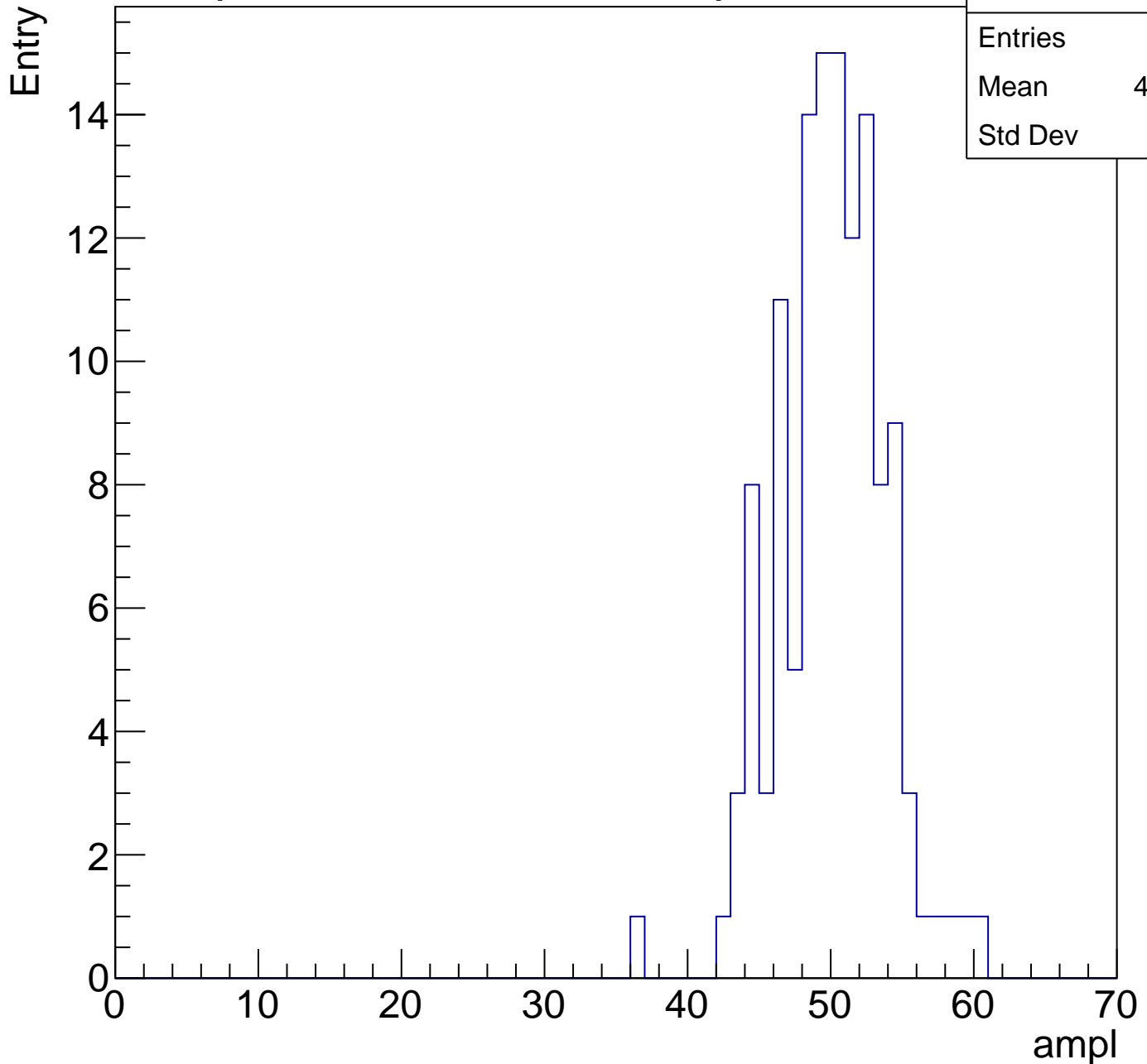
ampl



# B0L101S, U23-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

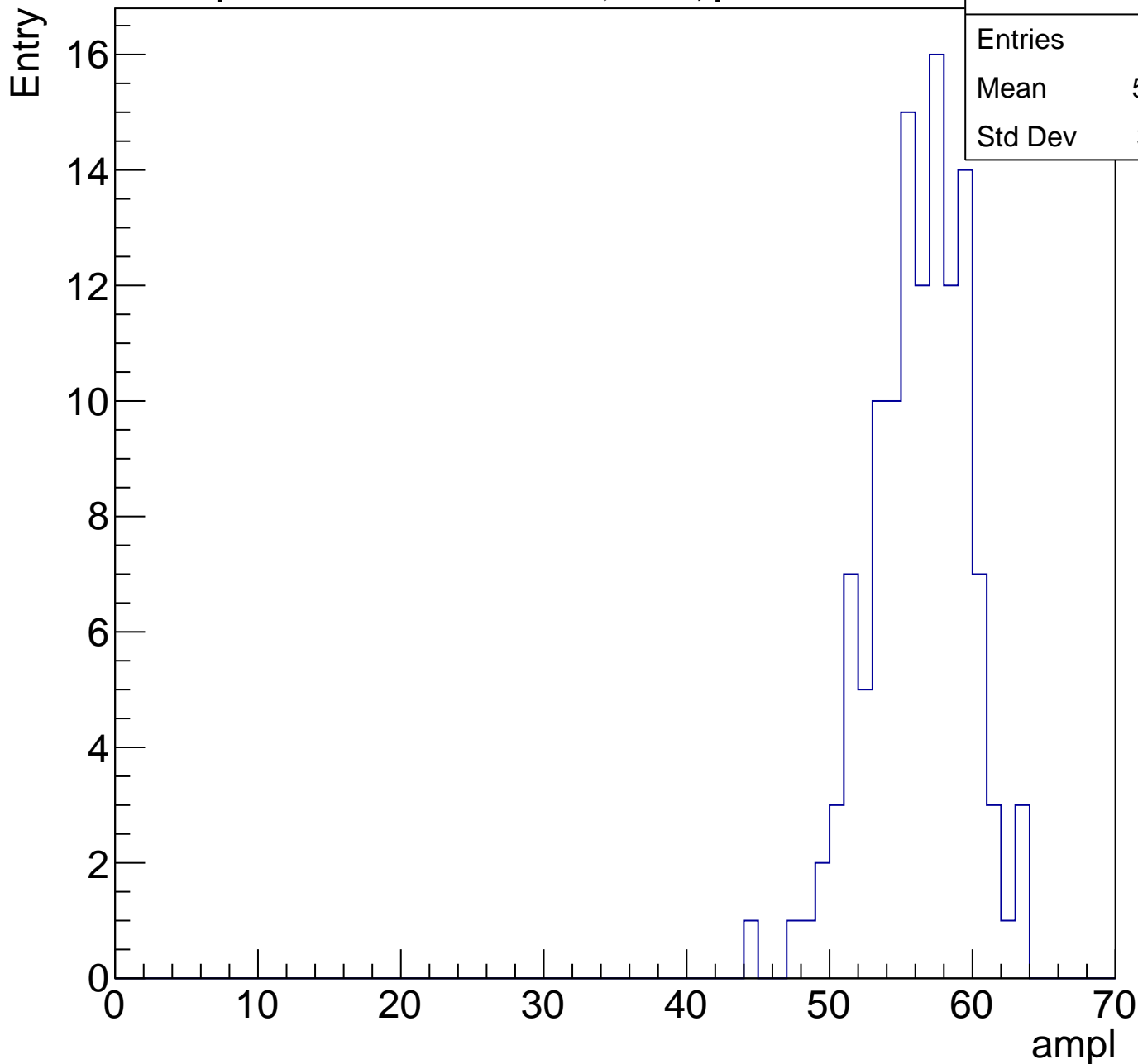
Entries	127
Mean	49.58
Std Dev	3.69



# B0L101S, U23-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	55.76
Std Dev	3.431



# B0L101S, U23-ch109, adc5

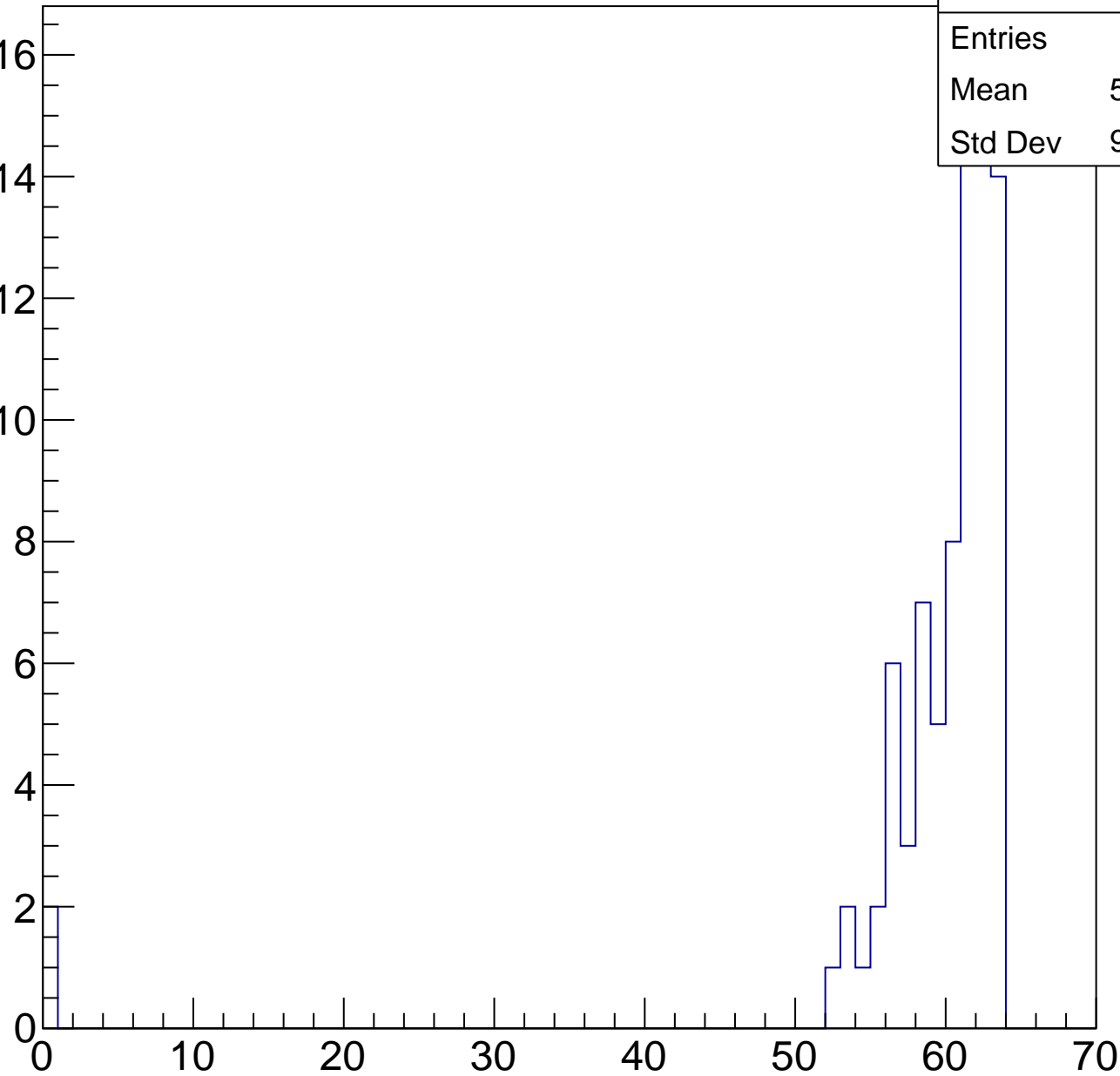
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	58.55
Std Dev	9.592

ampl



# B0L101S, U23-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

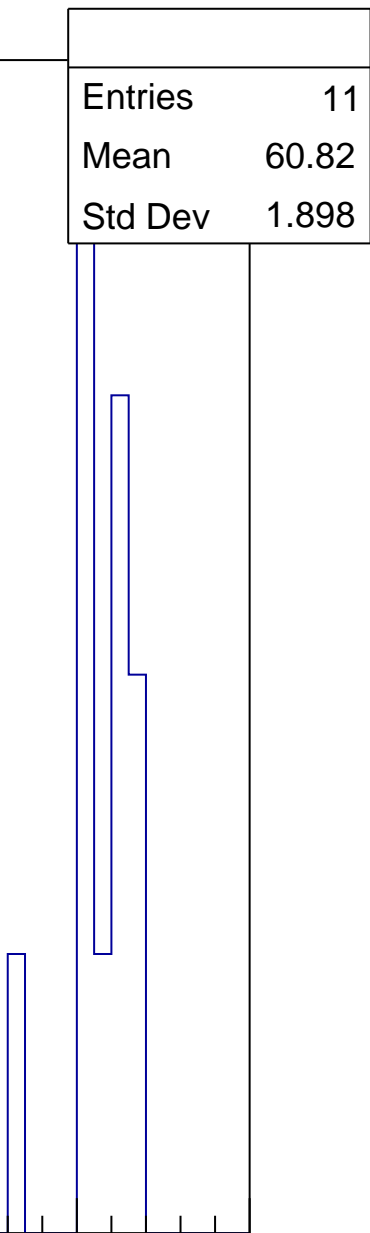
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	11
Mean	60.82
Std Dev	1.898

ampl

0 10 20 30 40 50 60 70

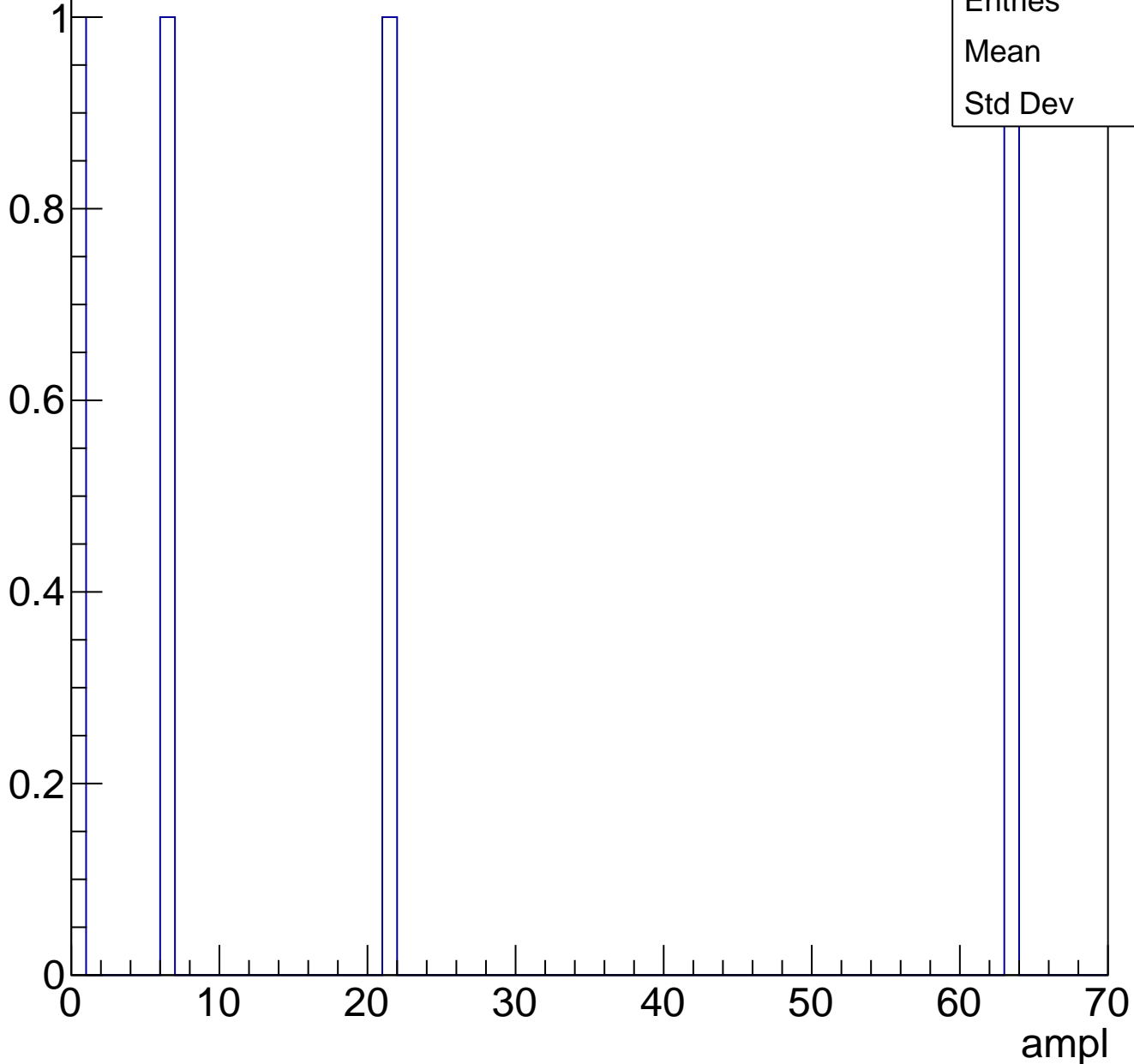




# B0L101S, U23-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch110, adc0

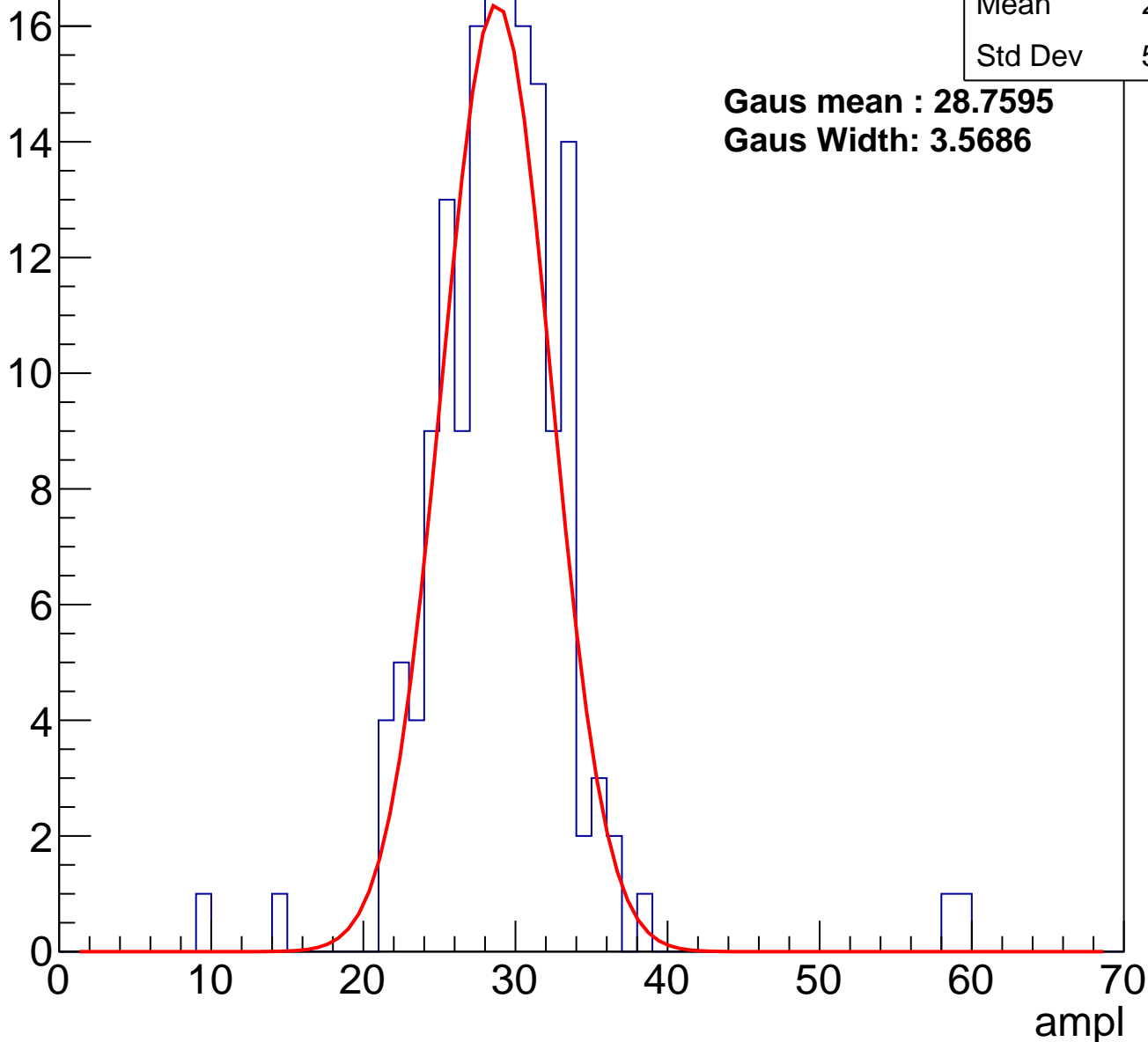
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	28.62
Std Dev	5.185

**Gaus mean : 28.7595**

**Gaus Width: 3.5686**

Entry



# B0L101S, U23-ch110, adc1

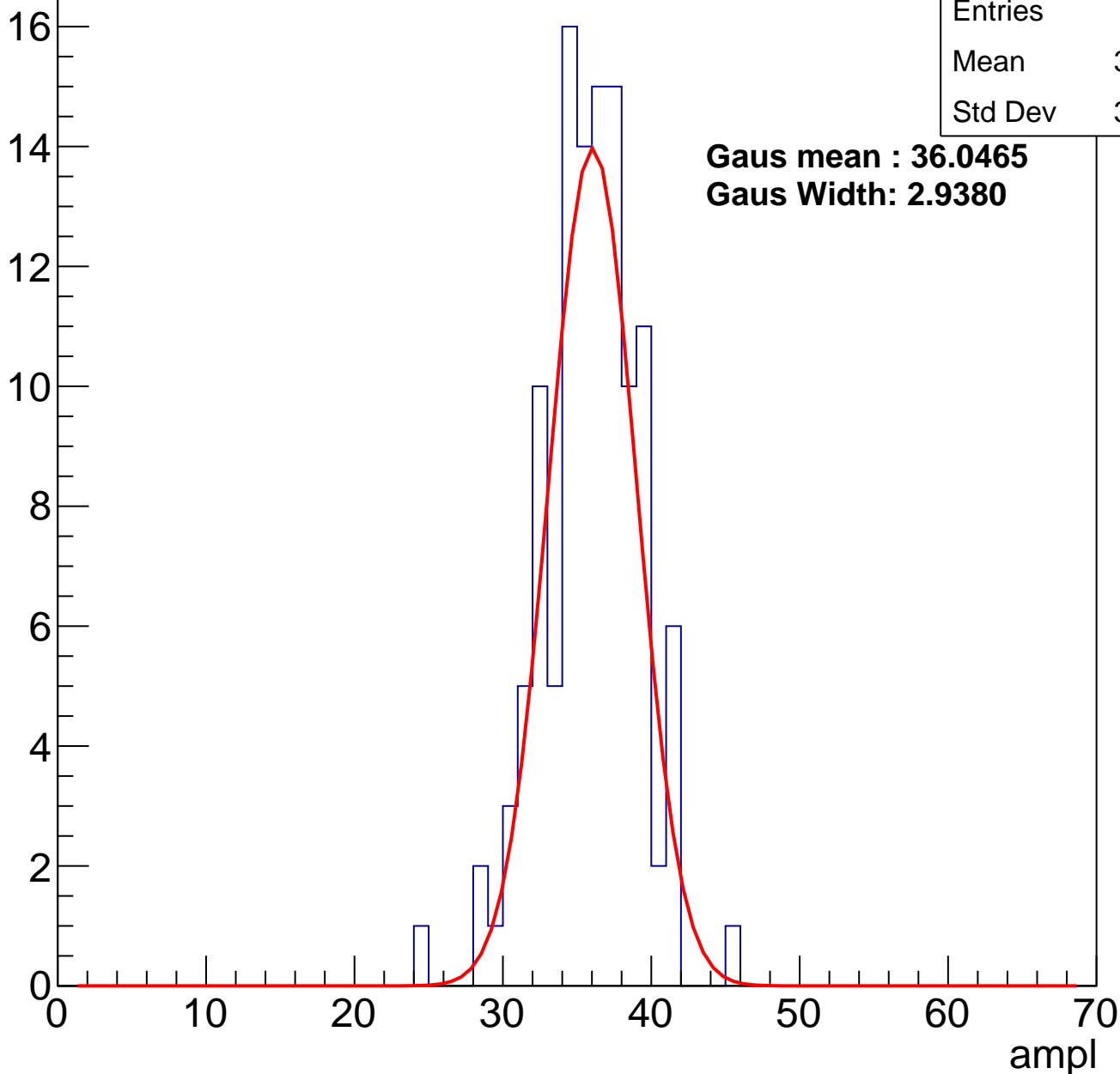
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	35.45
Std Dev	3.228

**Gaus mean : 36.0465**

**Gaus Width: 2.9380**

Entry



# B0L101S, U23-ch110, adc2

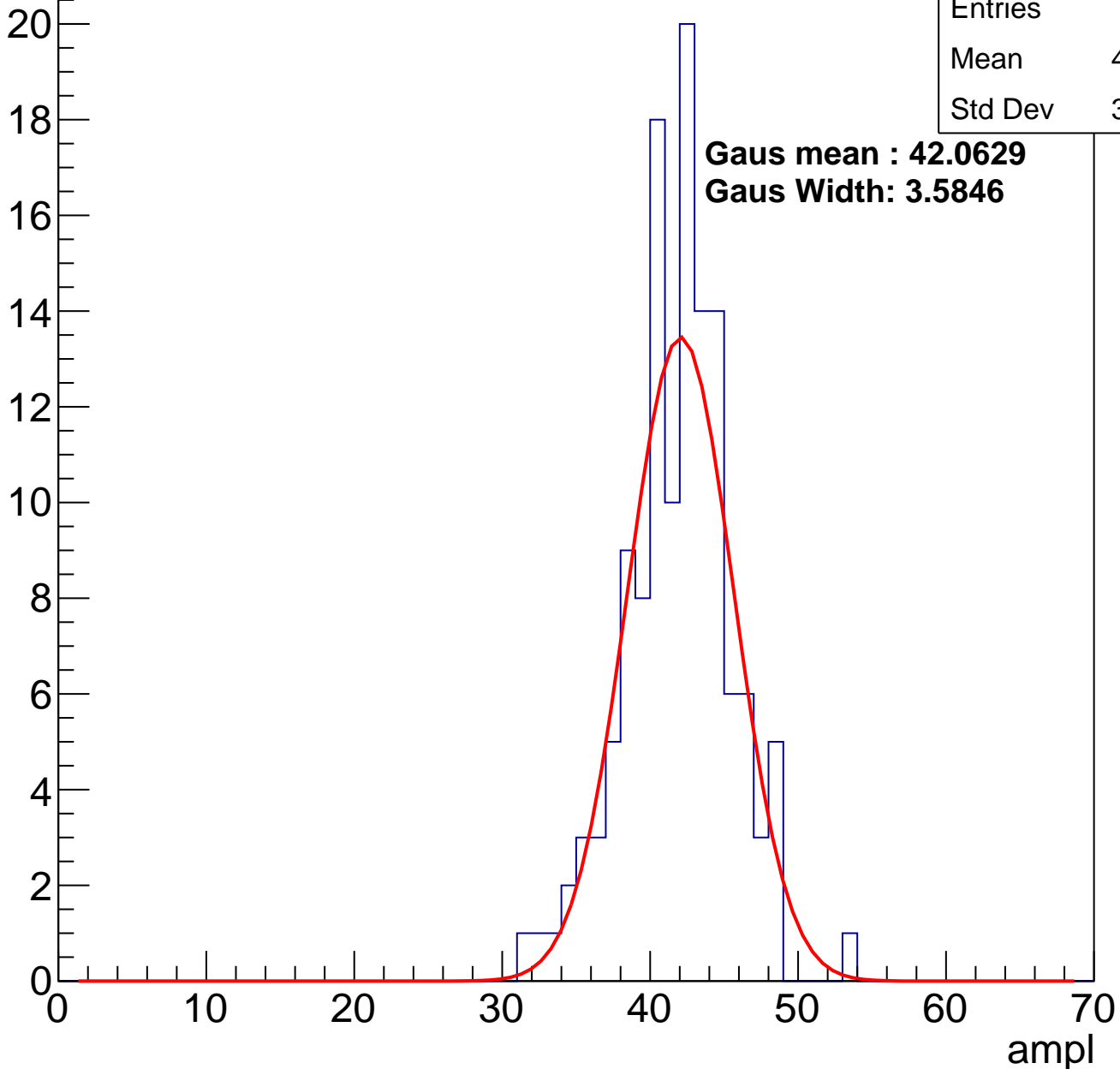
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	130
Mean	41.42
Std Dev	3.573

**Gaus mean : 42.0629**

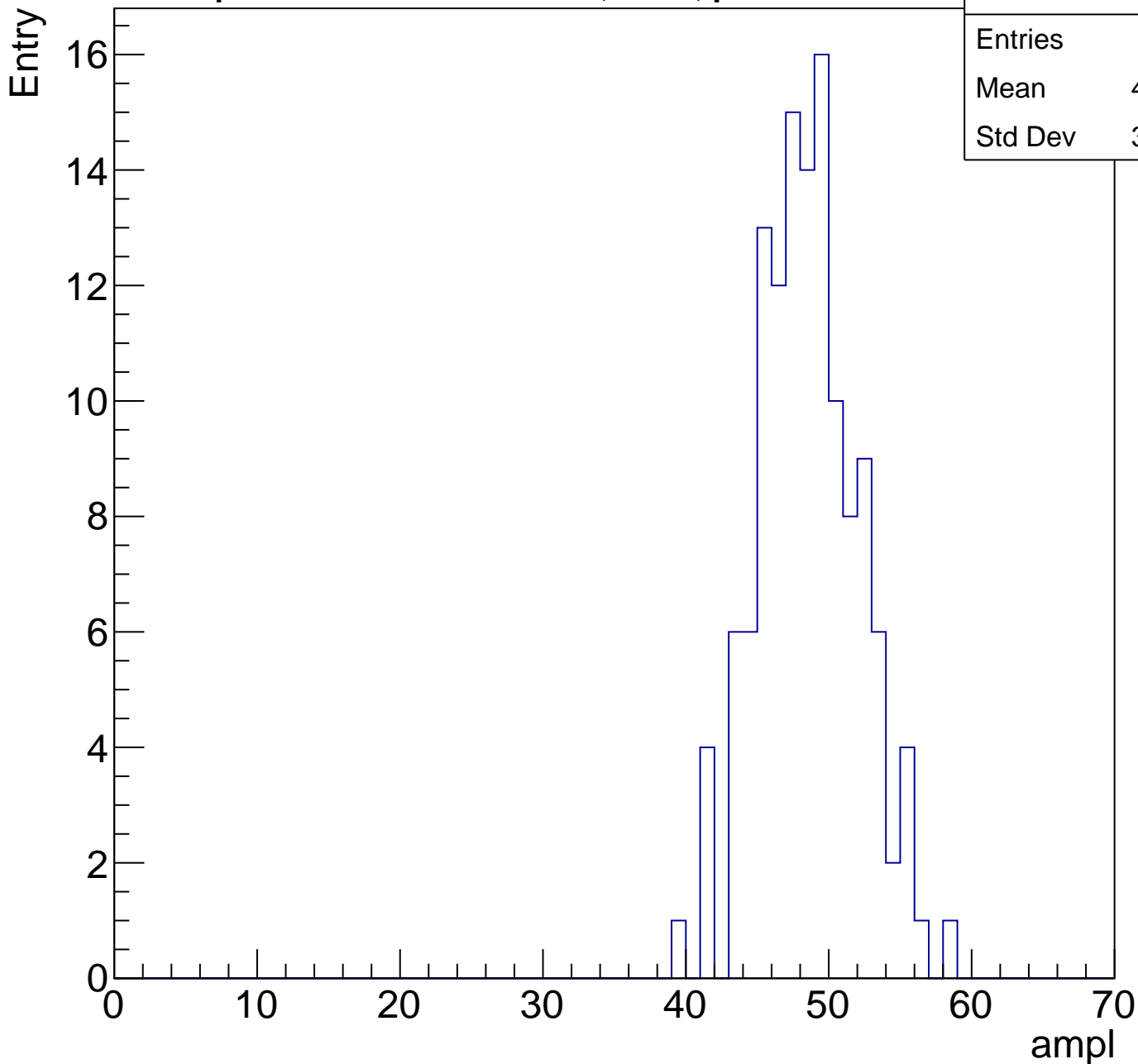
**Gaus Width: 3.5846**



# B0L101S, U23-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	48.12
Std Dev	3.486



# B0L101S, U23-ch110, adc4

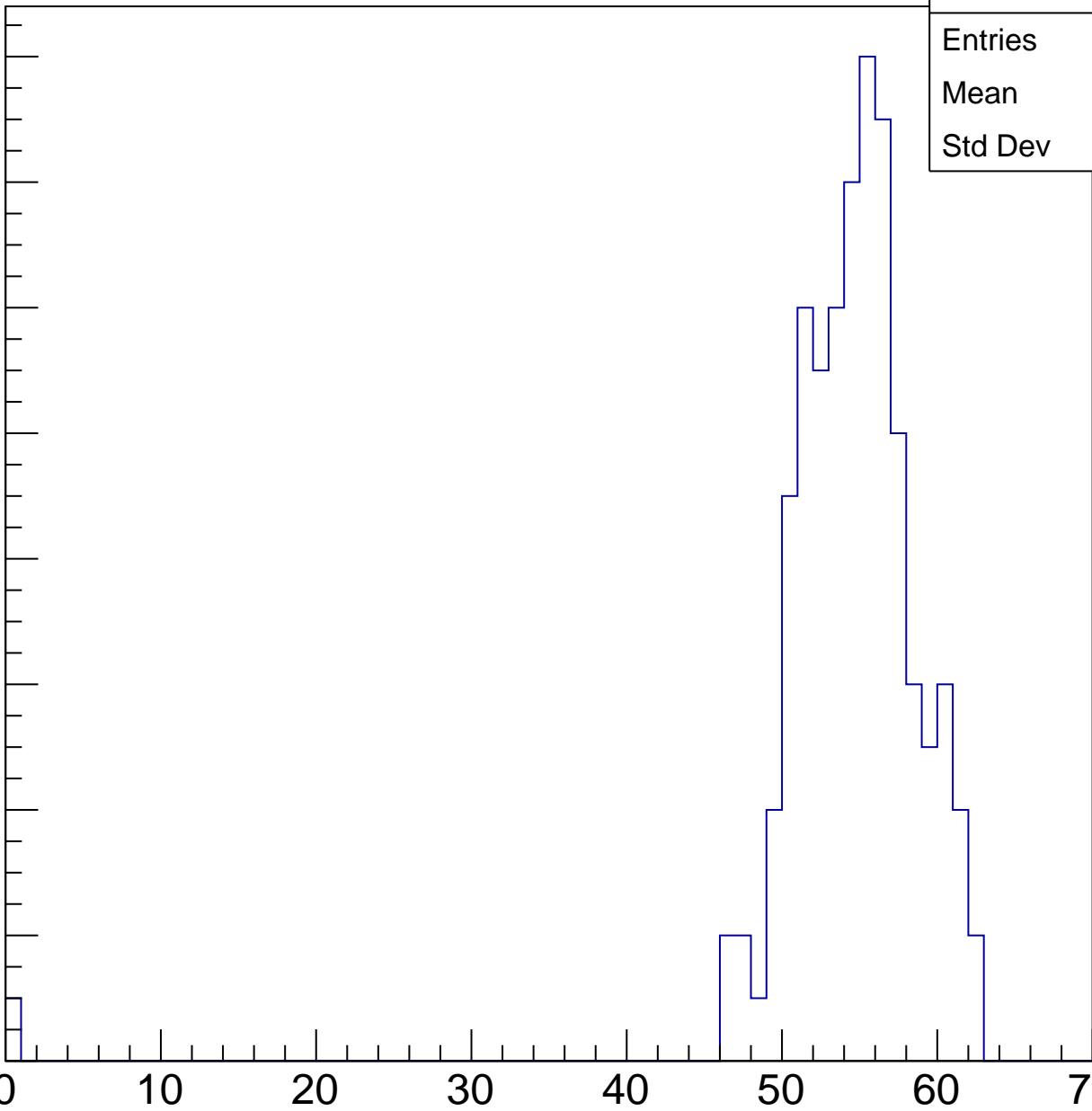
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	53.92
Std Dev	5.847

ampl



# B0L101S, U23-ch110, adc5

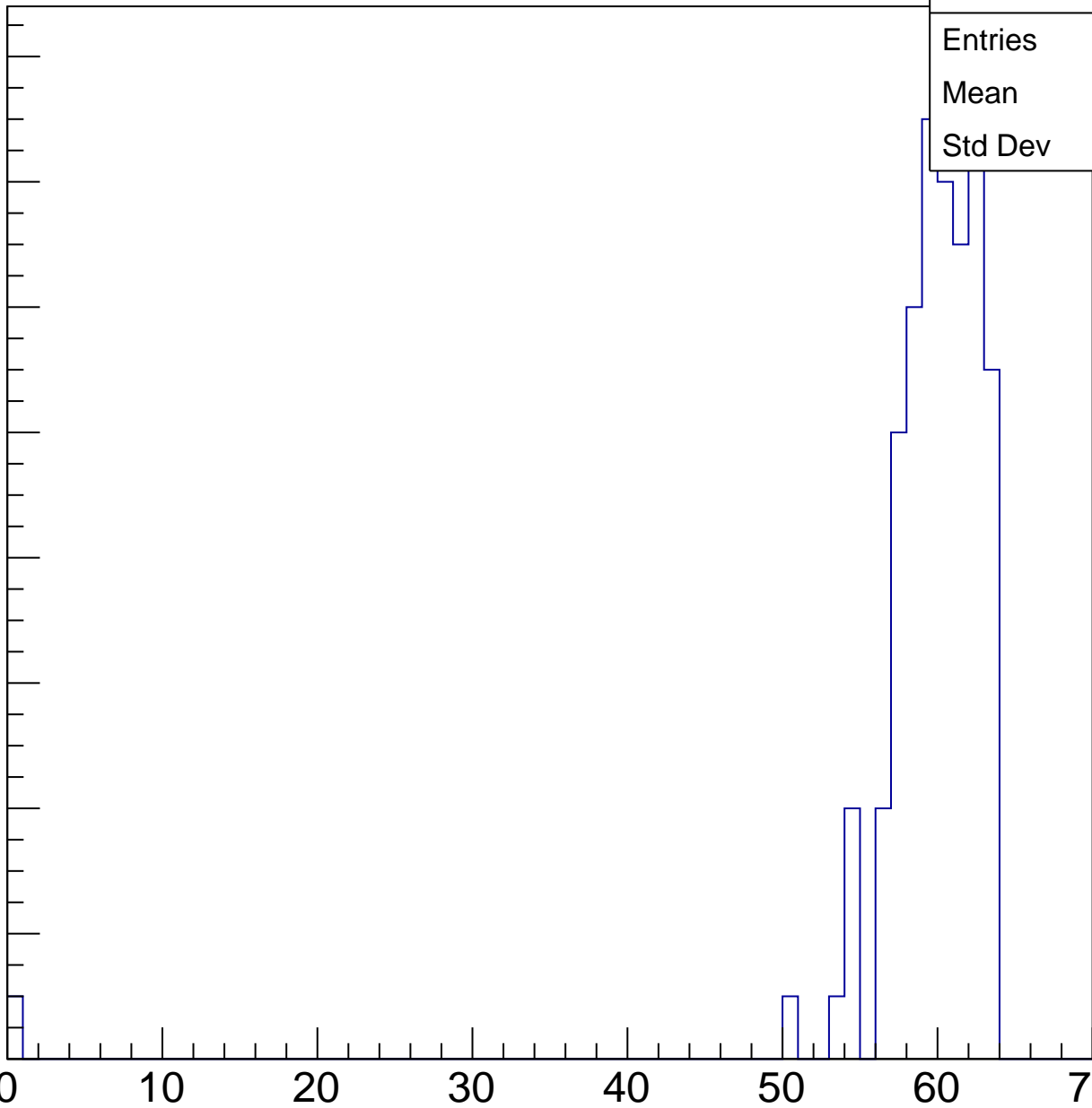
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	102
Mean	58.94
Std Dev	6.395

ampl

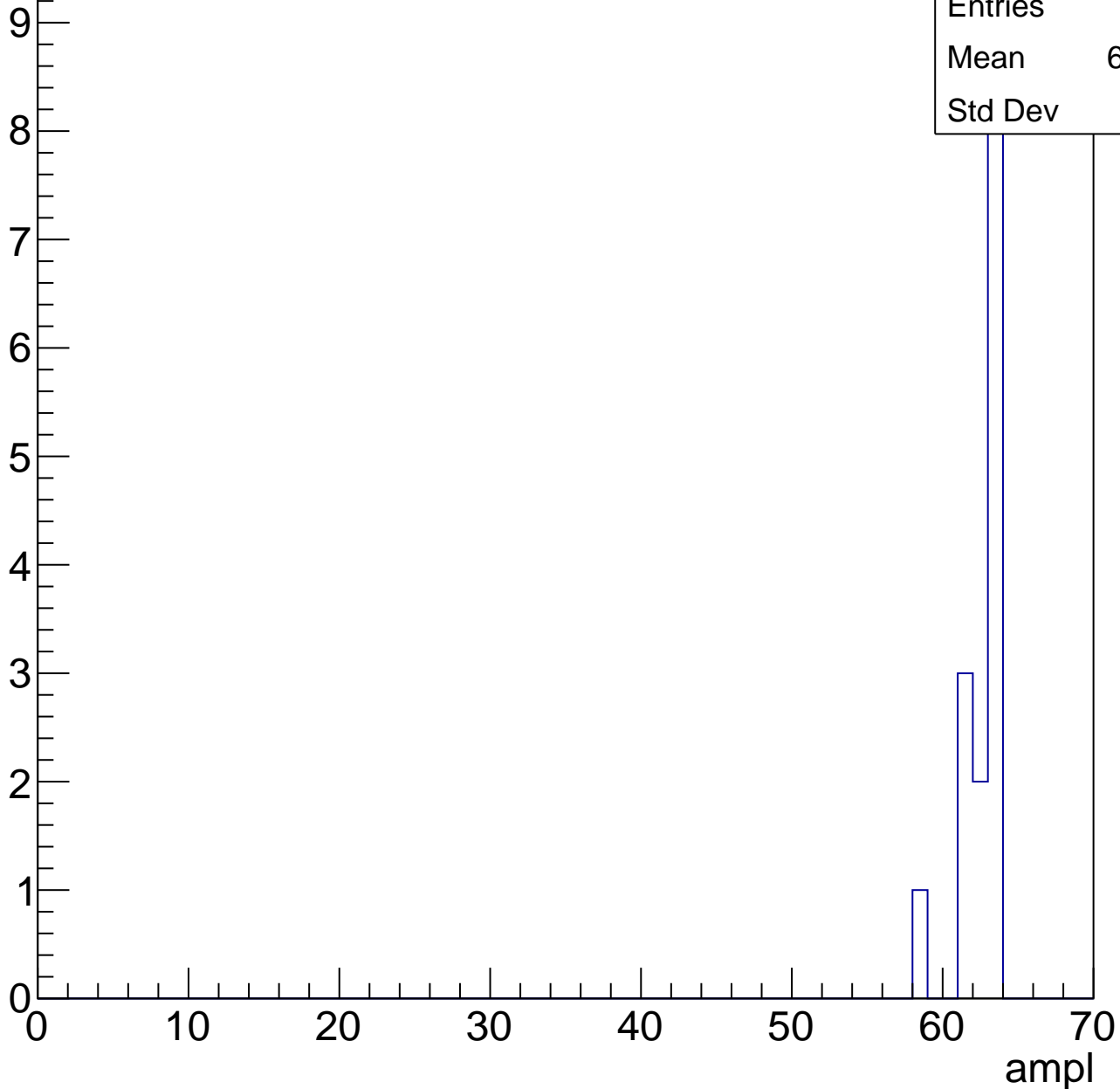


# B0L101S, U23-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	62.13
Std Dev	1.36

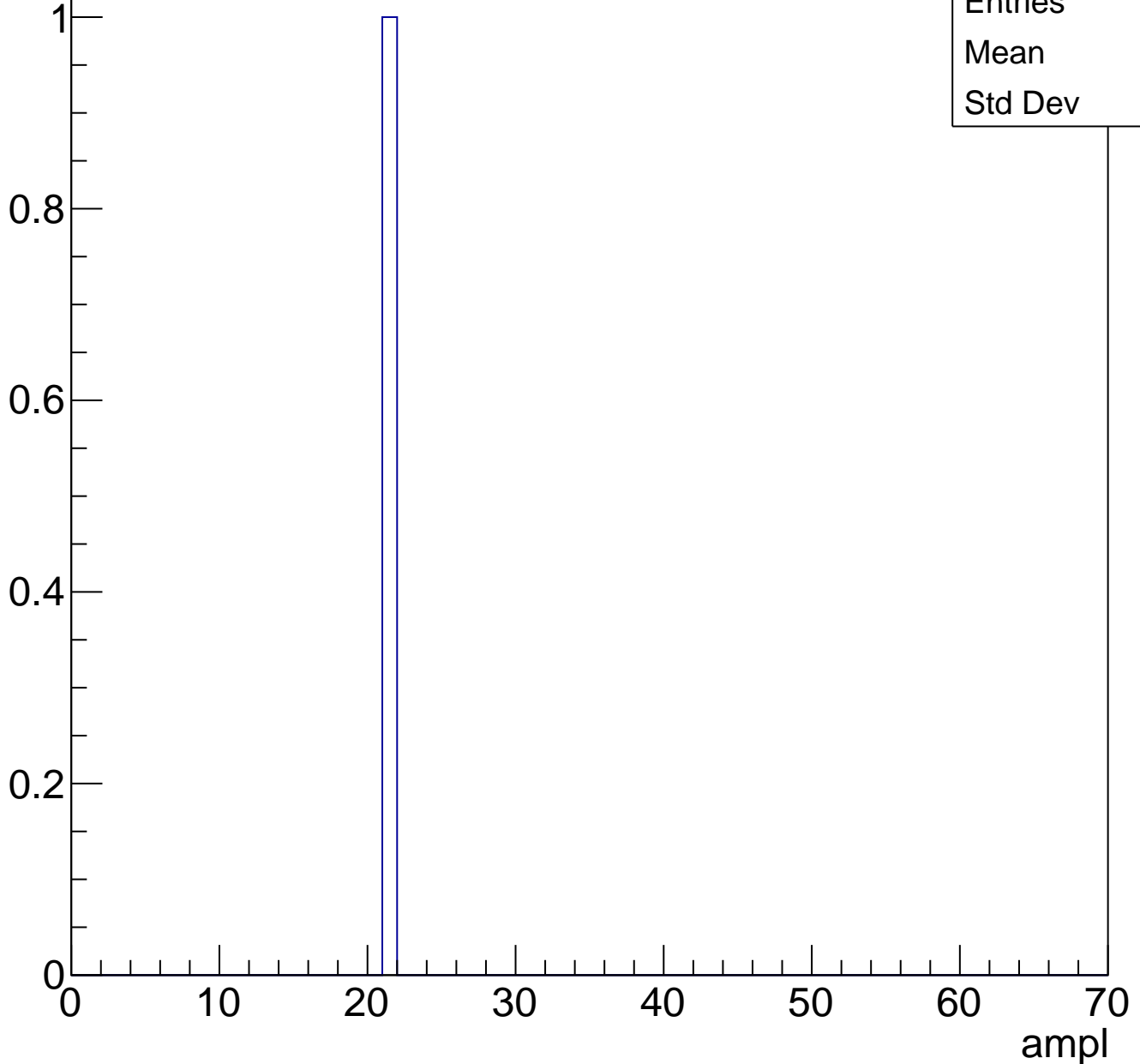




# B0L101S, U23-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch111, adc0

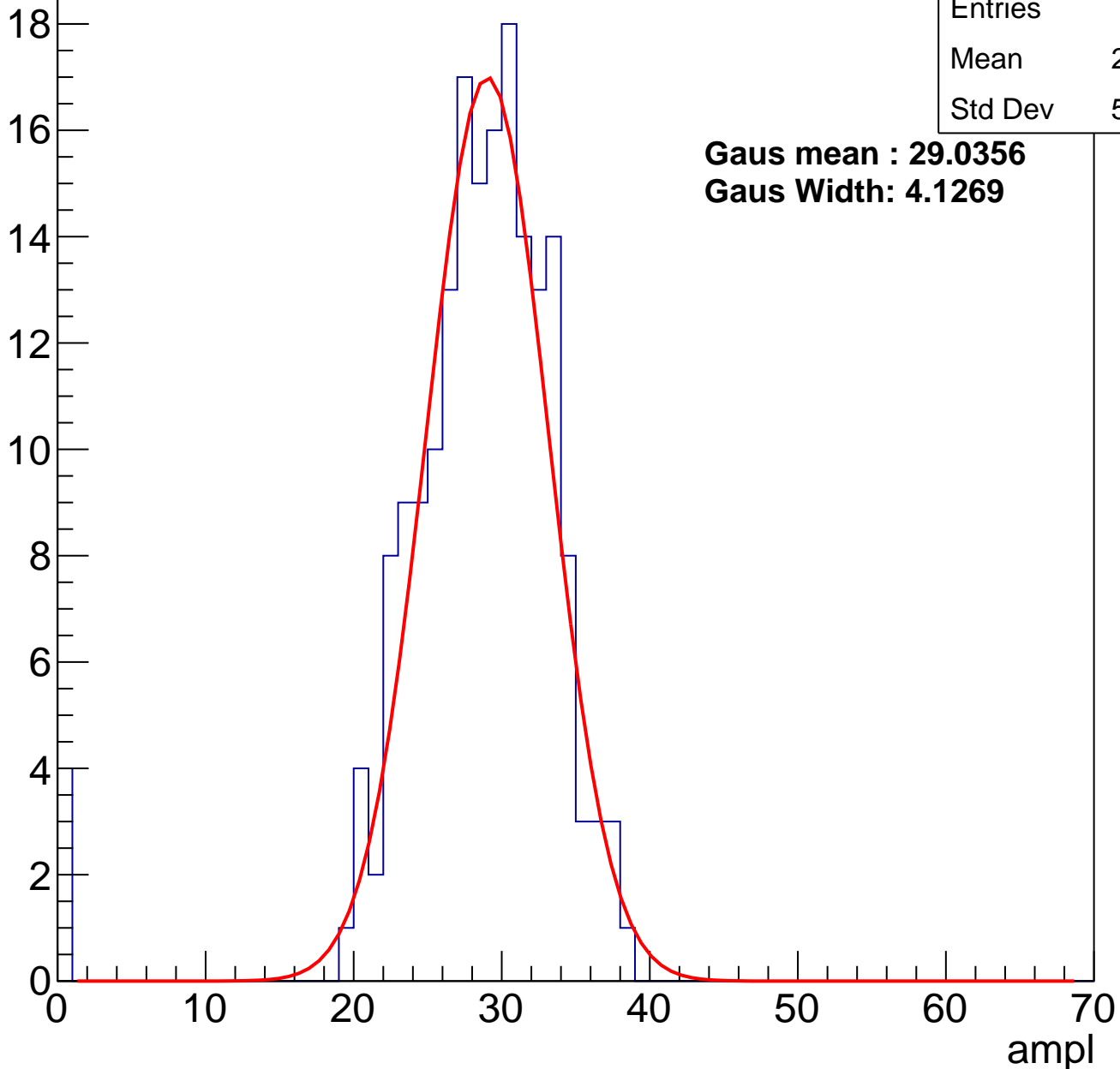
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	185
Mean	27.88
Std Dev	5.755

**Gaus mean : 29.0356**

**Gaus Width: 4.1269**

Entry



# B0L101S, U23-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	86
Mean	35.5
Std Dev	3.202

**Gaus mean : 36.2379**

**Gaus Width: 3.1004**

0

10

20

30

40

50

60

70

ampl

# B0L101S, U23-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

153

Mean

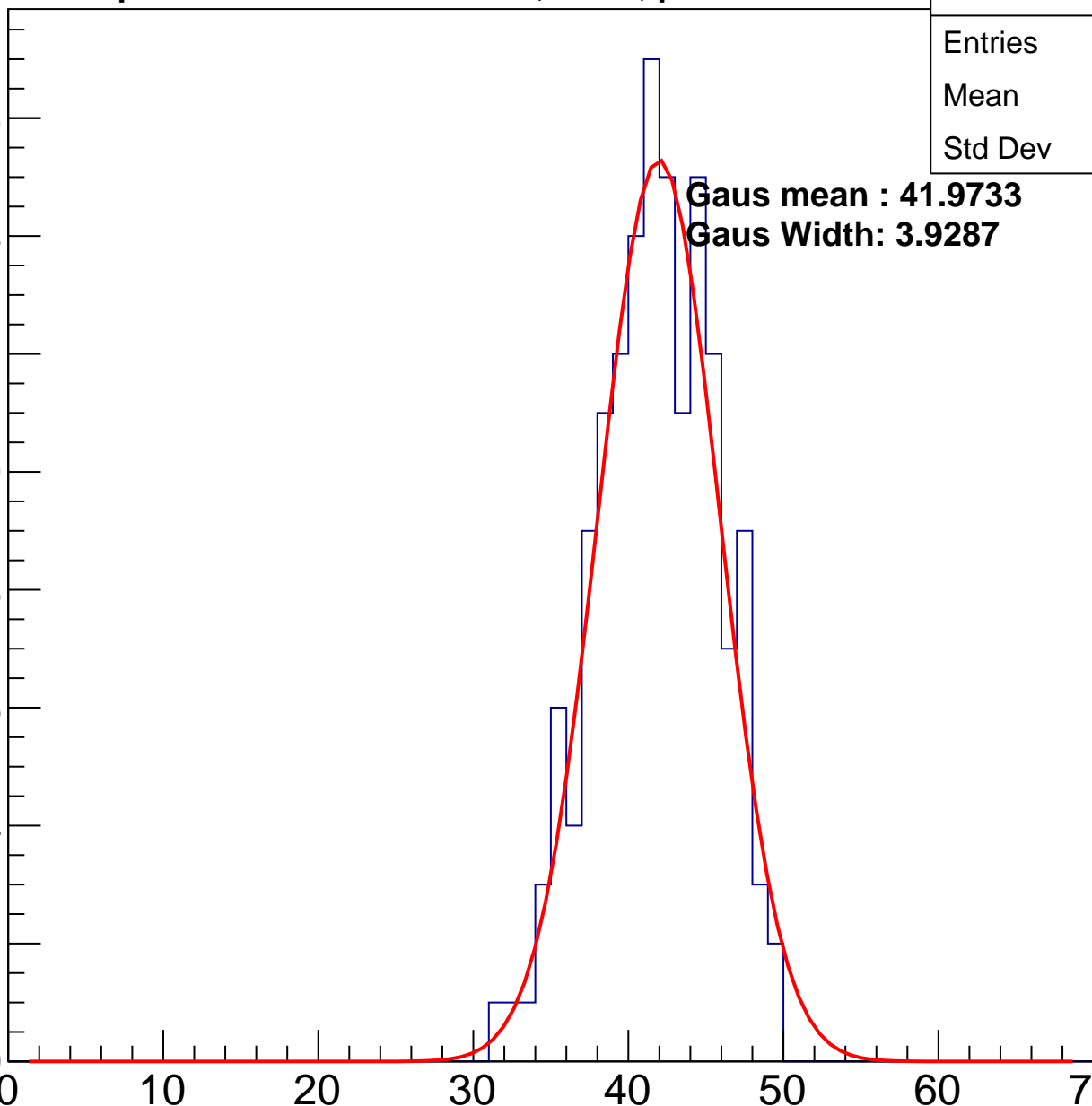
41.29

Std Dev

3.757

**Gaus mean : 41.9733**

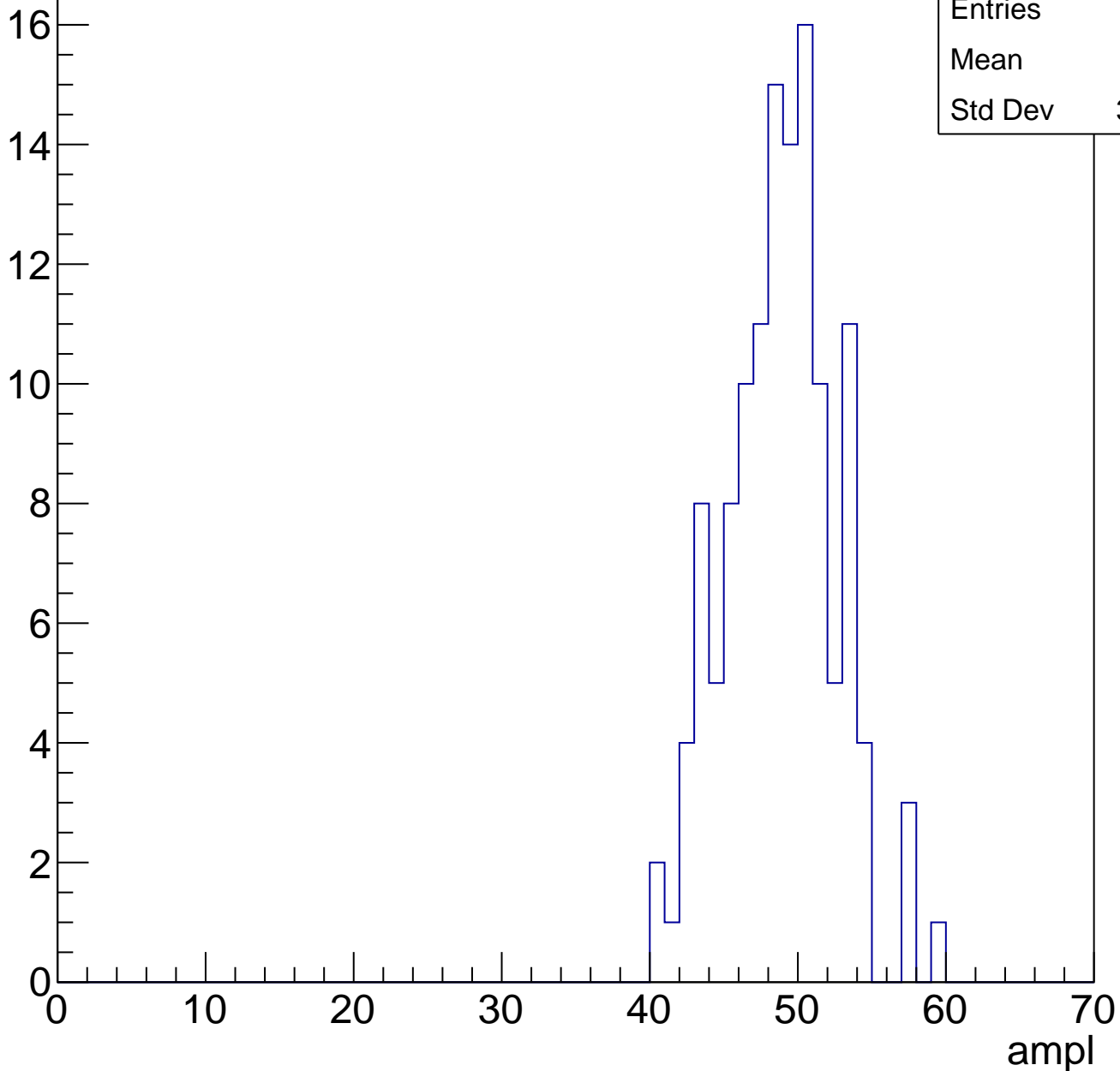
**Gaus Width: 3.9287**



# B0L101S, U23-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

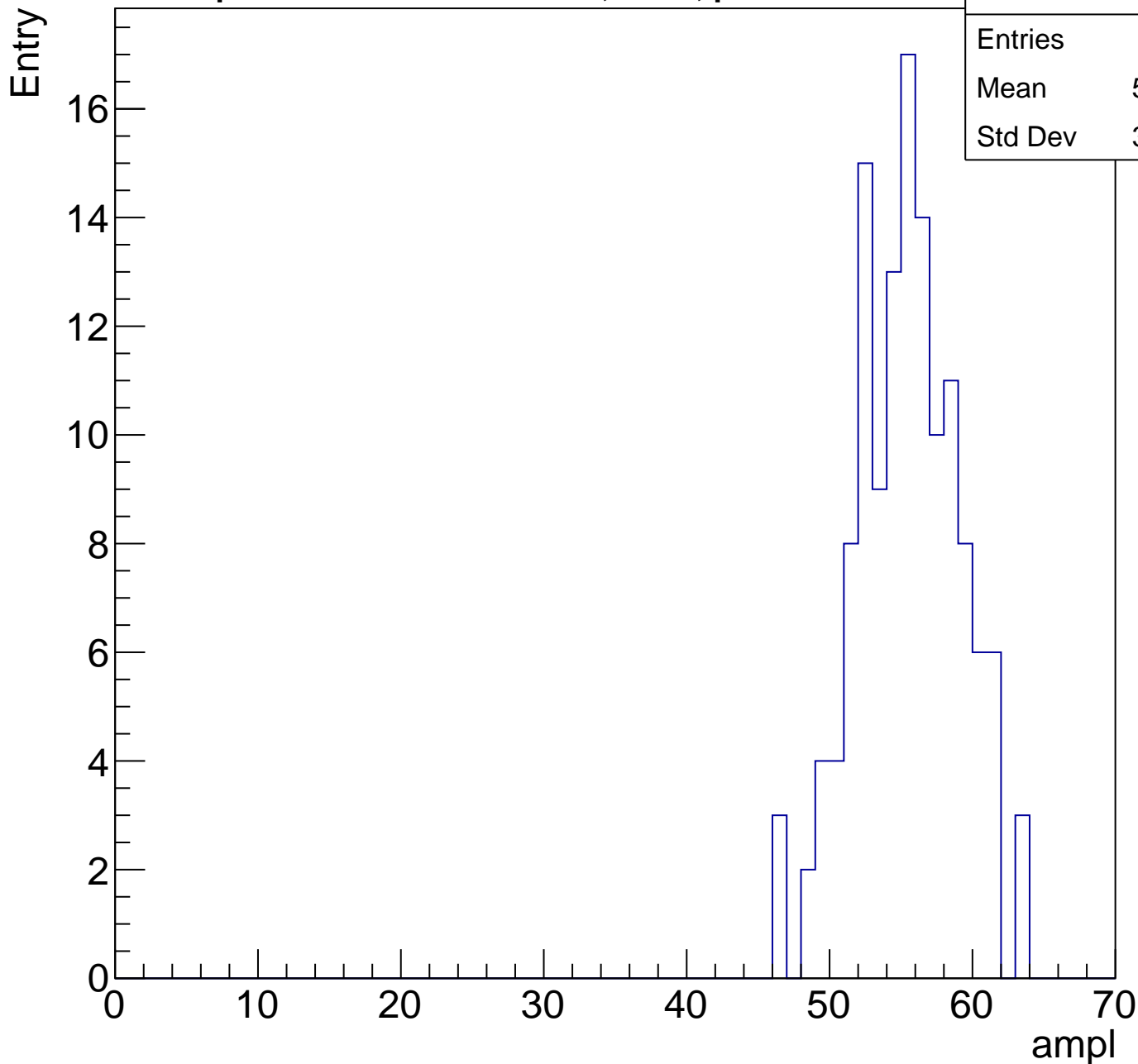
Entry



# B0L101S, U23-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

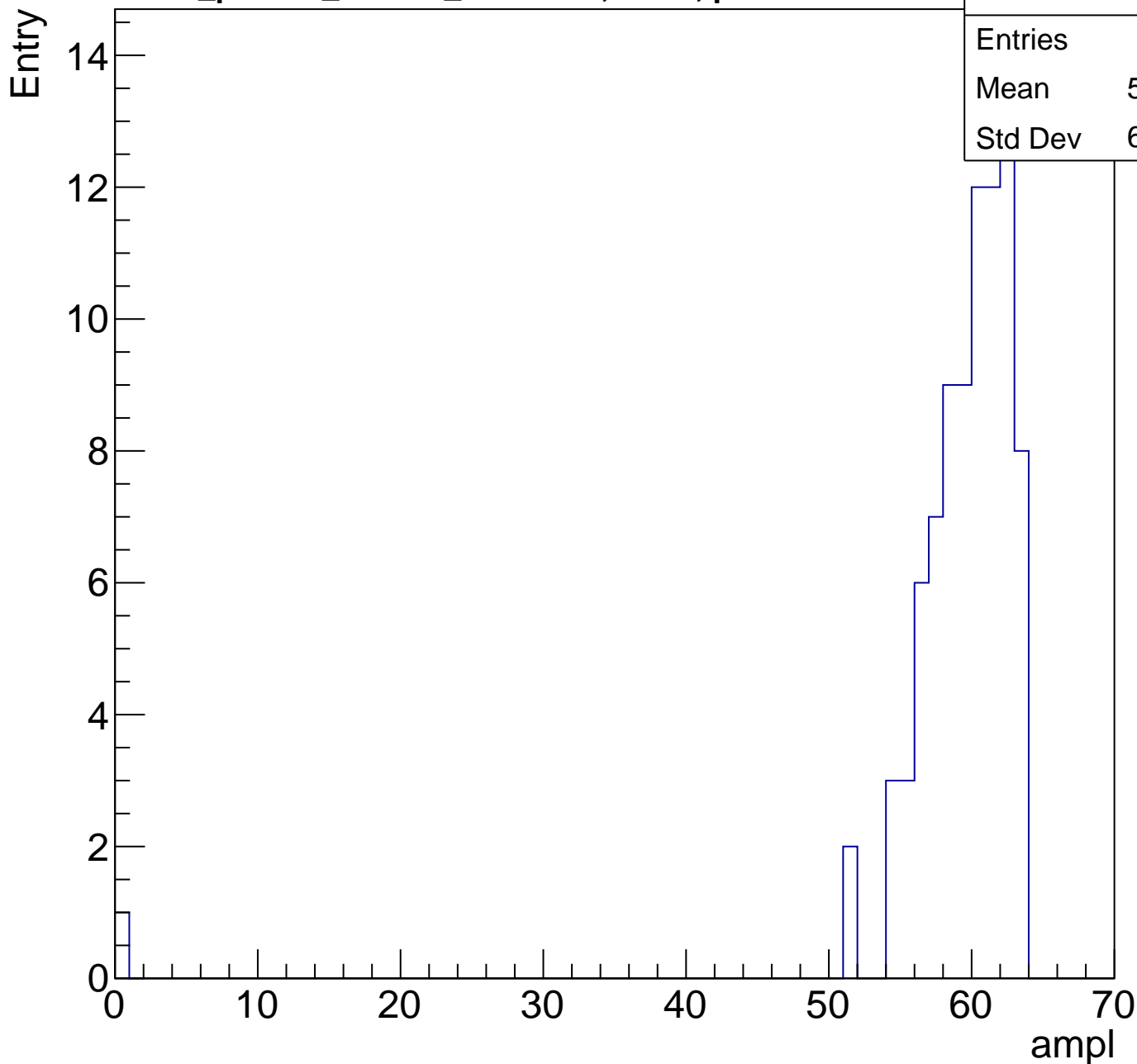
Entries	133
Mean	54.97
Std Dev	3.613



# B0L101S, U23-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	86
Mean	58.62
Std Dev	6.925

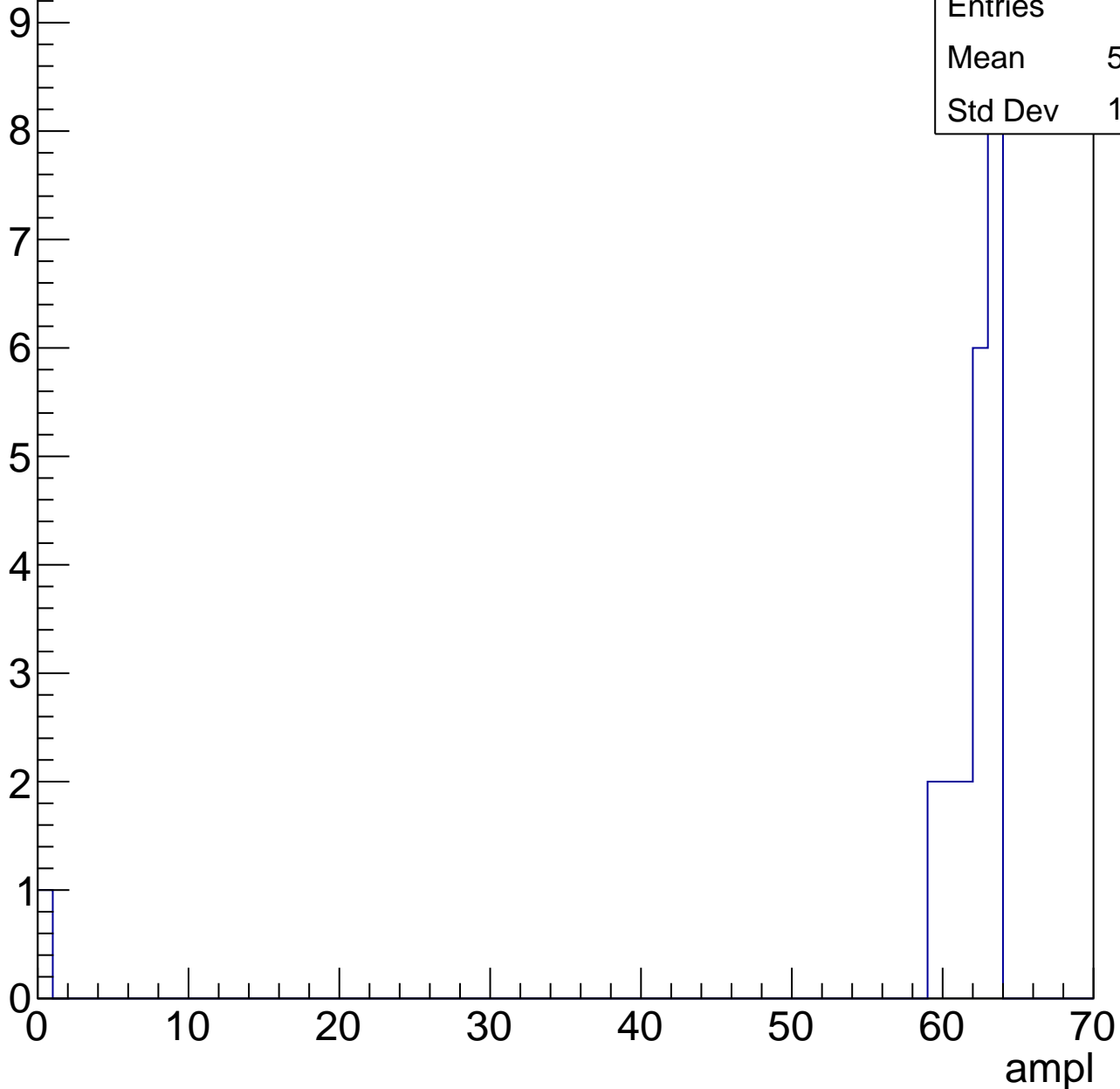


# B0L101S, U23-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	22
Mean	59.05
Std Dev	12.95





# B0L101S, U23-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch112, adc0

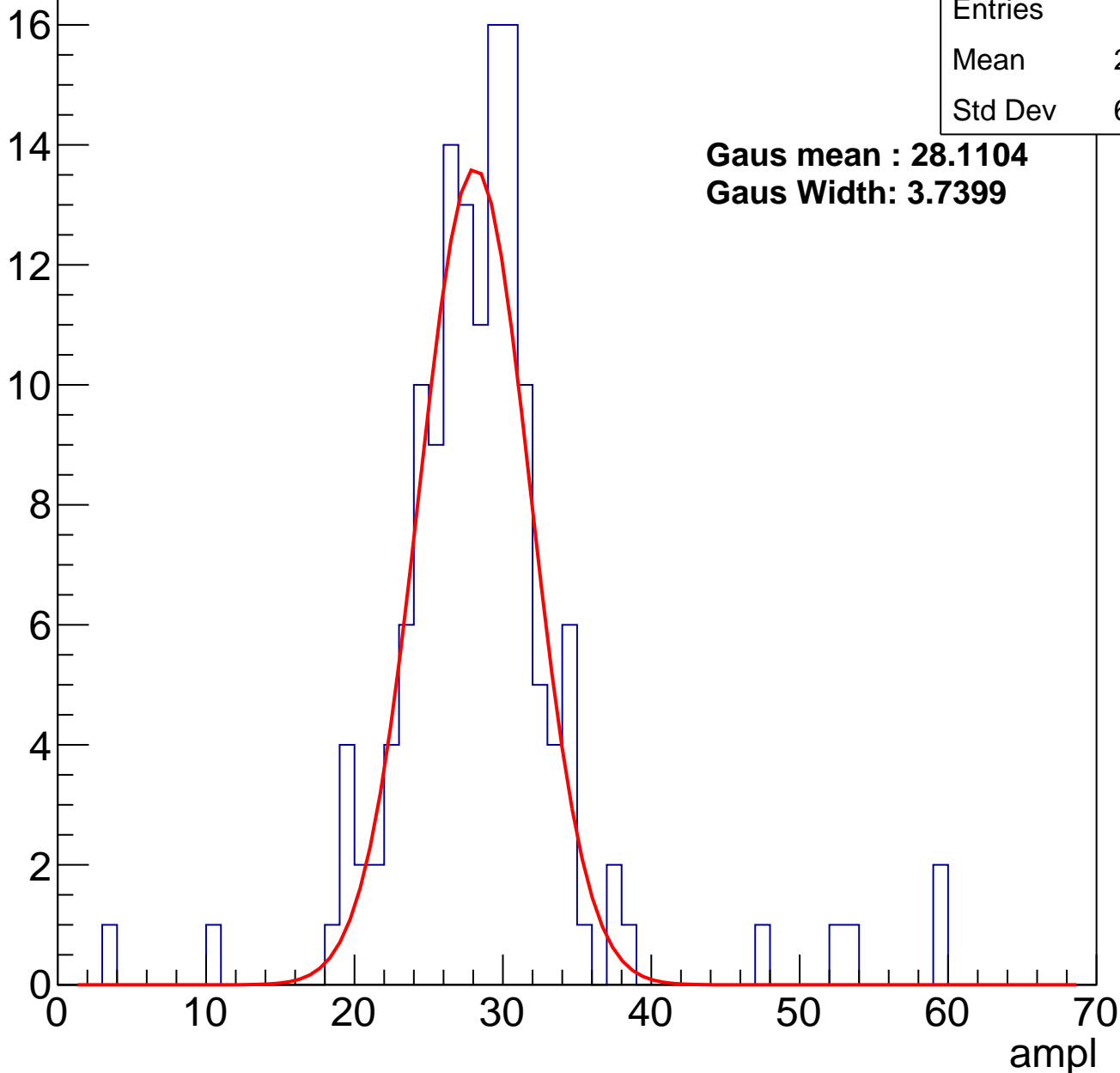
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	28.26
Std Dev	6.746

**Gaus mean : 28.1104**

**Gaus Width: 3.7399**

Entry



# B0L101S, U23-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

34.9

Std Dev

3.904

**Gaus mean : 35.4759**

**Gaus Width: 3.7994**

0

10

20

30

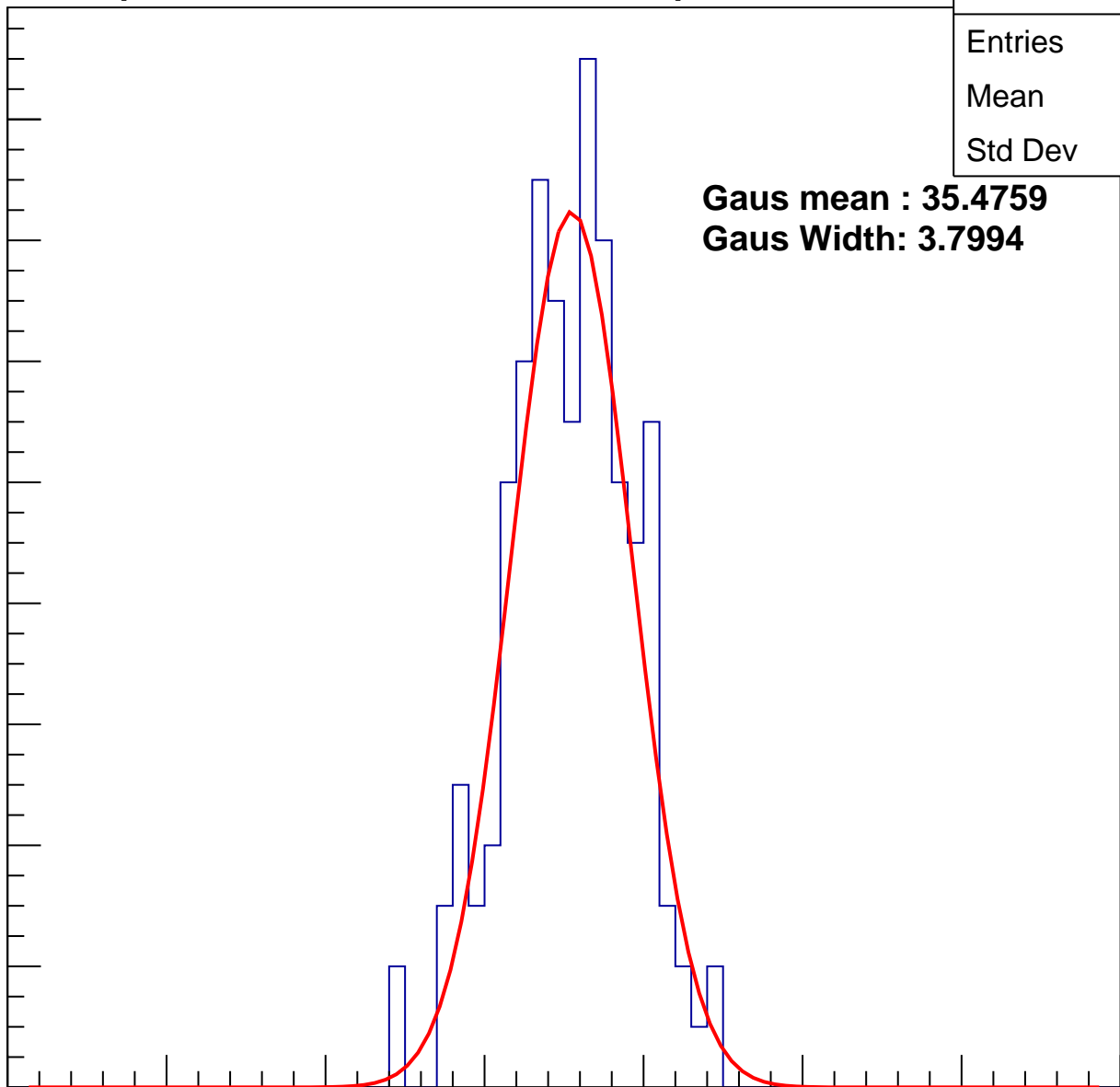
40

50

60

70

ampl



# B0L101S, U23-ch112, adc2

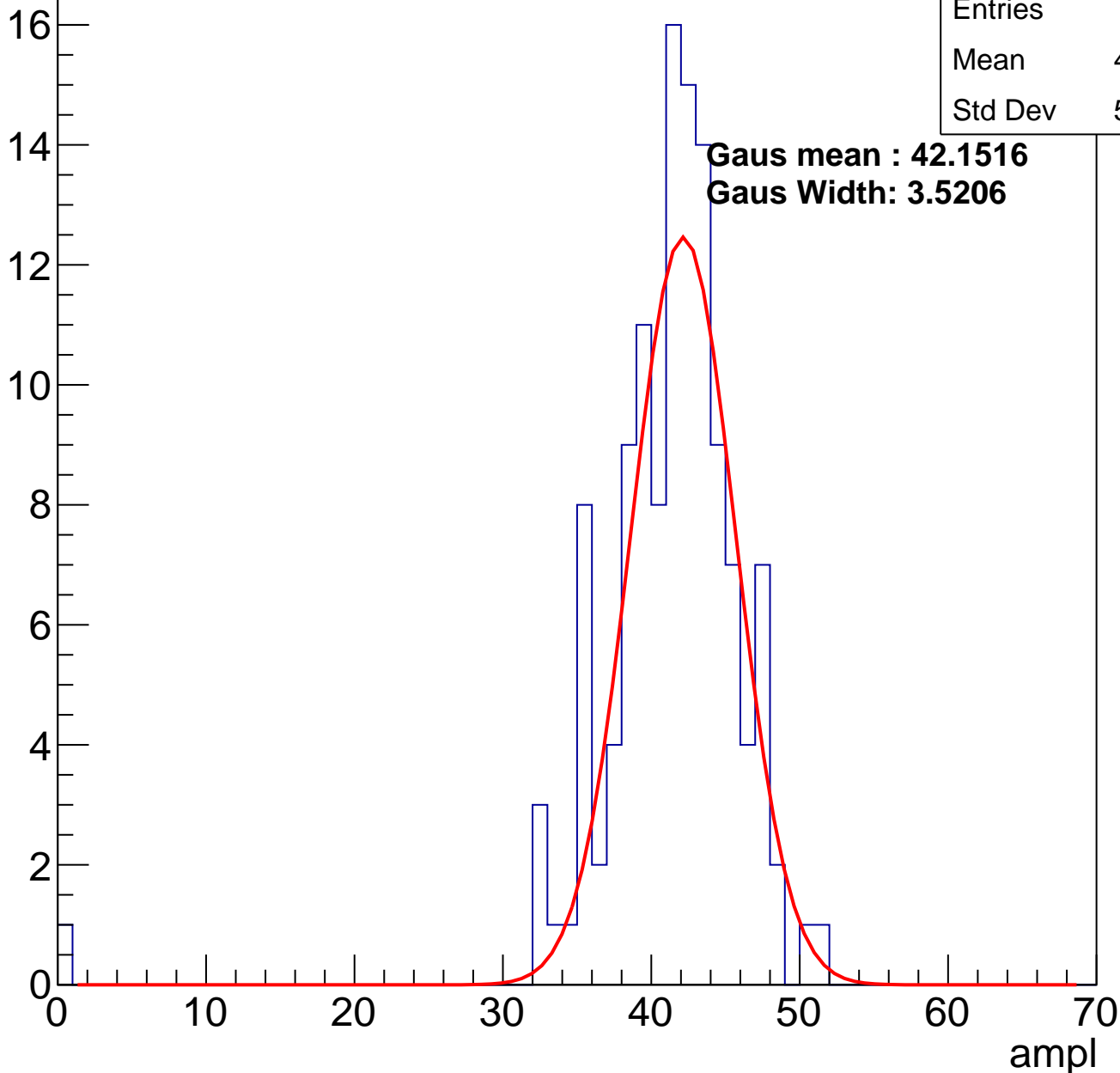
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	124
Mean	40.83
Std Dev	5.285

**Gaus mean : 42.1516**

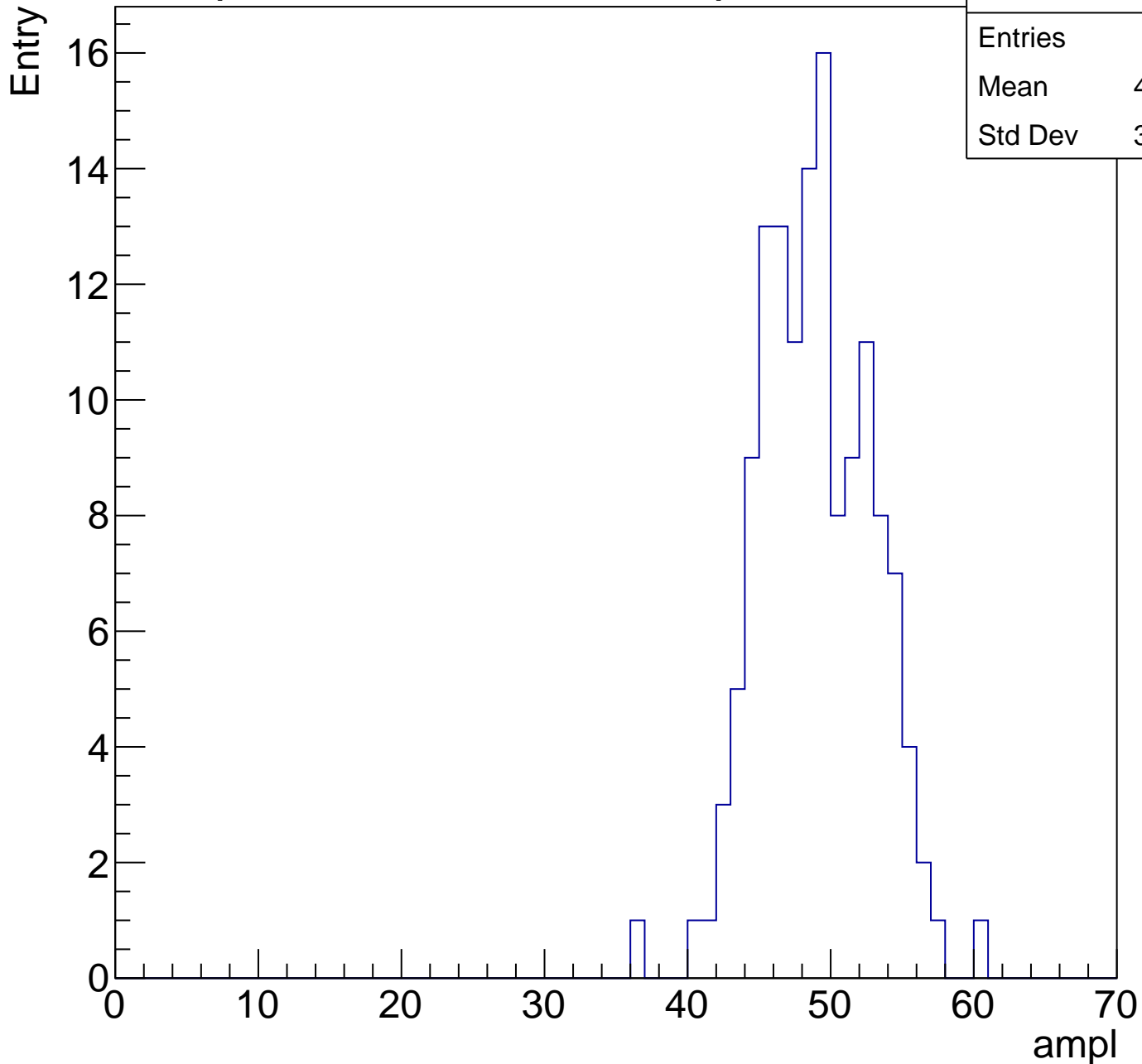
**Gaus Width: 3.5206**



# B0L101S, U23-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	48.49
Std Dev	3.888



# B0L101S, U23-ch112, adc4

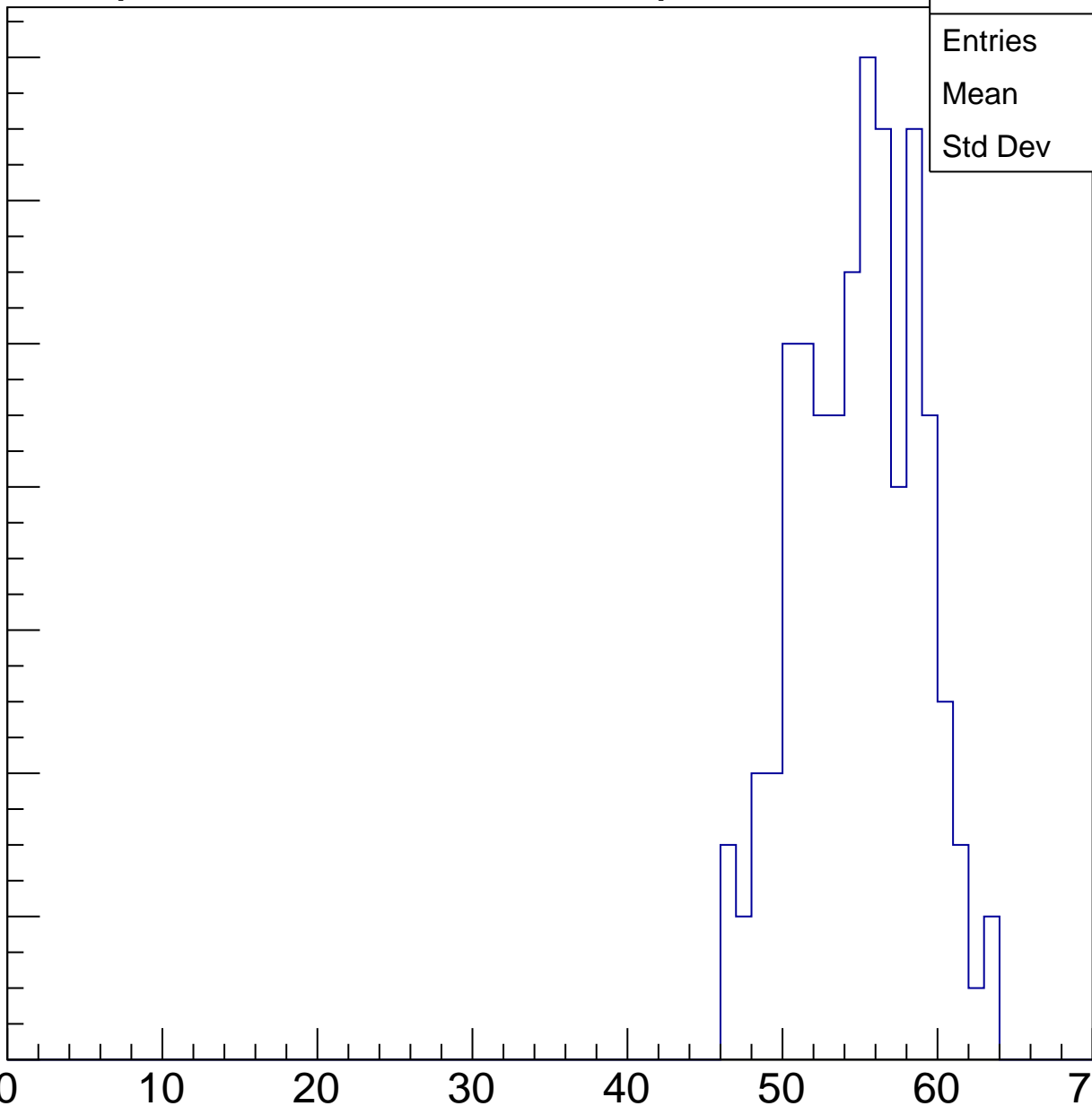
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	130
Mean	54.45
Std Dev	3.849

ampl



# B0L101S, U23-ch112, adc5

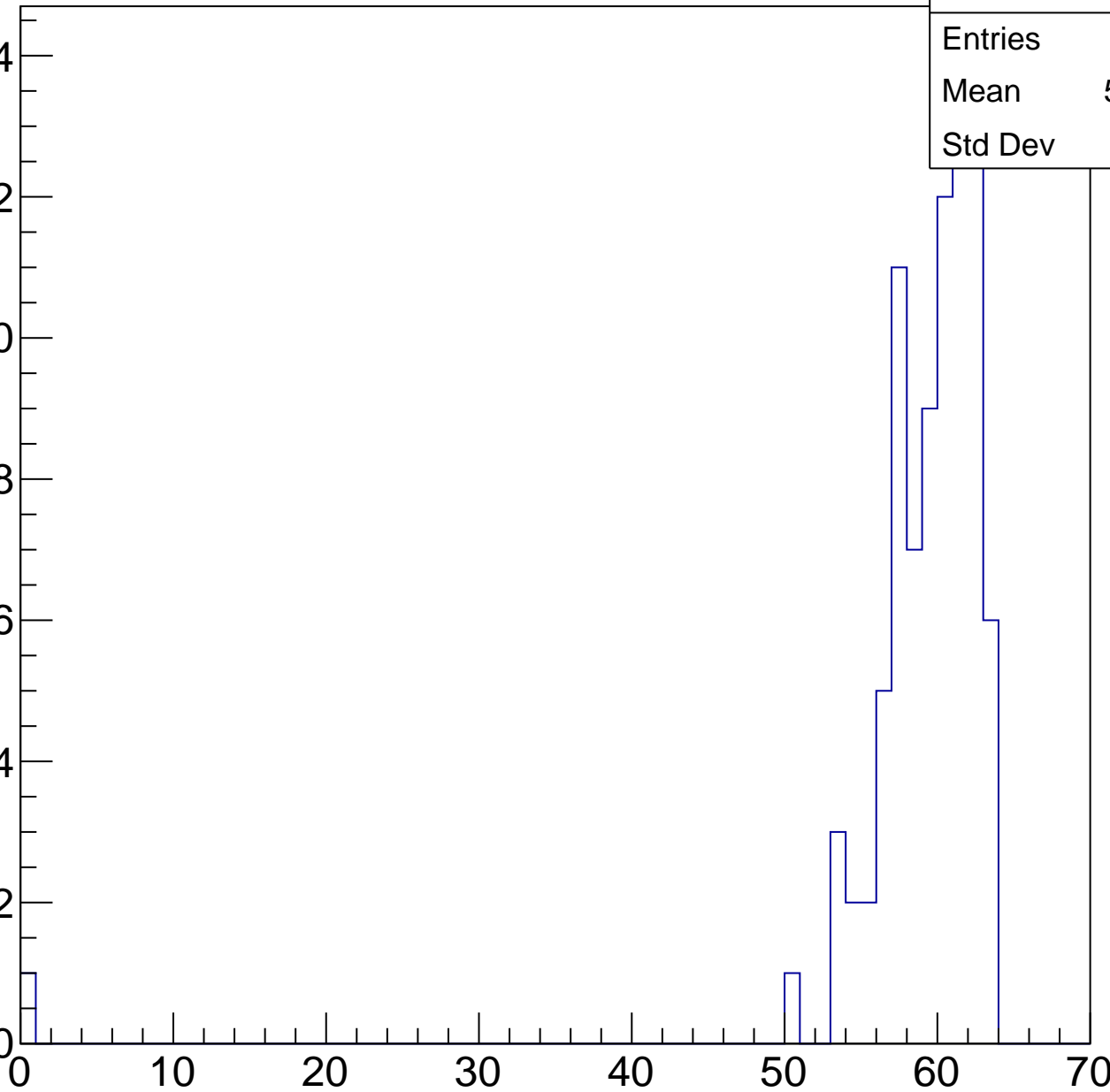
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	87
Mean	58.52
Std Dev	6.88

ampl



# B0L101S, U23-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	23
Mean	59.22
Std Dev	12.69



# B0L101S, U23-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch113, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	29.02
Std Dev	4.4

**Gaus mean : 30.2497**

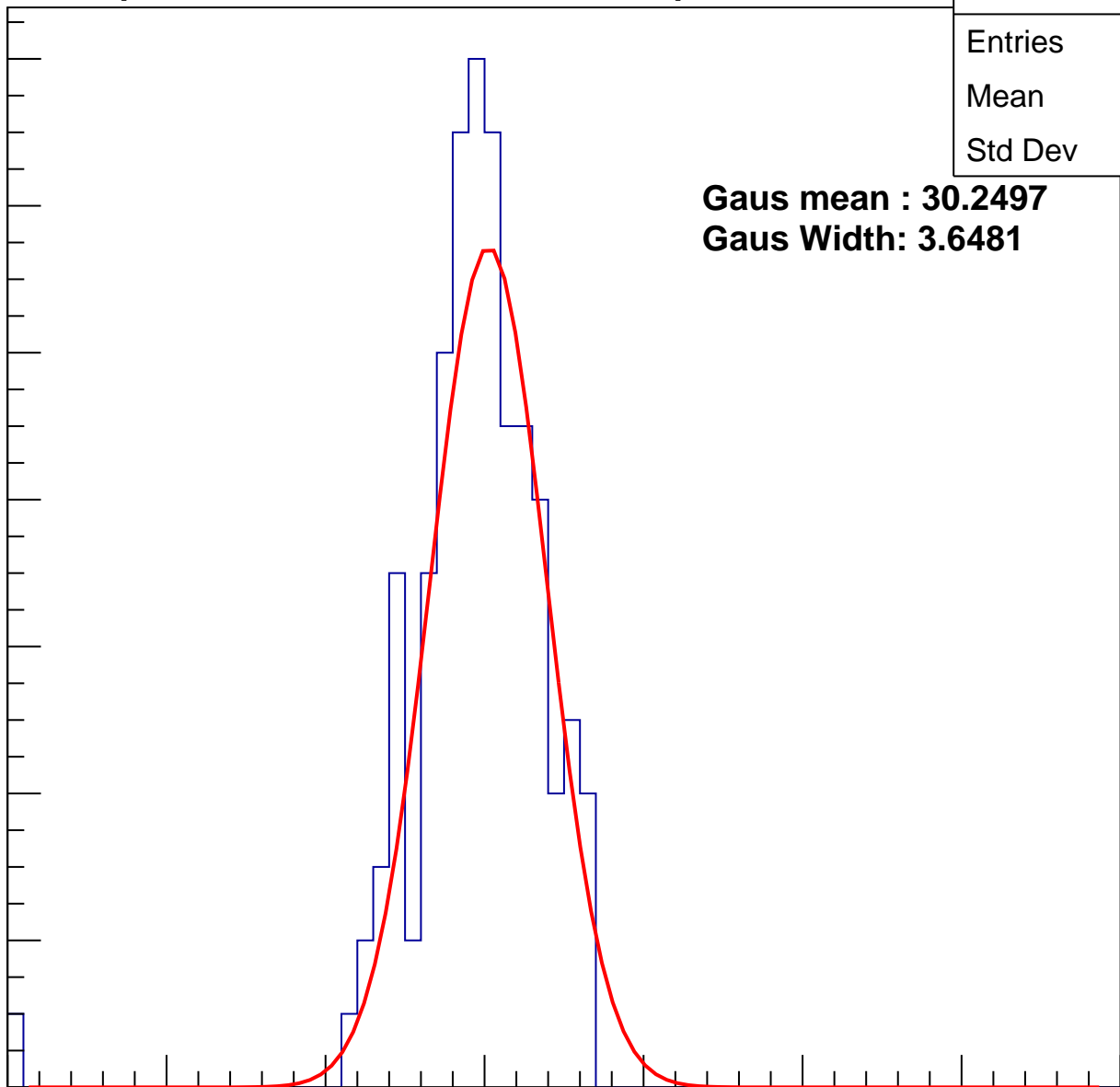
**Gaus Width: 3.6481**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	35.41
Std Dev	3.534

**Gaus mean : 35.9521**

**Gaus Width: 3.7021**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U23-ch113, adc2

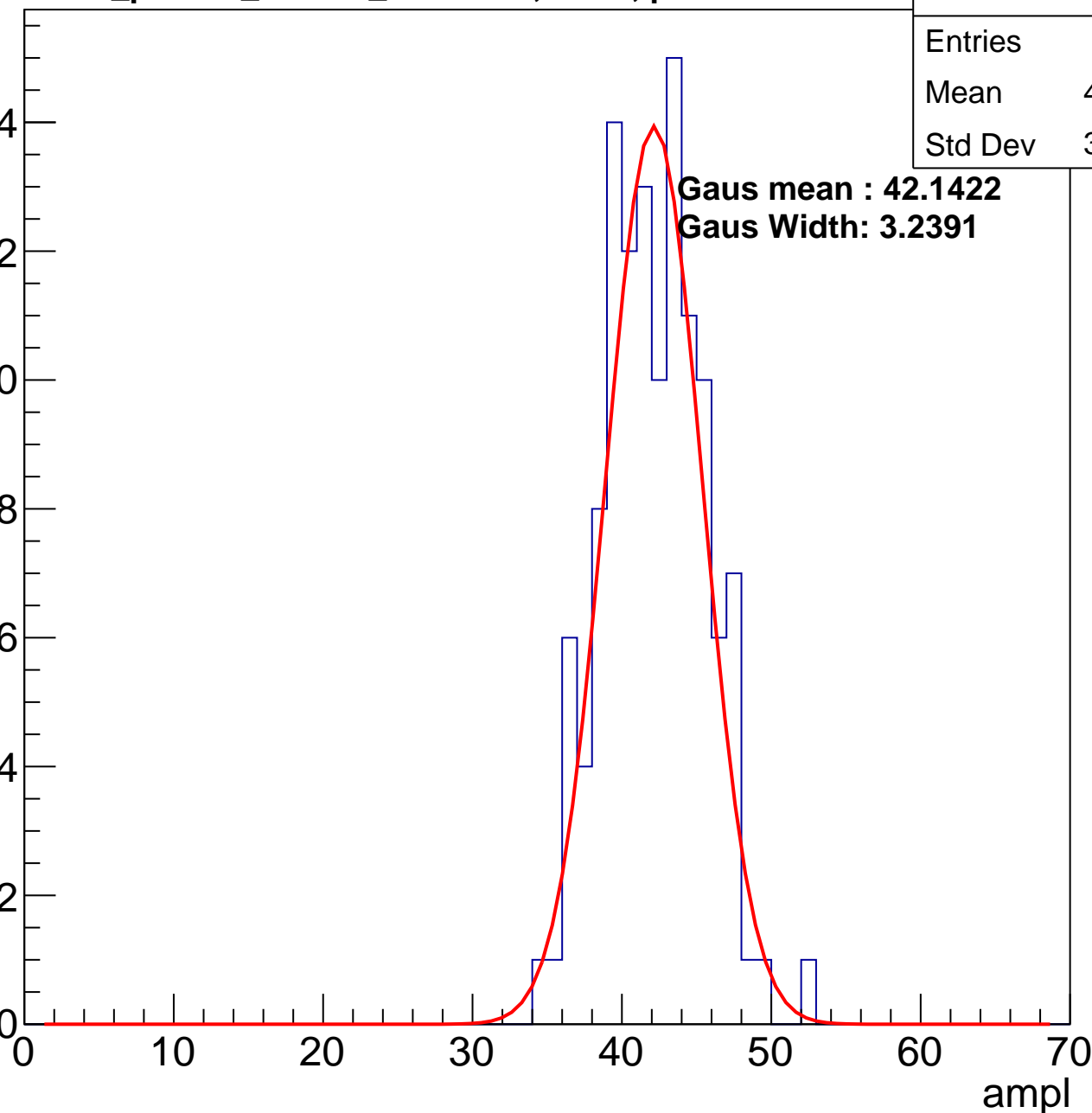
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	121
Mean	41.73
Std Dev	3.328

**Gaus mean : 42.1422**  
**Gaus Width: 3.2391**

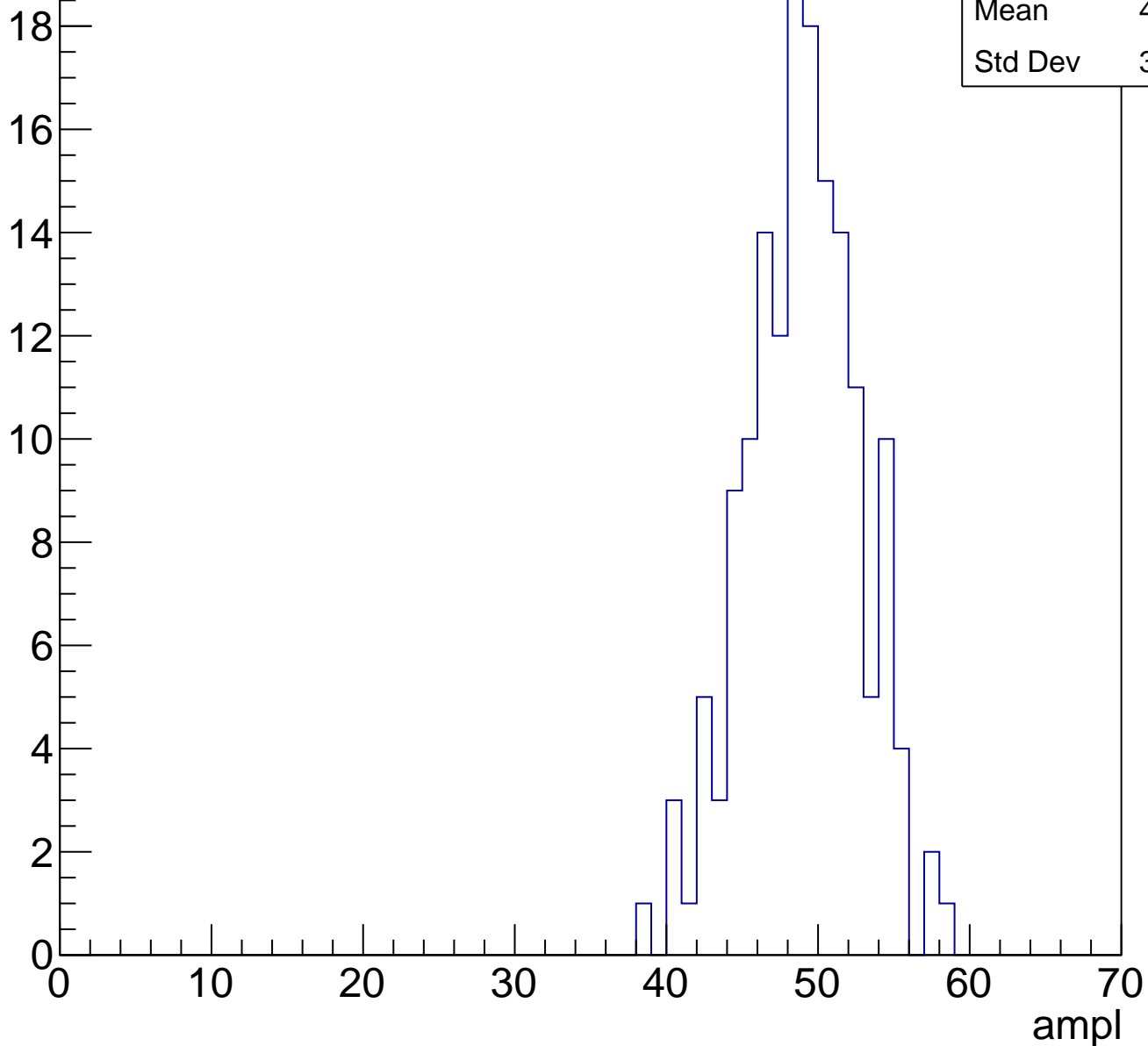


# B0L101S, U23-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	48.53
Std Dev	3.724

Entry



# B0L101S, U23-ch113, adc4

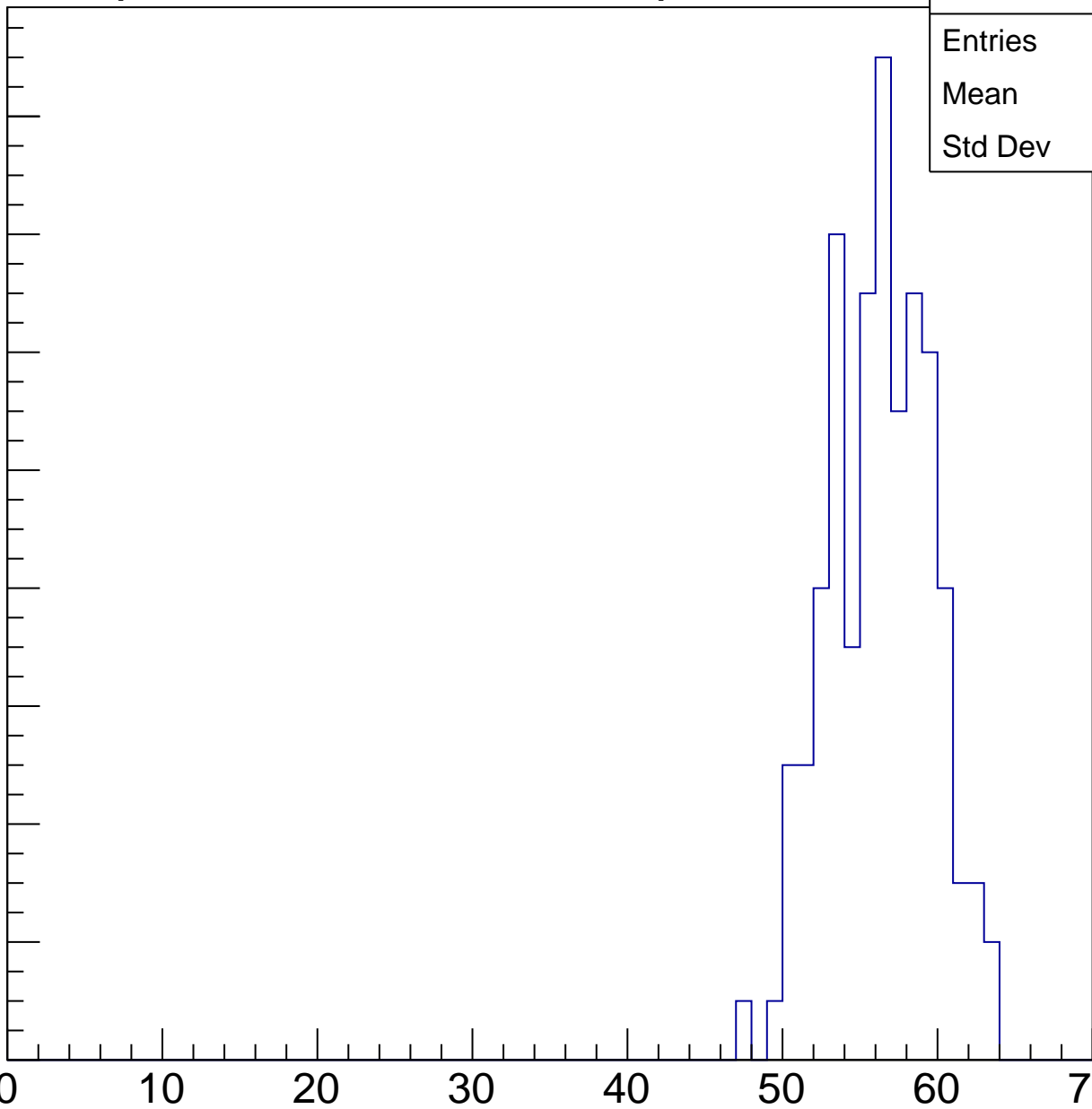
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	55.84
Std Dev	3.259

ampl



# B0L101S, U23-ch113, adc5

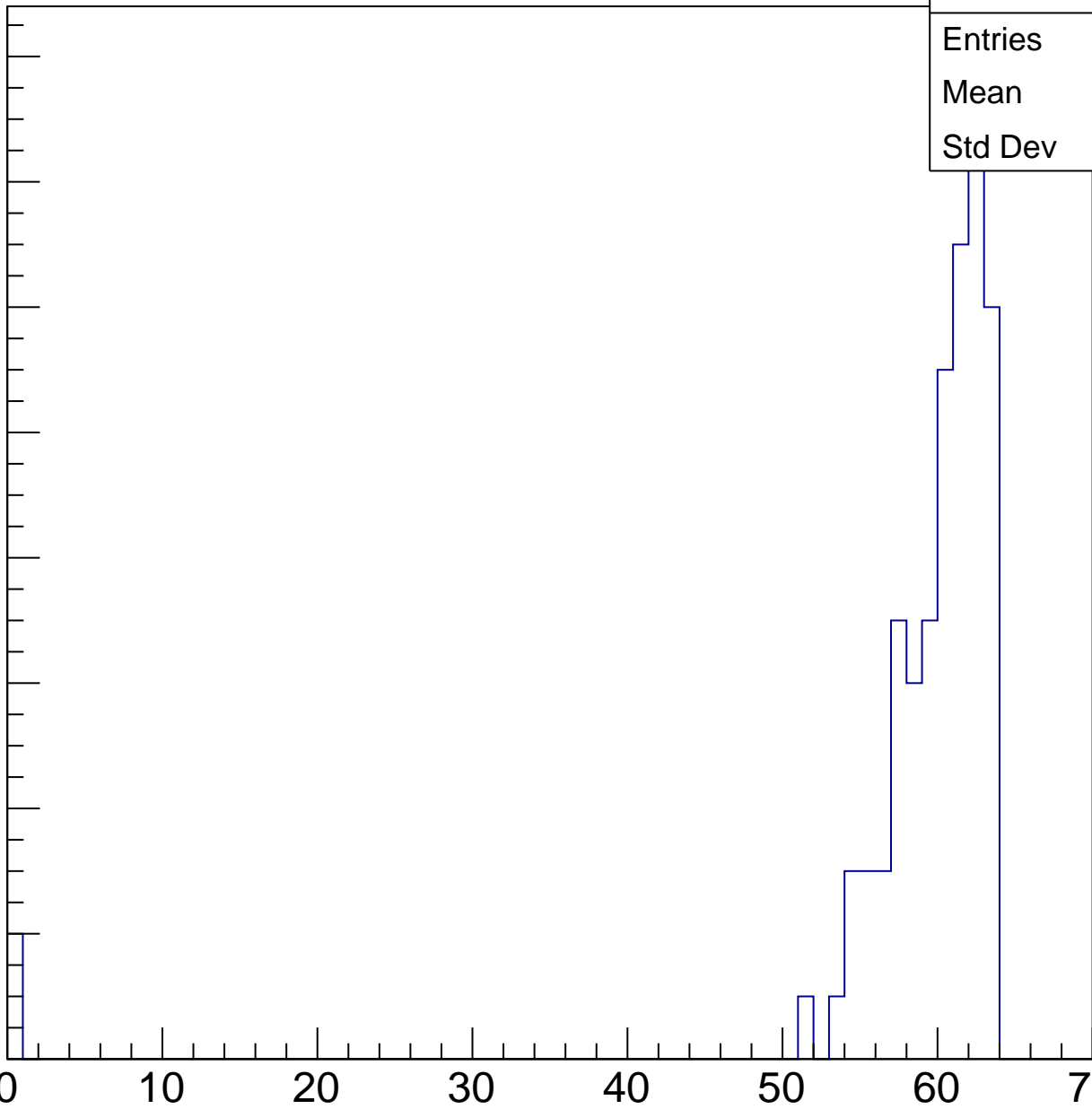
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	58.35
Std Dev	9.462

ampl

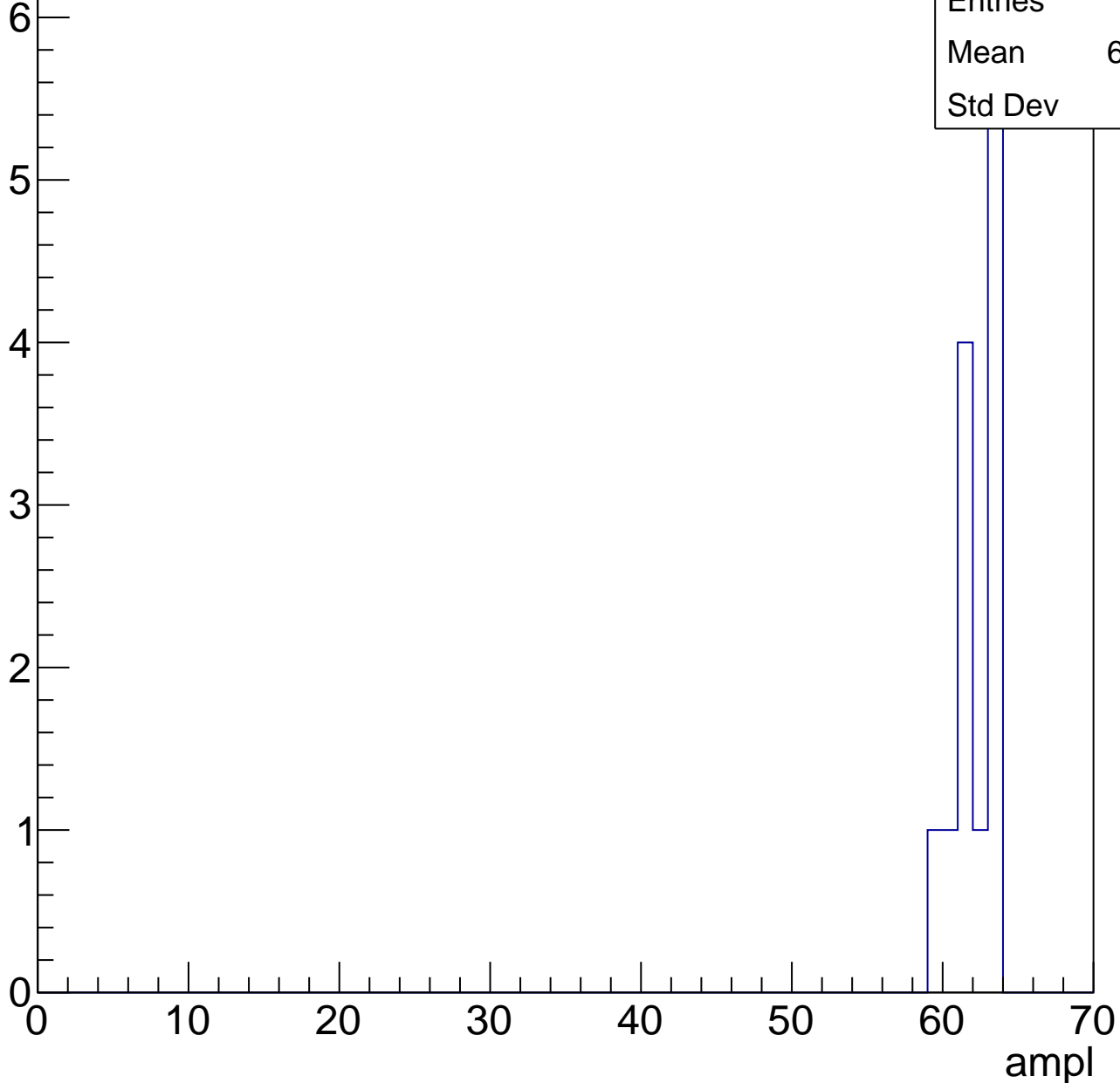


# B0L101S, U23-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.77
Std Dev	1.31





# B0L101S, U23-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

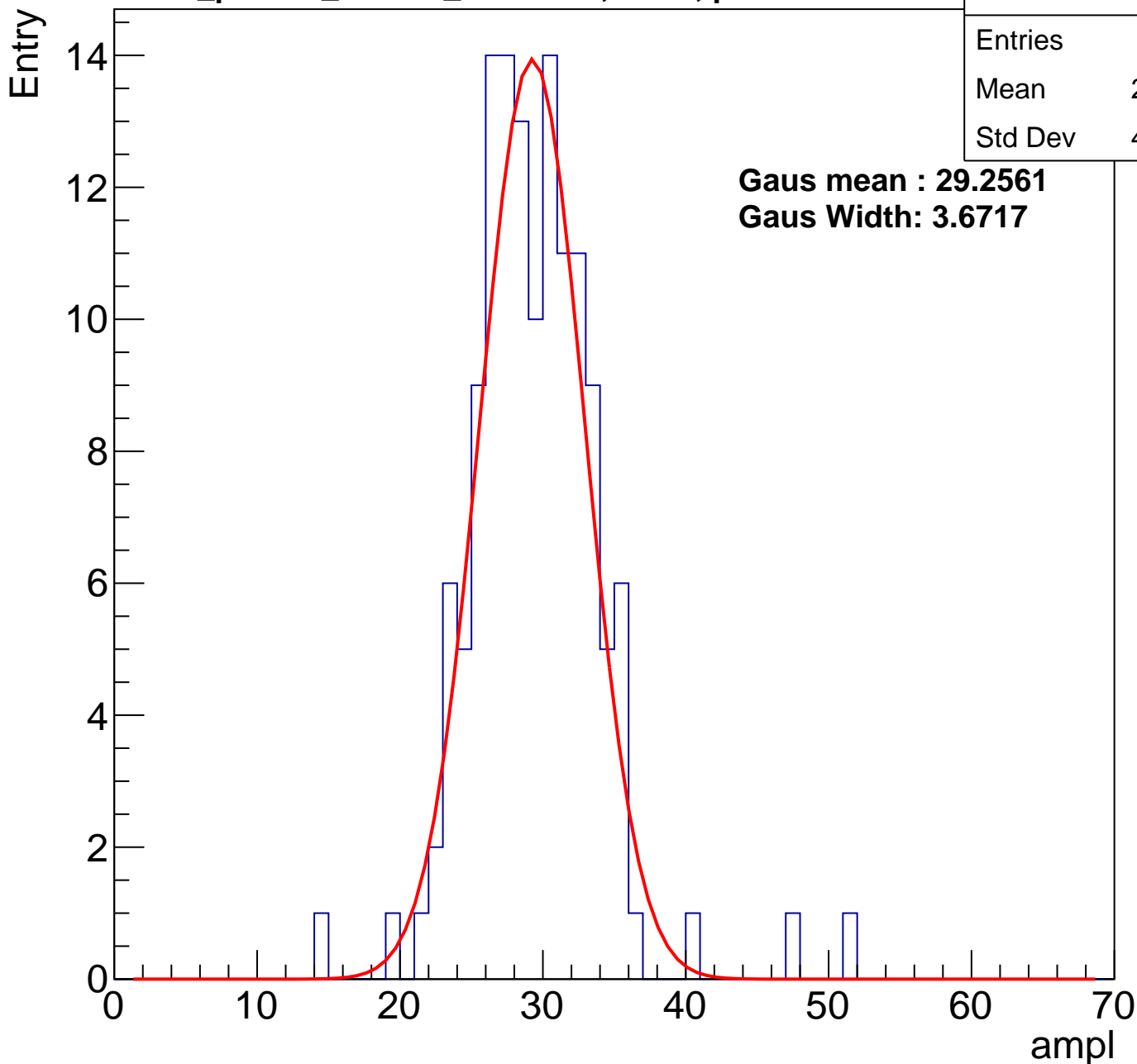
# B0L101S, U23-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	28.98
Std Dev	4.527

**Gaus mean : 29.2561**

**Gaus Width: 3.6717**



# B0L101S, U23-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	34.65
Std Dev	4.307

**Gaus mean : 35.3453**

**Gaus Width: 4.2971**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U23-ch114, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

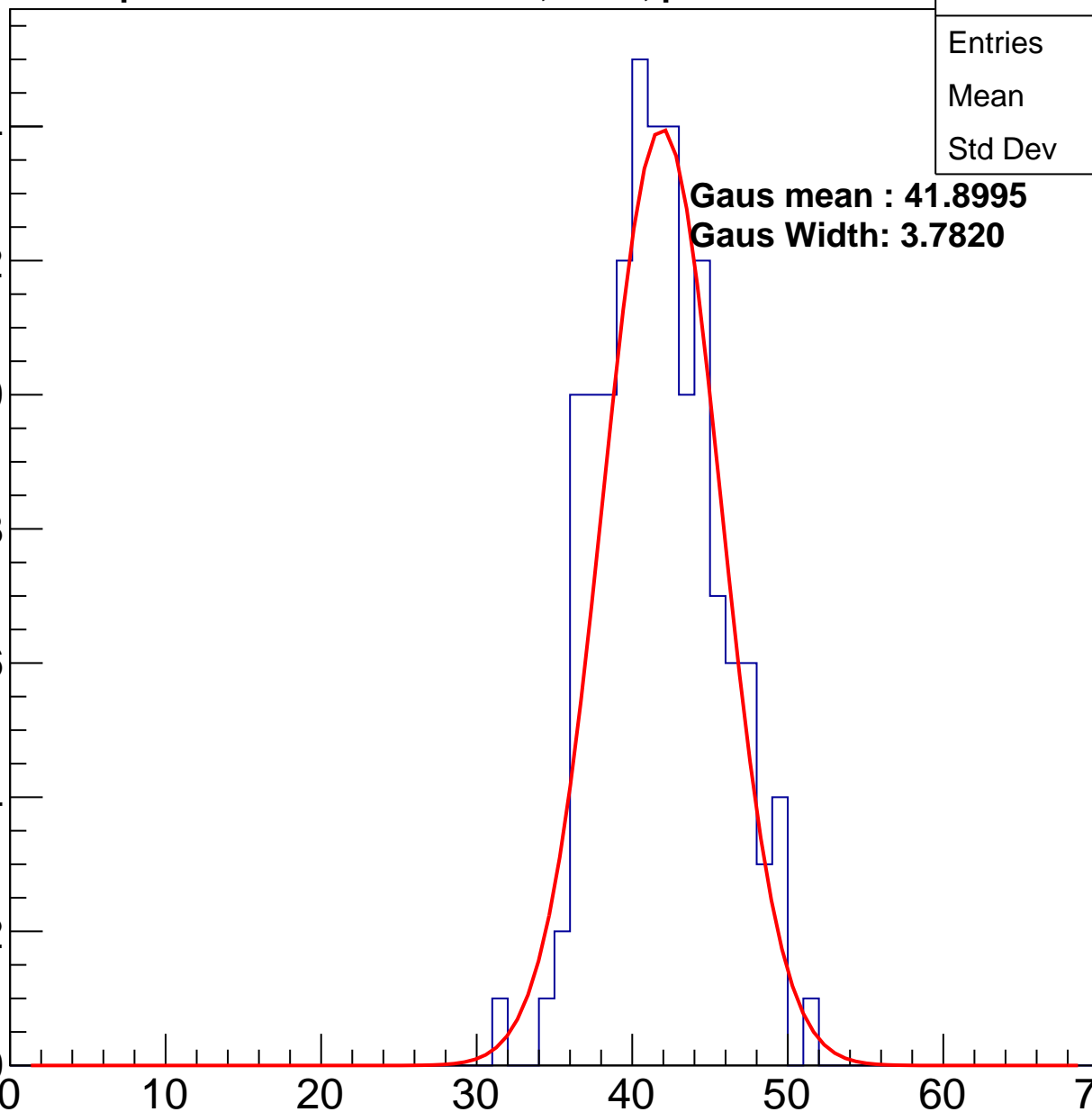
70

ampl

Entries	138
Mean	41.28
Std Dev	3.726

**Gaus mean : 41.8995**

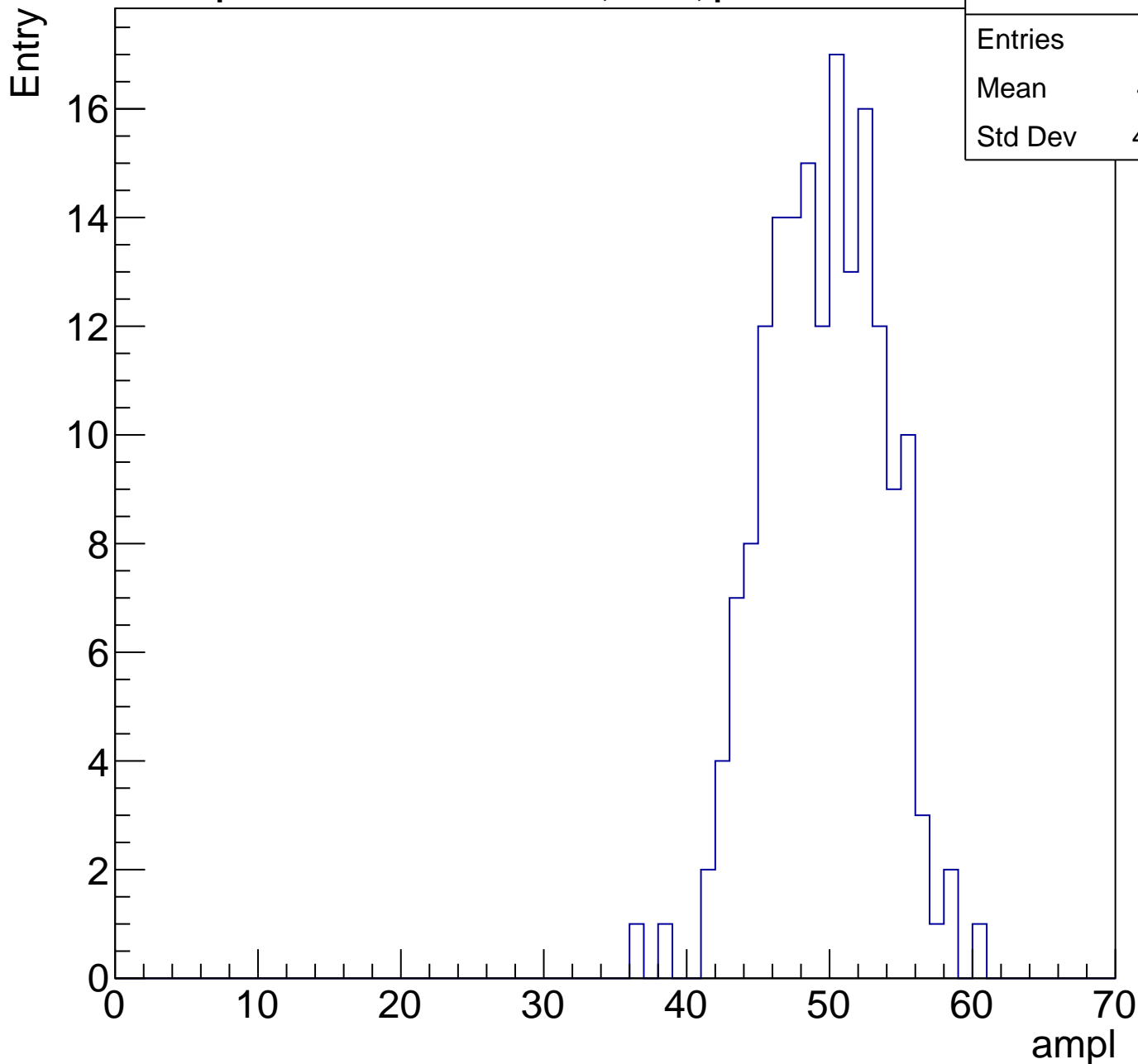
**Gaus Width: 3.7820**



# B0L101S, U23-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

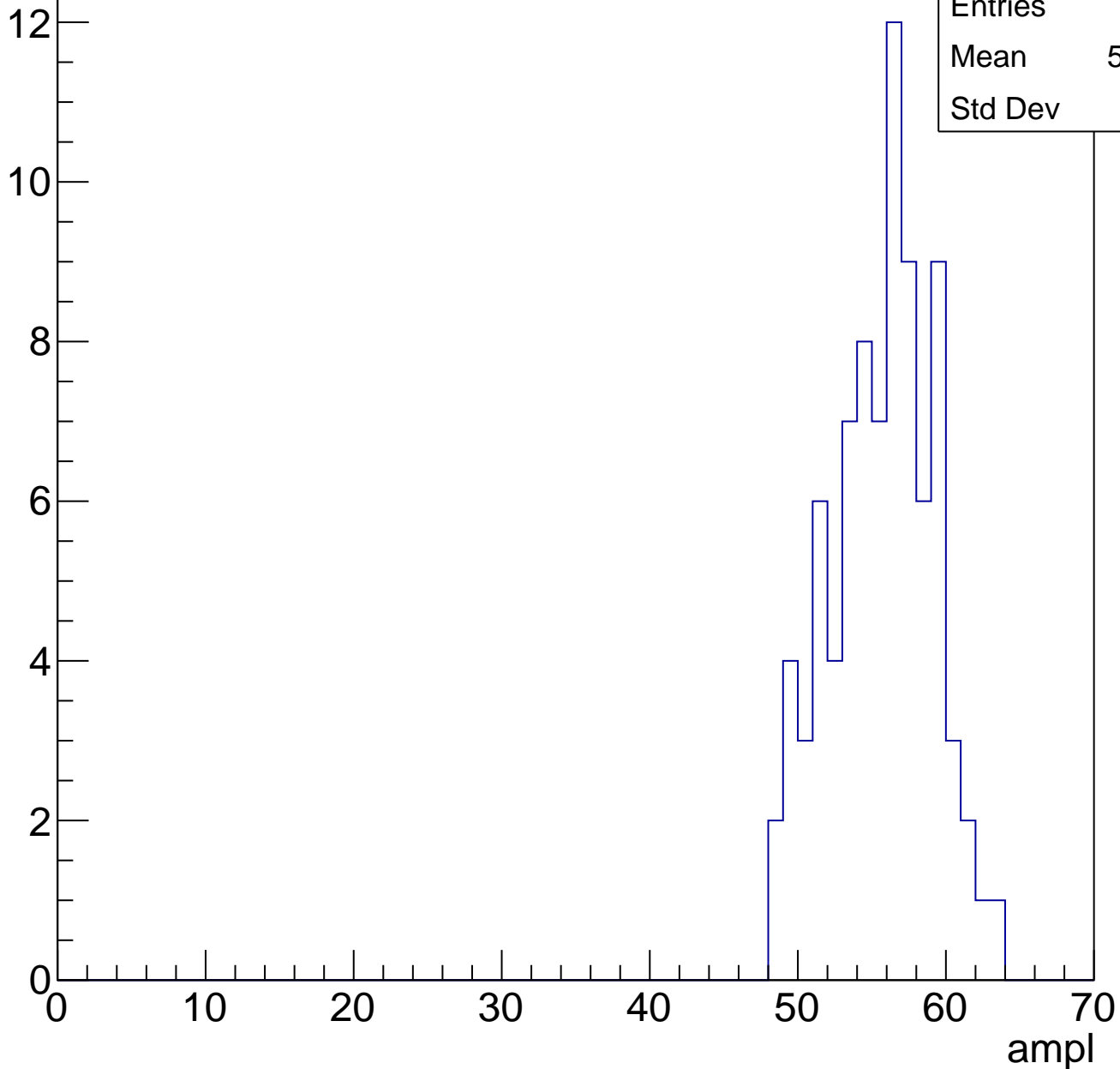
Entries	174
Mean	49.11
Std Dev	4.108



# B0L101S, U23-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch114, adc5

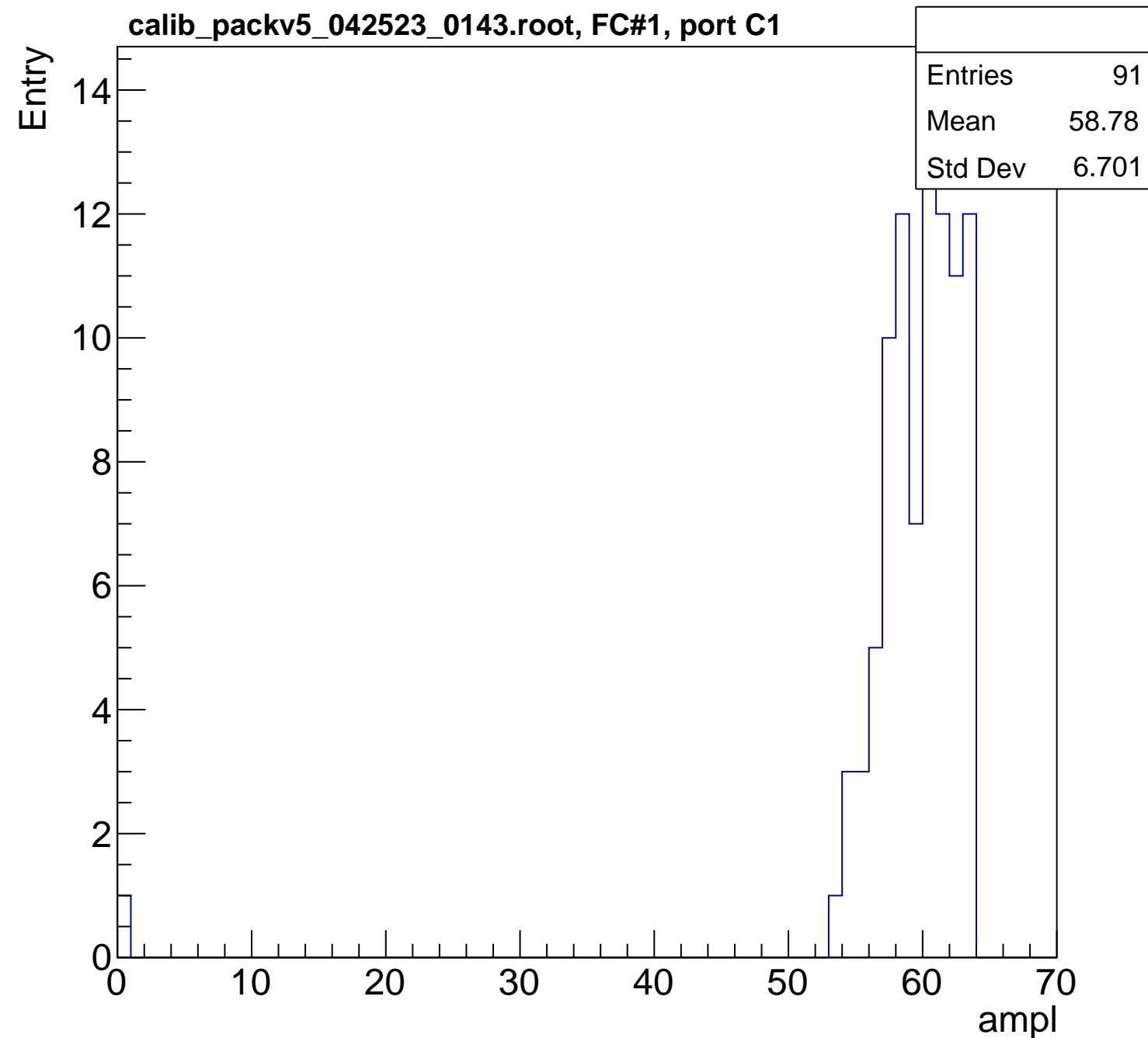
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	91
Mean	58.78
Std Dev	6.701

ampl

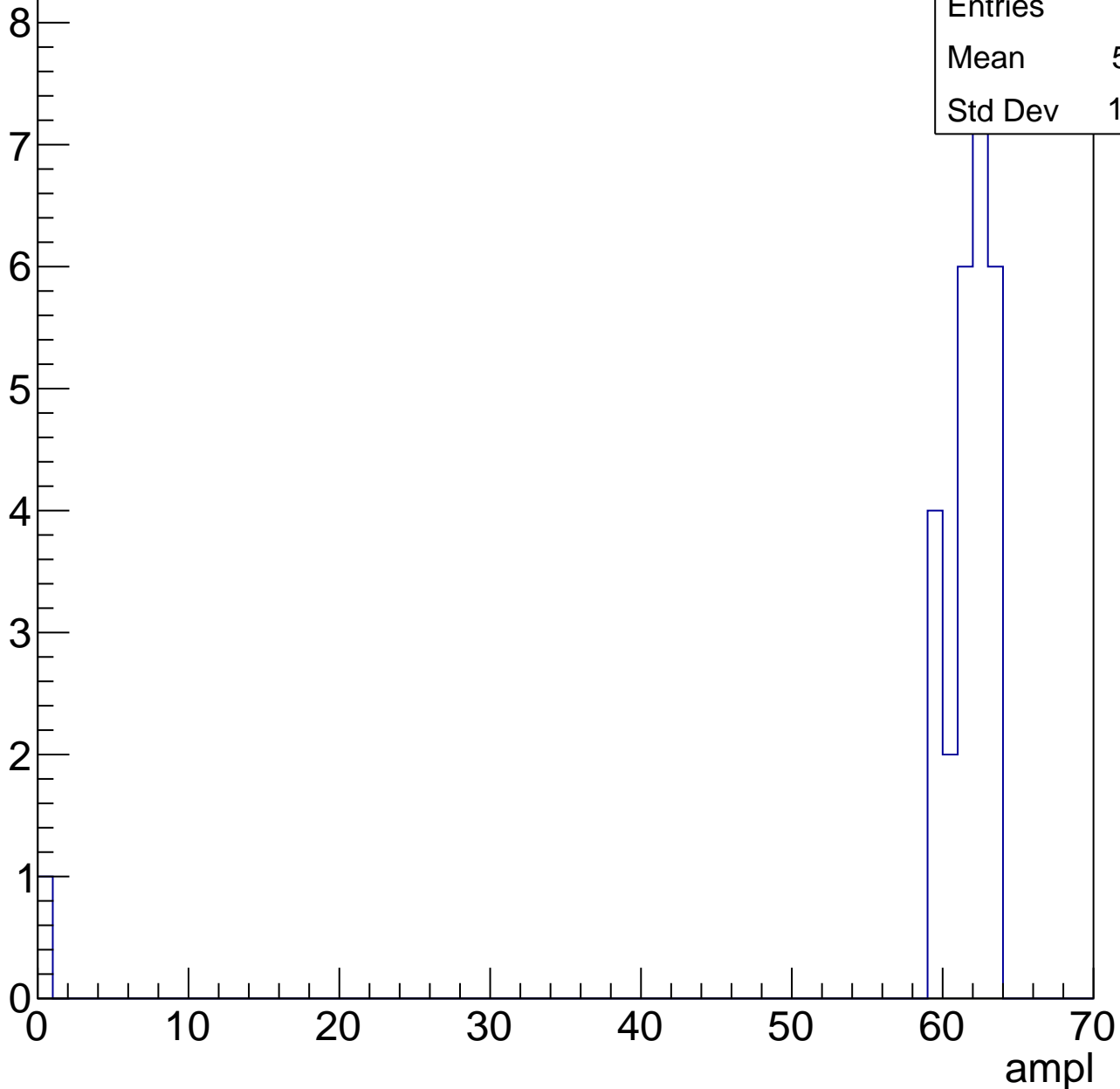


# B0L101S, U23-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	27
Mean	59.11
Std Dev	11.67

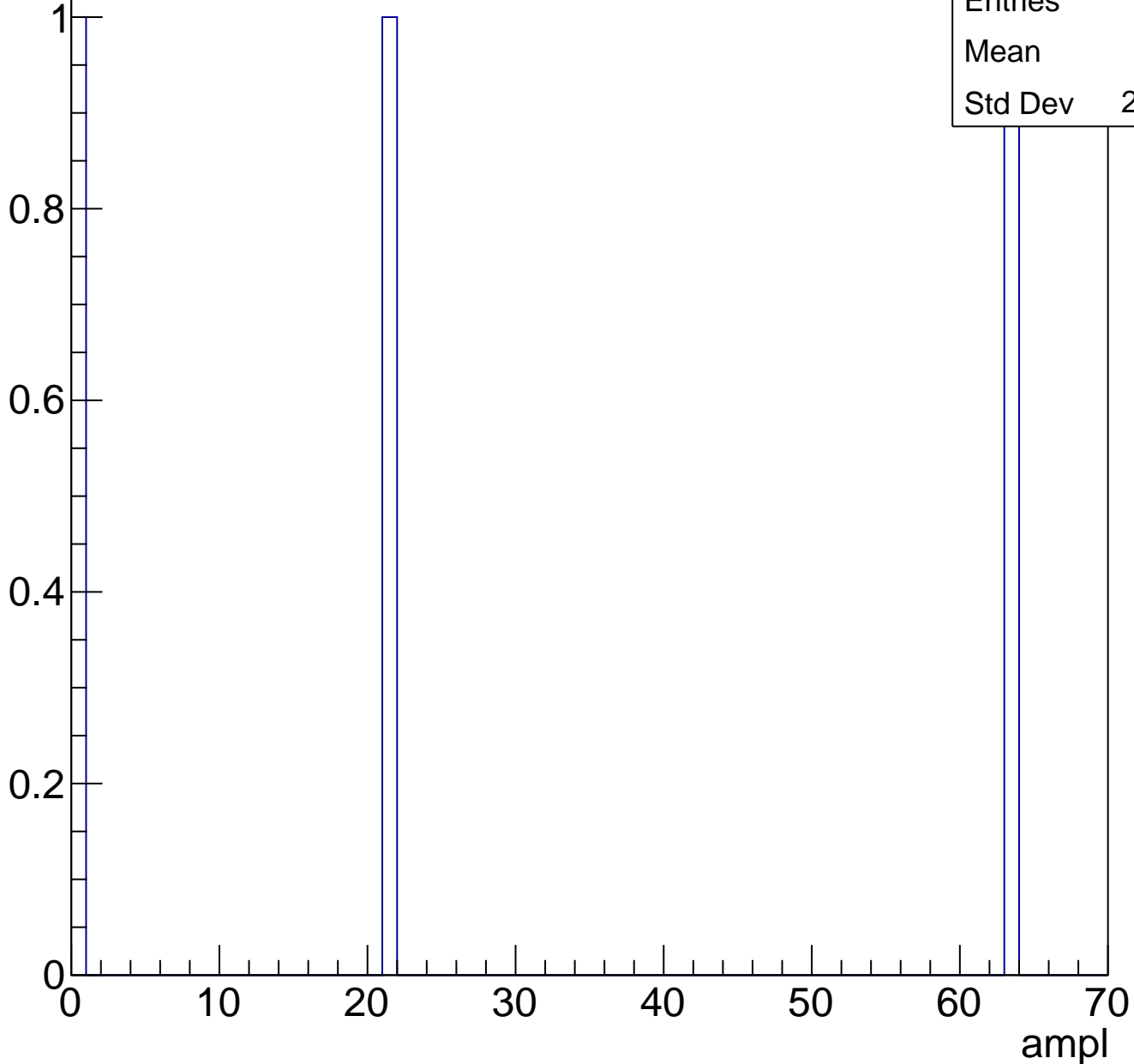




# B0L101S, U23-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	28
Std Dev	26.19

# B0L101S, U23-ch115, adc0

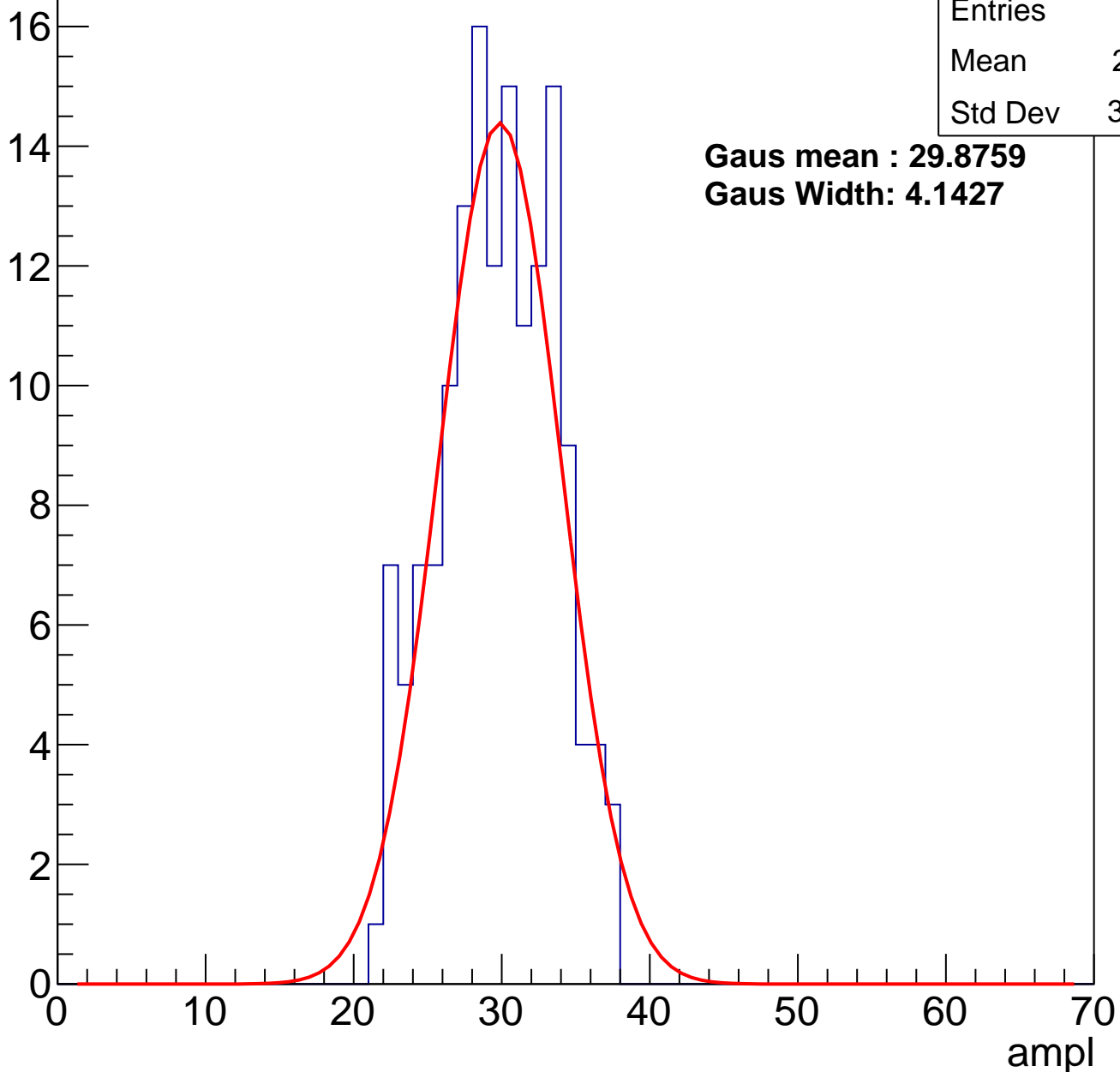
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	29.21
Std Dev	3.808

**Gaus mean : 29.8759**

**Gaus Width: 4.1427**

Entry



# B0L101S, U23-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	37.07
Std Dev	4.018

**Gaus mean : 37.4884**

**Gaus Width: 4.1460**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

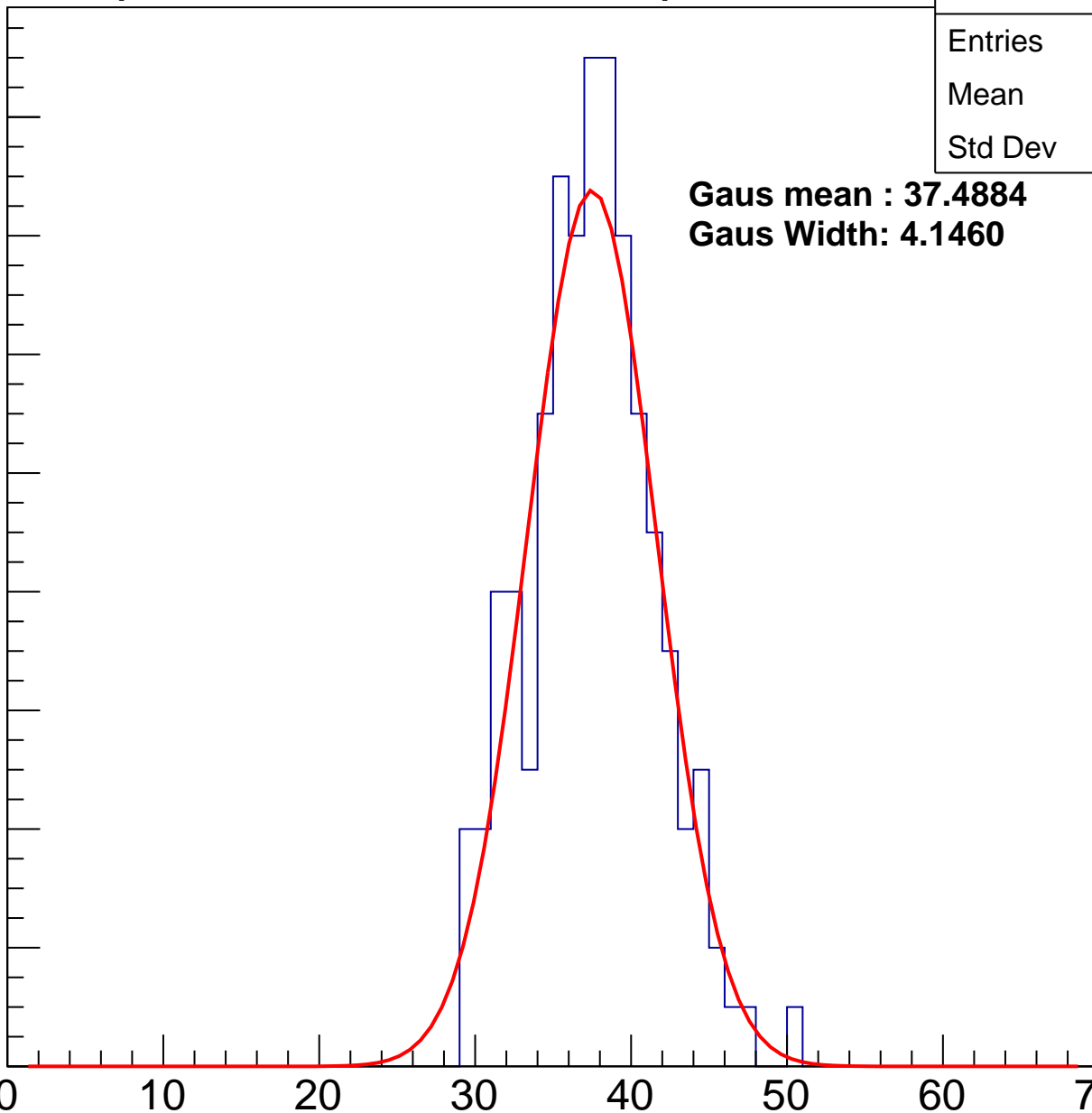
40

50

60

70

ampl



# B0L101S, U23-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

109

Mean

43.8

Std Dev

3.532

**Gaus mean : 43.9983**

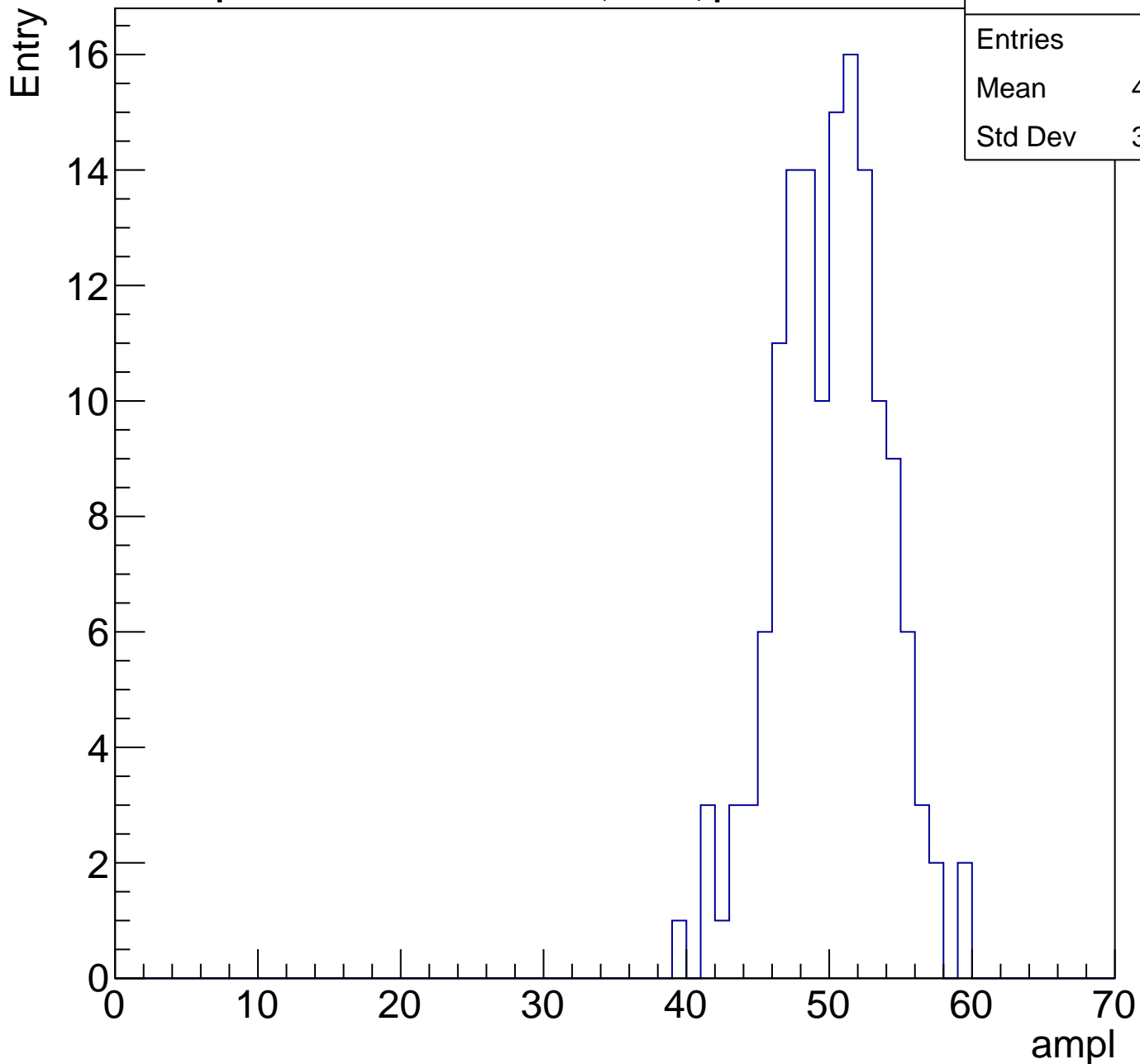
**Gaus Width: 3.7491**

0

# B0L101S, U23-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

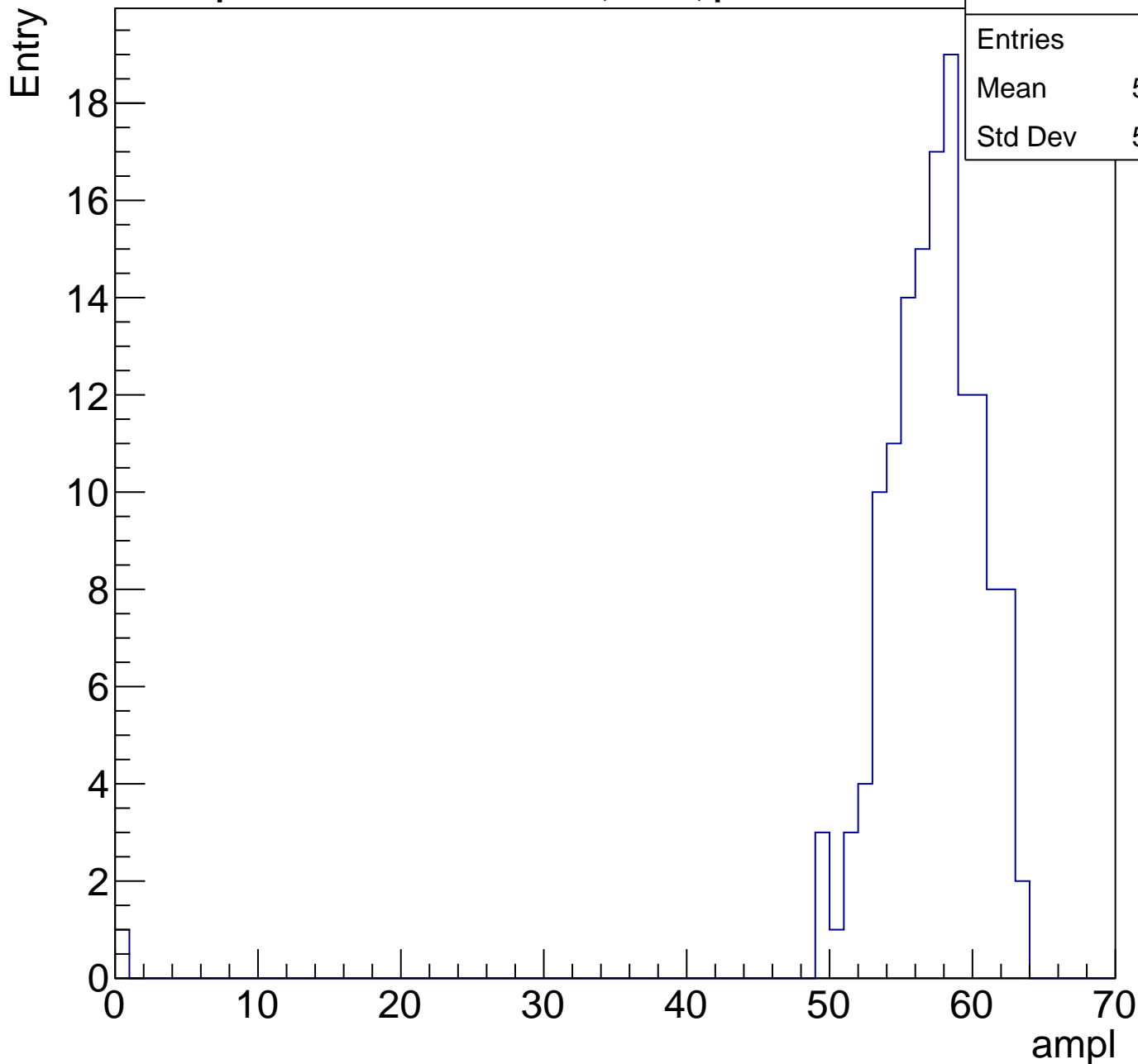
Entries	143
Mean	49.66
Std Dev	3.747



# B0L101S, U23-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	56.44
Std Dev	5.706



# B0L101S, U23-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

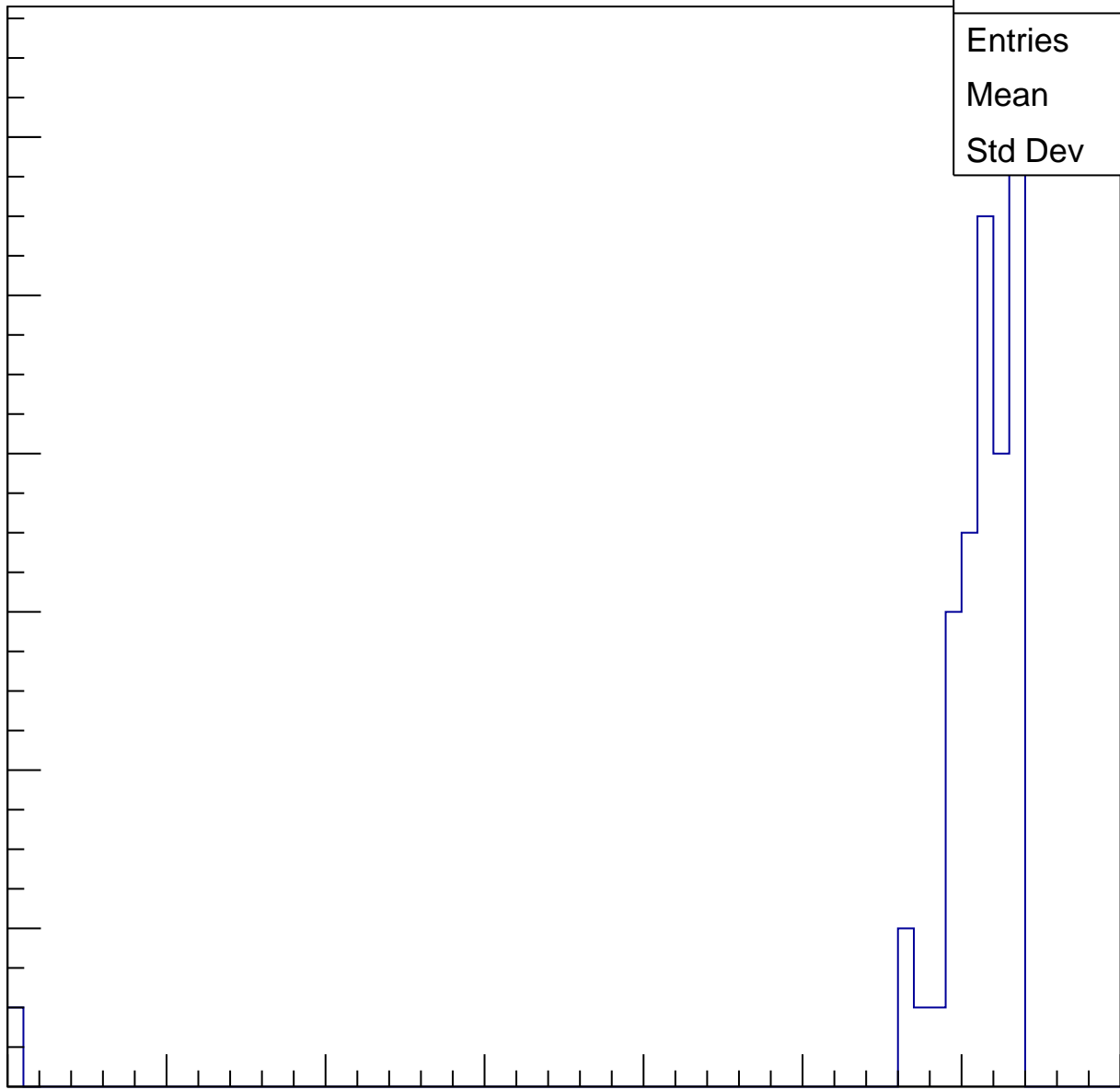
2

0

Entries	50
Mean	59.74
Std Dev	8.727

ampl

0 10 20 30 40 50 60 70

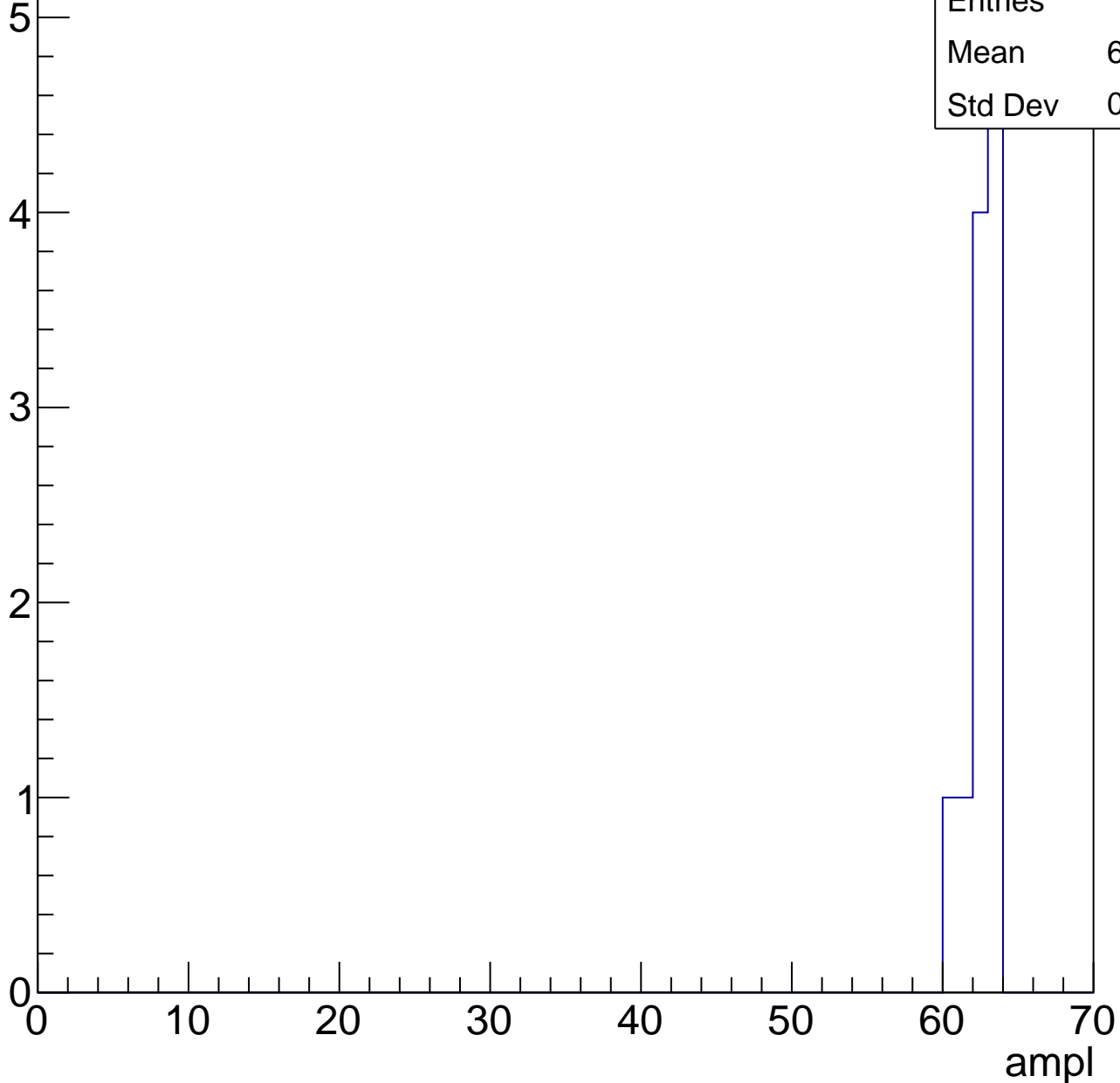


# B0L101S, U23-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.18
Std Dev	0.936





# B0L101S, U23-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L101S, U23-ch116, adc0

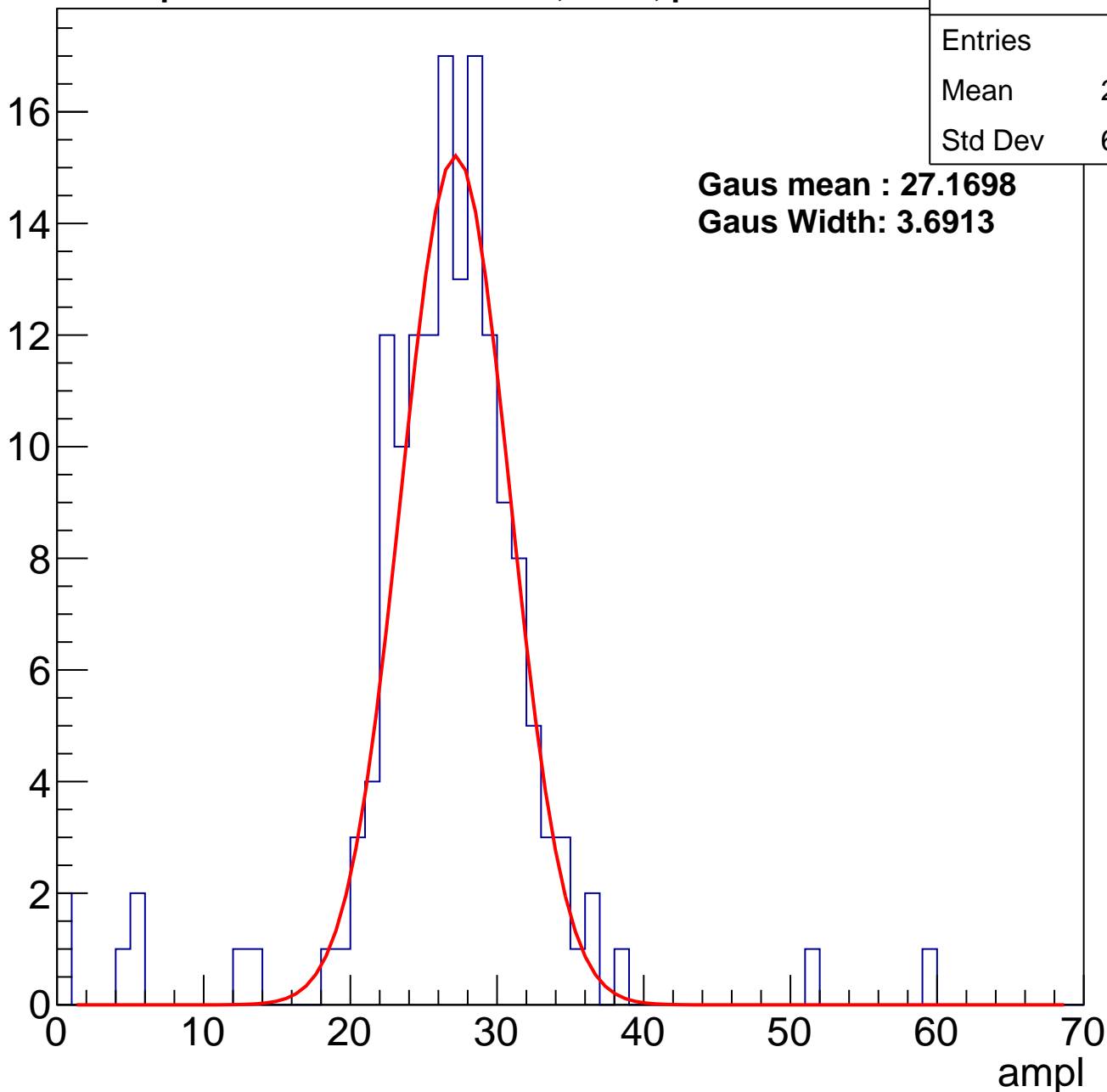
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	26.15
Std Dev	6.688

**Gaus mean : 27.1698**

**Gaus Width: 3.6913**

Entry



# B0L101S, U23-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	33.45
Std Dev	4.667

**Gaus mean : 33.9062**

**Gaus Width: 3.7498**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

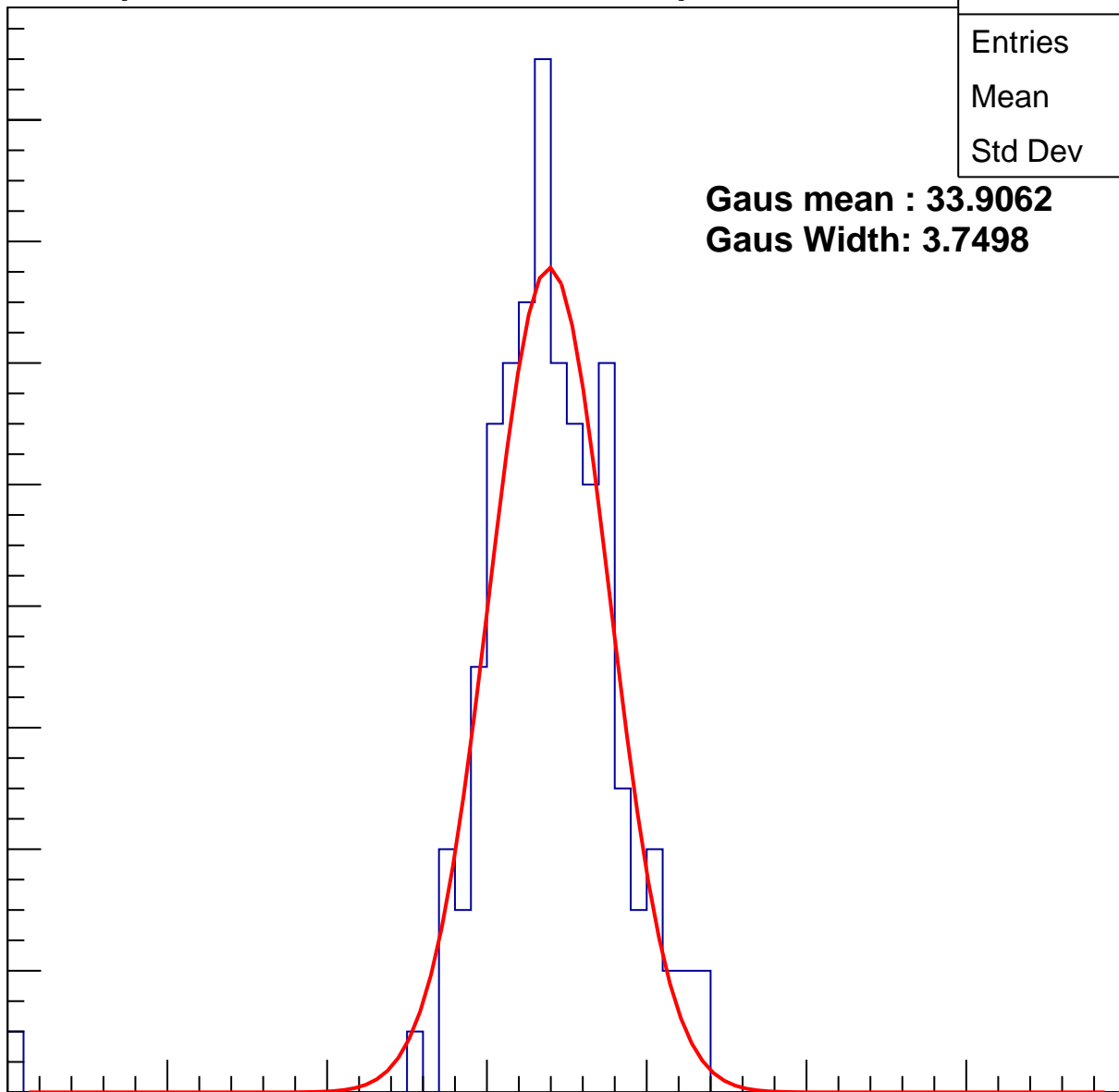
40

50

60

70

ampl



# B0L101S, U23-ch116, adc2

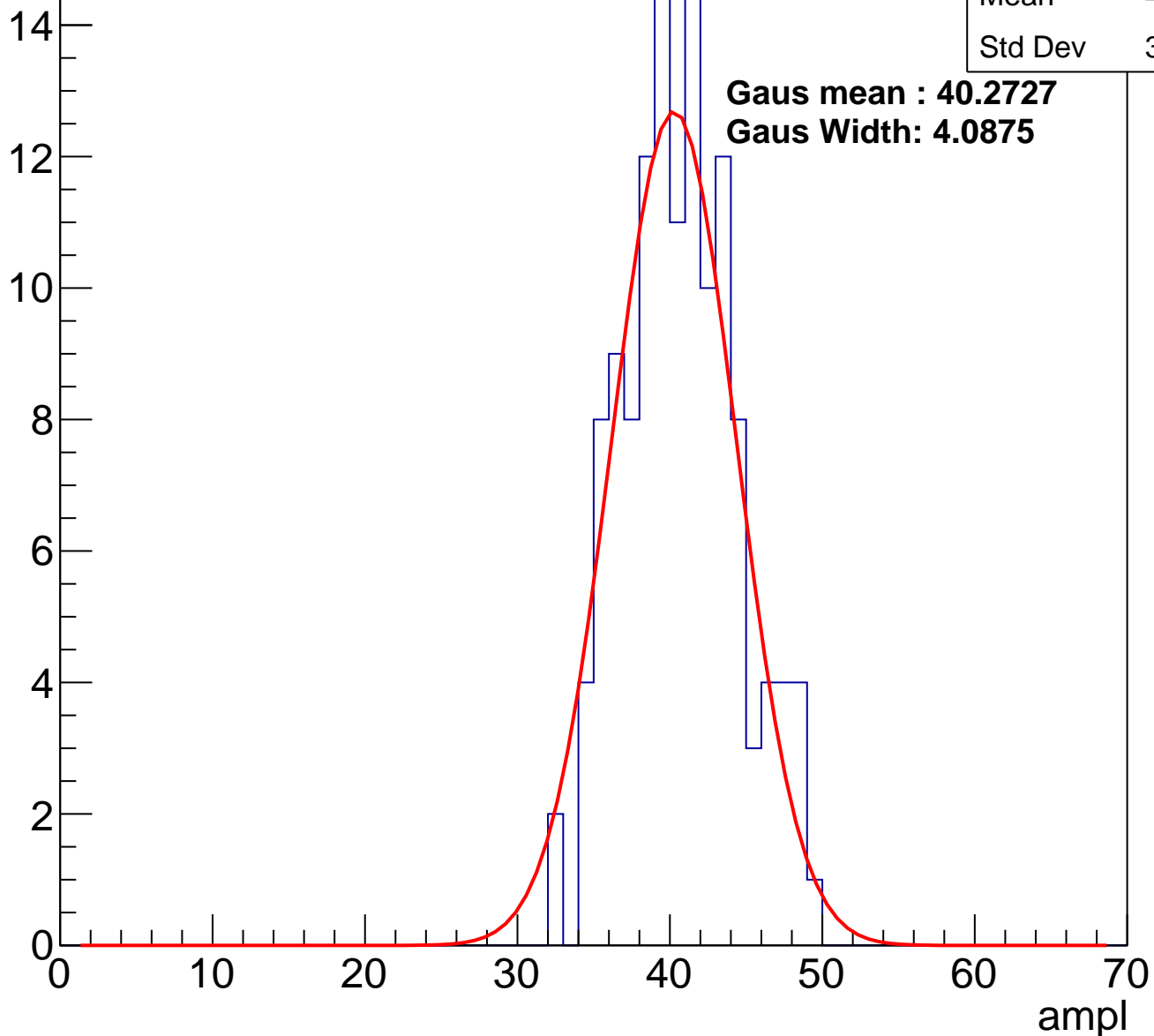
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	40.25
Std Dev	3.707

**Gaus mean : 40.2727**

**Gaus Width: 4.0875**

Entry



# B0L101S, U23-ch116, adc3

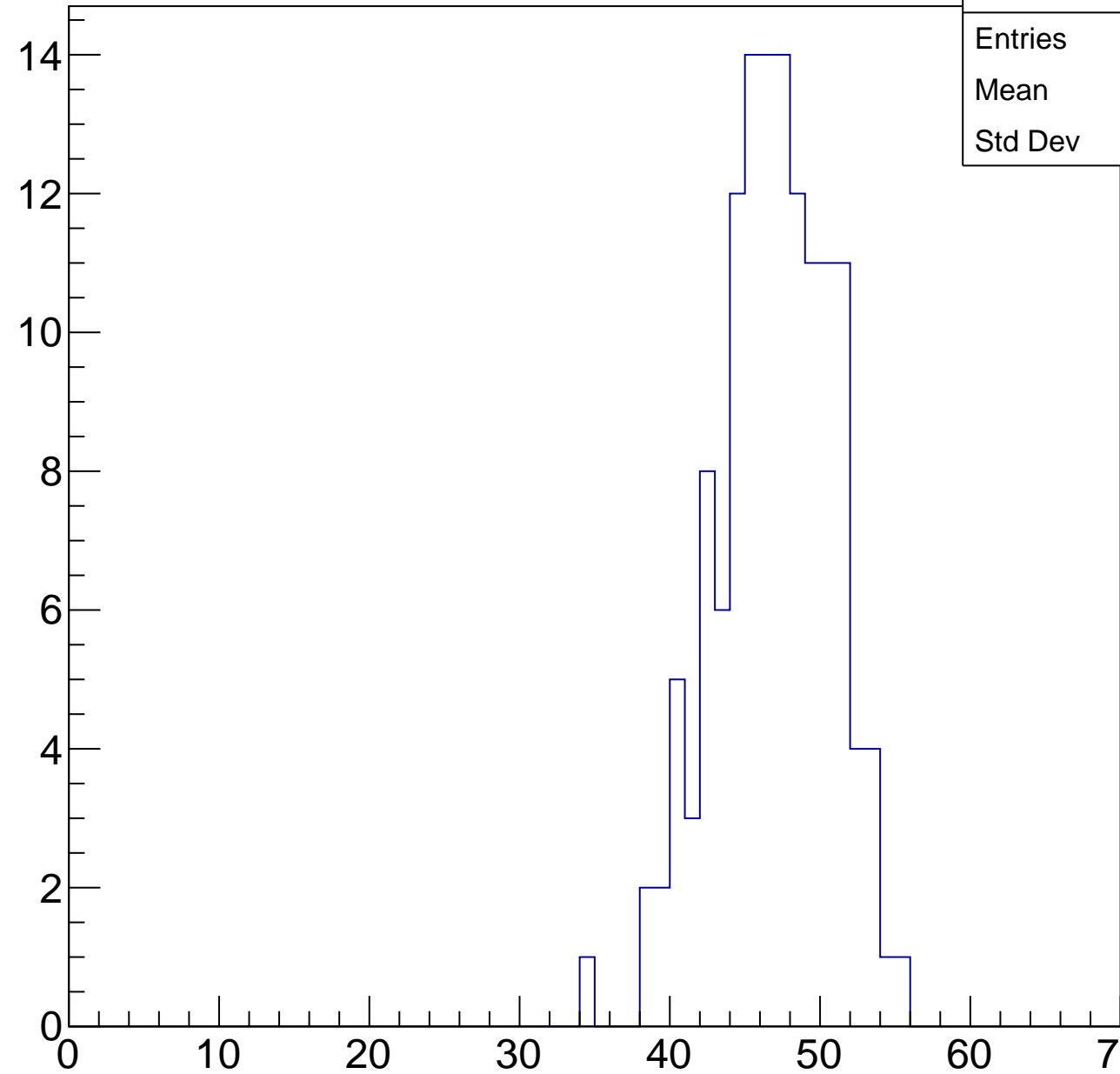
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	136
Mean	46.47
Std Dev	3.773

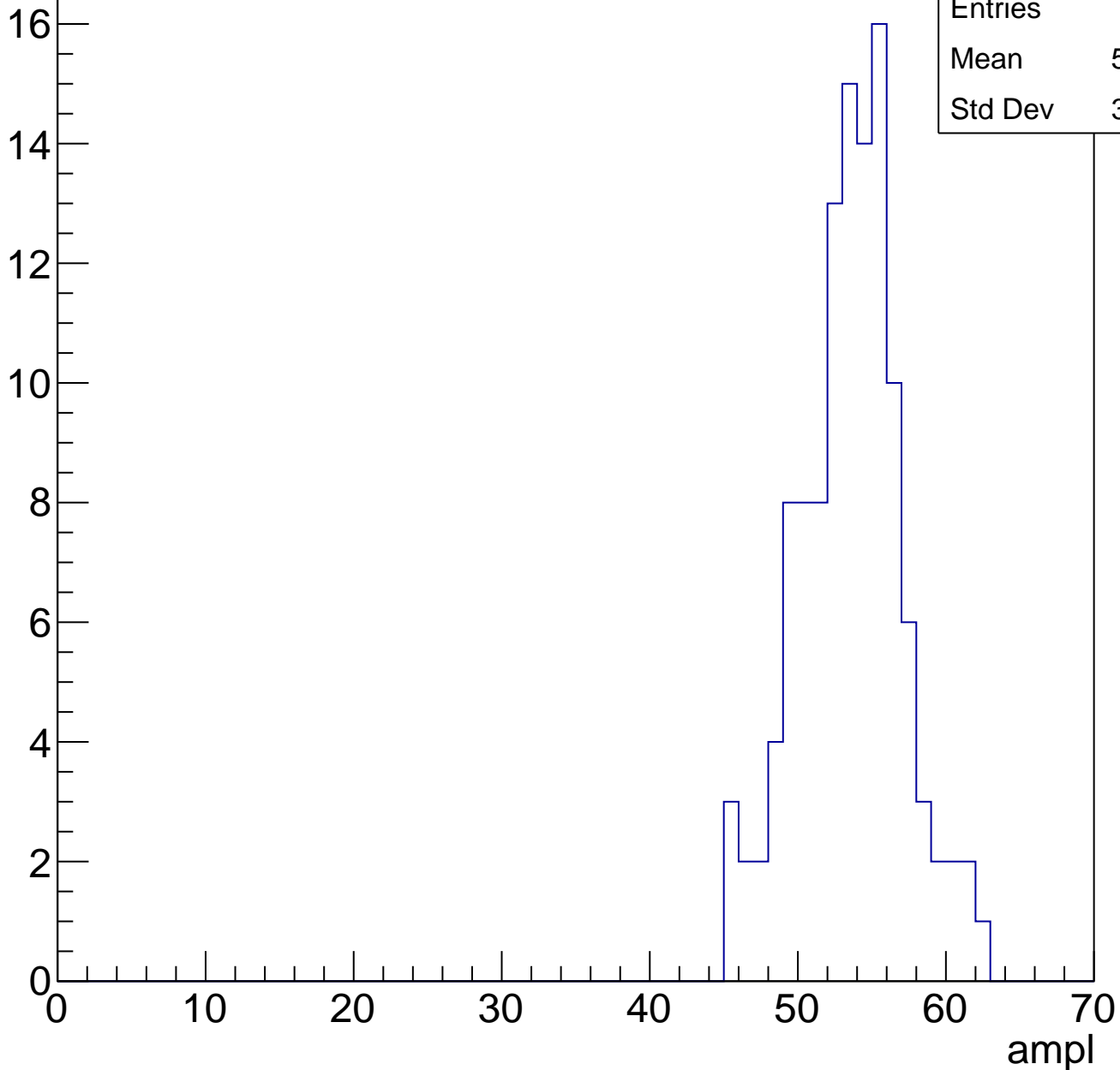
ampl



# B0L101S, U23-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch116, adc5

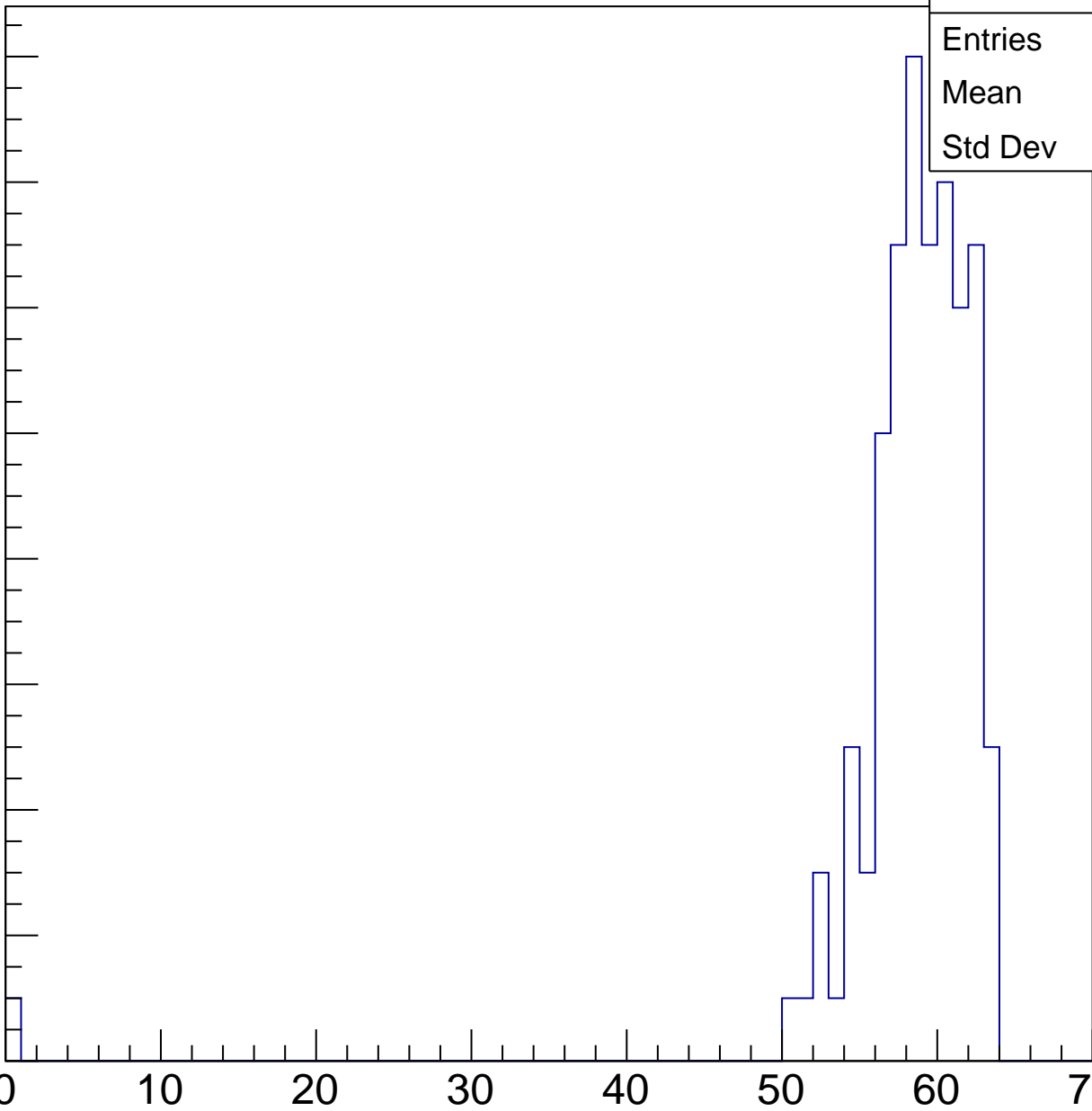
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	111
Mean	57.96
Std Dev	6.209

ampl



# B0L101S, U23-ch116, adc6

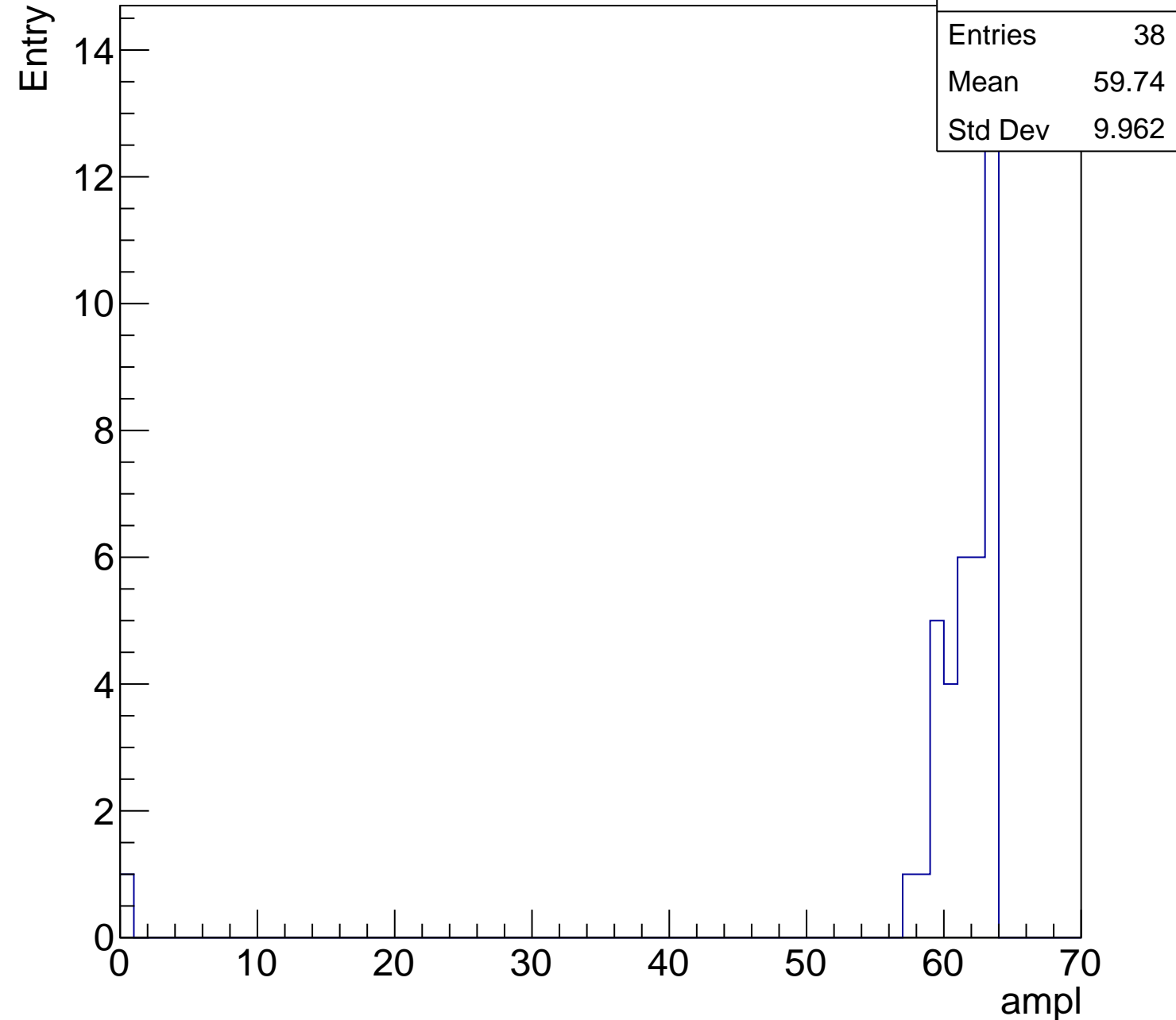
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	38
Mean	59.74
Std Dev	9.962

ampl





# B0L101S, U23-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch117, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	28.84
Std Dev	3.902

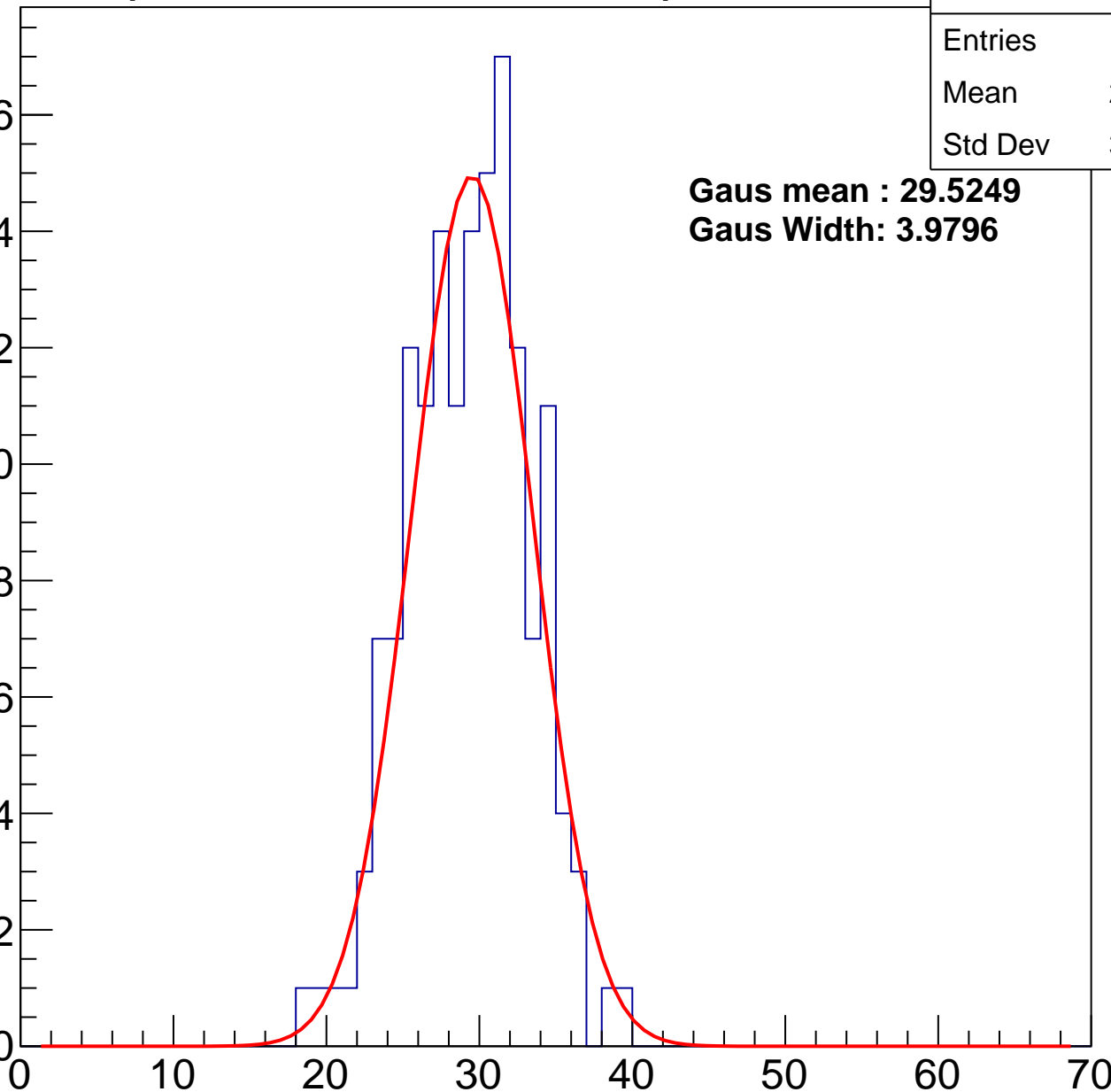
**Gaus mean : 29.5249**

**Gaus Width: 3.9796**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U23-ch117, adc1

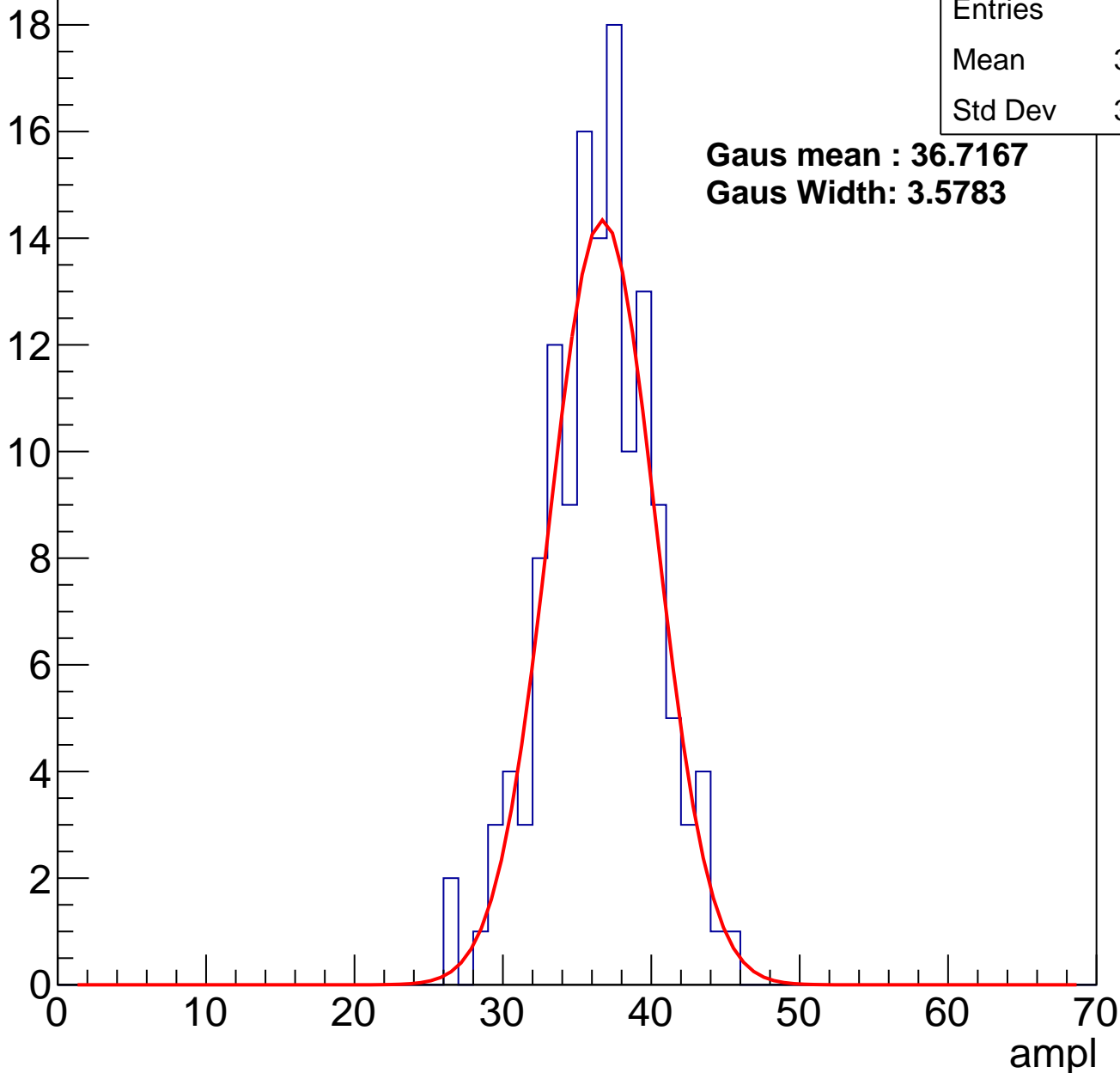
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	36.08
Std Dev	3.648

**Gaus mean : 36.7167**

**Gaus Width: 3.5783**

Entry



# B0L101S, U23-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

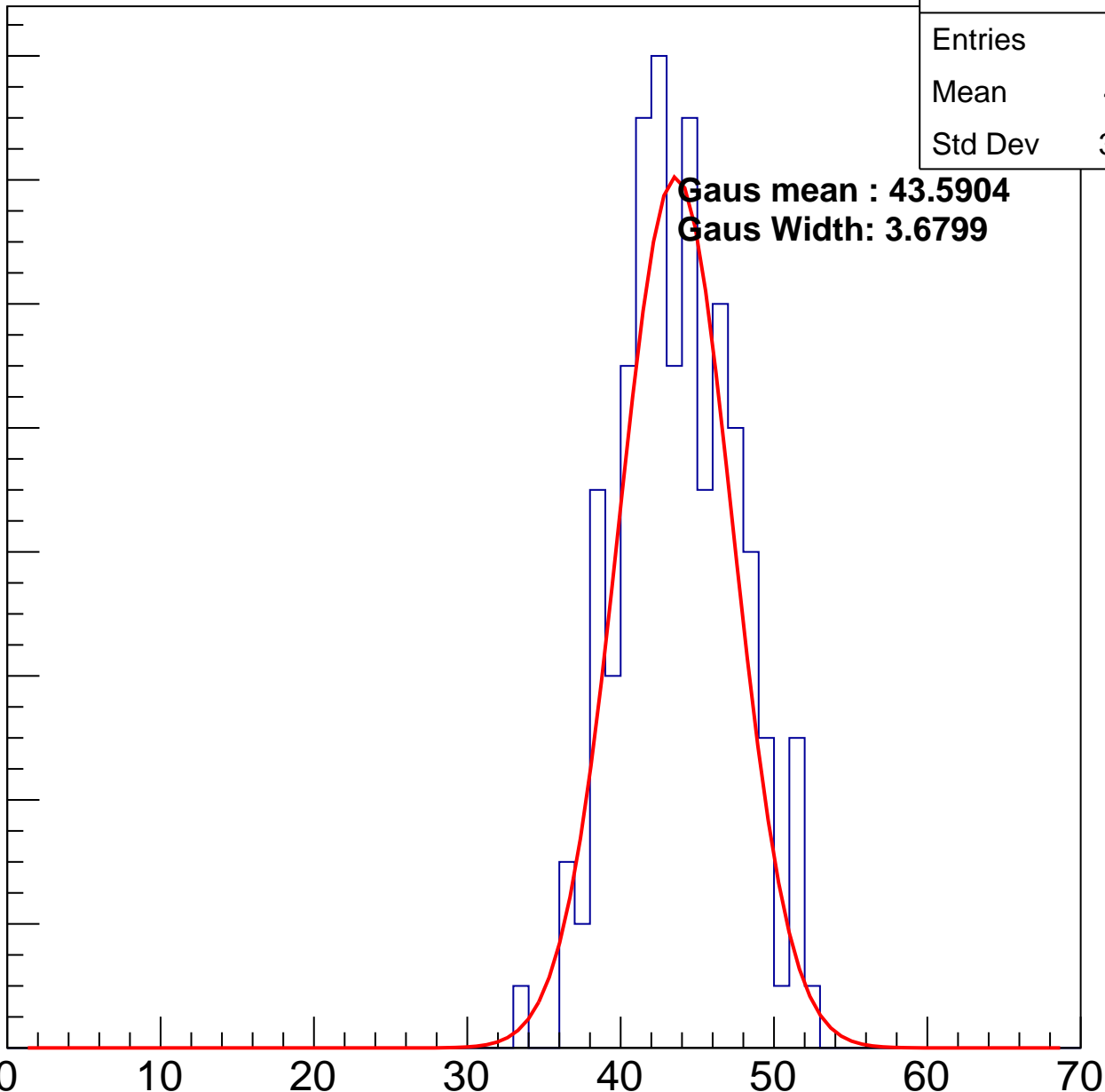
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	140
Mean	43.31
Std Dev	3.732

**Gaus mean : 43.5904**

**Gaus Width: 3.6799**

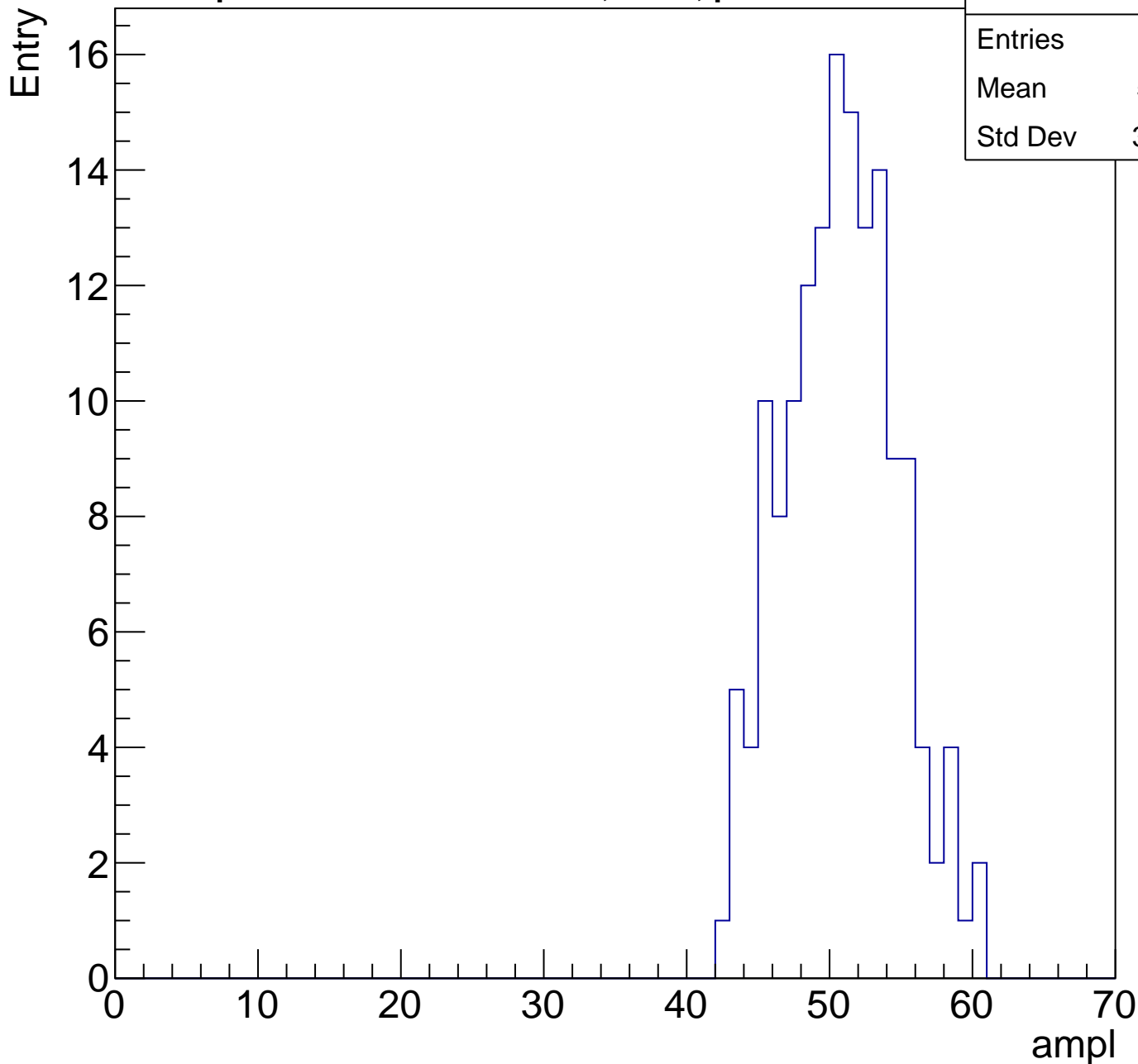
ampl



# B0L101S, U23-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	50.31
Std Dev	3.897



# B0L101S, U23-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	72
Mean	56.29
Std Dev	3.116

Entry

10

8

6

4

2

0

0

10

20

30

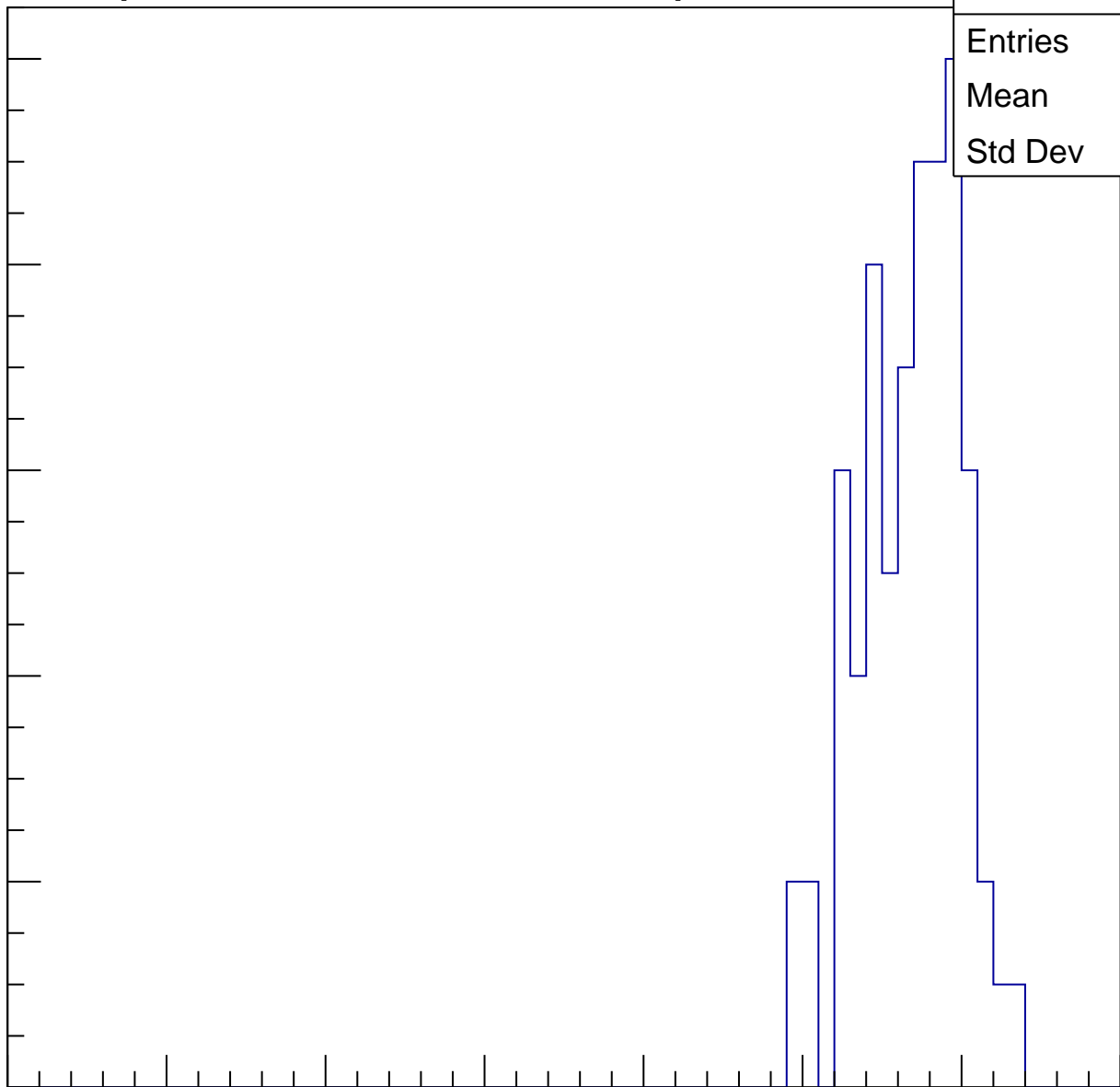
40

50

60

ampl

70



# B0L101S, U23-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

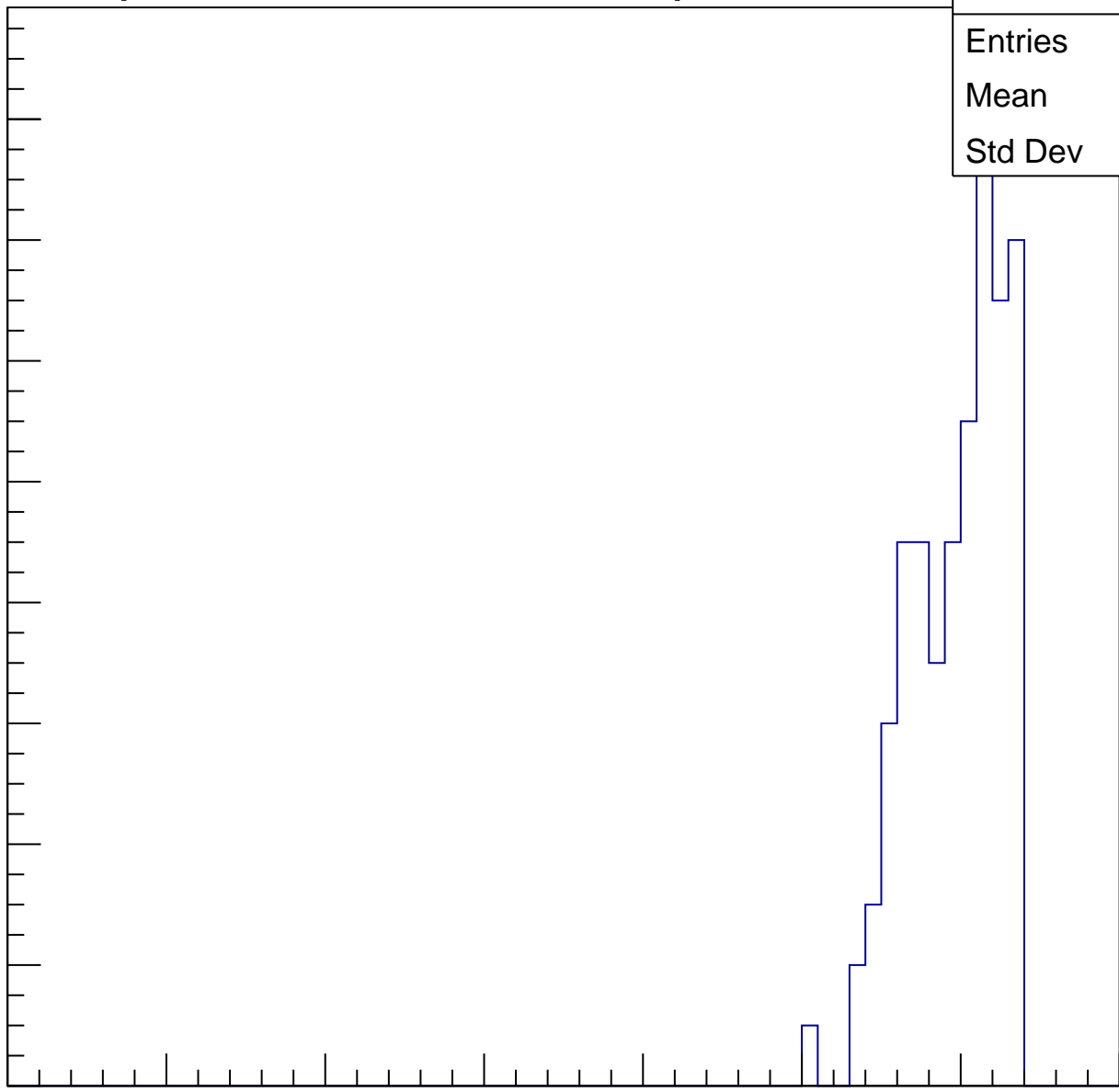
Entries	101
Mean	59.28
Std Dev	2.918

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

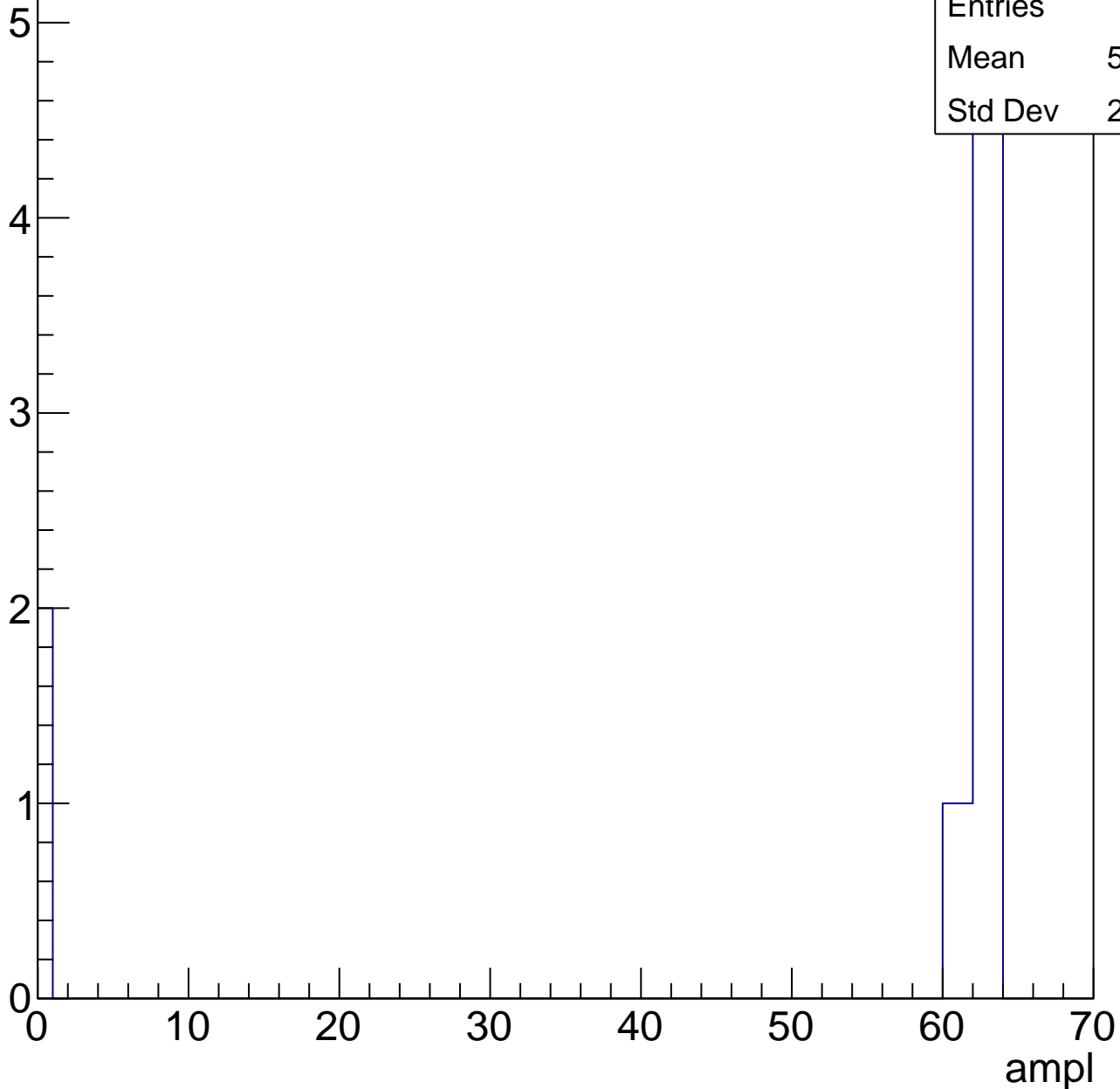


# B0L101S, U23-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	14
Mean	53.29
Std Dev	21.77

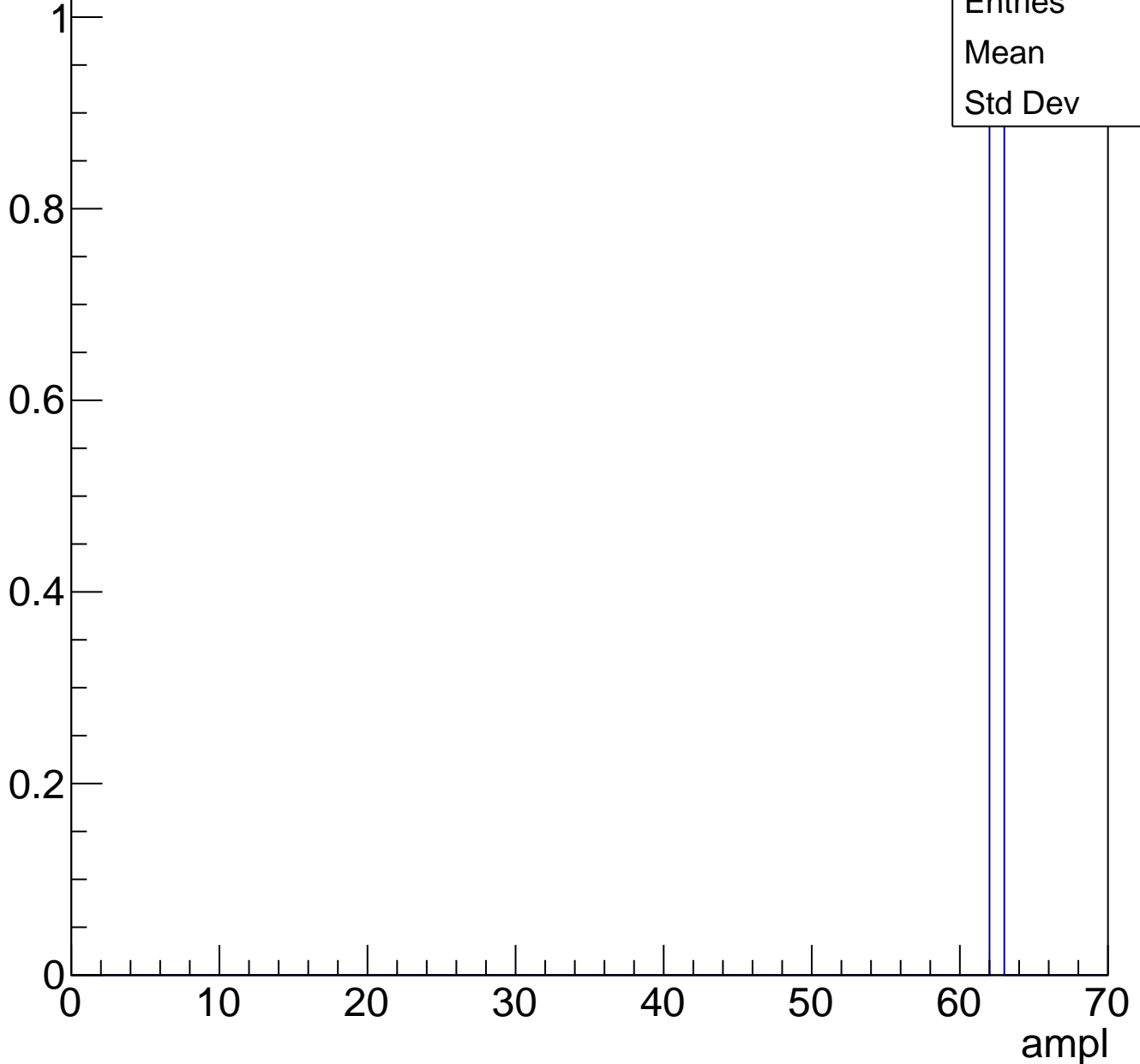




# B0L101S, U23-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch118, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	27.12
Std Dev	5.916

**Gaus mean : 27.8629**

**Gaus Width: 4.3262**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

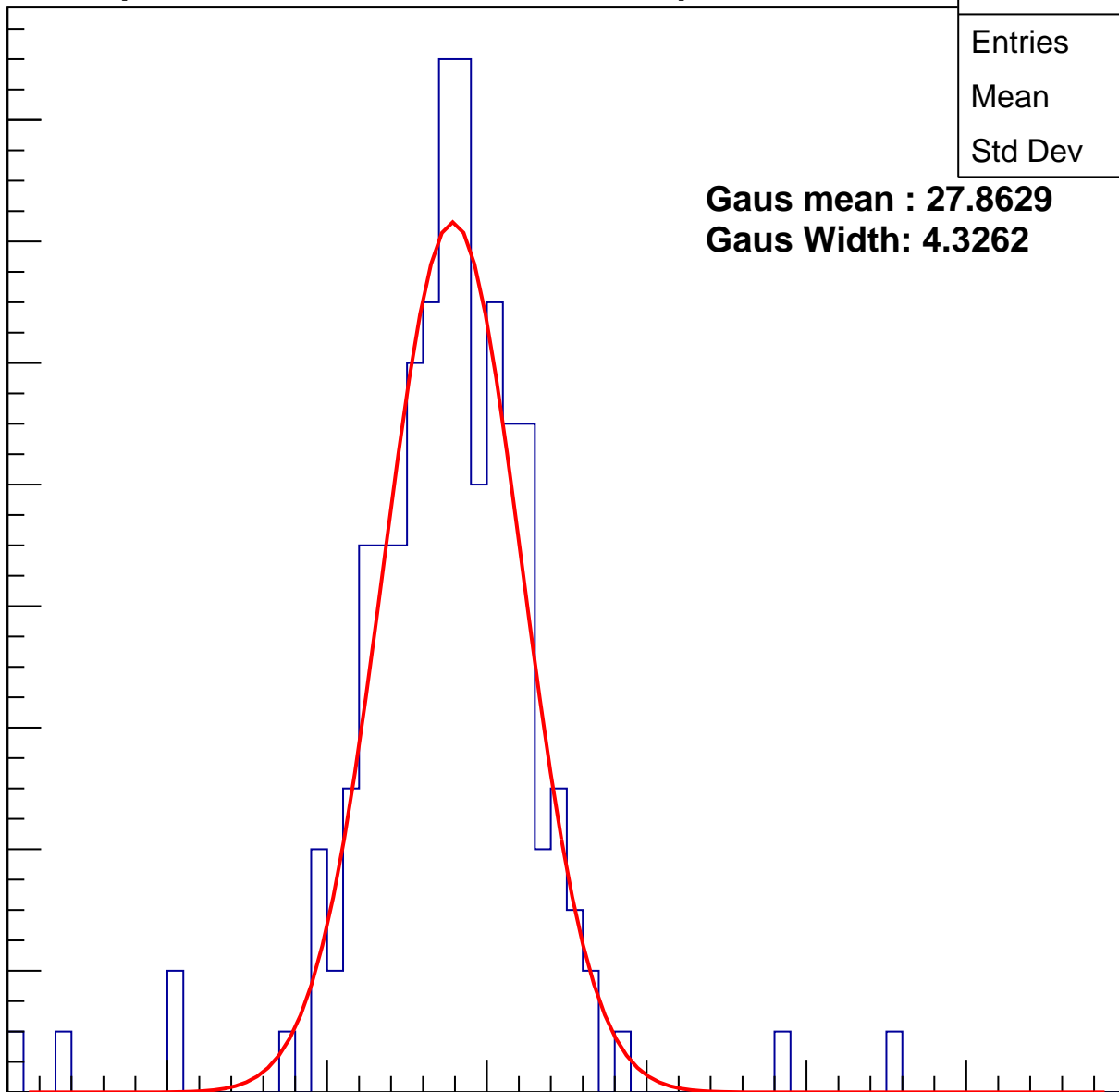
40

50

60

70

ampl



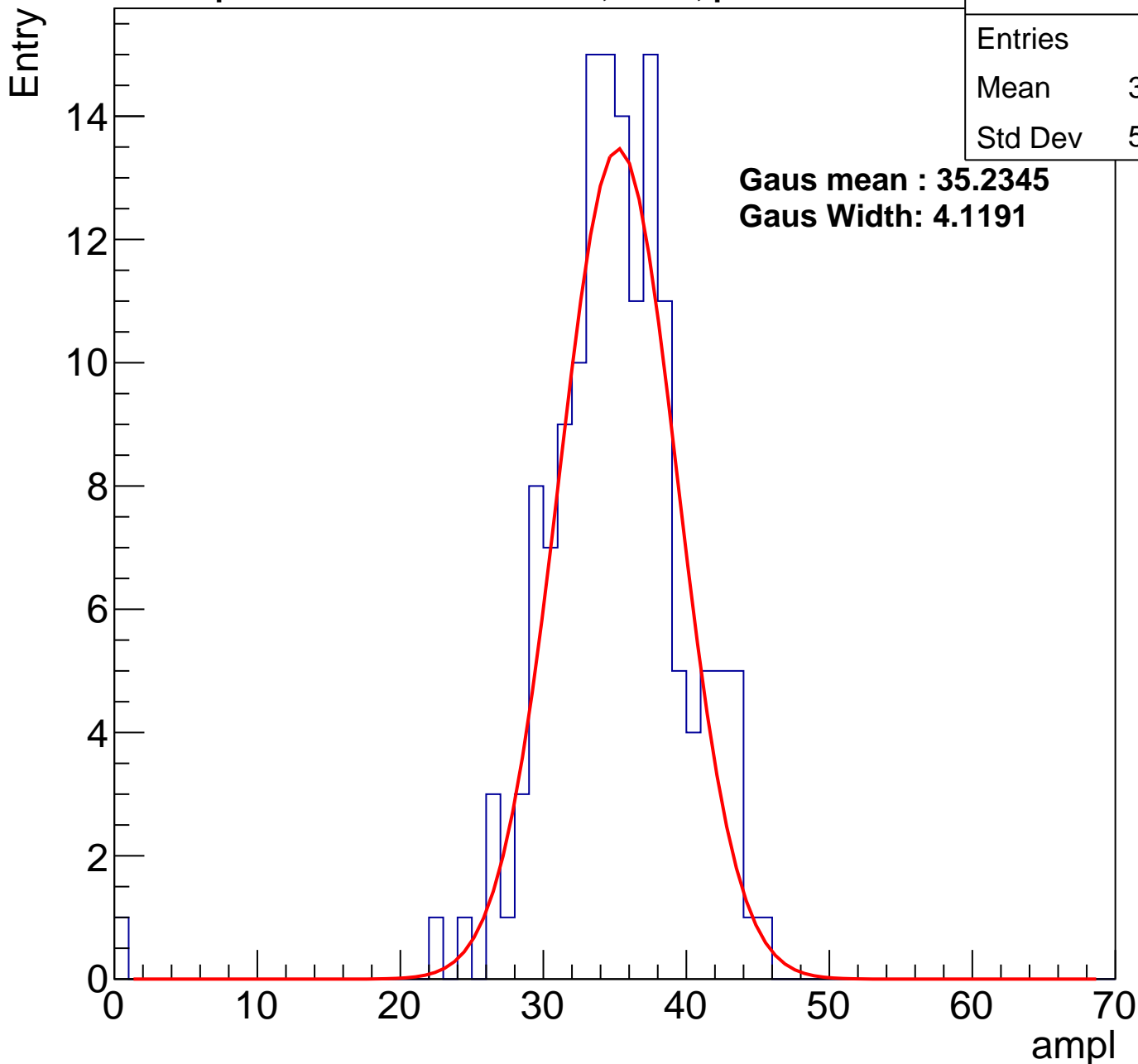
# B0L101S, U23-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	34.53
Std Dev	5.136

**Gaus mean : 35.2345**

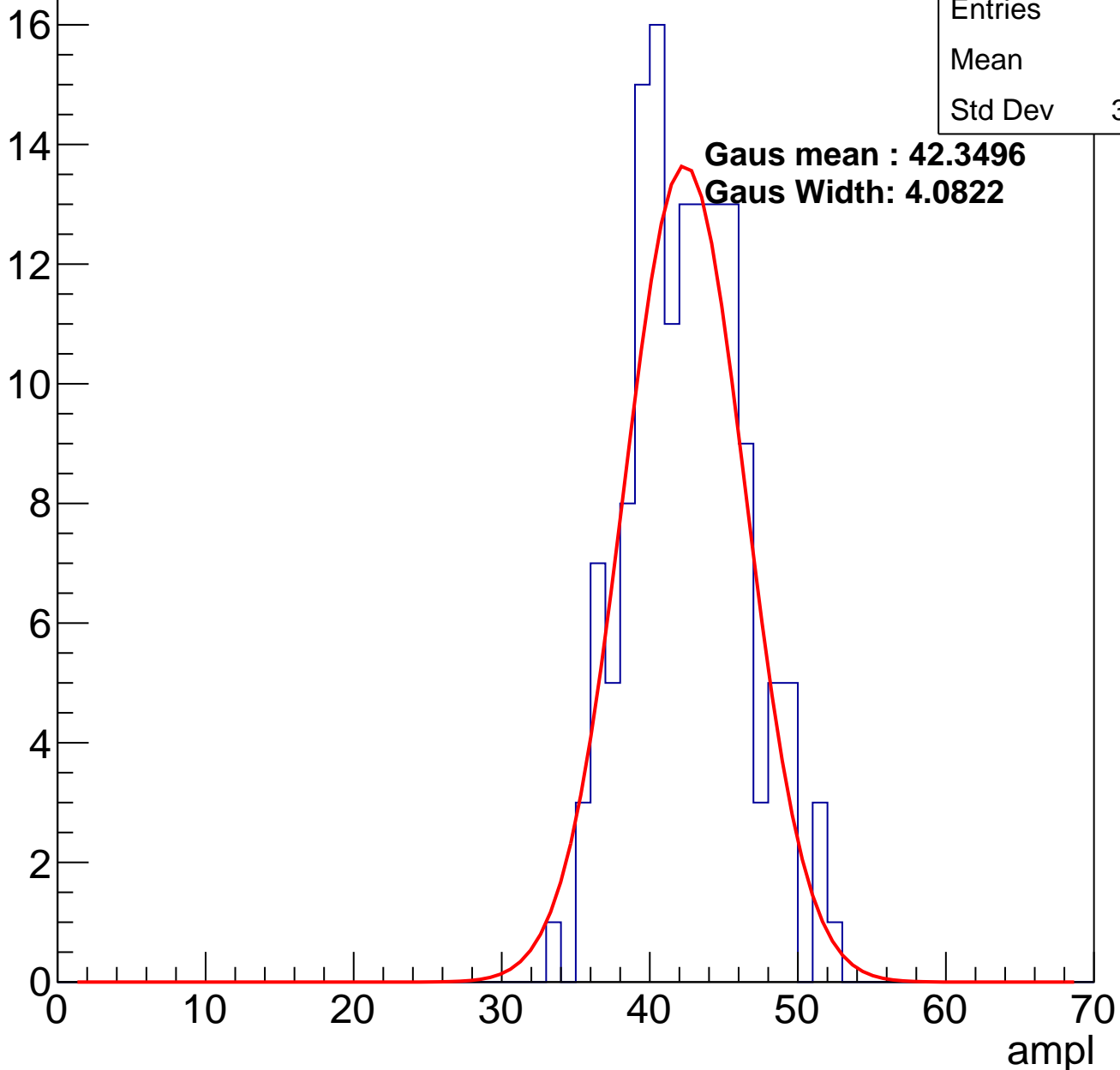
**Gaus Width: 4.1191**



# B0L101S, U23-ch118, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

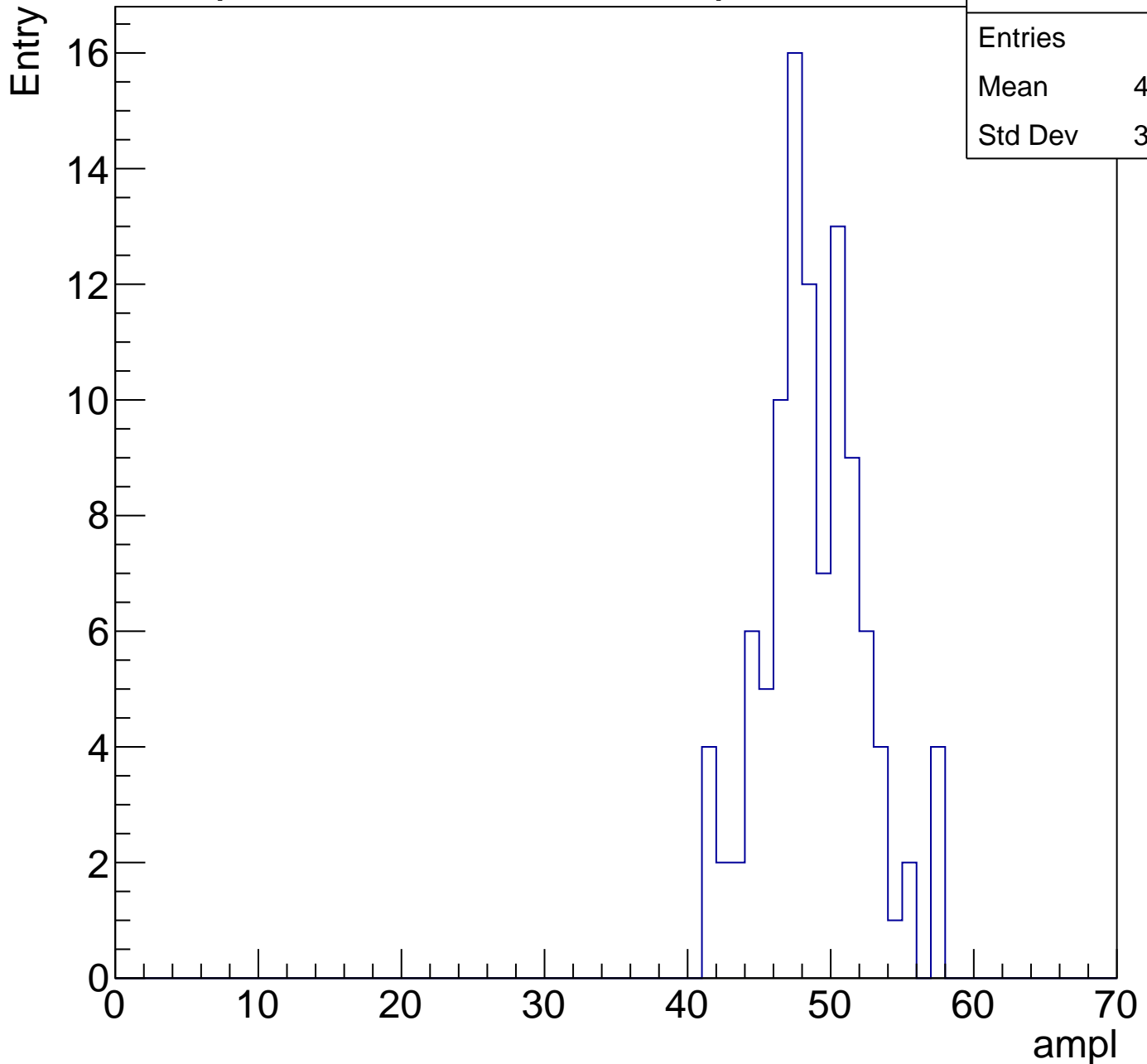
Entry



# B0L101S, U23-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	103
Mean	48.34
Std Dev	3.549



# B0L101S, U23-ch118, adc4

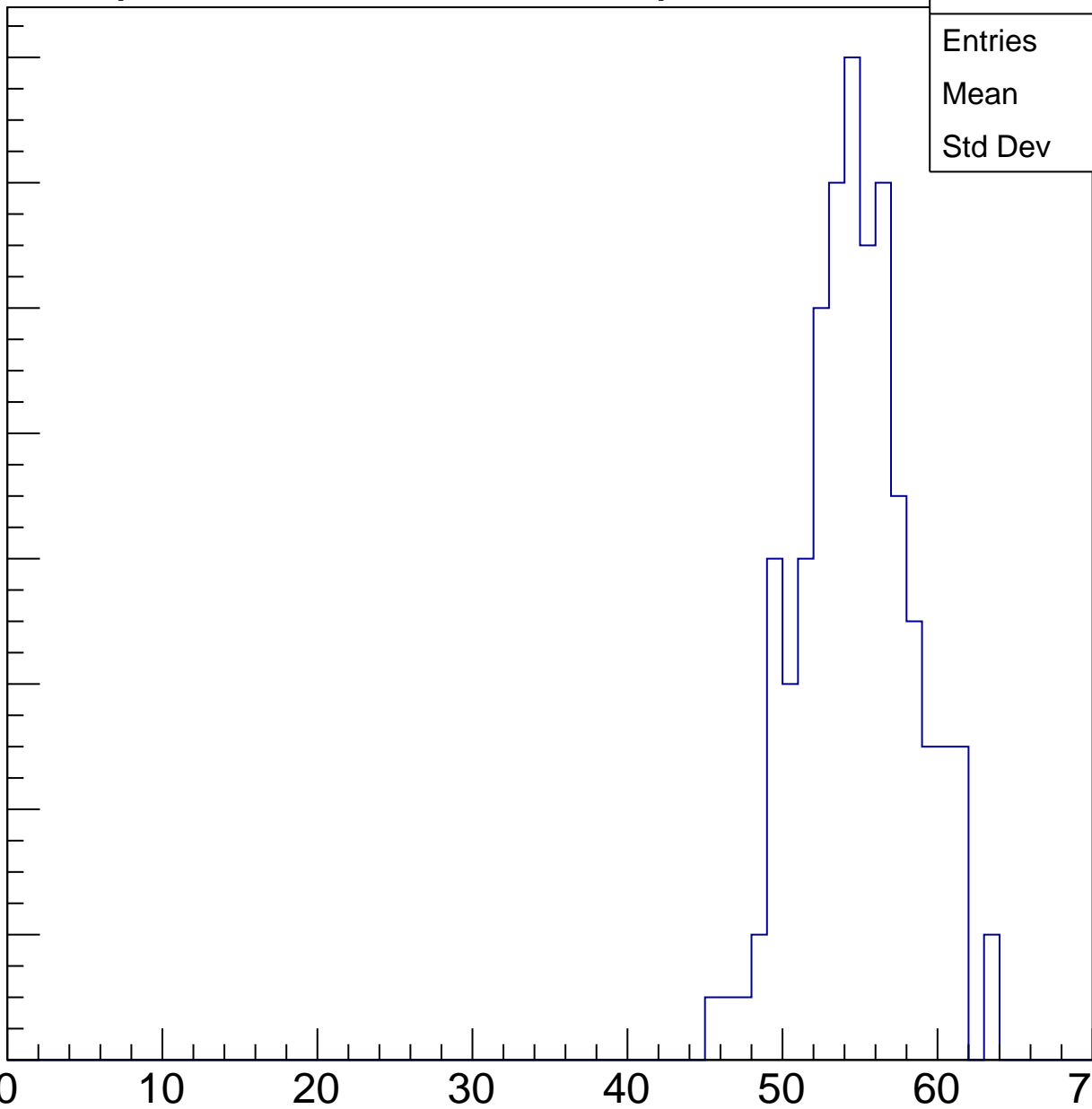
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	129
Mean	54.33
Std Dev	3.583

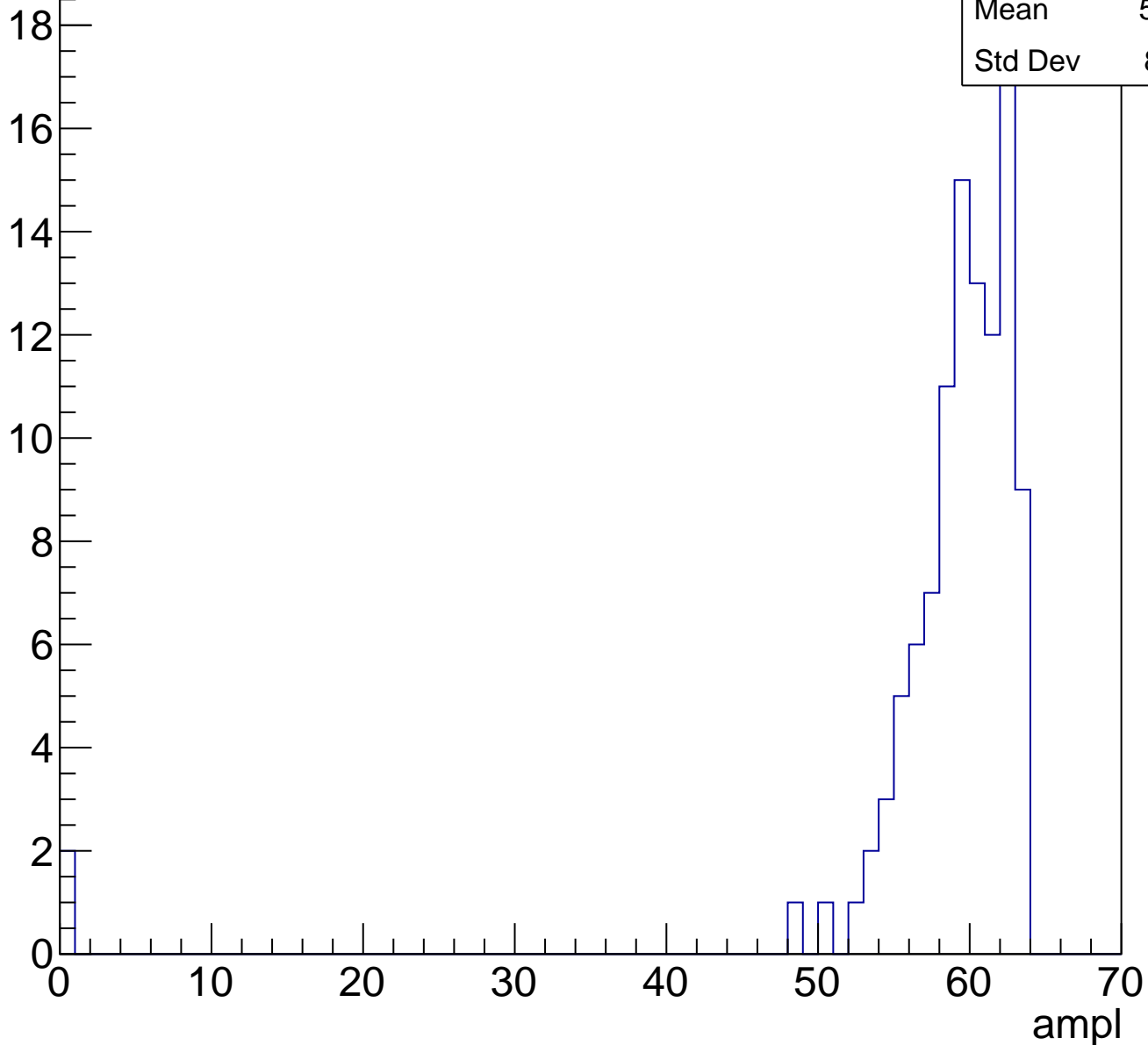
ampl



# B0L101S, U23-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

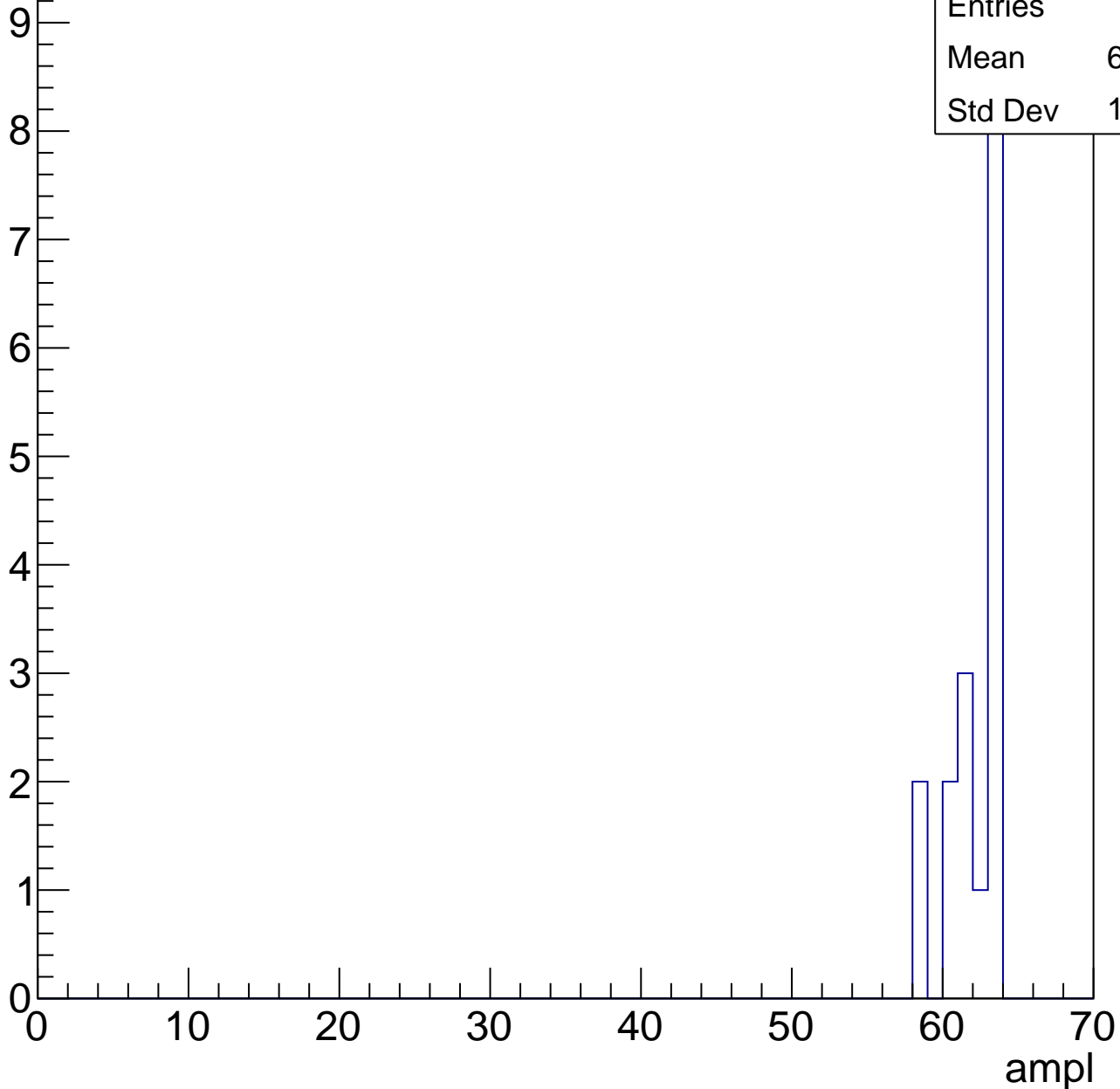


# B0L101S, U23-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	61.65
Std Dev	1.713





# B0L101S, U23-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch119, adc0

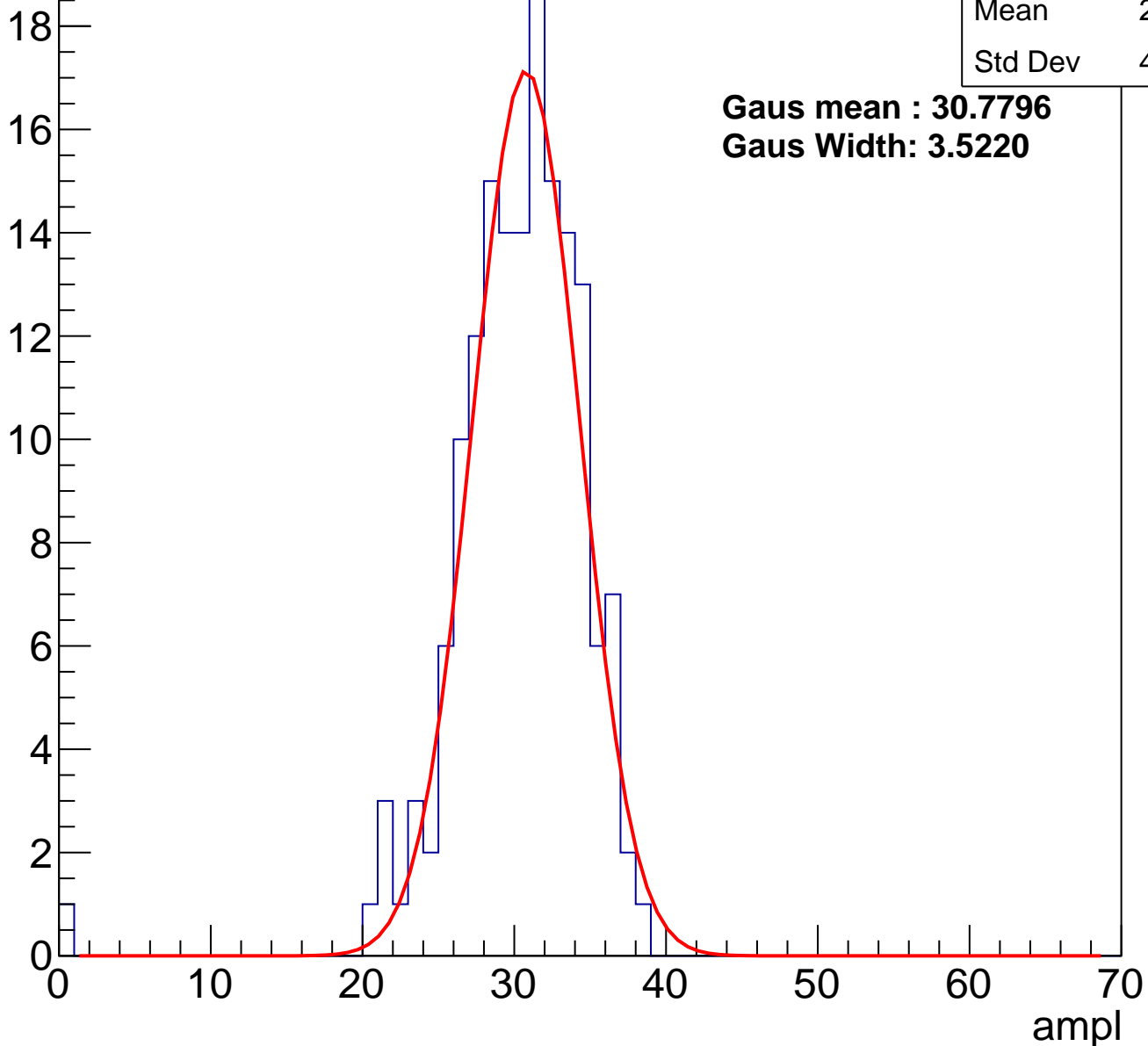
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	29.87
Std Dev	4.338

**Gaus mean : 30.7796**

**Gaus Width: 3.5220**

Entry



# B0L101S, U23-ch119, adc1

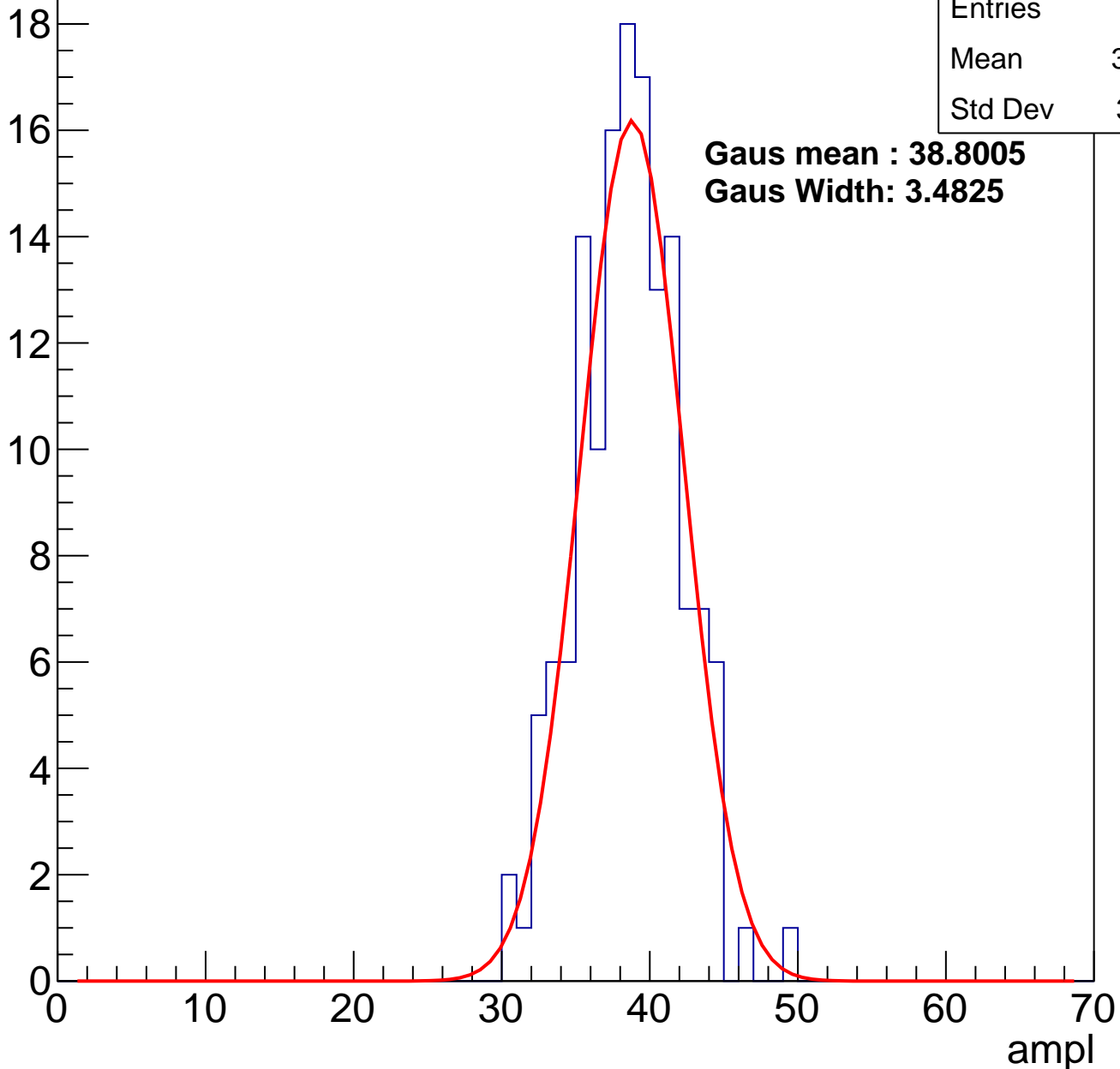
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	38.12
Std Dev	3.401

**Gaus mean : 38.8005**

**Gaus Width: 3.4825**

Entry



# B0L101S, U23-ch119, adc2

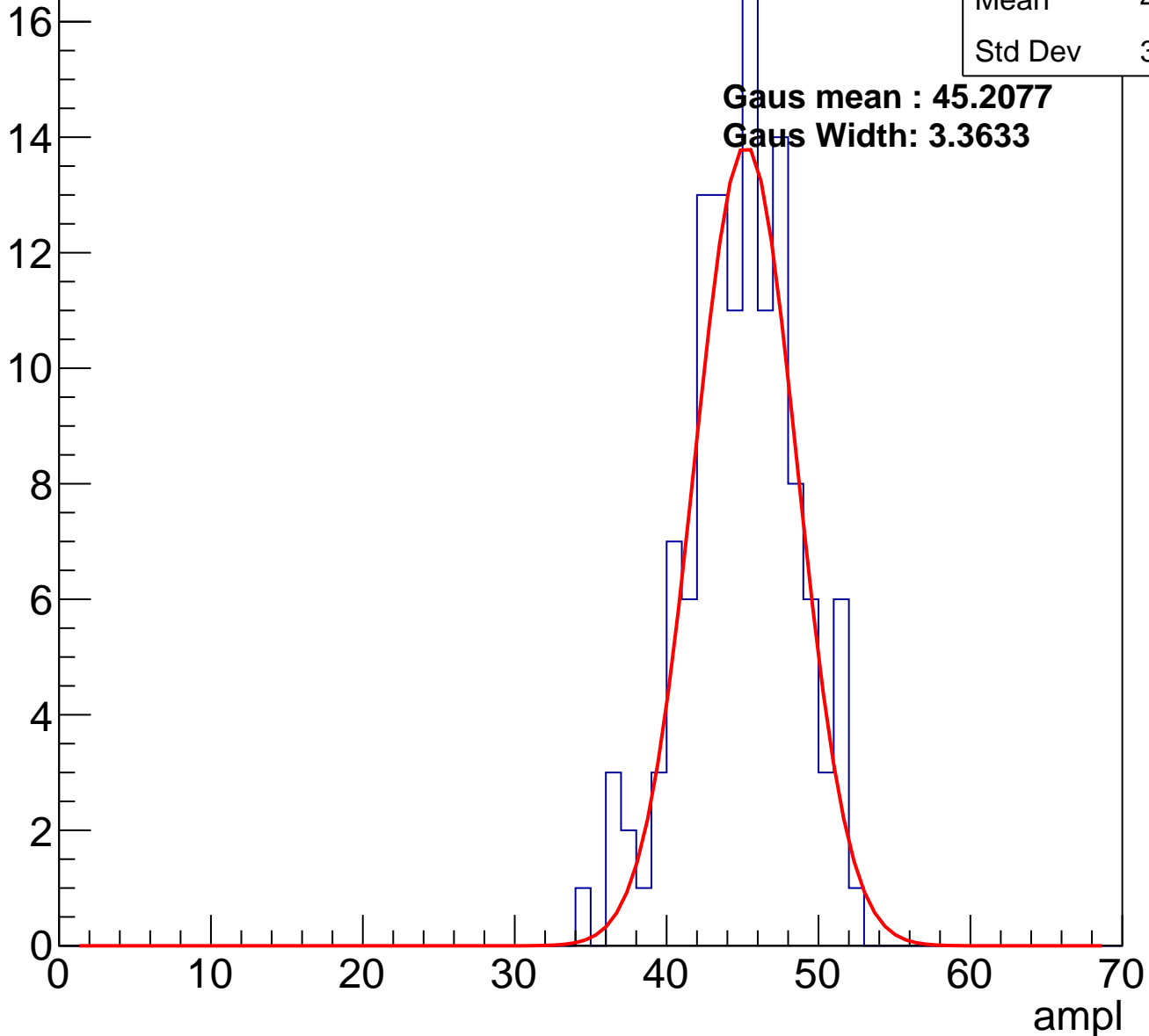
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	126
Mean	44.45
Std Dev	3.605

**Gaus mean : 45.2077**

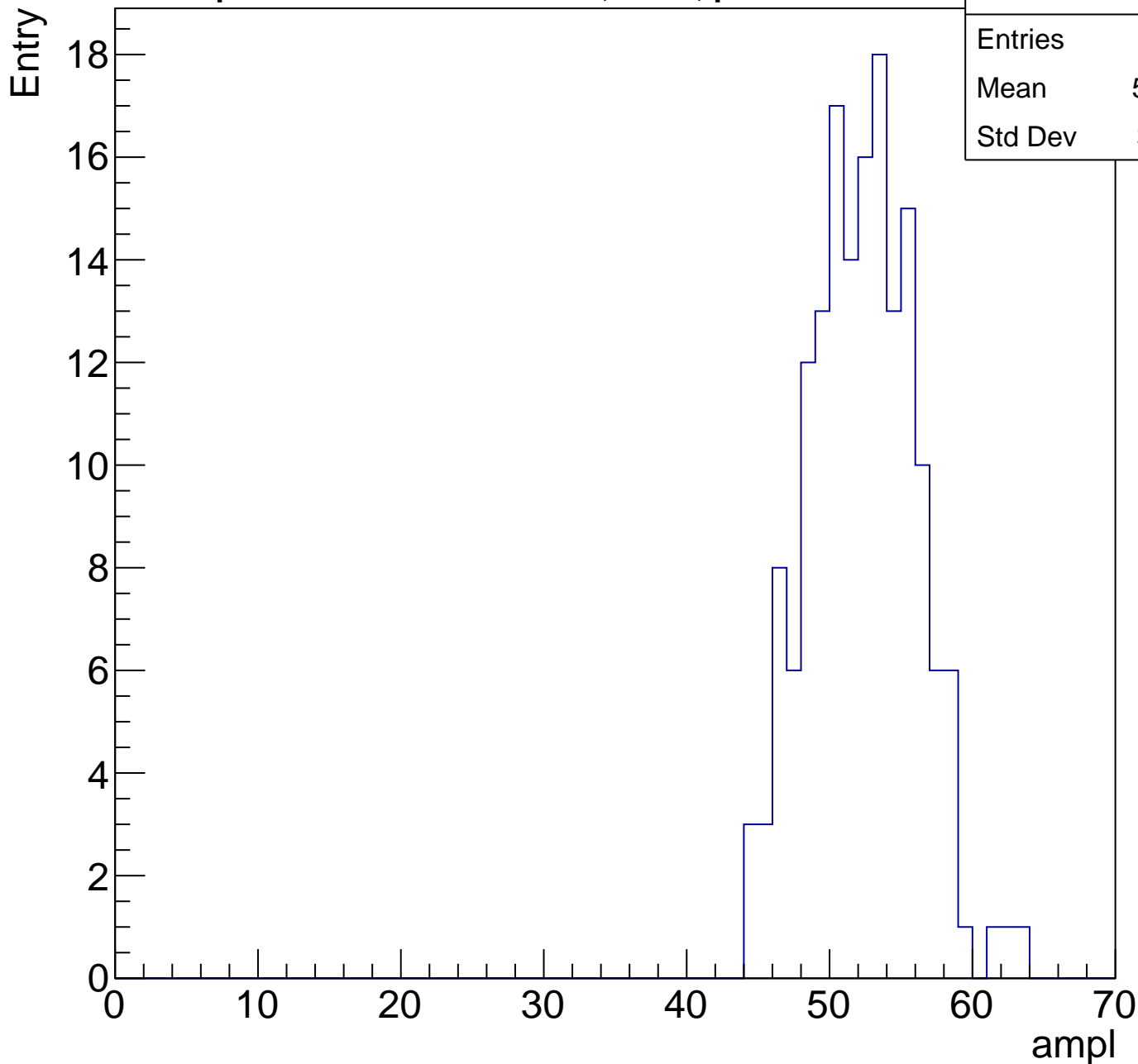
**Gaus Width: 3.3633**



# B0L101S, U23-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	51.84
Std Dev	3.701



# B0L101S, U23-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

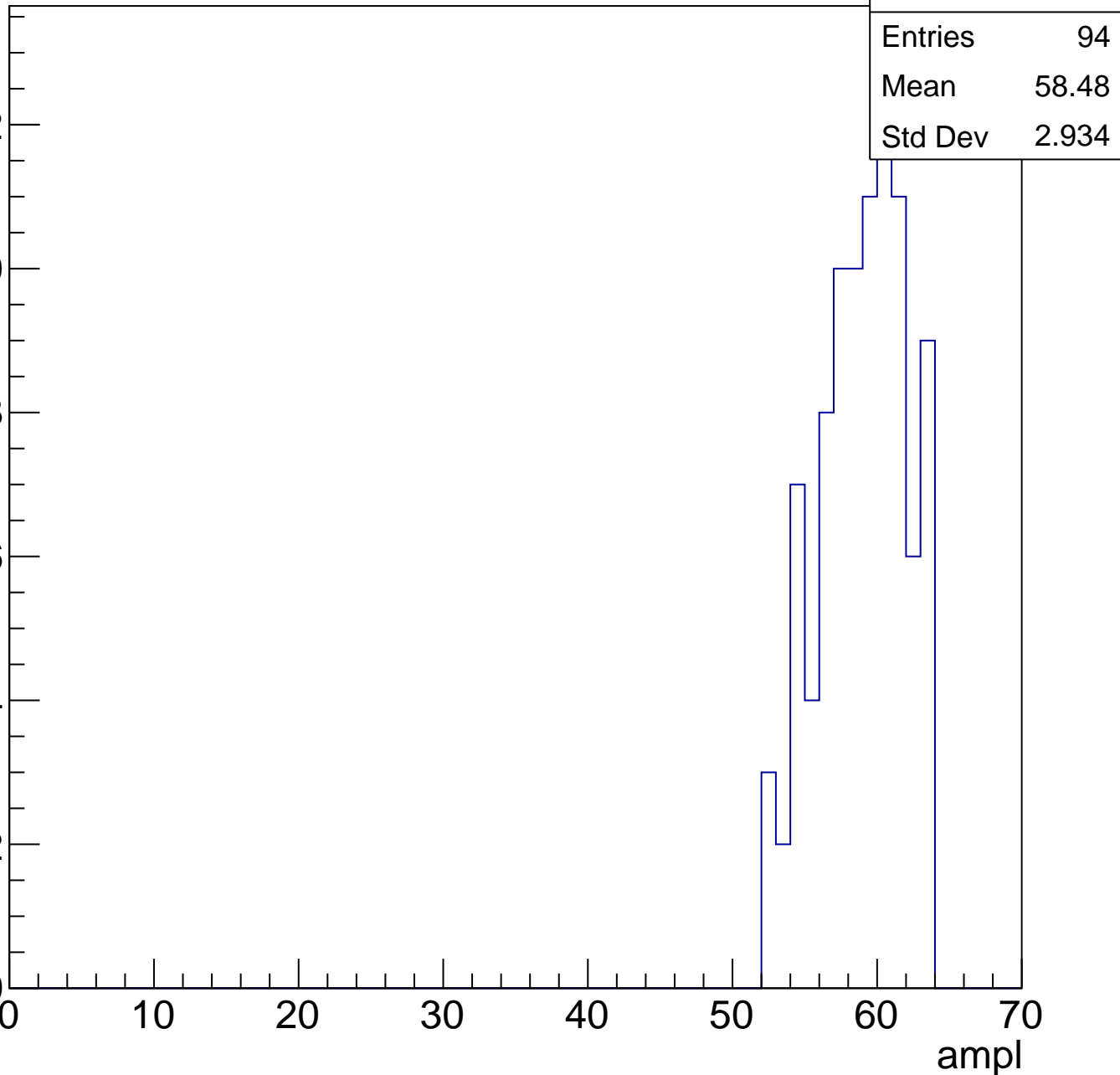
40

50

60

ampl

Entries	94
Mean	58.48
Std Dev	2.934



# B0L101S, U23-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	55
Mean	59.25
Std Dev	8.337

ampl

0

10

20

30

40

50

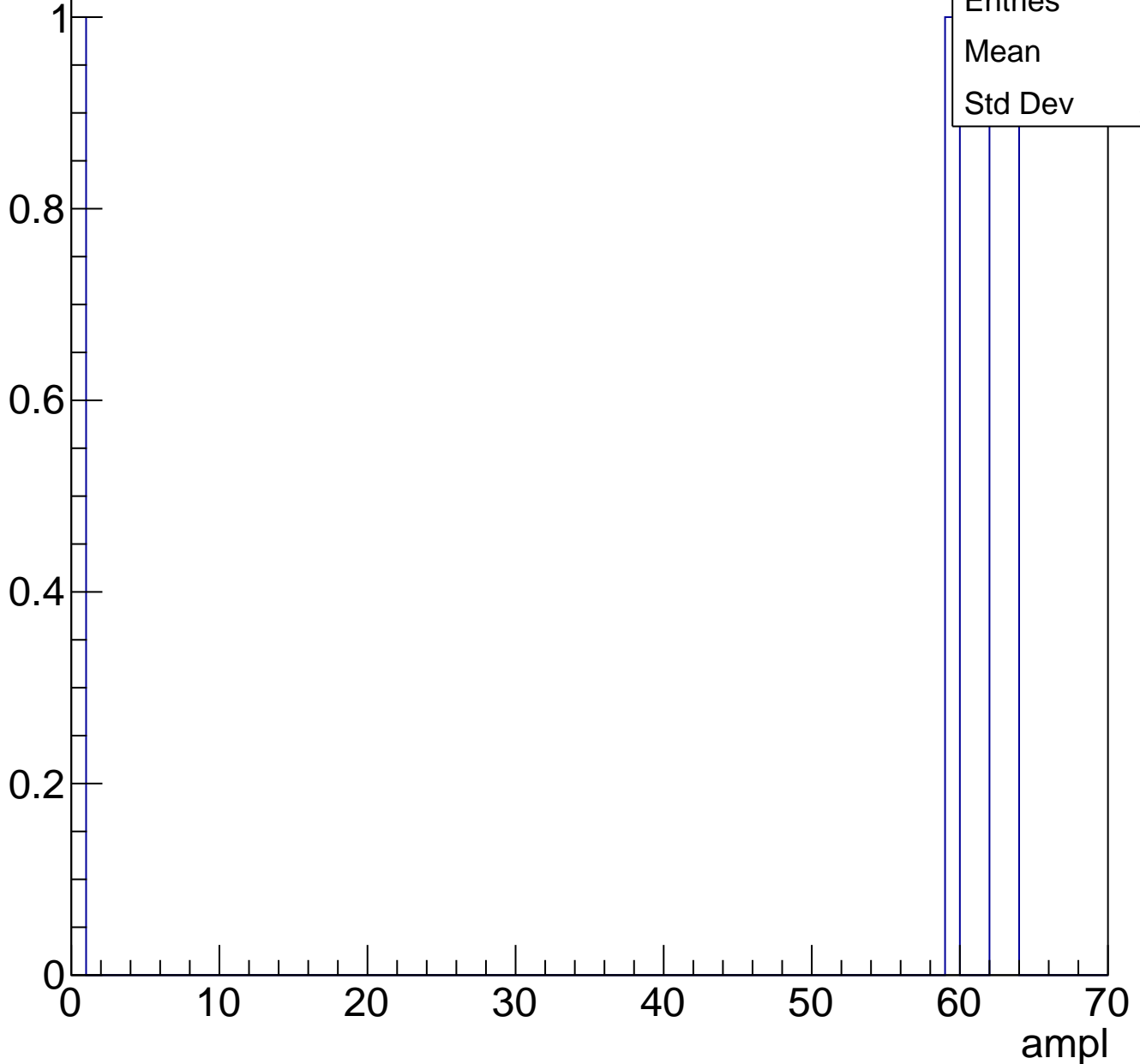
60

70

# B0L101S, U23-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



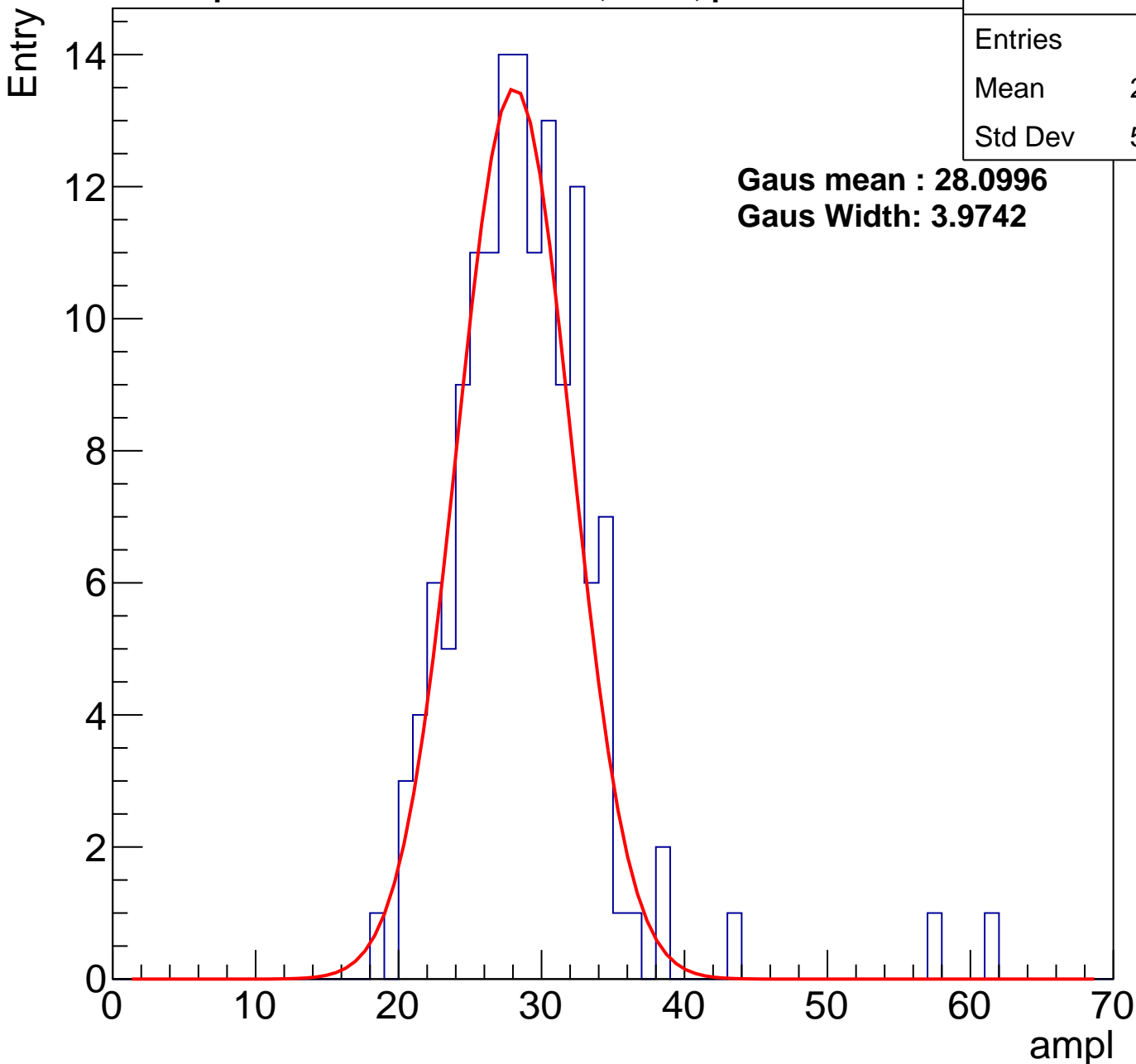
# B0L101S, U23-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	28.48
Std Dev	5.486

**Gaus mean : 28.0996**

**Gaus Width: 3.9742**



# B0L101S, U23-ch120, adc1

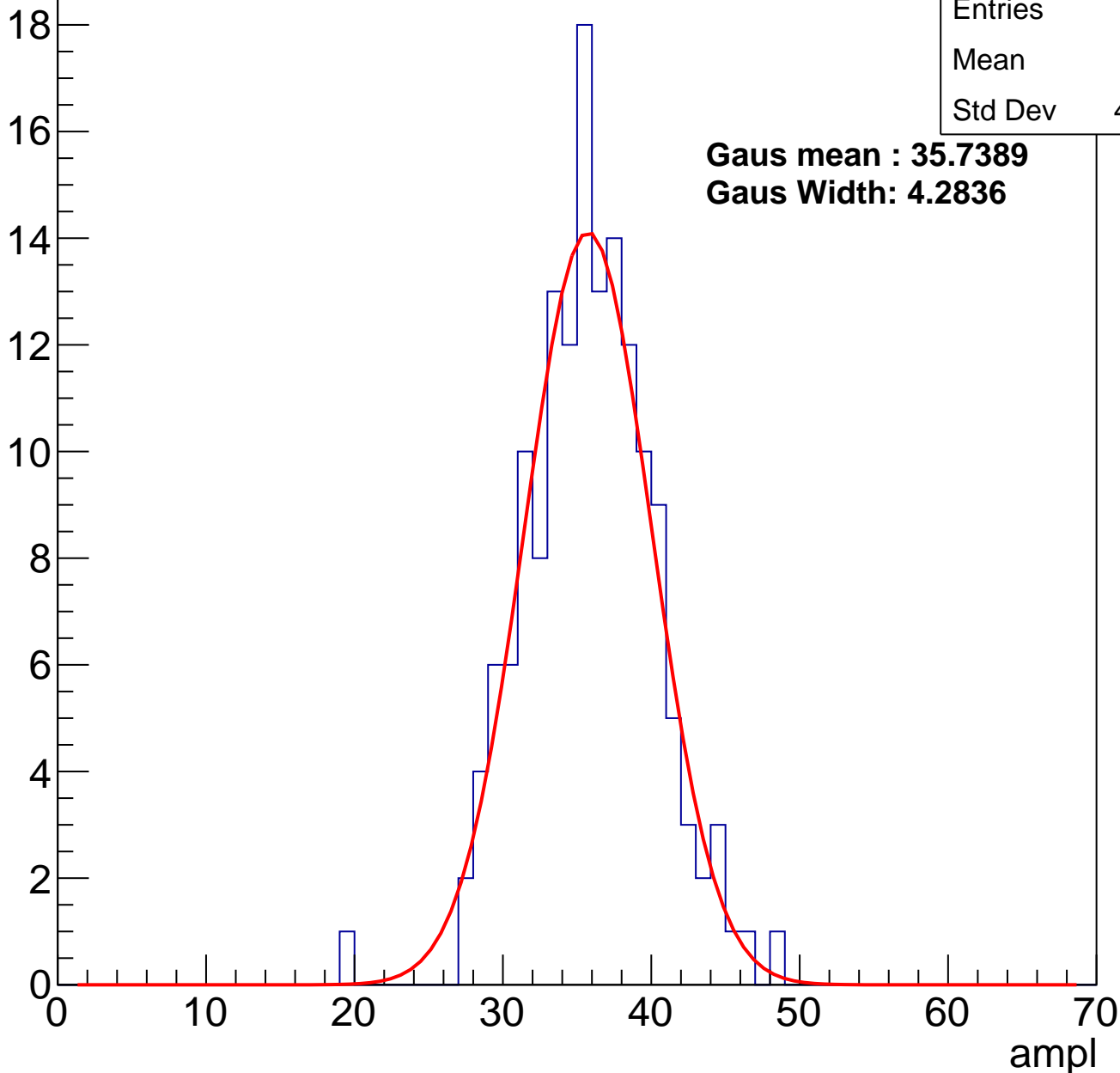
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	35.4
Std Dev	4.315

**Gaus mean : 35.7389**

**Gaus Width: 4.2836**

Entry



# B0L101S, U23-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

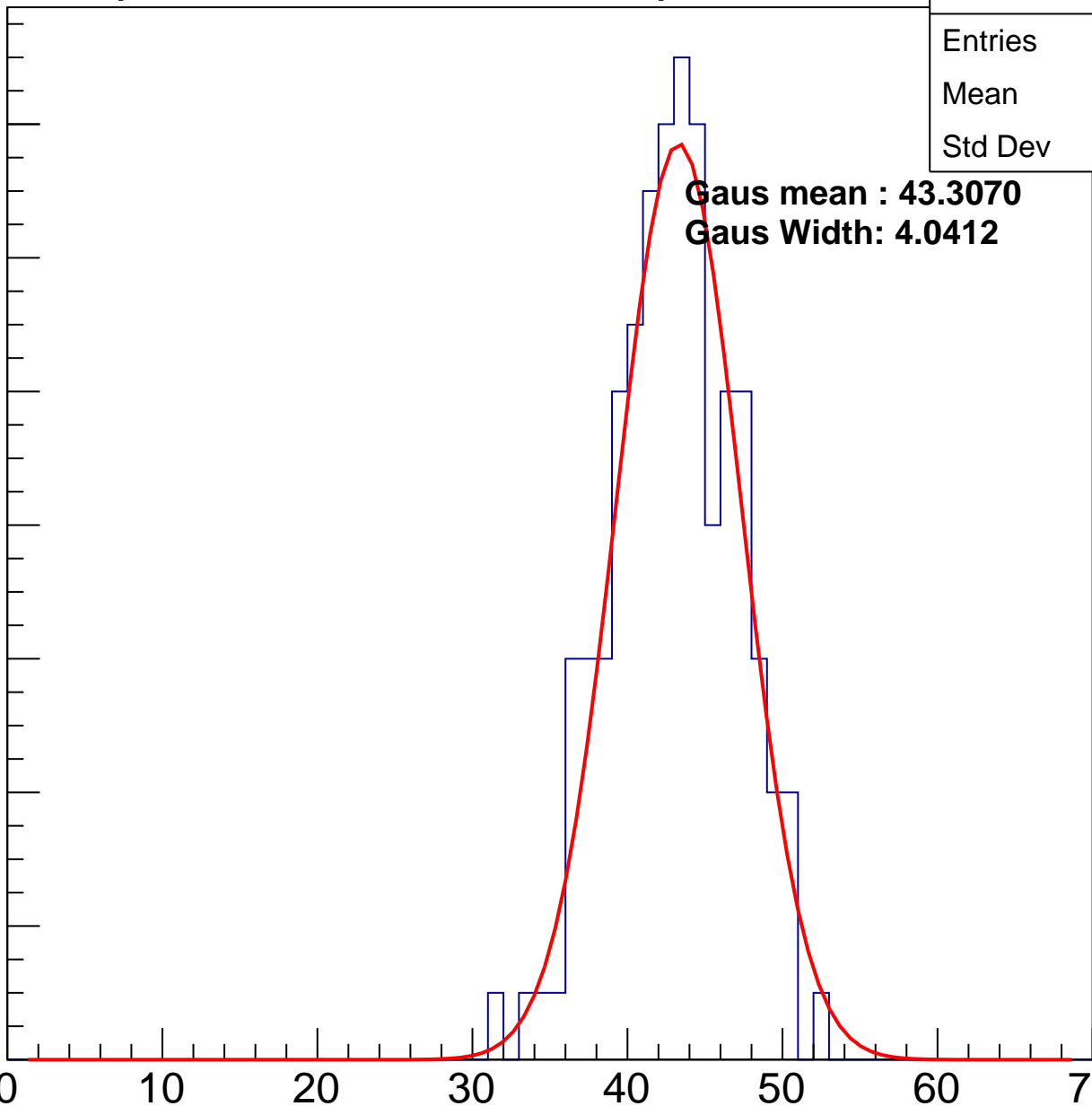
14  
12  
10  
8  
6  
4  
2  
0

Entries	142
Mean	42.51
Std Dev	3.943

**Gaus mean : 43.3070**

**Gaus Width: 4.0412**

ampl



# B0L101S, U23-ch120, adc3

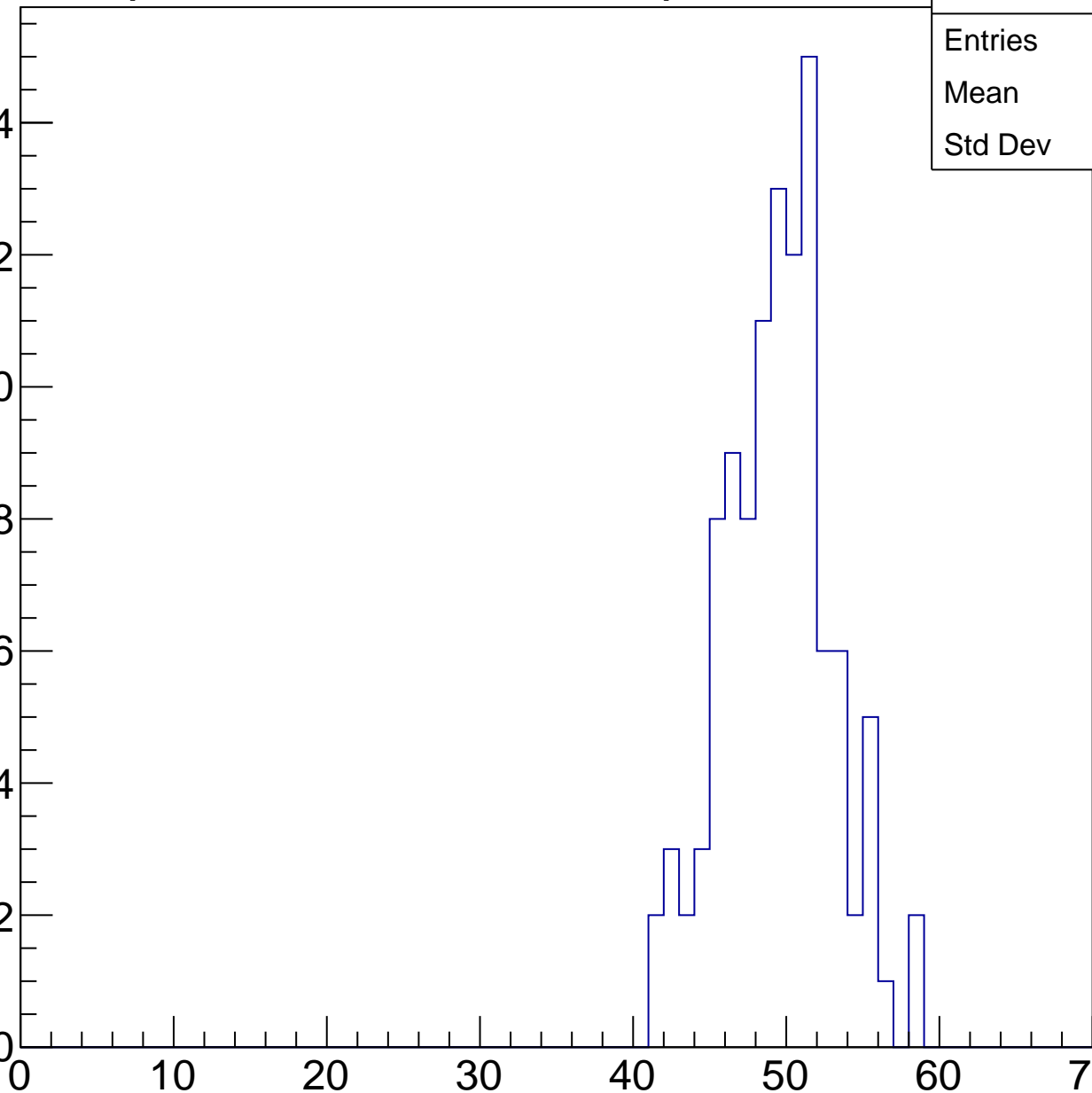
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	108
Mean	48.99
Std Dev	3.518

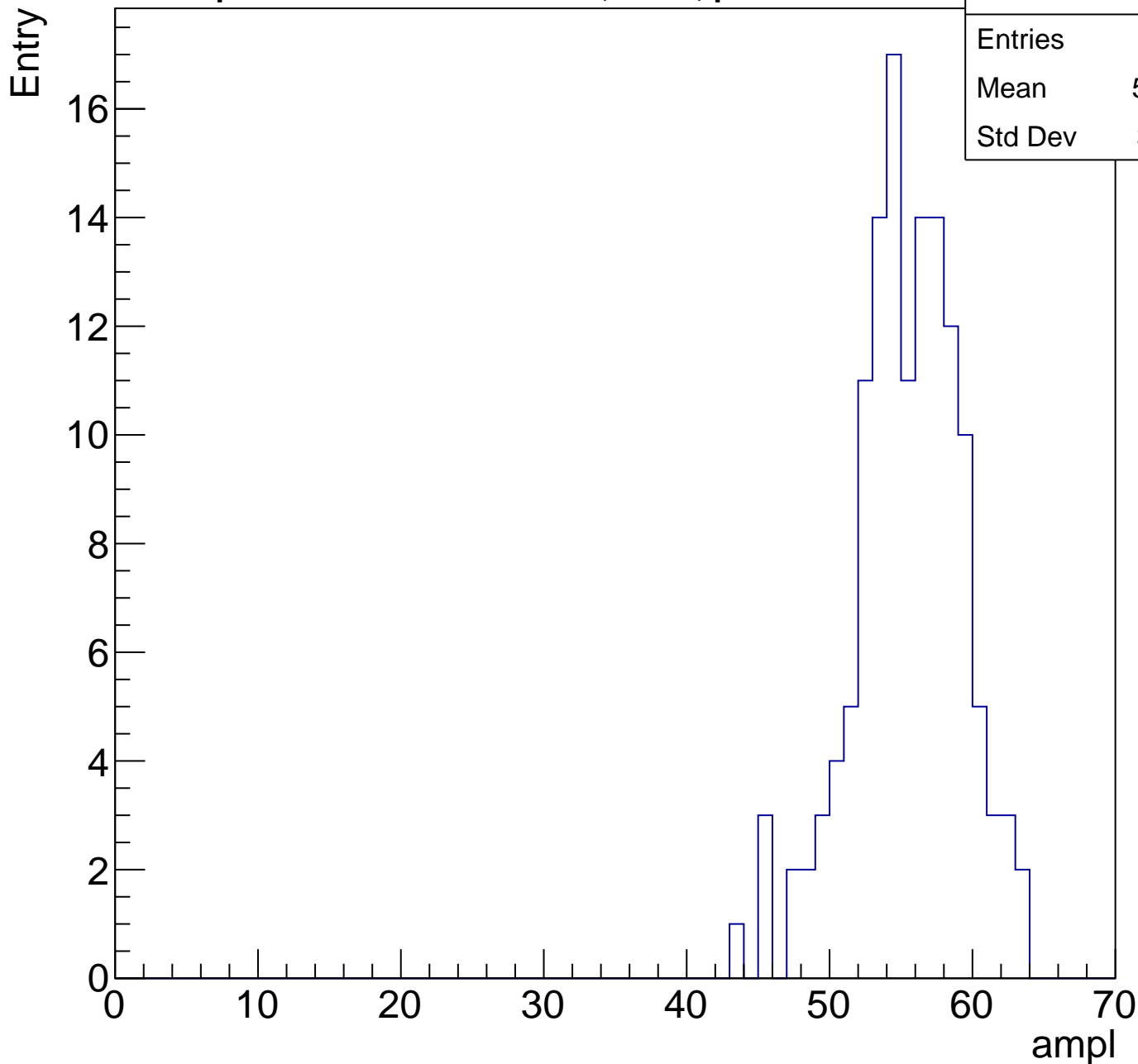
ampl



# B0L101S, U23-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	54.93
Std Dev	3.811



# B0L101S, U23-ch120, adc5

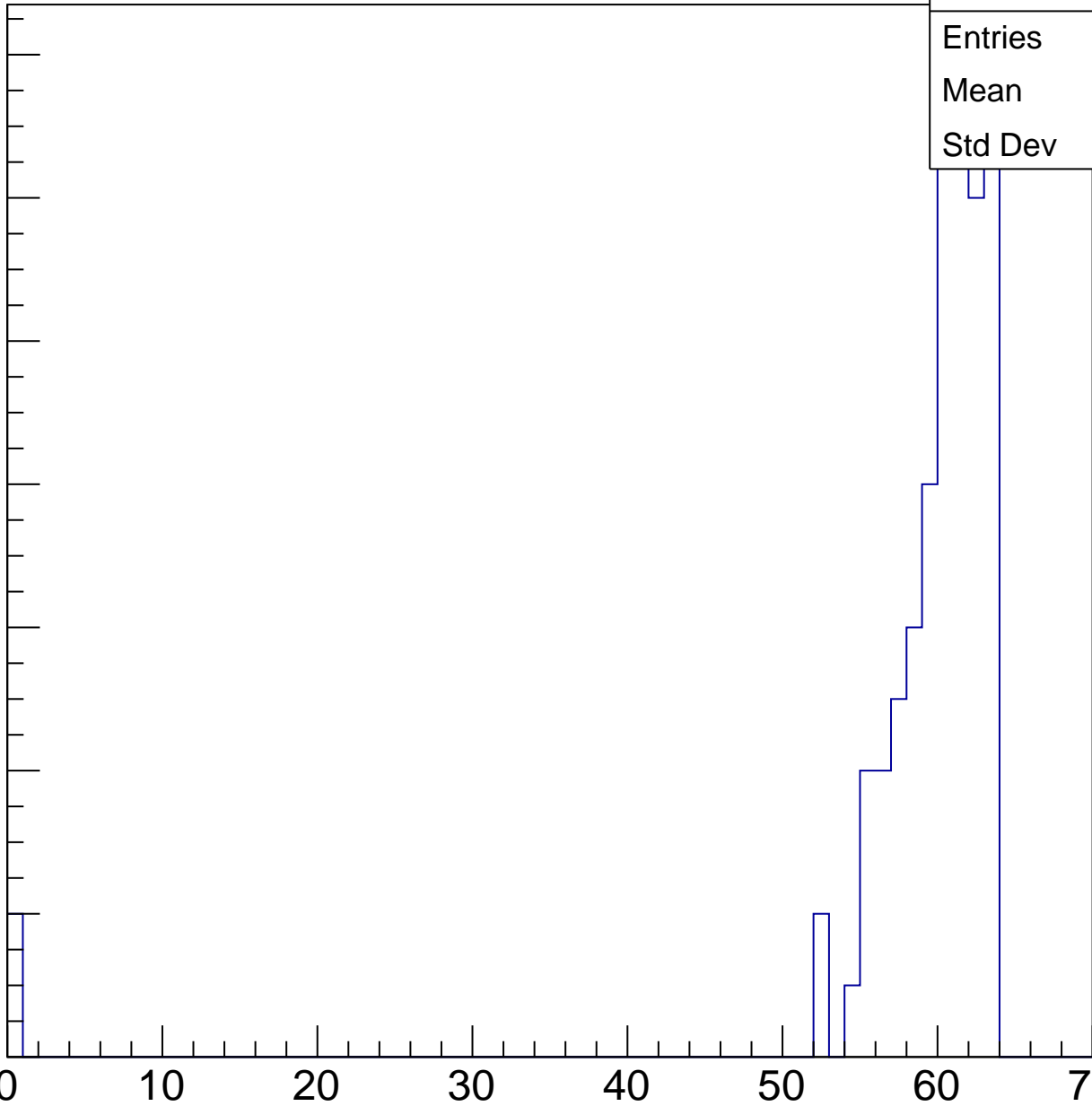
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	58.4
Std Dev	9.435

ampl

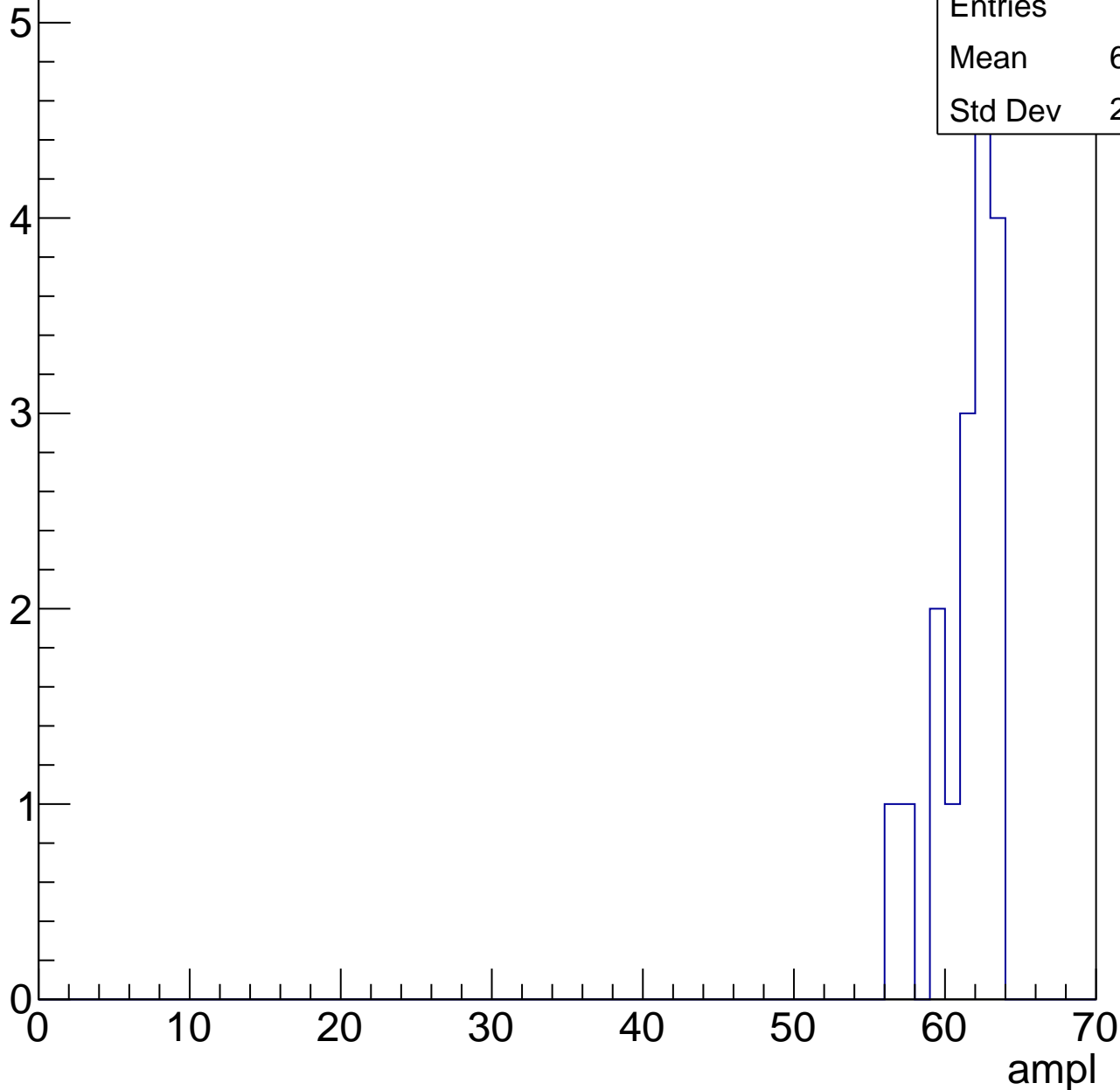


# B0L101S, U23-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	60.94
Std Dev	2.043





# B0L101S, U23-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	33
Std Dev	23.72

ampl

# B0L101S, U23-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	27.64
Std Dev	4.074

**Gaus mean : 27.9210**

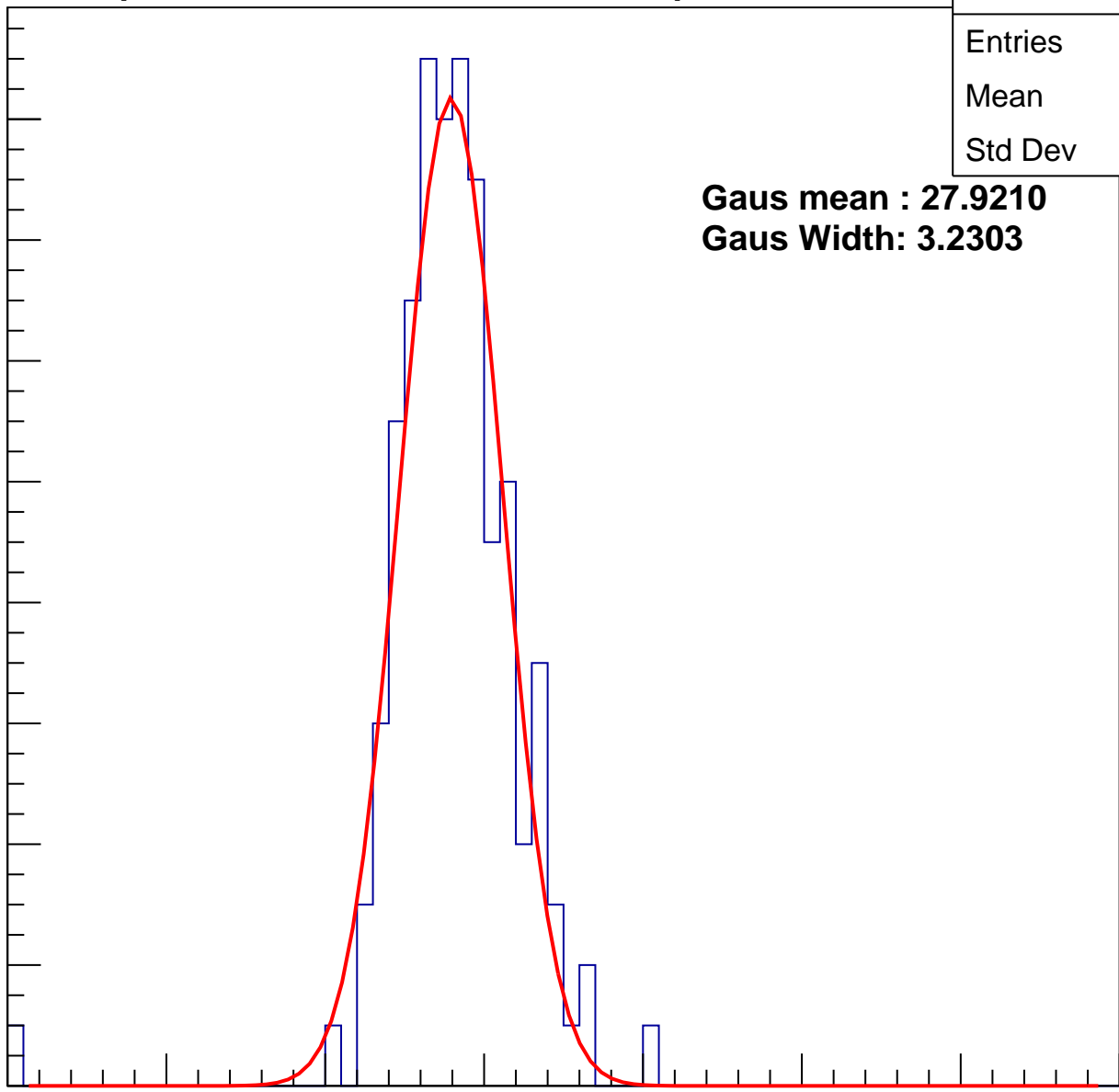
**Gaus Width: 3.2303**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	35.38
Std Dev	3.794

**Gaus mean : 35.9105**

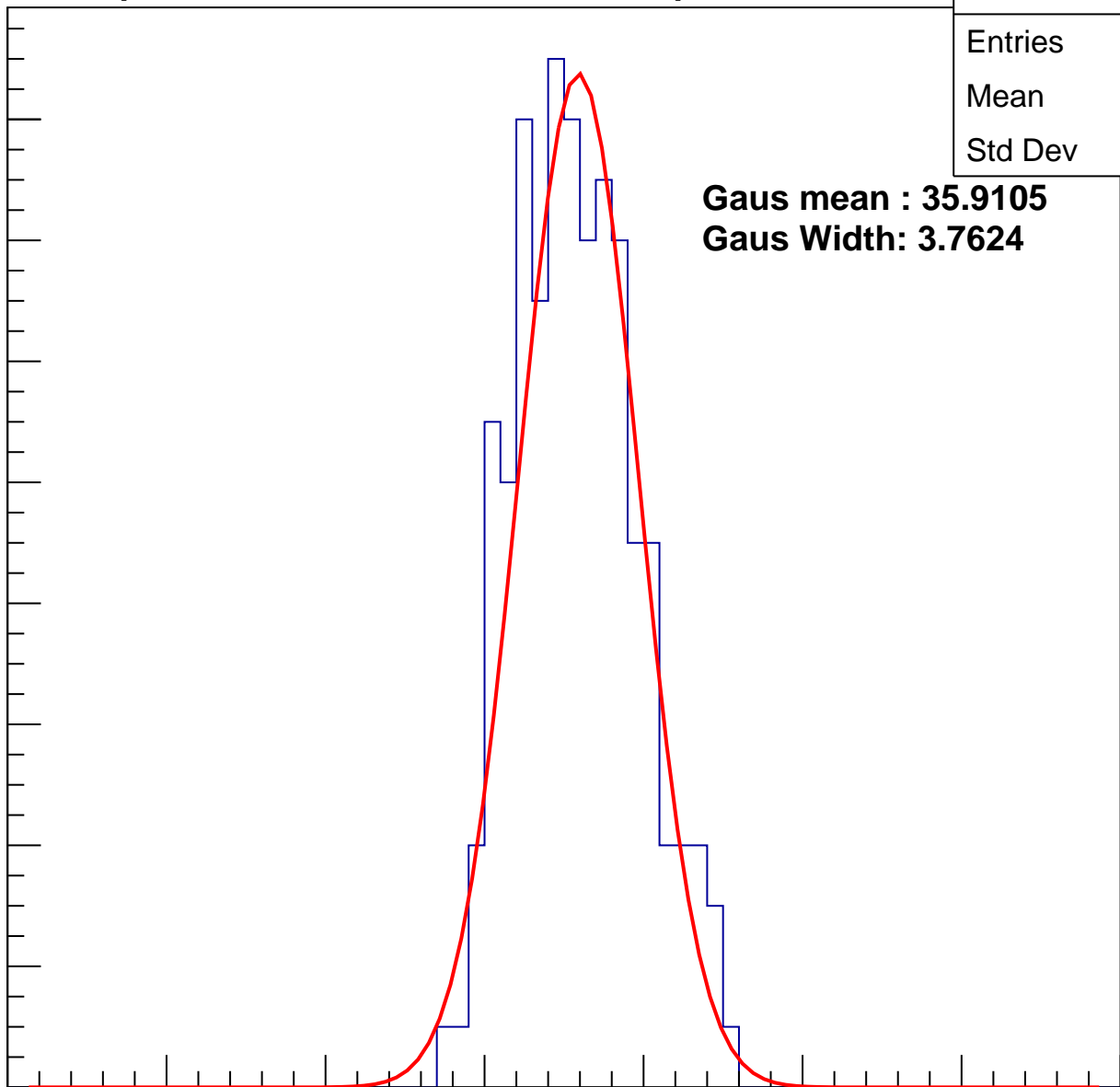
**Gaus Width: 3.7624**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch121, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

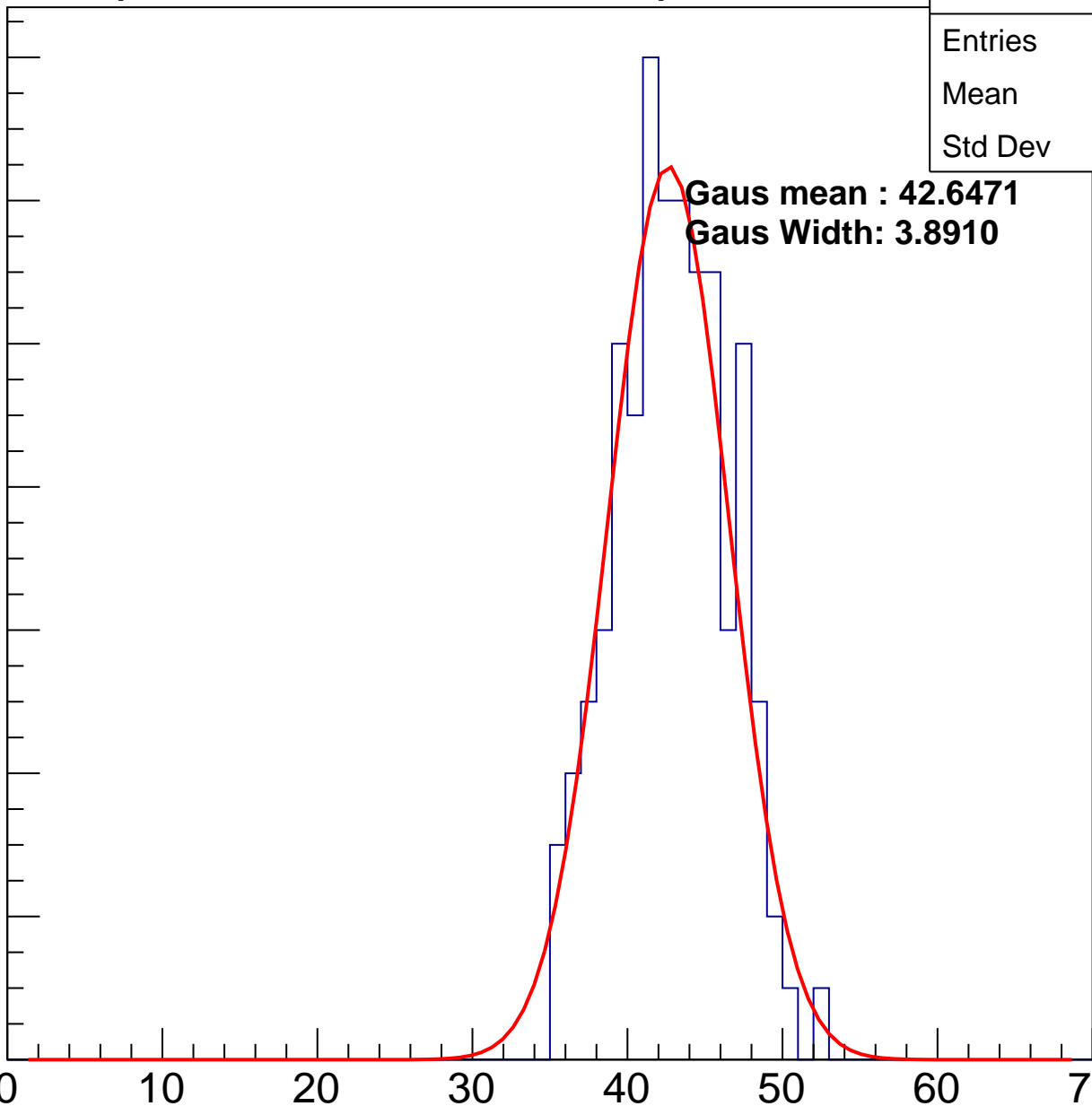
14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	42.39
Std Dev	3.595

**Gaus mean : 42.6471**

**Gaus Width: 3.8910**

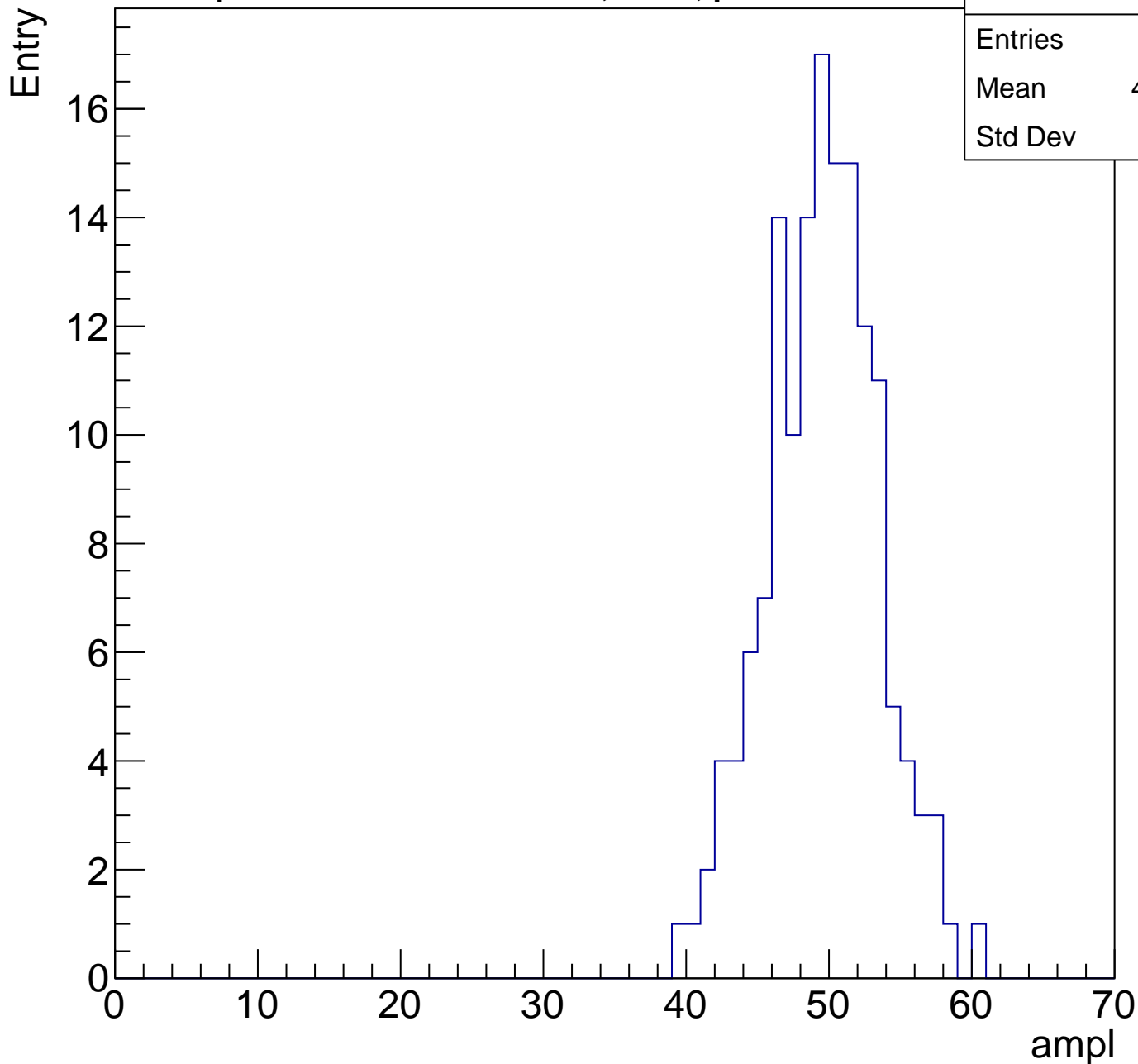
ampl



# B0L101S, U23-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

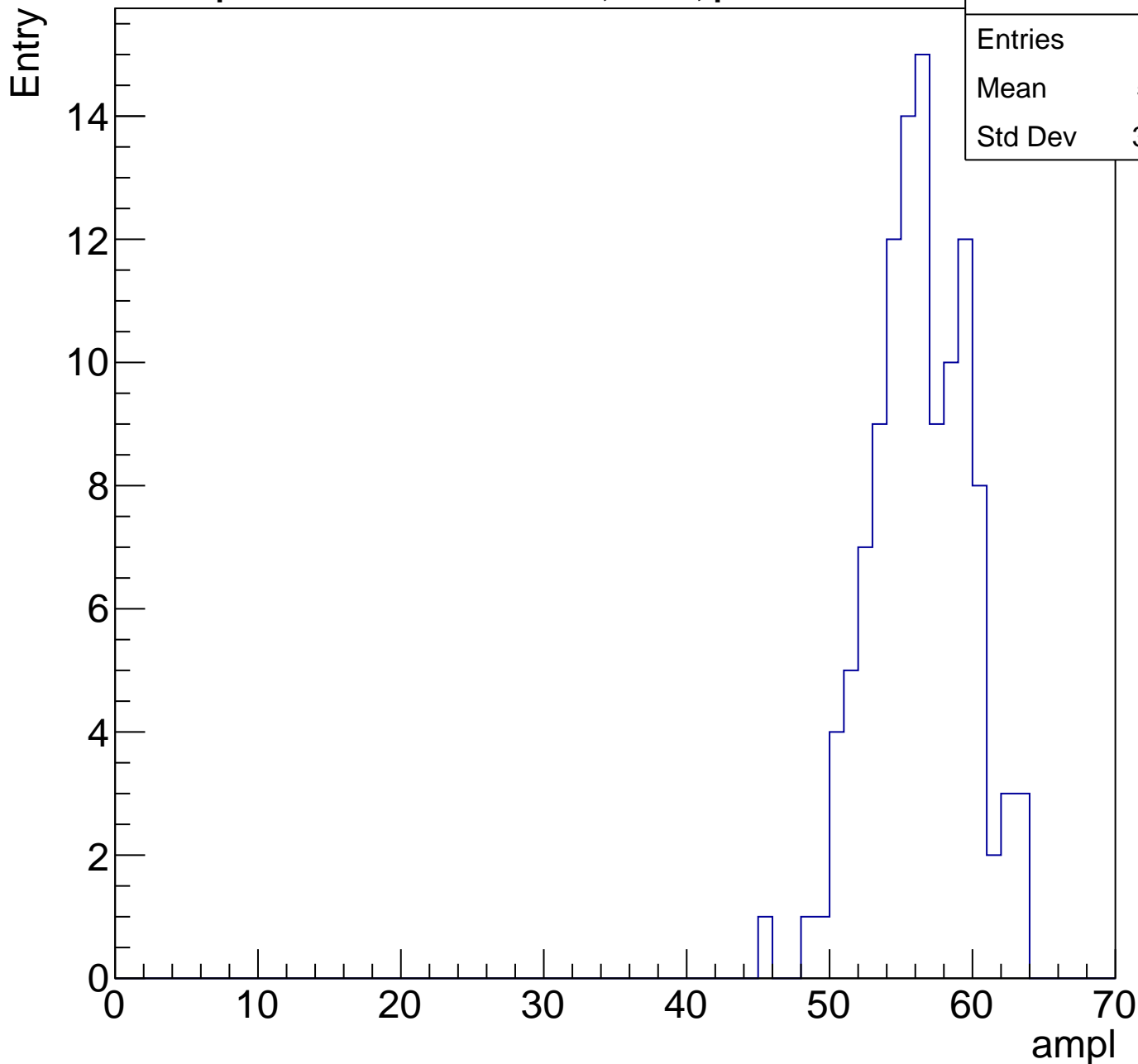
Entries	150
Mean	49.12
Std Dev	3.88



# B0L101S, U23-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	55.81
Std Dev	3.404

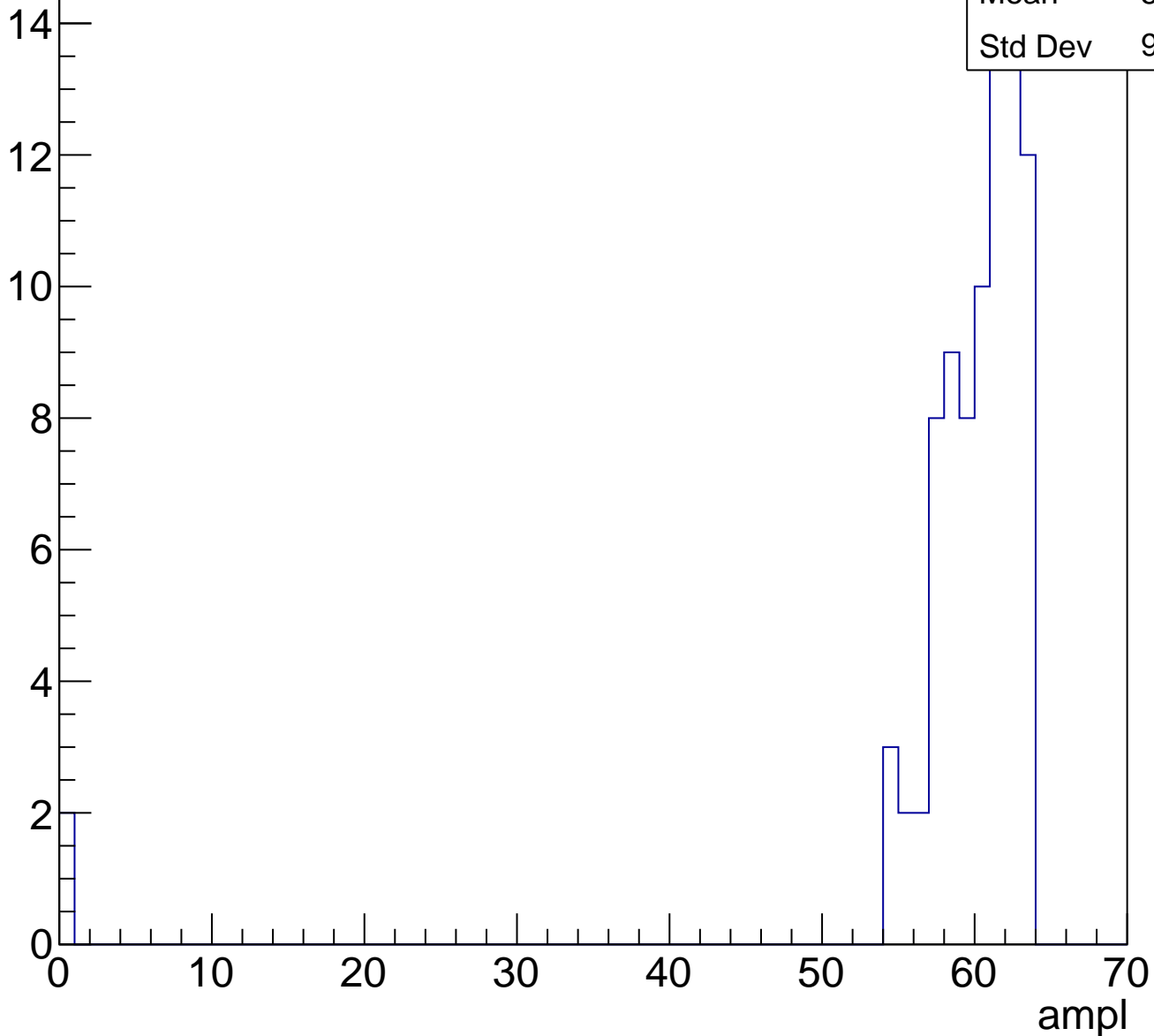


# B0L101S, U23-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	86
Mean	58.55
Std Dev	9.343

Entry

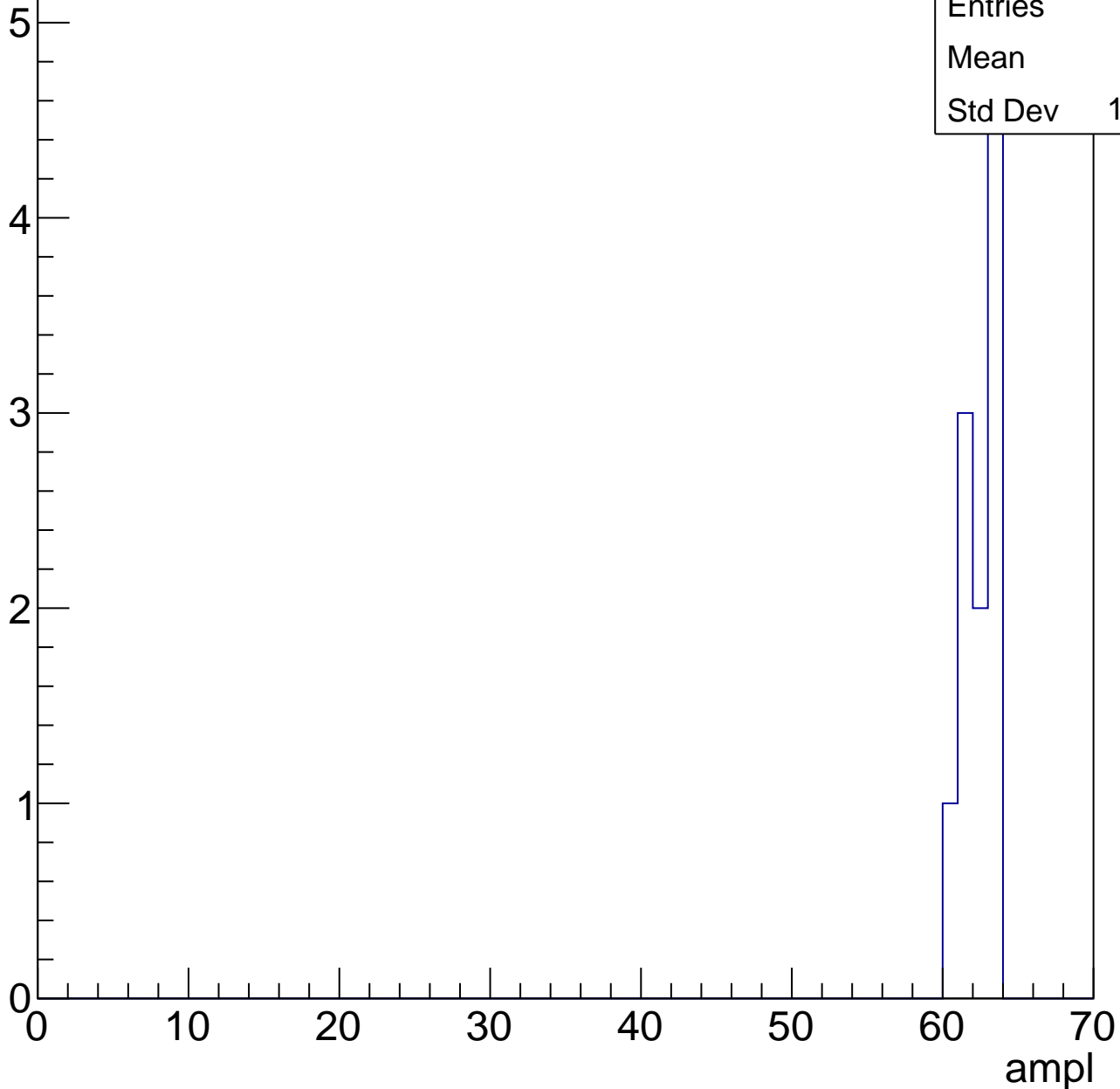


# B0L101S, U23-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62
Std Dev	1.044

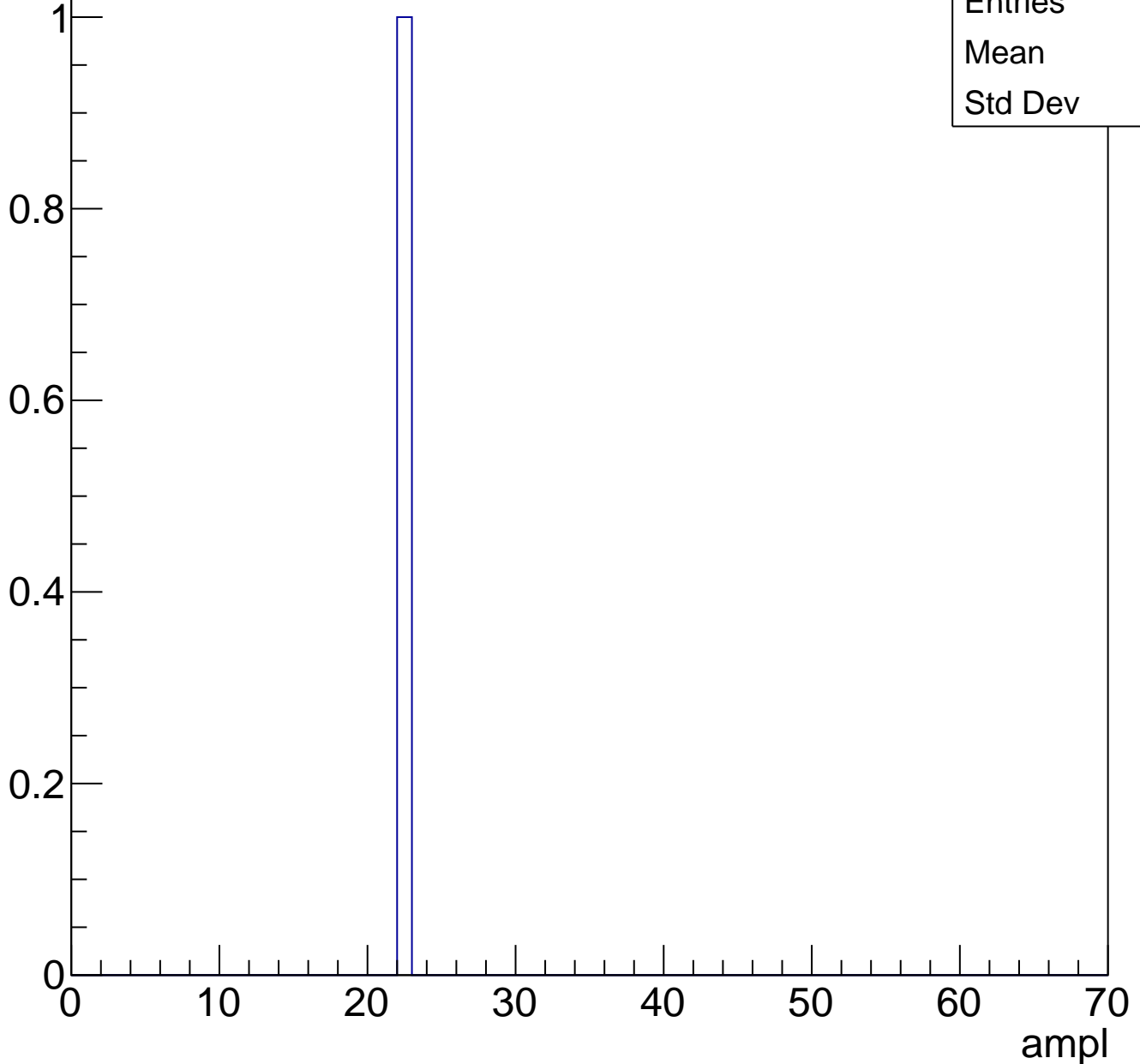




# B0L101S, U23-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	27.34
Std Dev	6.427

**Gaus mean : 28.3632**

**Gaus Width: 4.3318**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

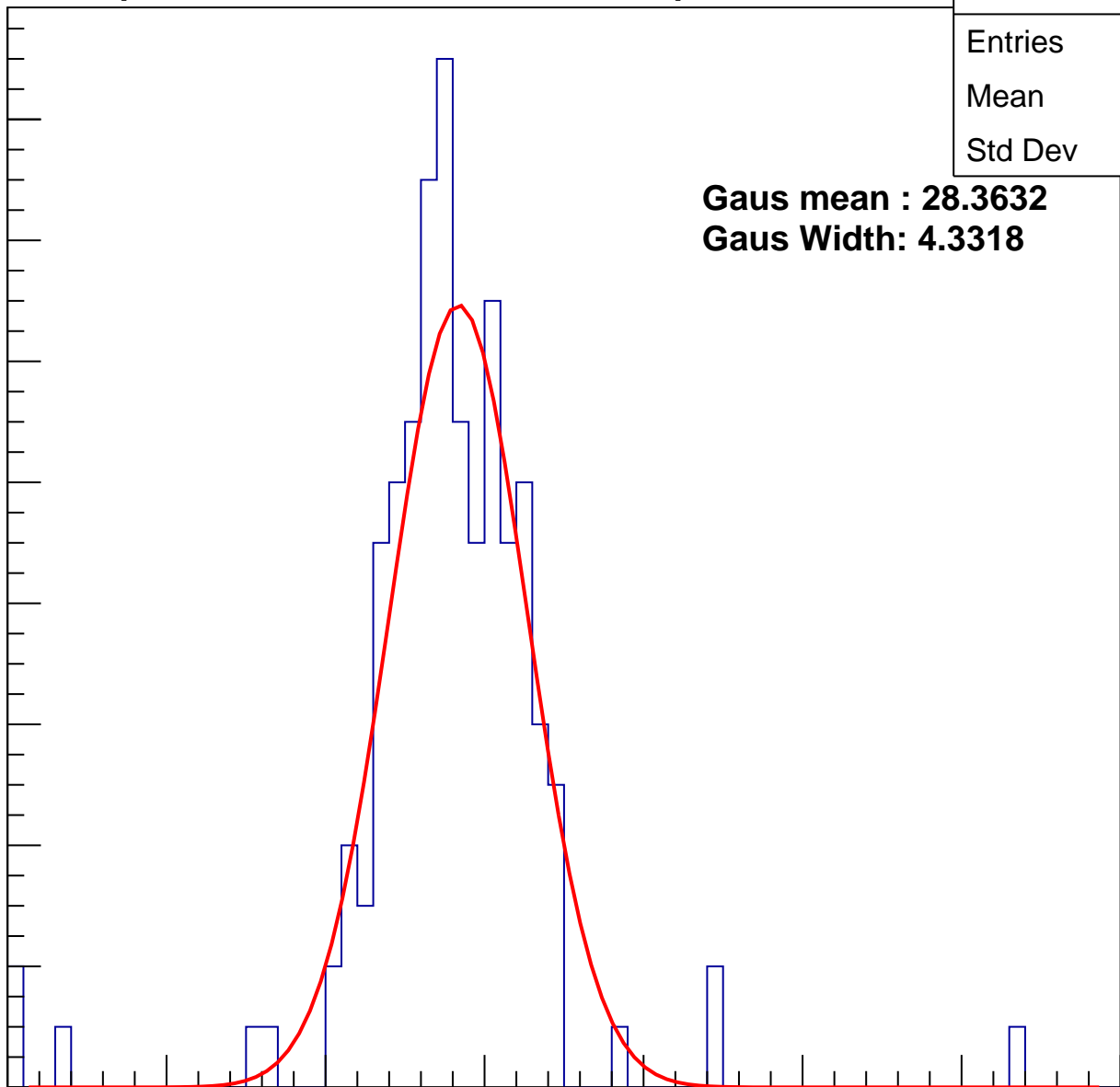
40

50

60

70

ampl



# B0L101S, U23-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	34.38
Std Dev	4.612

**Gaus mean : 35.3390**

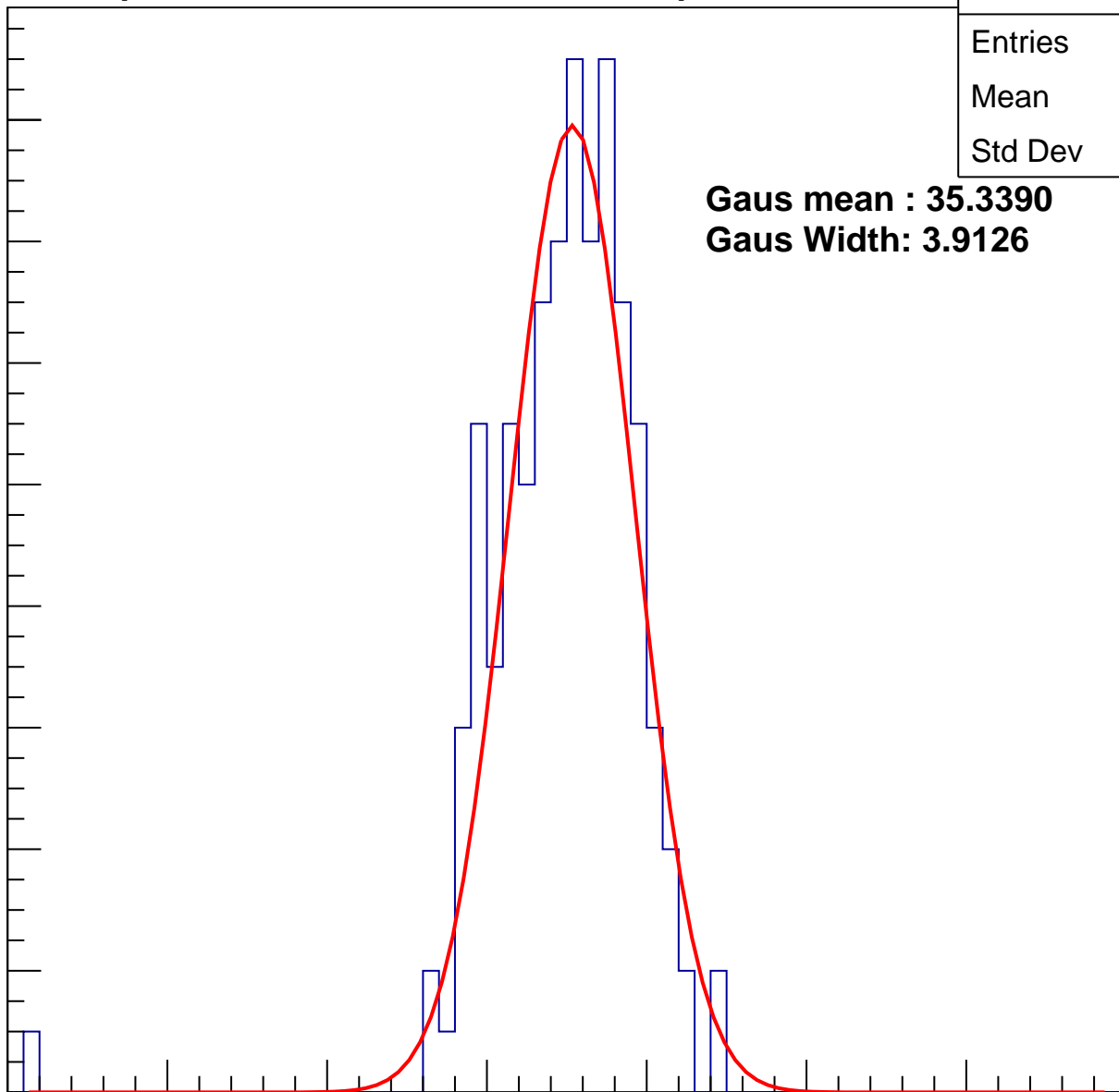
**Gaus Width: 3.9126**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

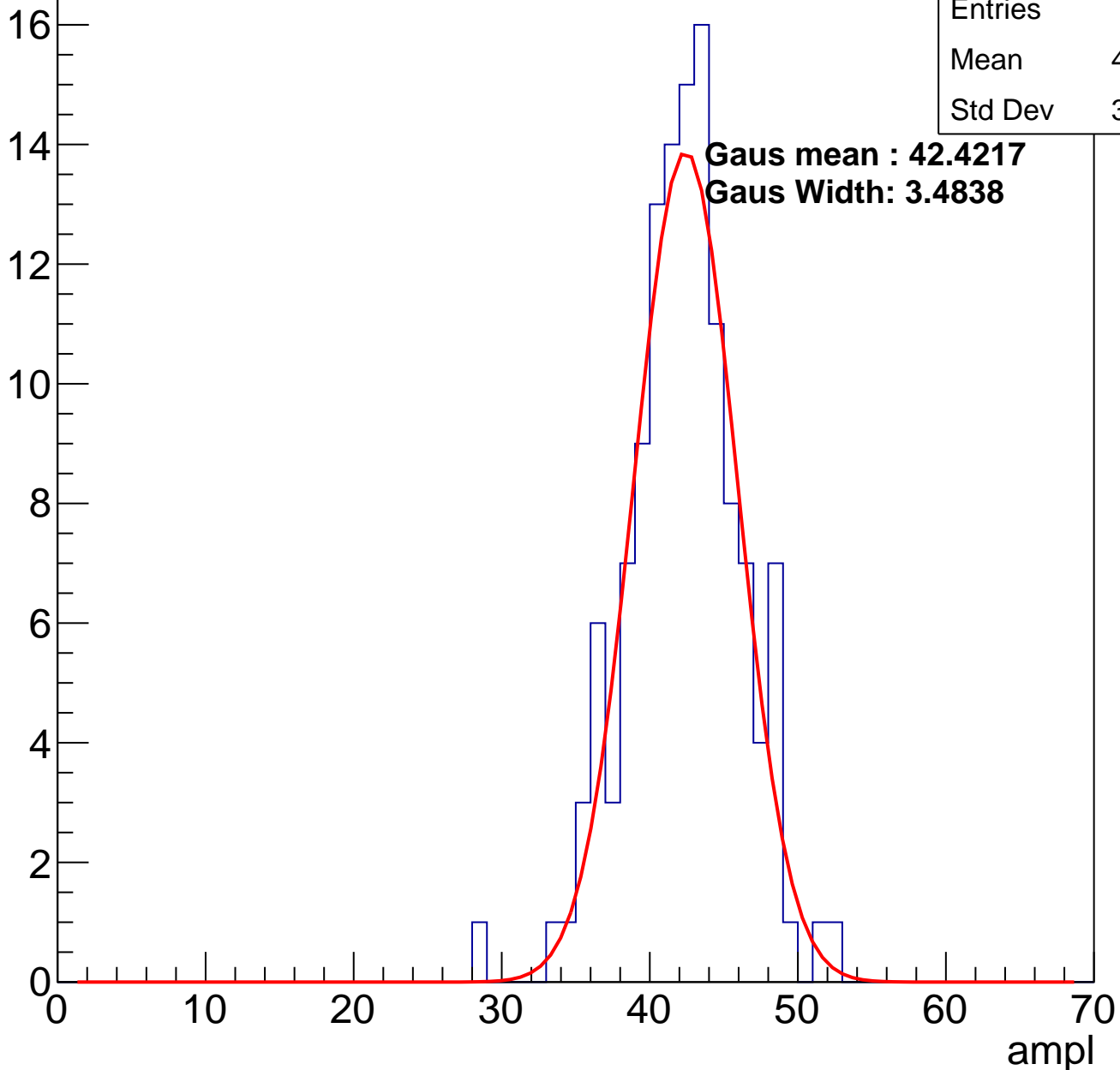
ampl



# B0L101S, U23-ch122, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

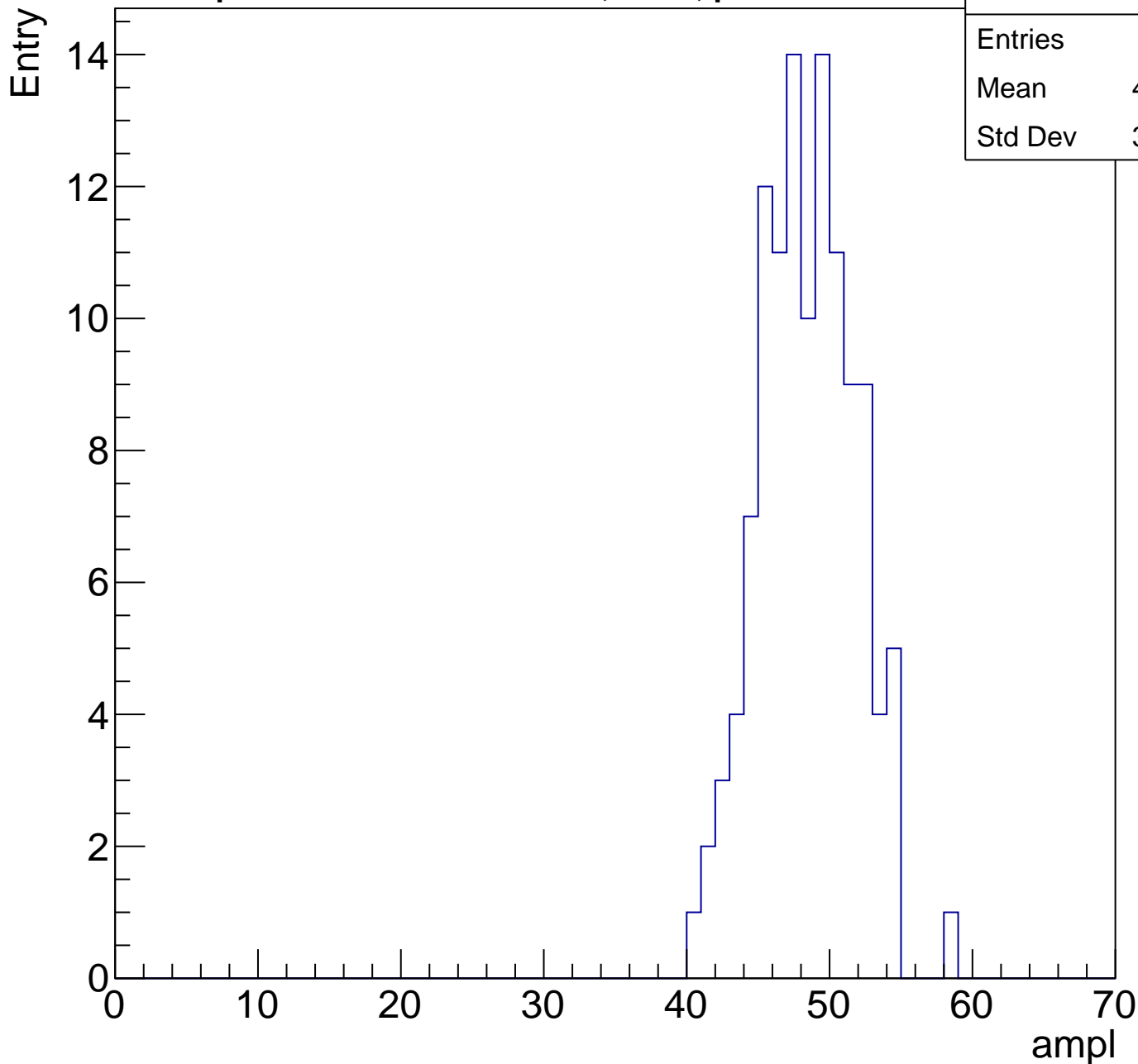
Entry



# B0L101S, U23-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	47.99
Std Dev	3.347



# B0L101S, U23-ch122, adc4

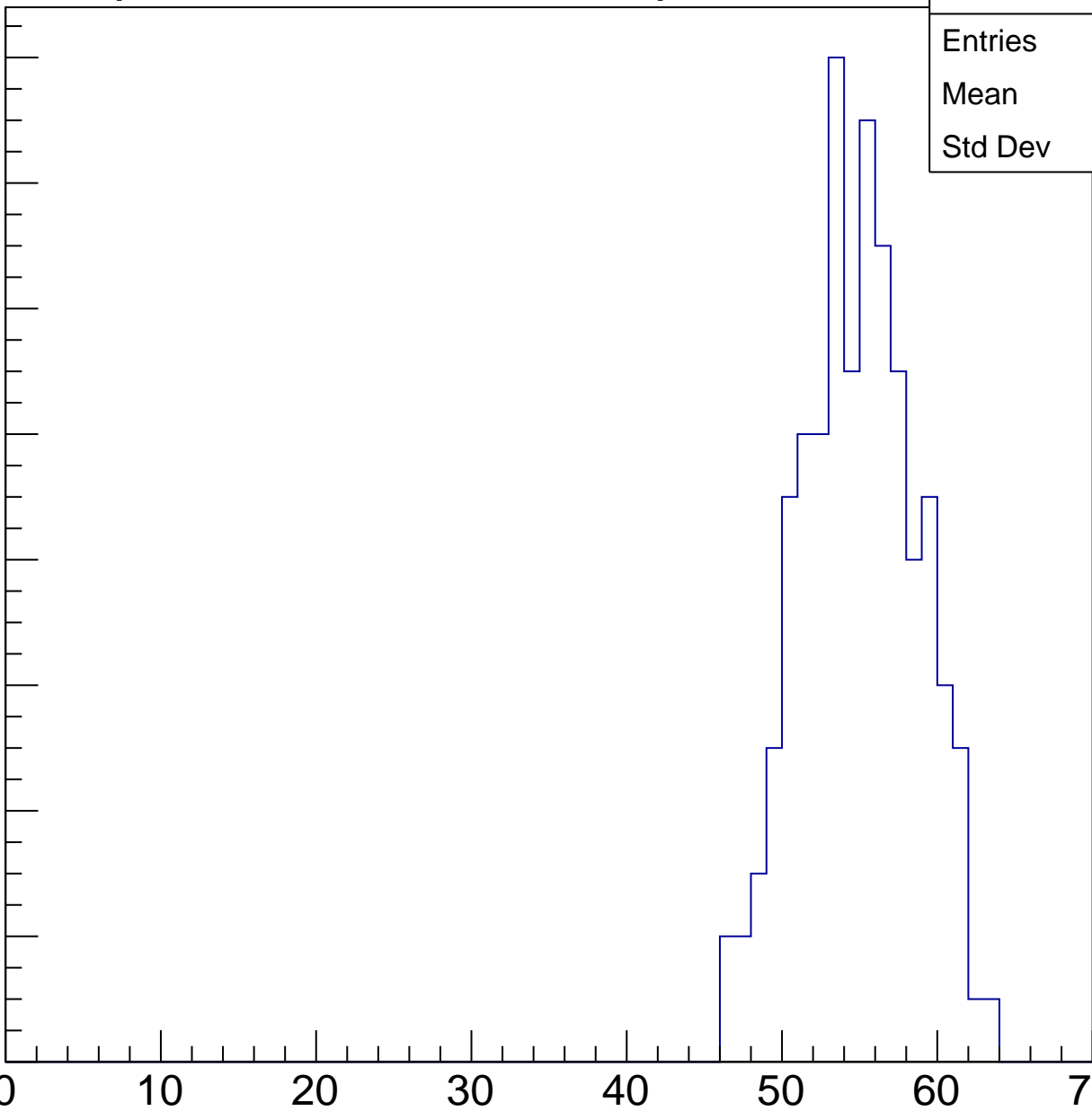
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	137
Mean	54.47
Std Dev	3.669

ampl



# B0L101S, U23-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

86

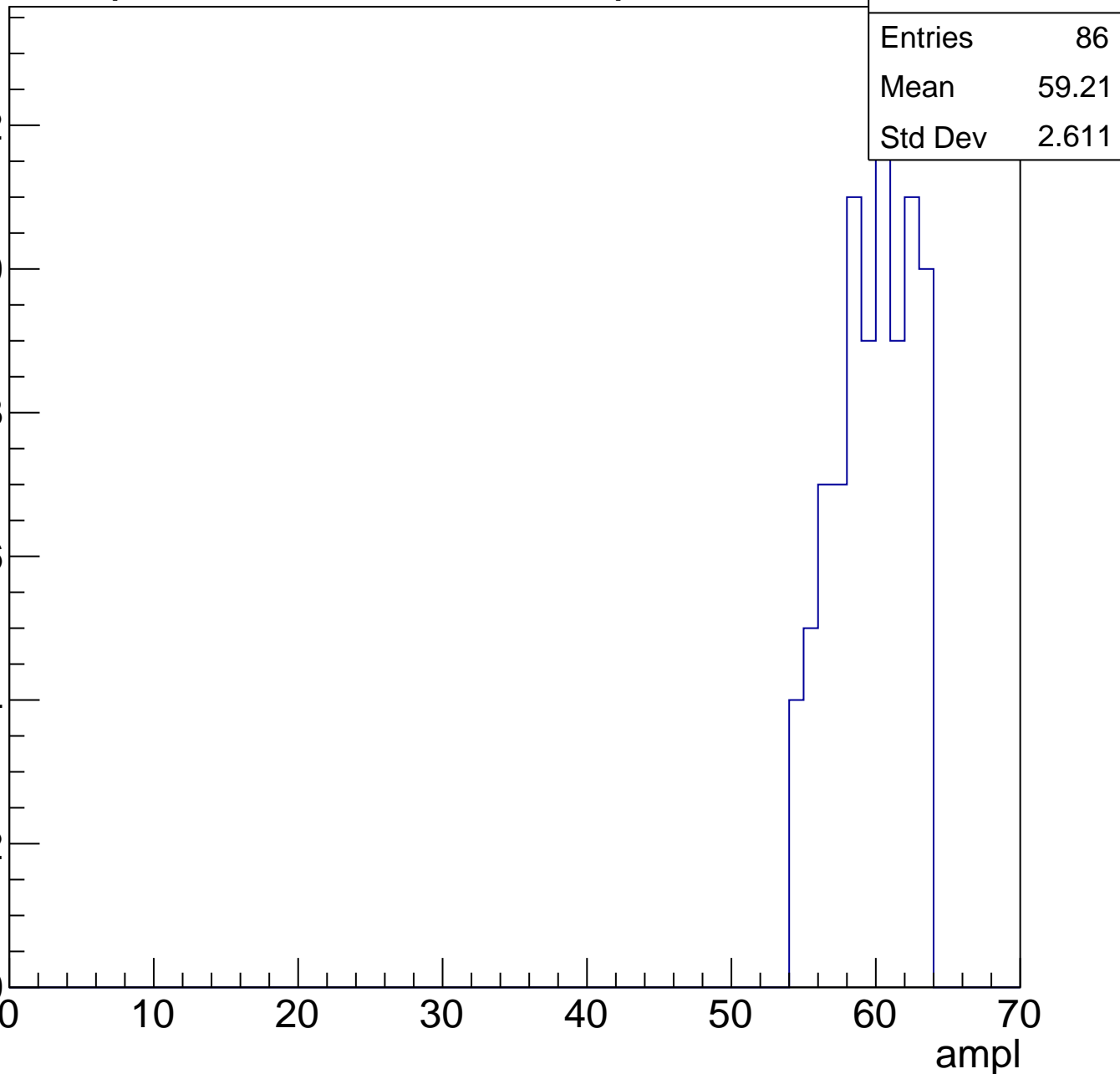
Mean

59.21

Std Dev

2.611

ampl

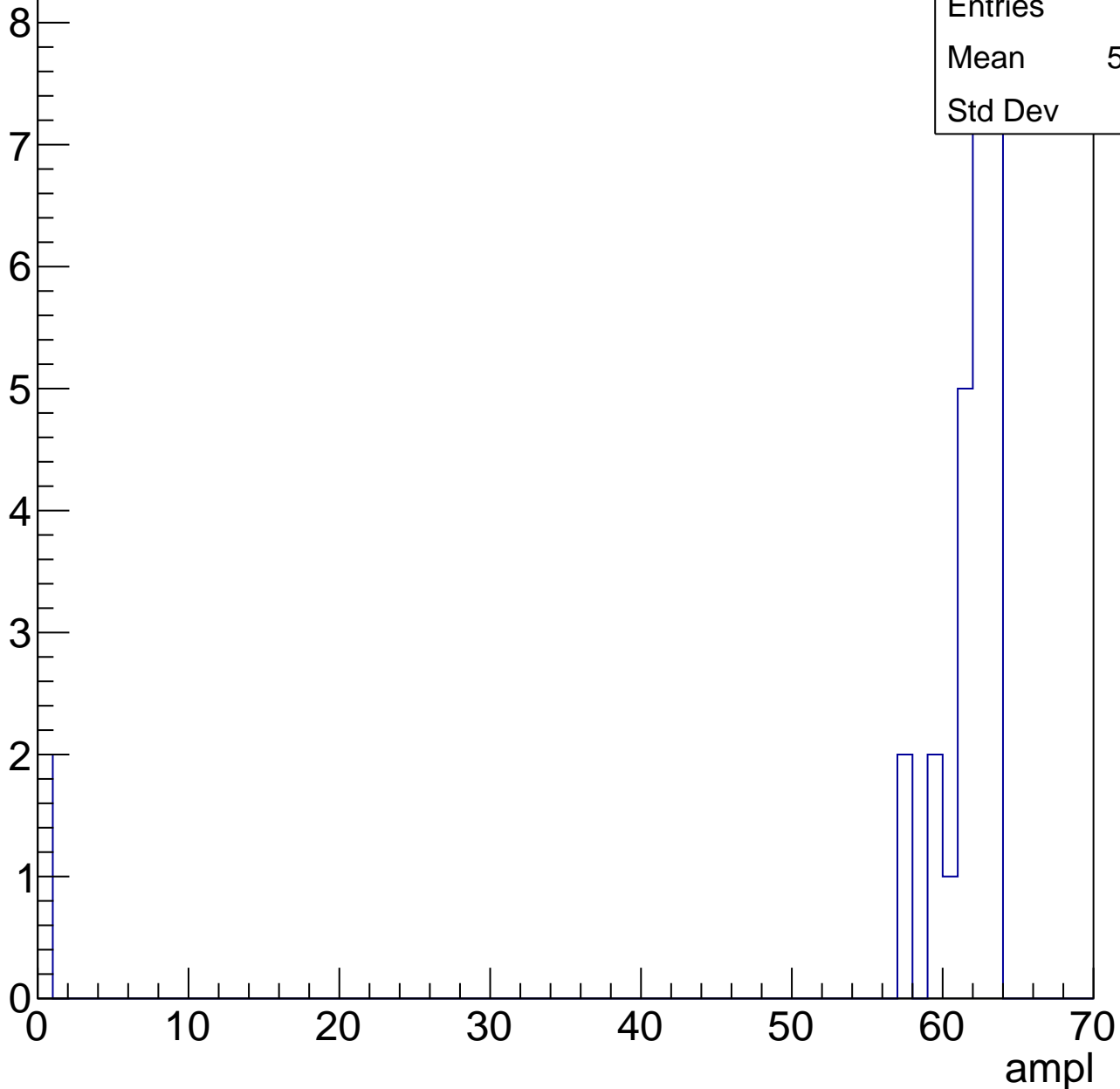


# B0L101S, U23-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	28
Mean	57.04
Std Dev	15.9





# B0L101S, U23-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch123, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	28.43
Std Dev	3.848

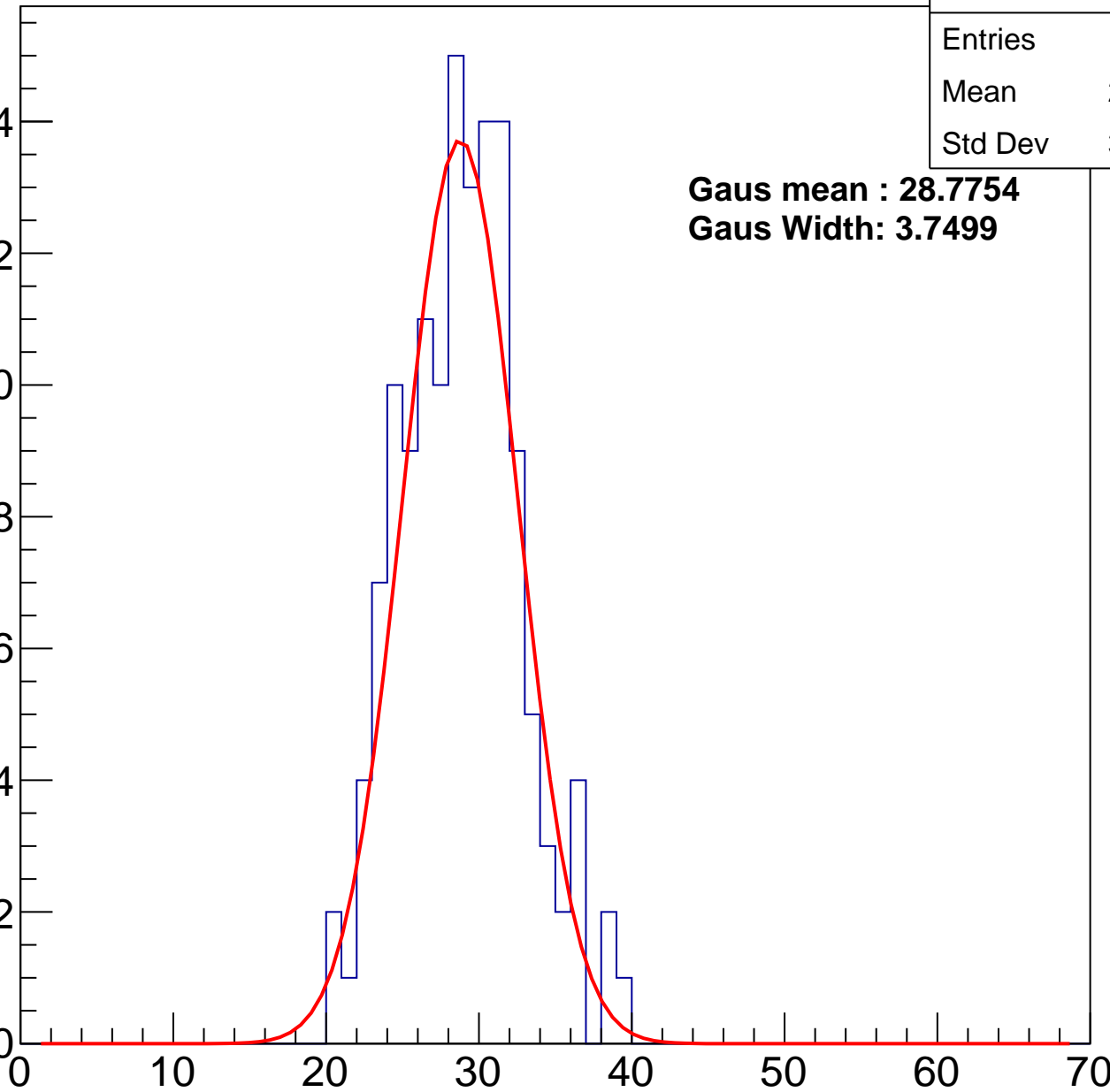
**Gaus mean : 28.7754**

**Gaus Width: 3.7499**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



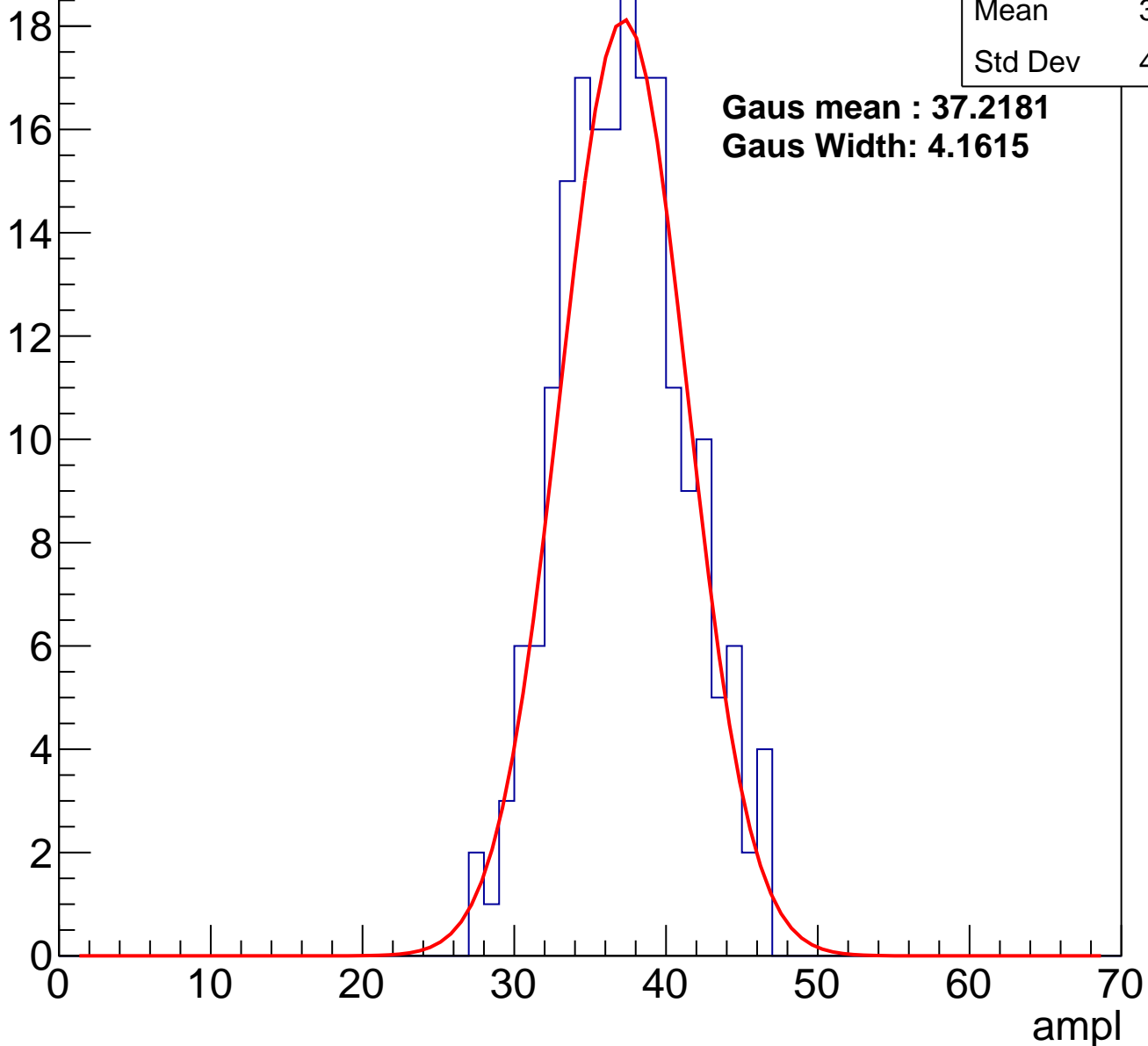
# B0L101S, U23-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	193
Mean	36.74
Std Dev	4.086

**Gaus mean : 37.2181**  
**Gaus Width: 4.1615**

Entry



# B0L101S, U23-ch123, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

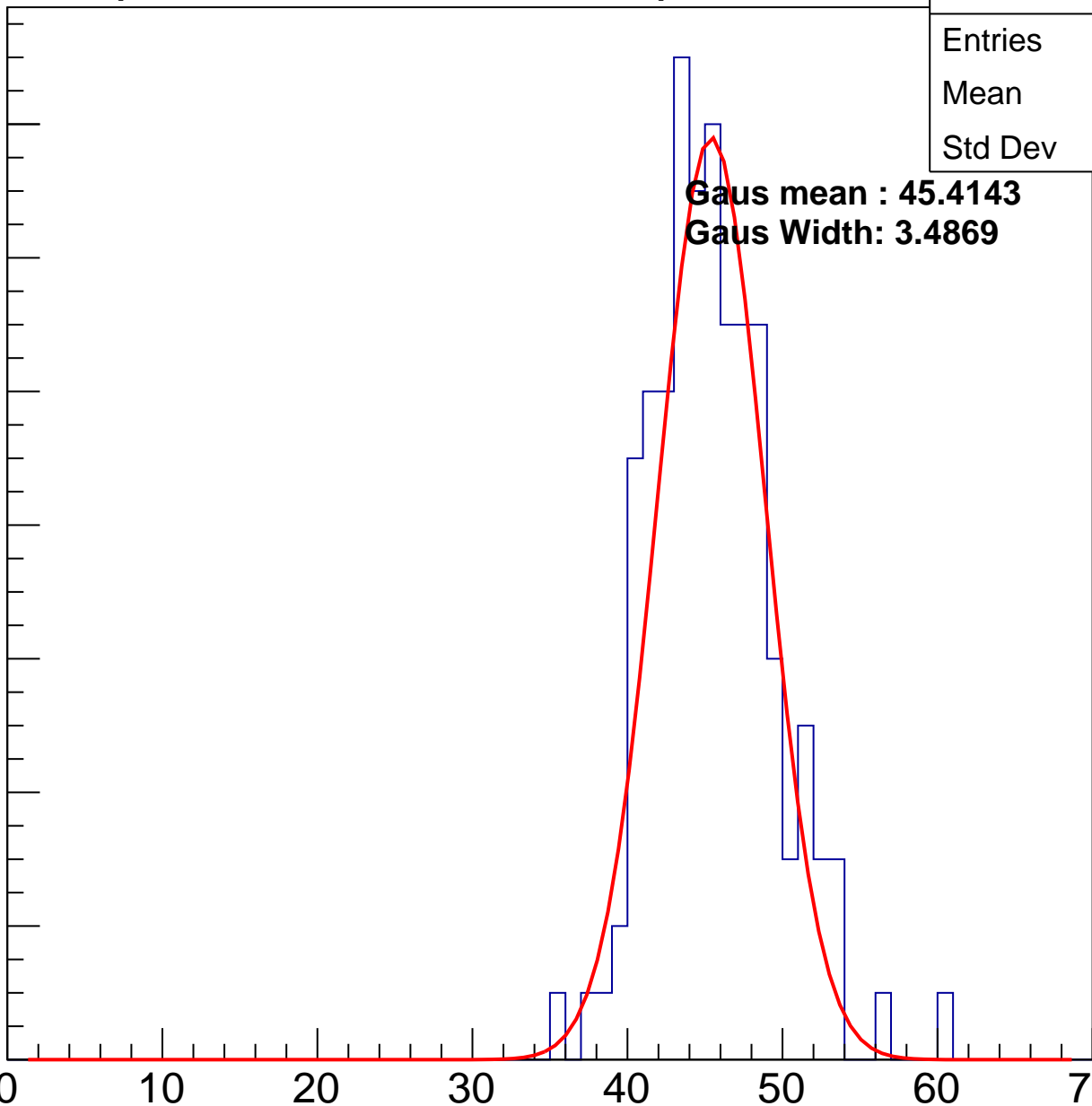
14  
12  
10  
8  
6  
4  
2  
0

Entries	131
Mean	45.08
Std Dev	3.929

**Gaus mean : 45.4143**

**Gaus Width: 3.4869**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

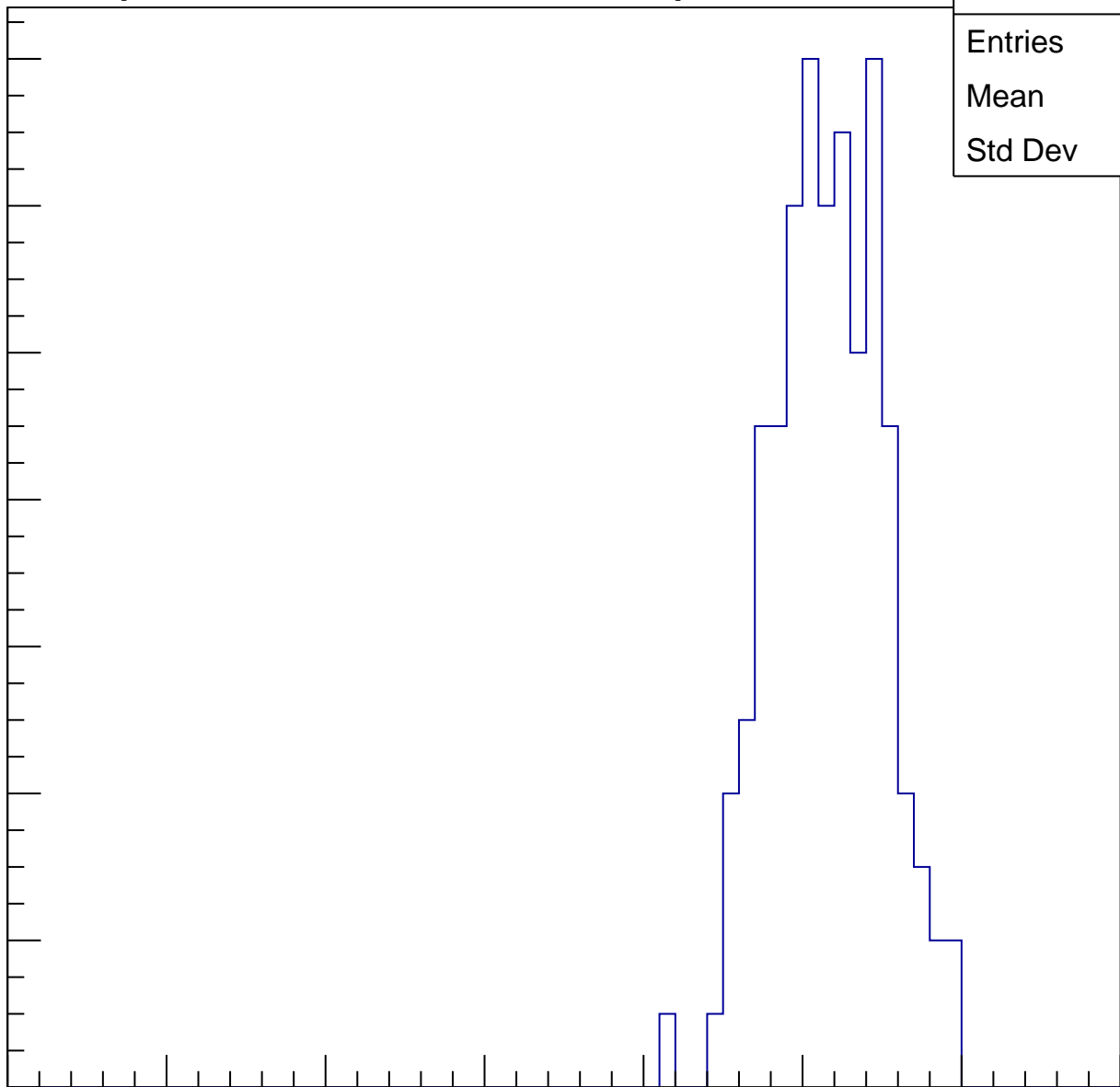
Entries	124
Mean	51.1
Std Dev	3.432

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

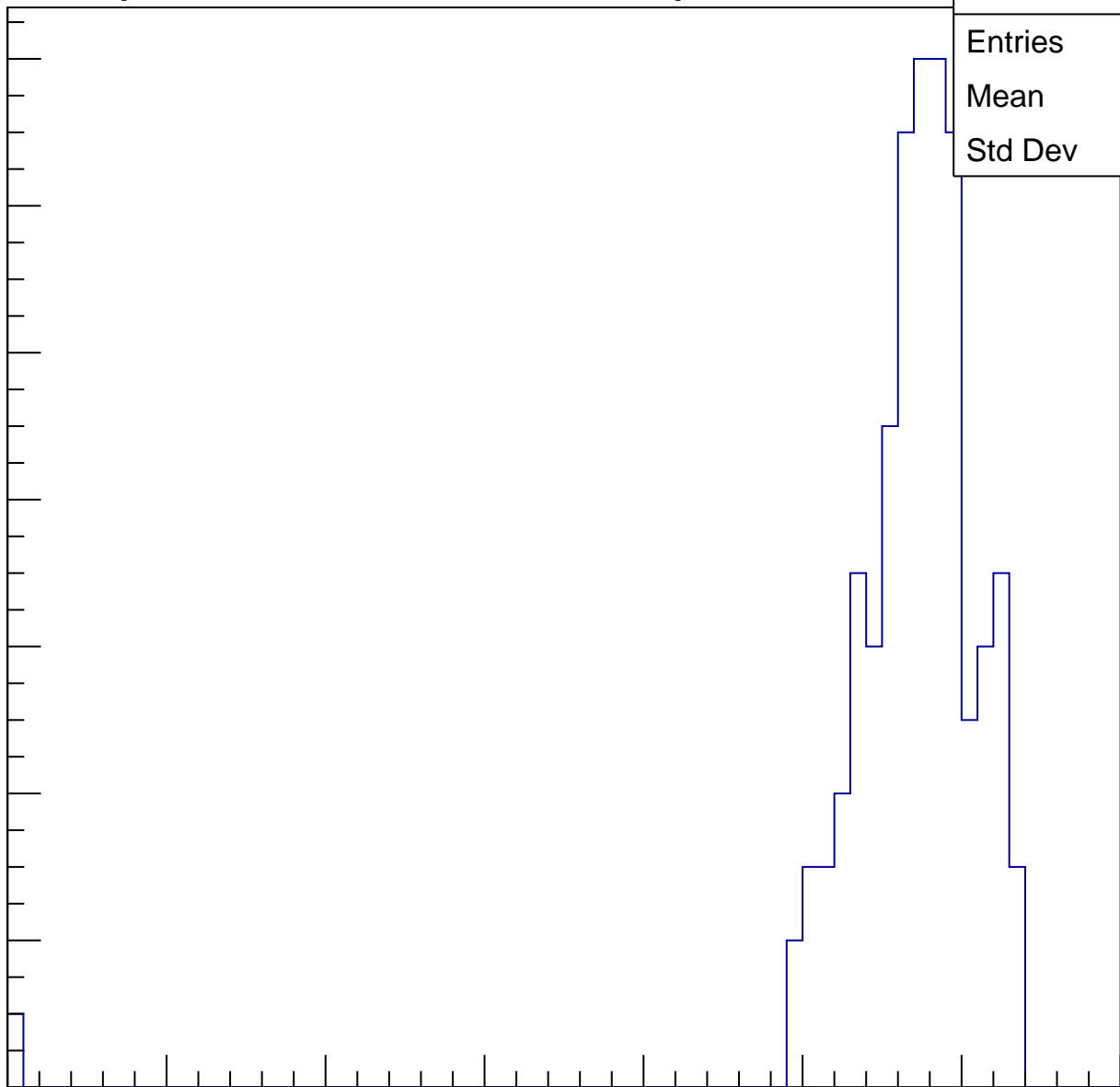
Entries	110
Mean	56.3
Std Dev	6.312

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch123, adc5

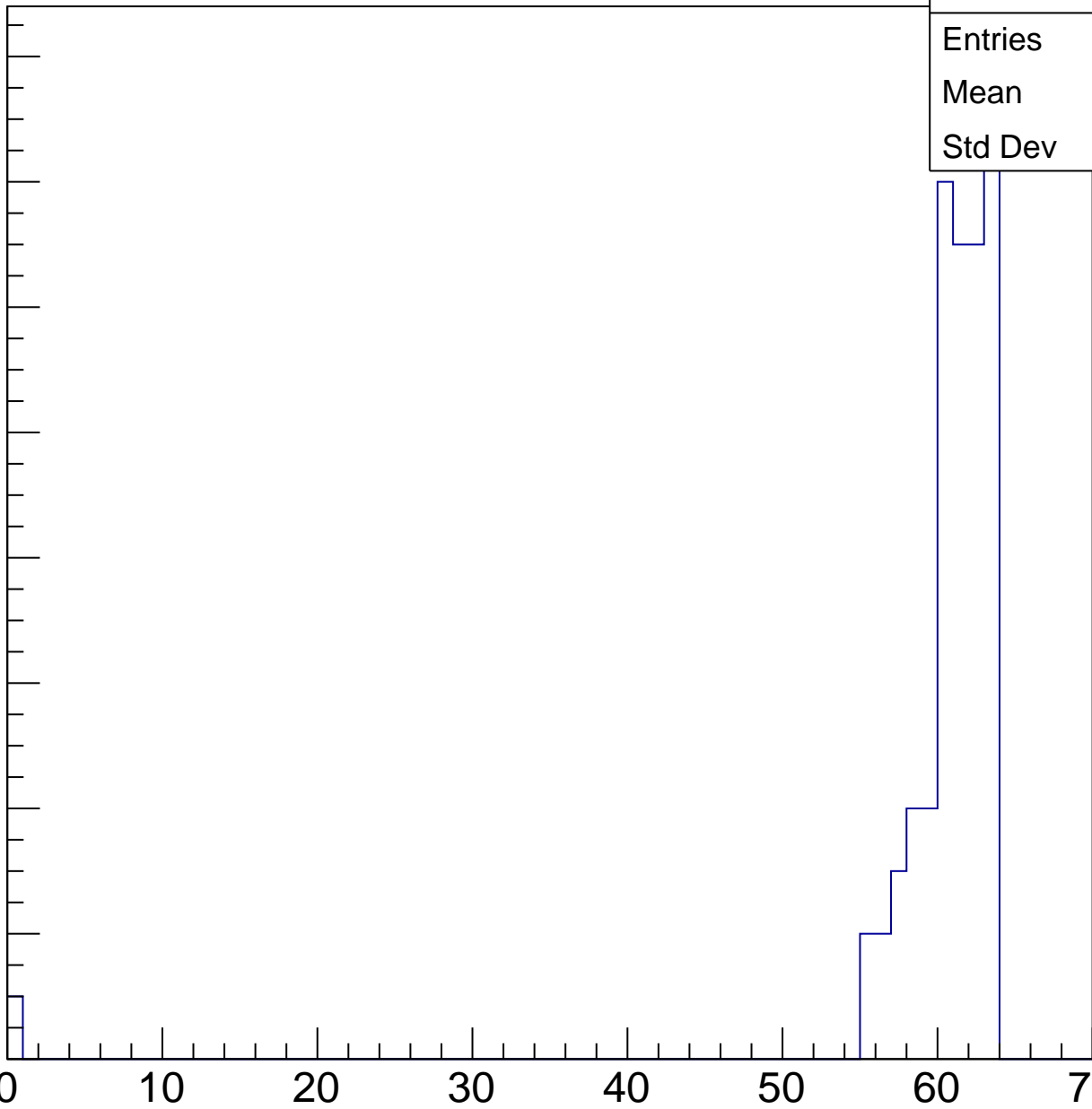
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	59.83
Std Dev	7.394

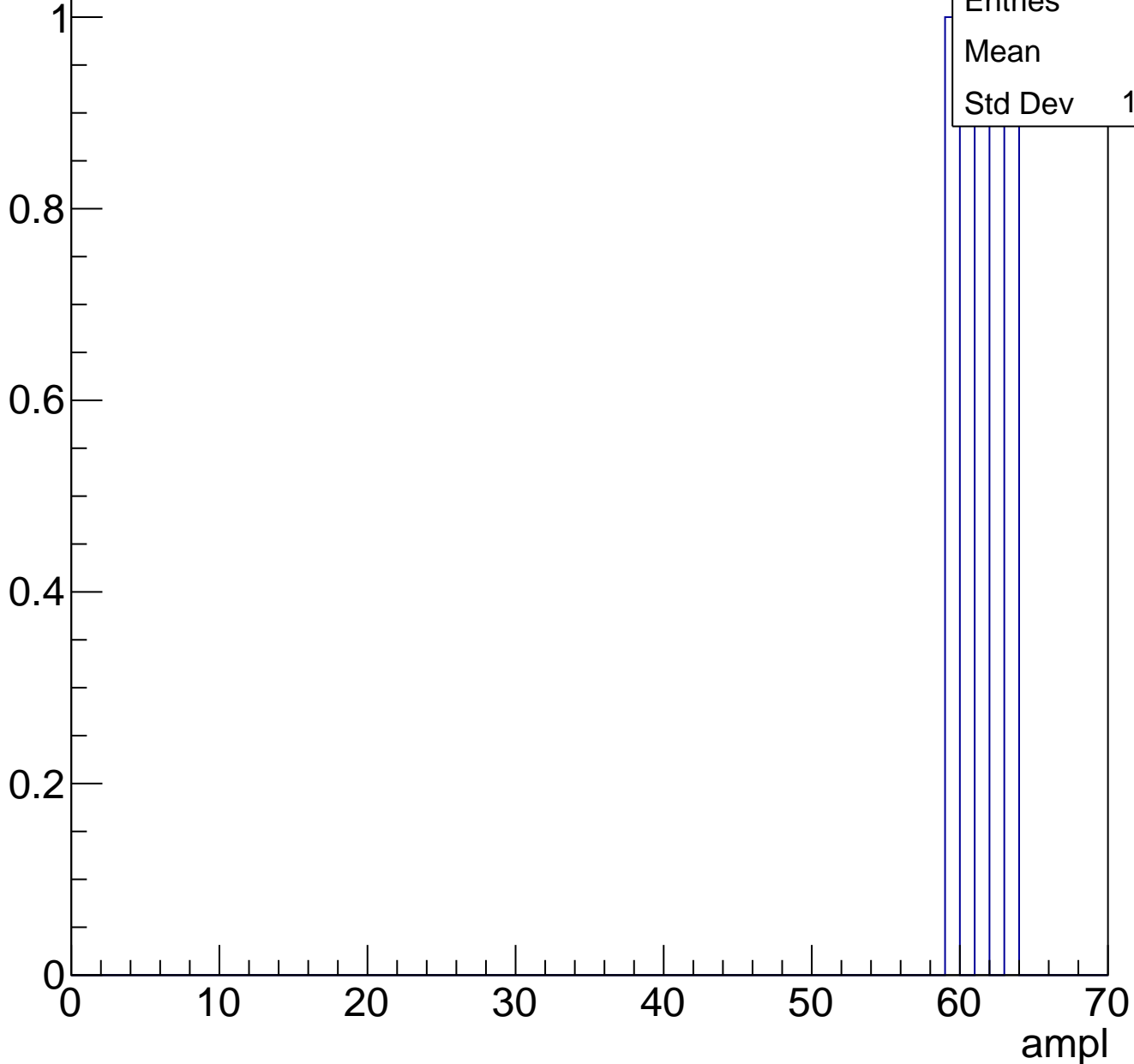
ampl



# B0L101S, U23-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U23-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	28.96
Std Dev	6.467

**Gaus mean : 29.7462**

**Gaus Width: 4.2189**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

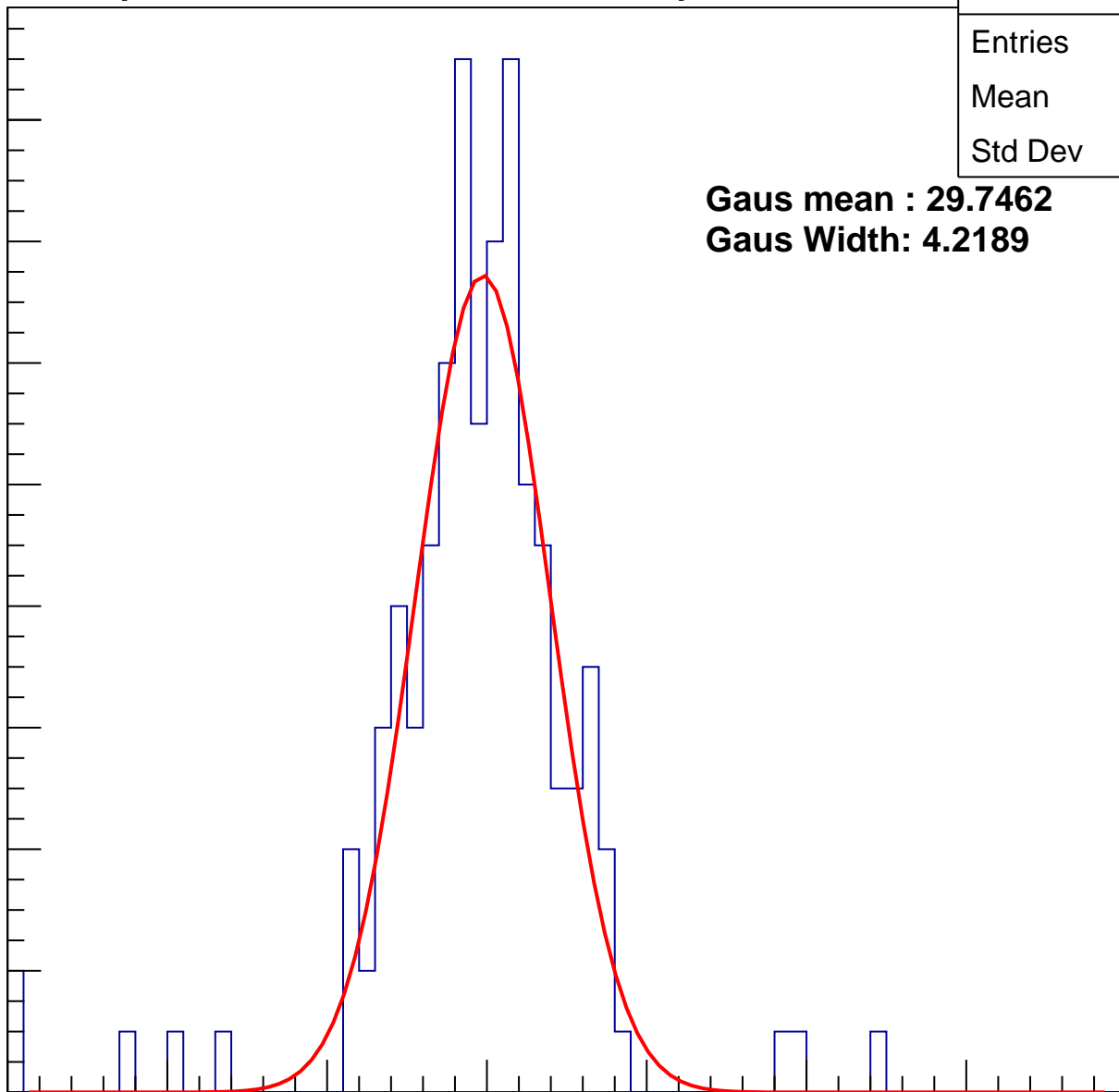
40

50

60

70

ampl



# B0L101S, U23-ch124, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	36.73
Std Dev	3.976

**Gaus mean : 37.1777**

**Gaus Width: 3.8823**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

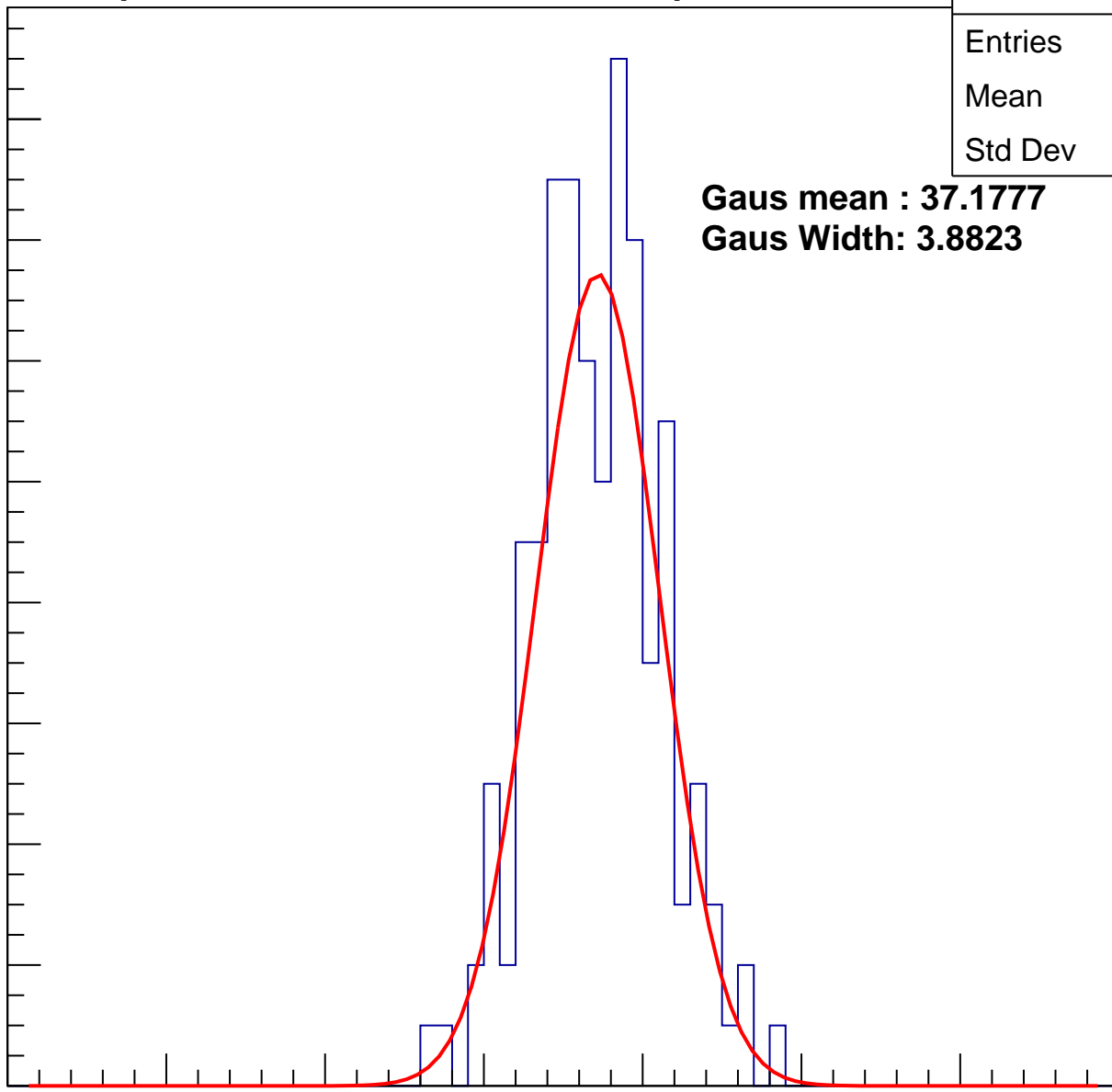
40

50

60

70

ampl



# B0L101S, U23-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

128

Mean

43.46

Std Dev

4.1

**Gaus mean : 44.5986**

**Gaus Width: 3.9749**

0

10

20

30

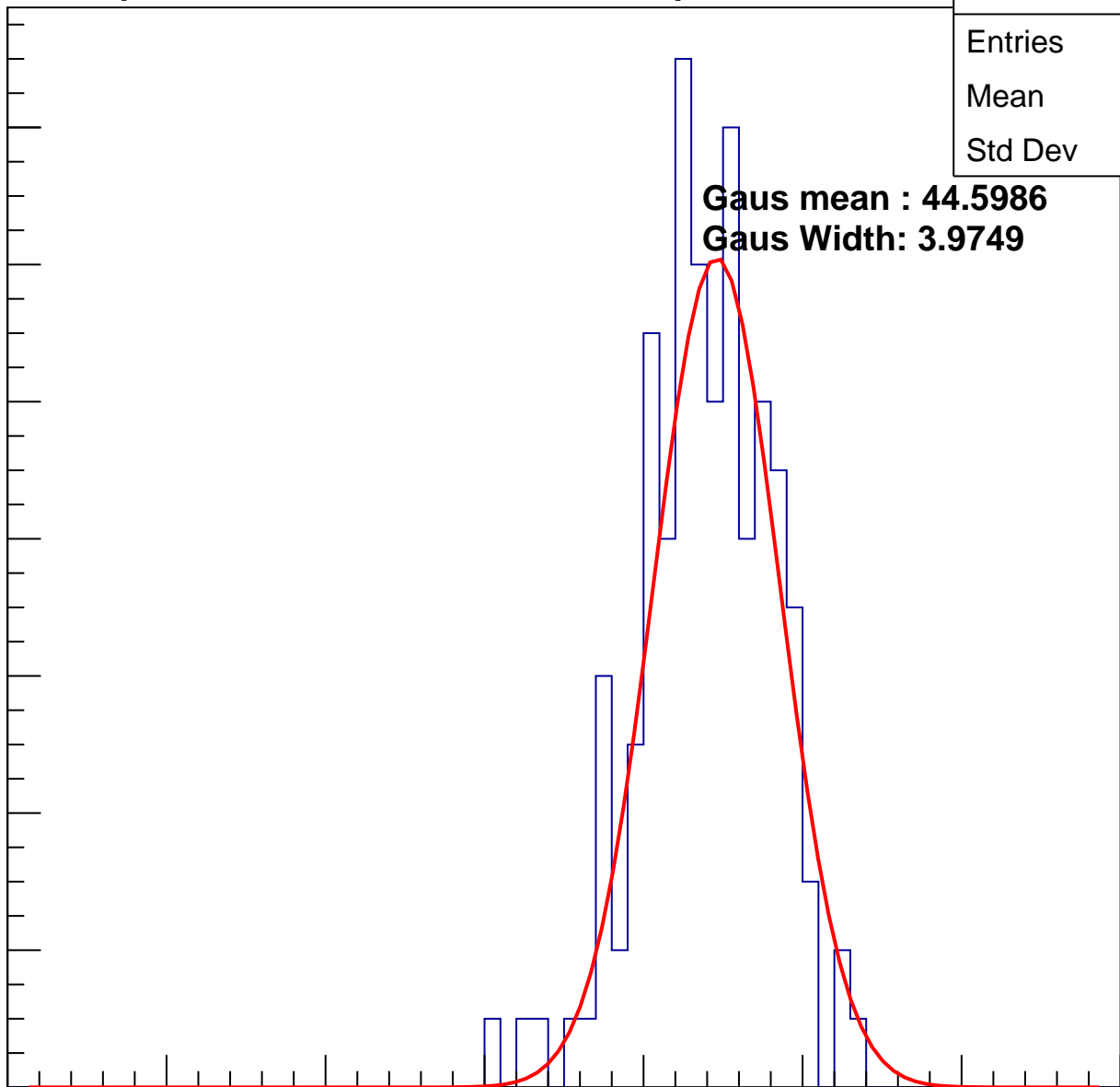
40

50

60

70

ampl



# B0L101S, U23-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

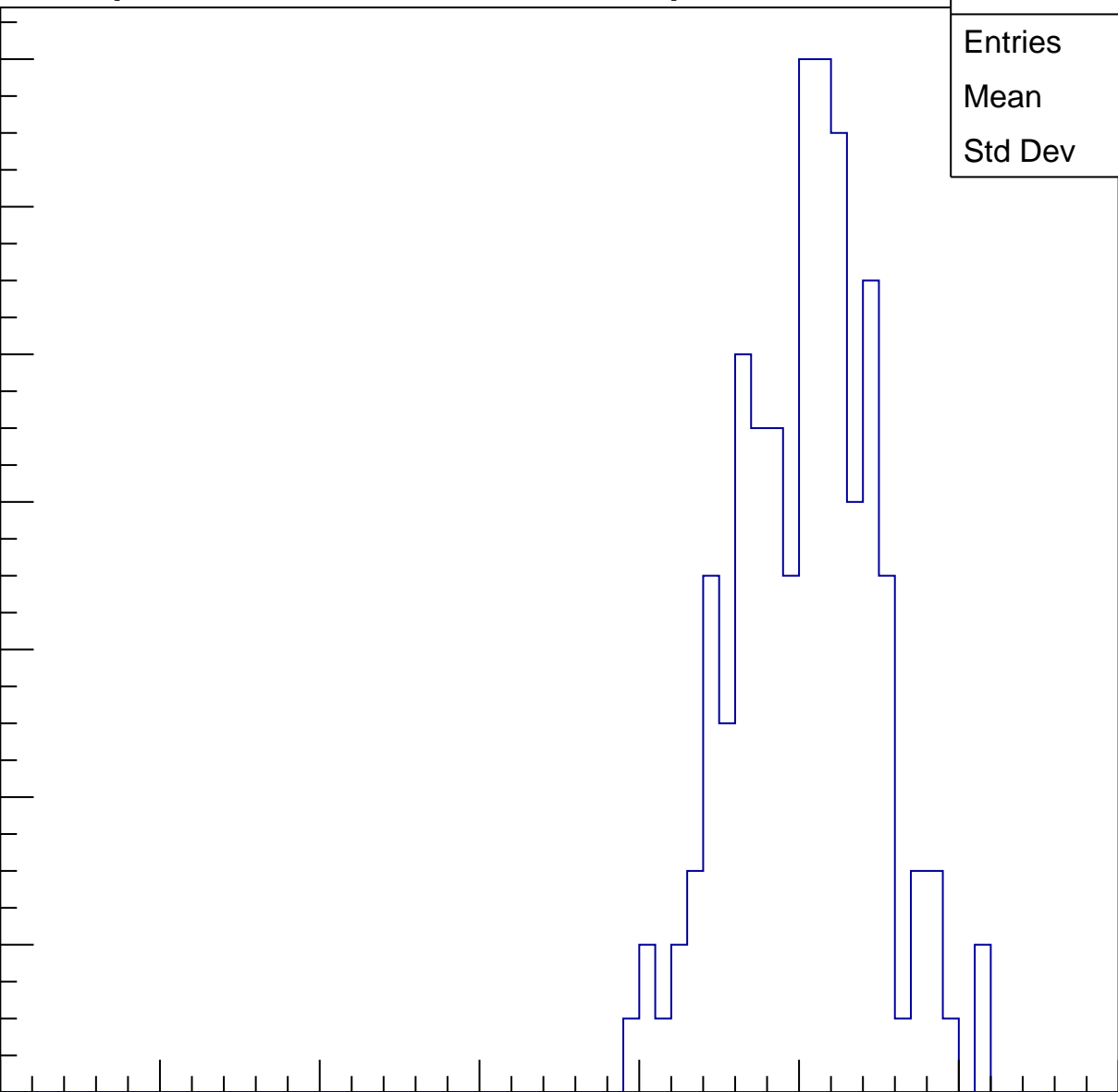
Entries	133
Mean	49.92
Std Dev	4.356

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

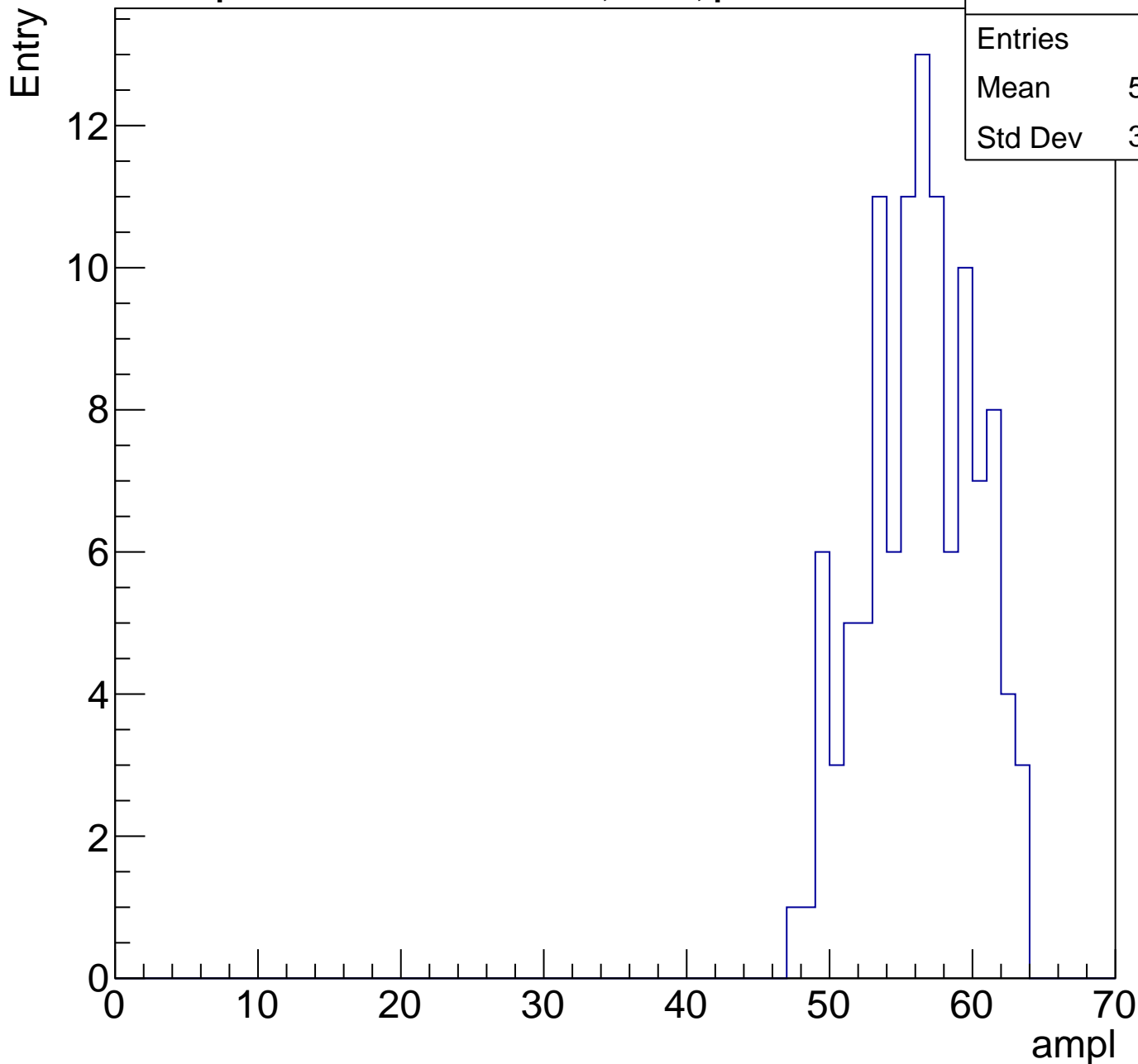
ampl



# B0L101S, U23-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	111
Mean	55.89
Std Dev	3.812



# B0L101S, U23-ch124, adc5

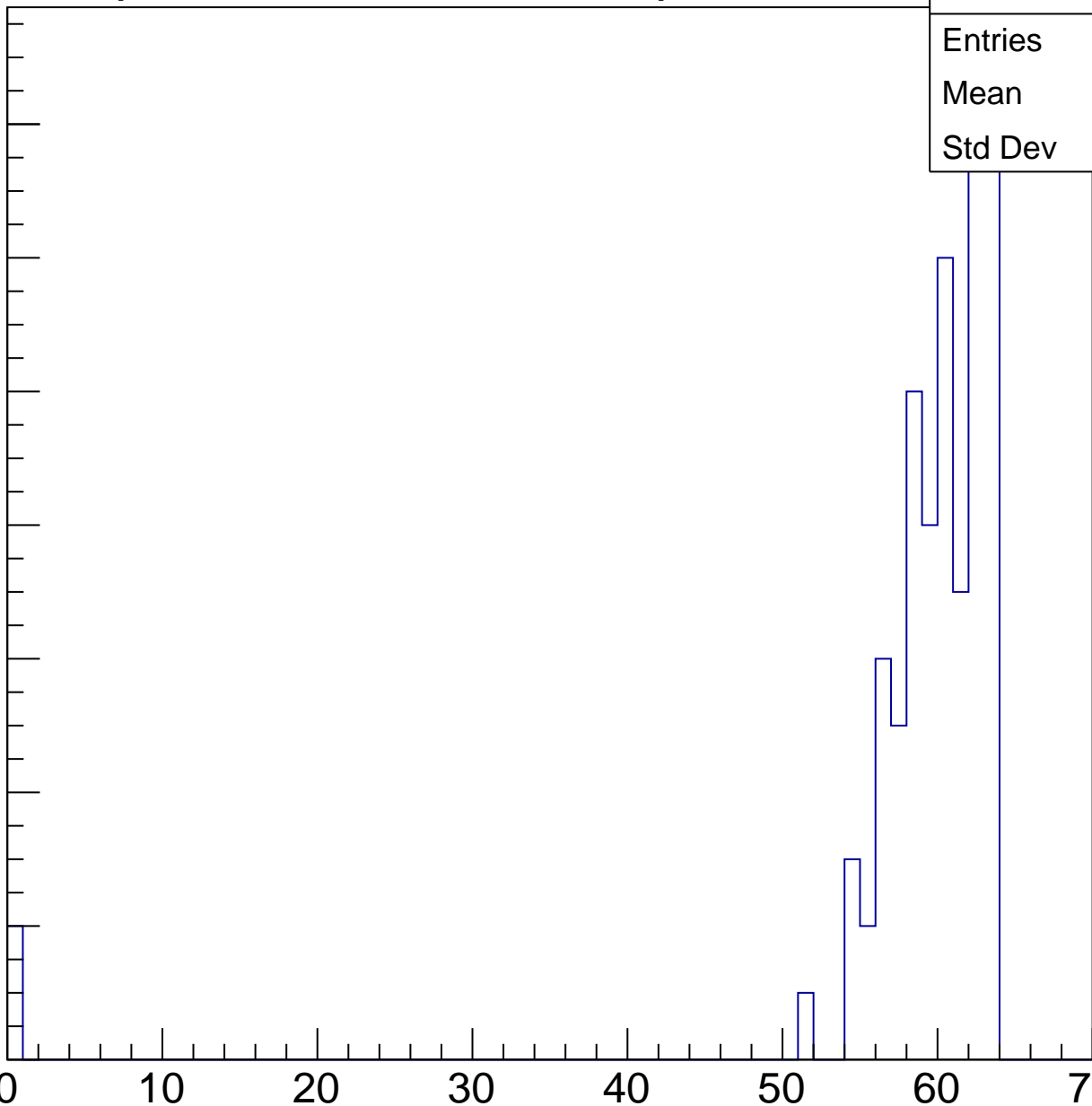
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	86
Mean	58.35
Std Dev	9.398

ampl



# B0L101S, U23-ch124, adc6

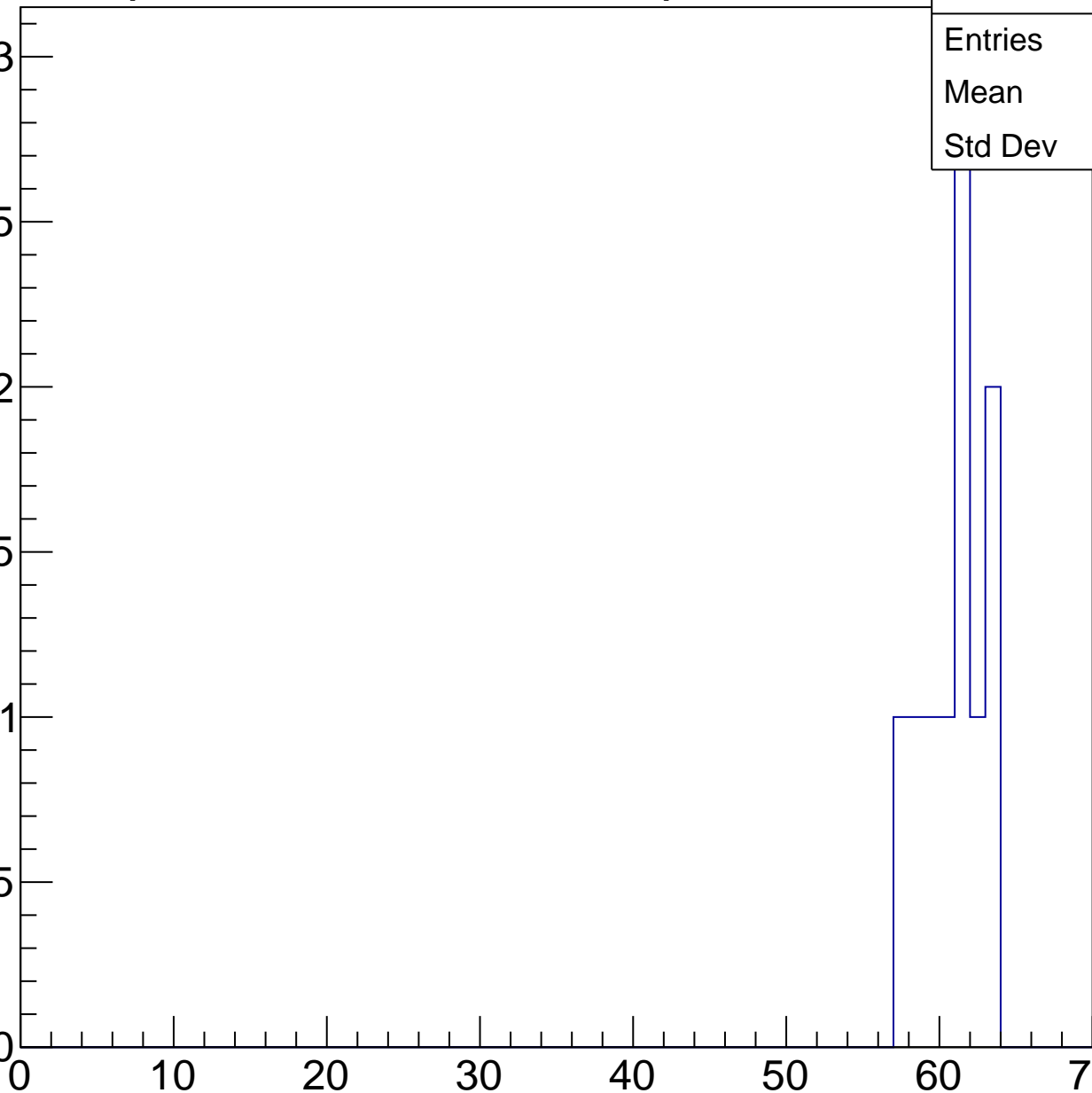
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	10
Mean	60.5
Std Dev	1.91

ampl





# B0L101S, U23-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

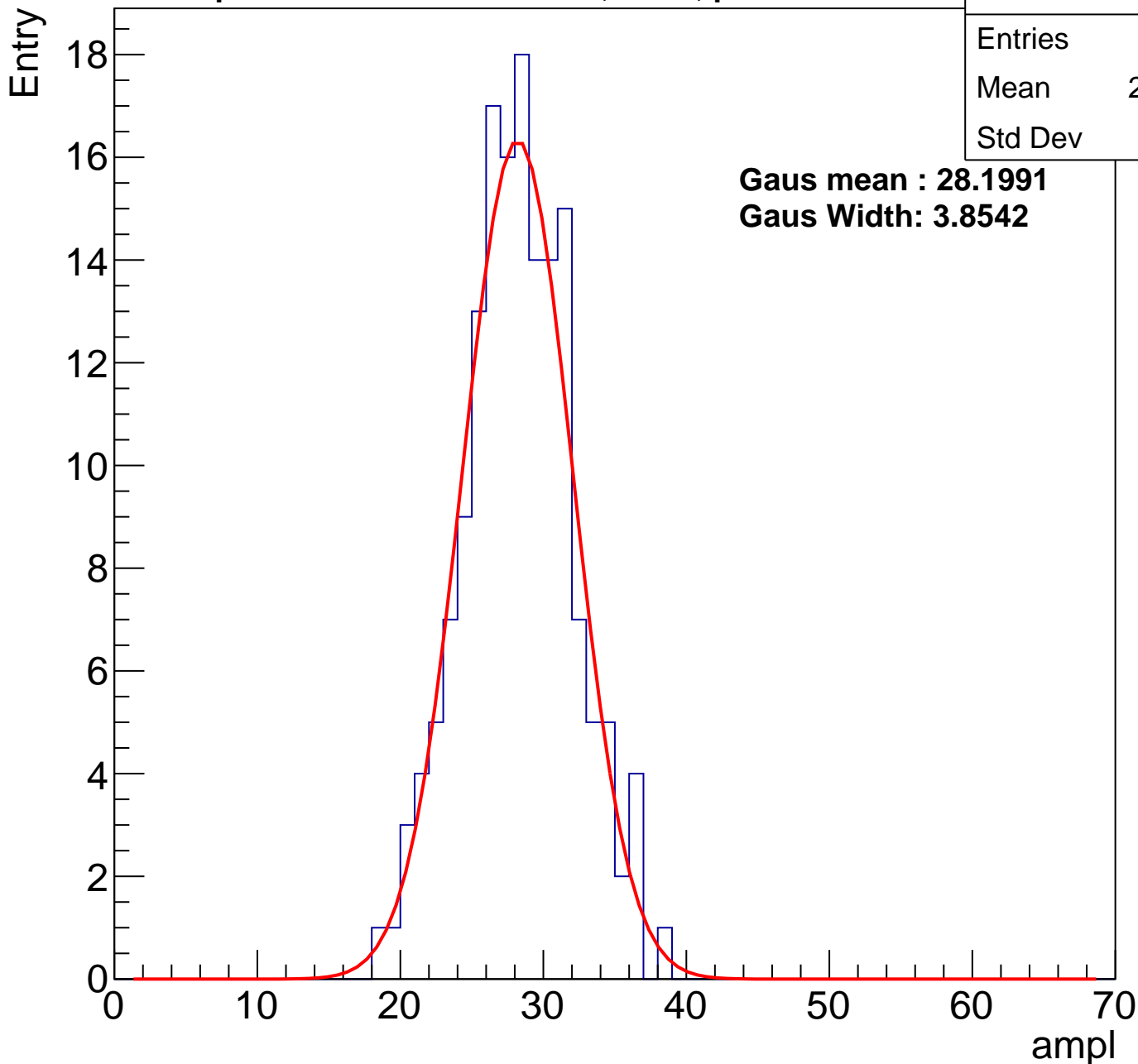
# B0L101S, U23-ch125, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	27.78
Std Dev	3.81

**Gaus mean : 28.1991**

**Gaus Width: 3.8542**



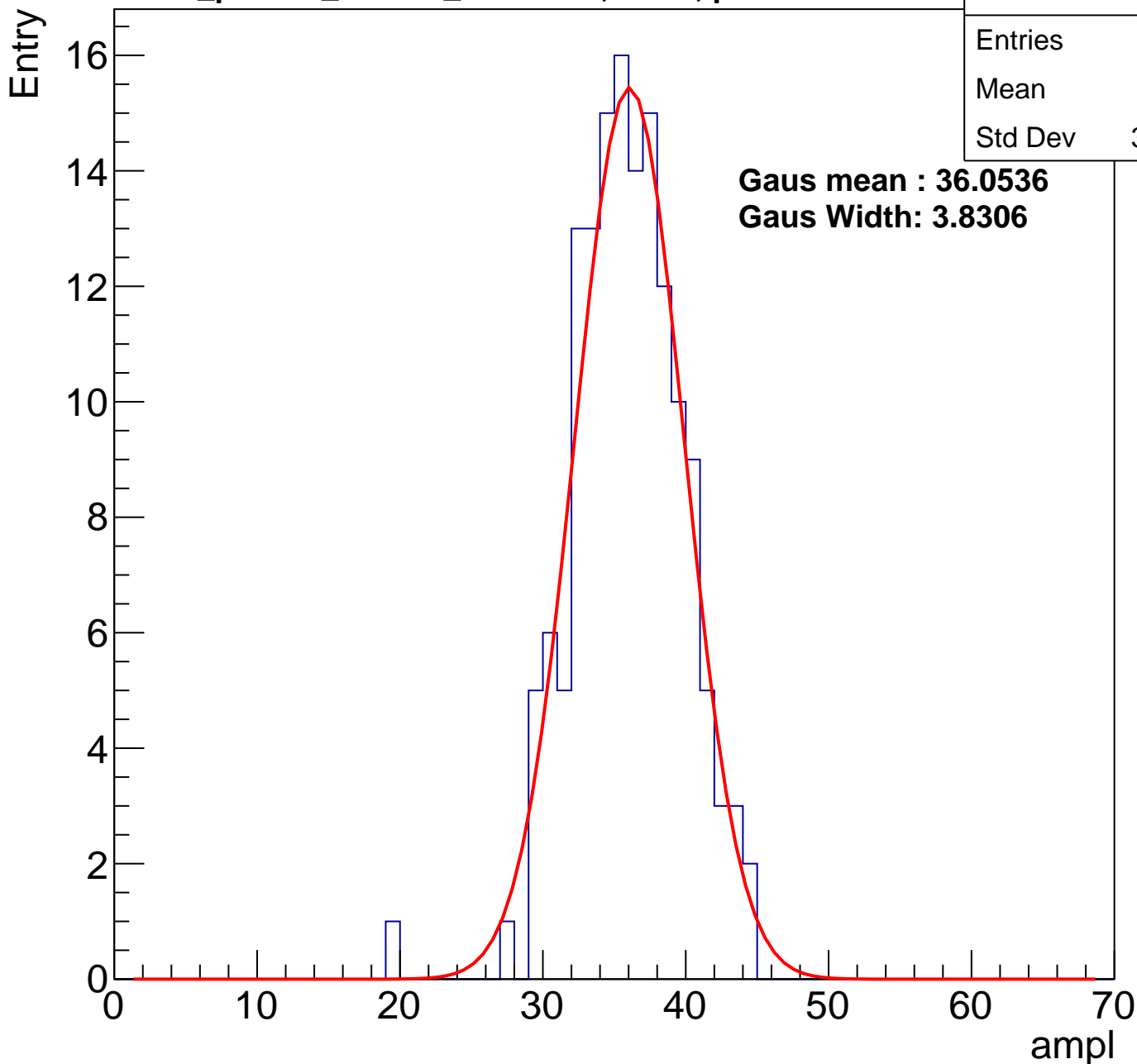
# B0L101S, U23-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	35.5
Std Dev	3.789

**Gaus mean : 36.0536**

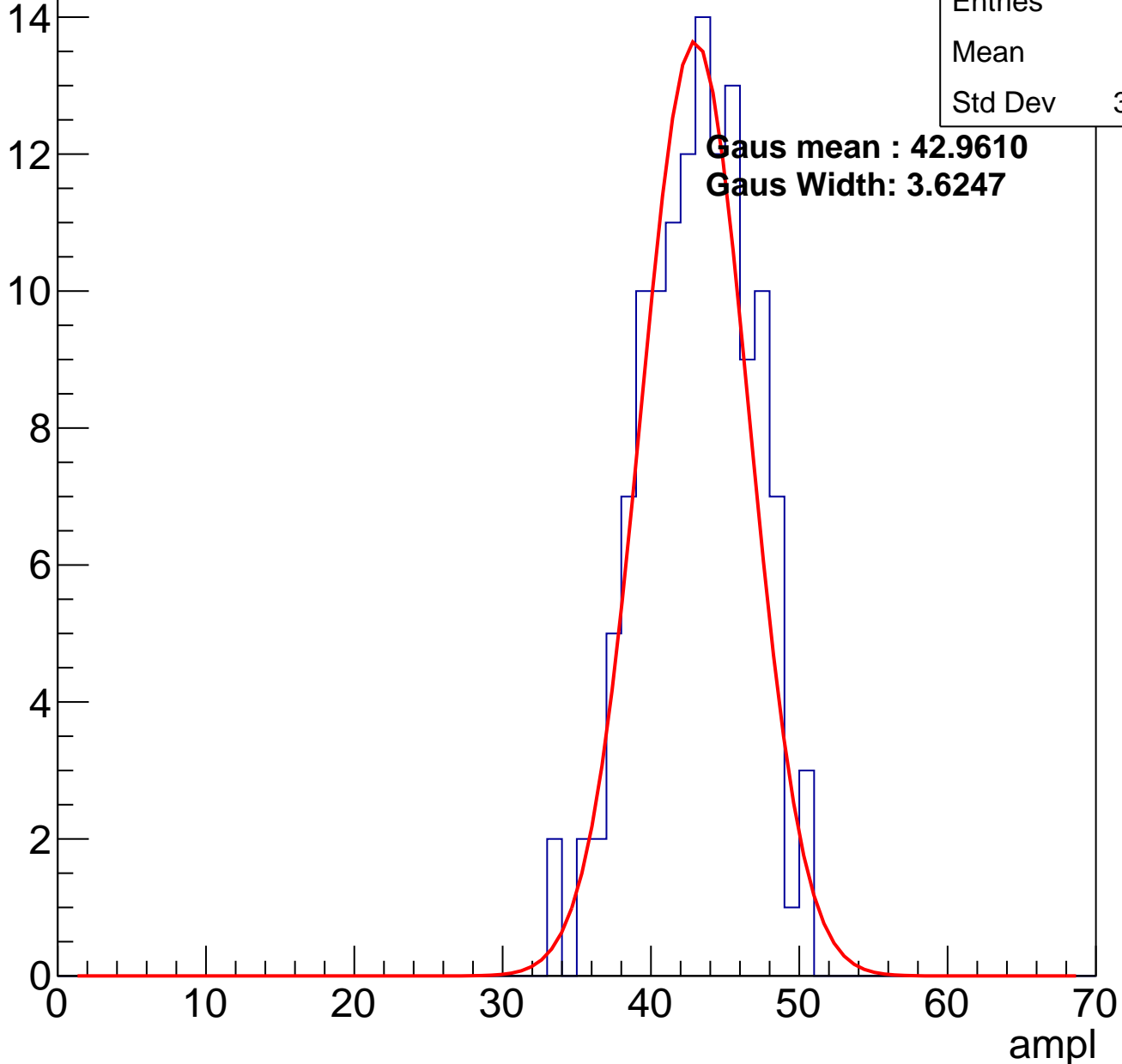
**Gaus Width: 3.8306**



# B0L101S, U23-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch125, adc3

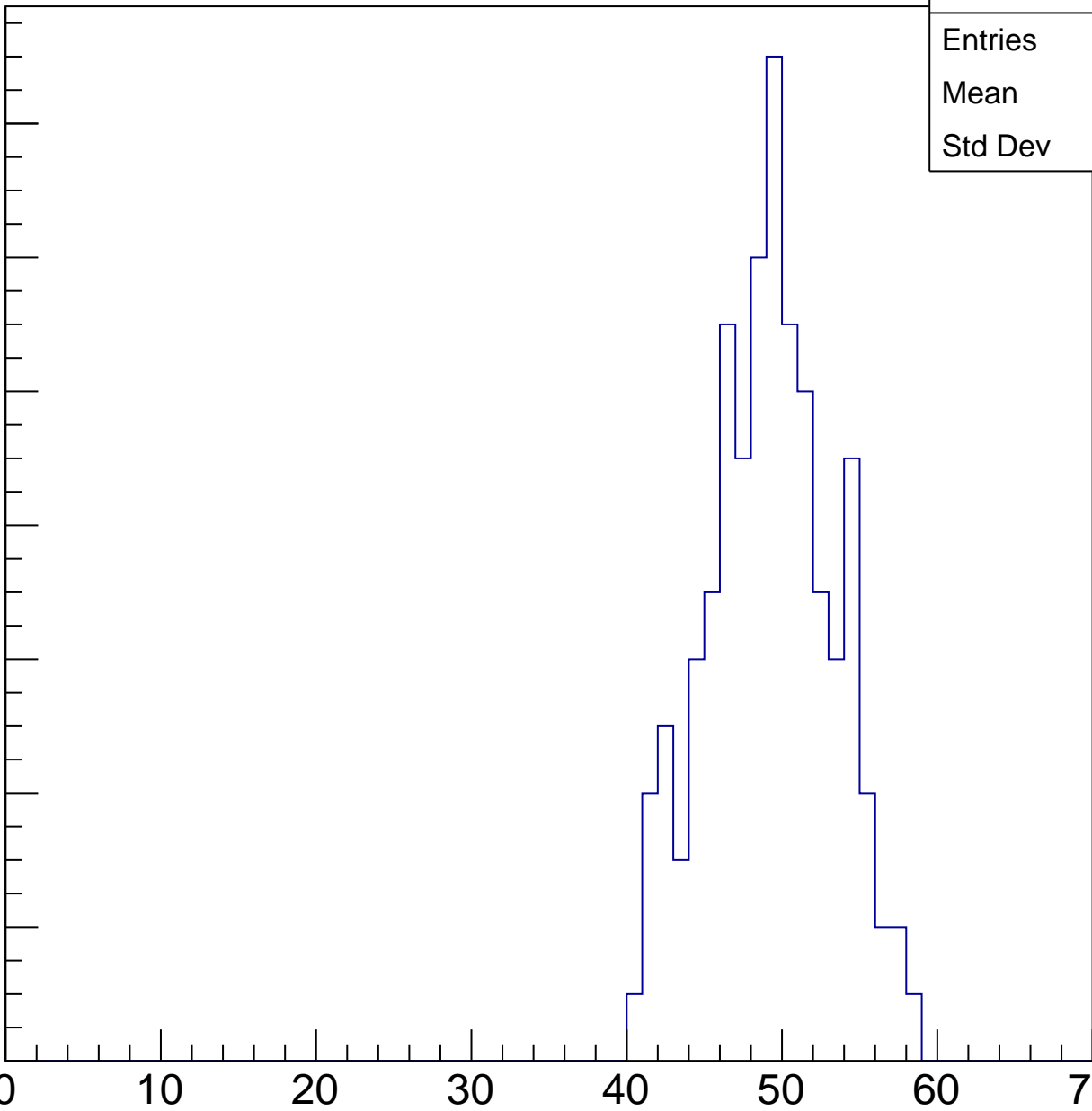
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	125
Mean	48.75
Std Dev	3.977

ampl



# B0L101S, U23-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

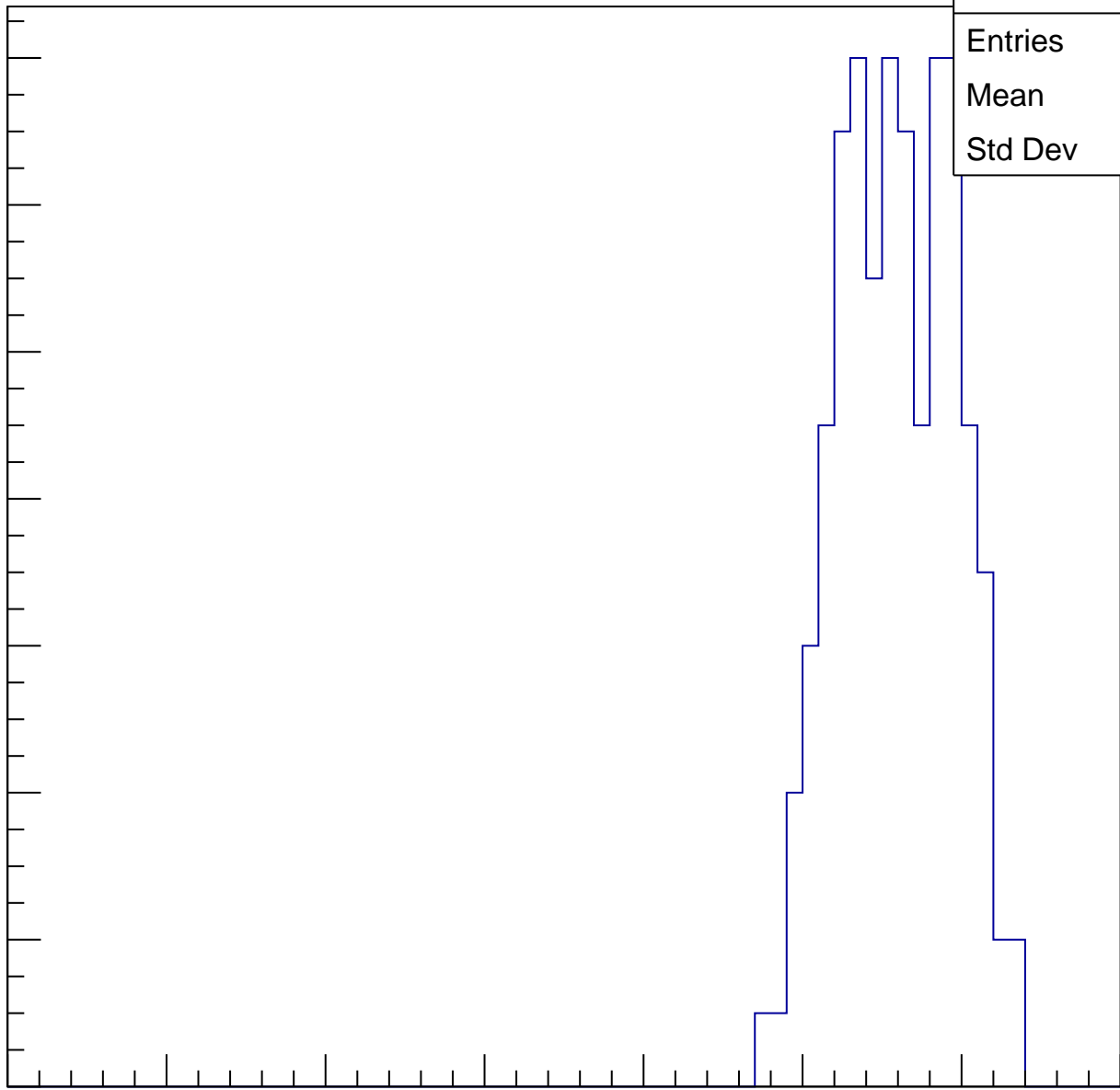
Entries	143
Mean	55.44
Std Dev	3.557

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U23-ch125, adc5

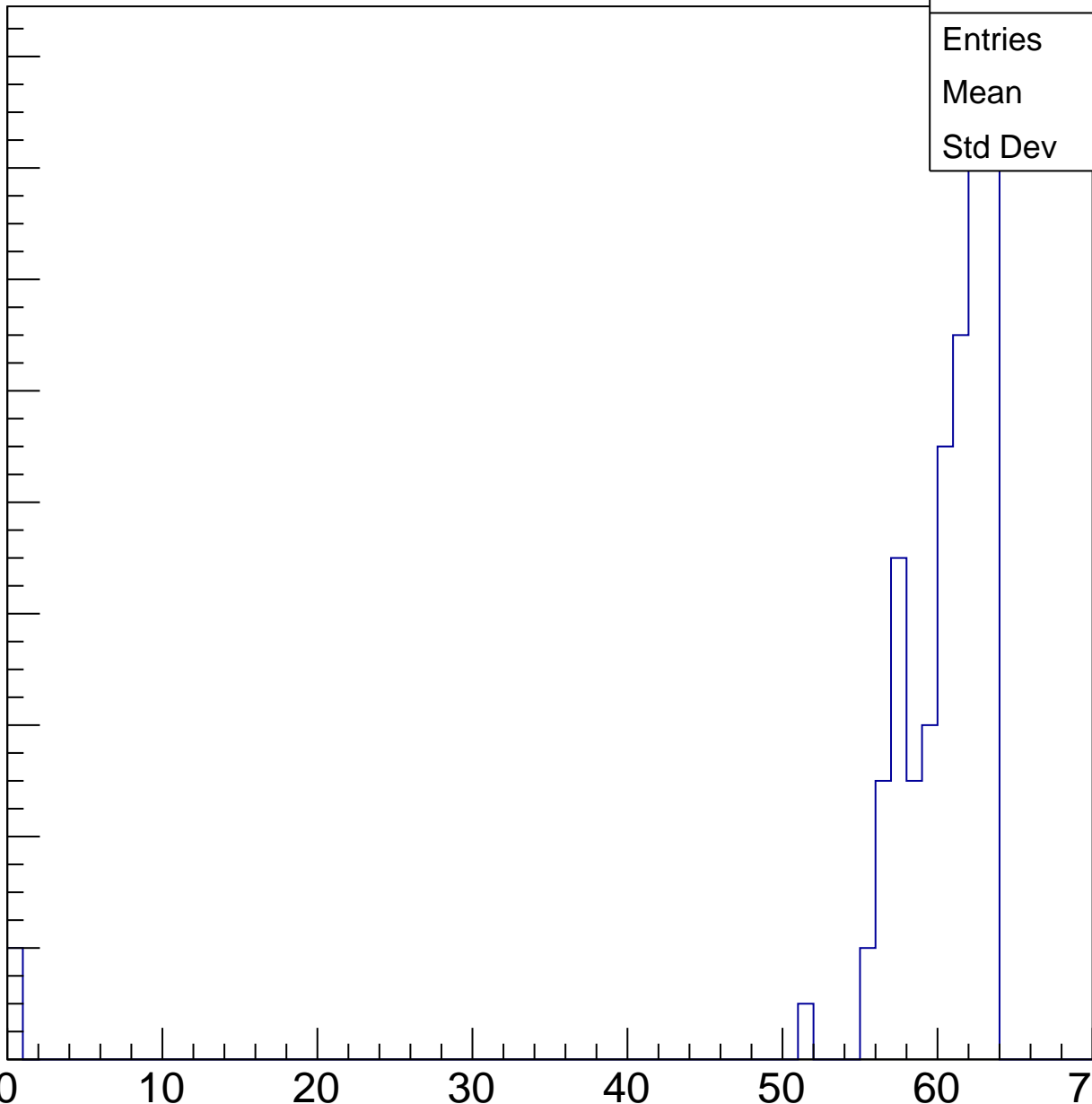
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	58.81
Std Dev	9.306

ampl



# B0L101S, U23-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U23-ch126, adc0

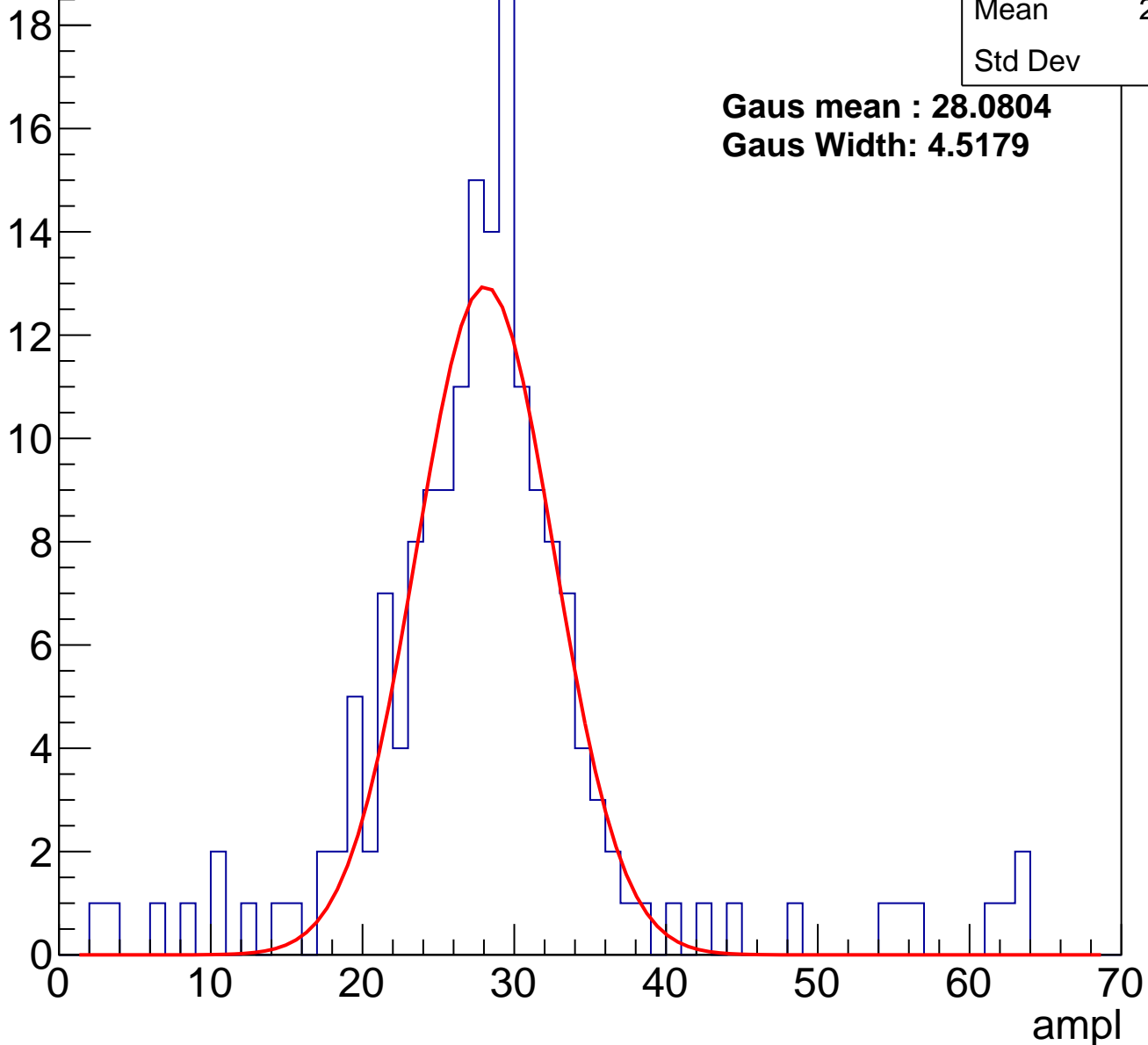
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	173
Mean	28.04
Std Dev	9.1

**Gaus mean : 28.0804**

**Gaus Width: 4.5179**

Entry



# B0L101S, U23-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

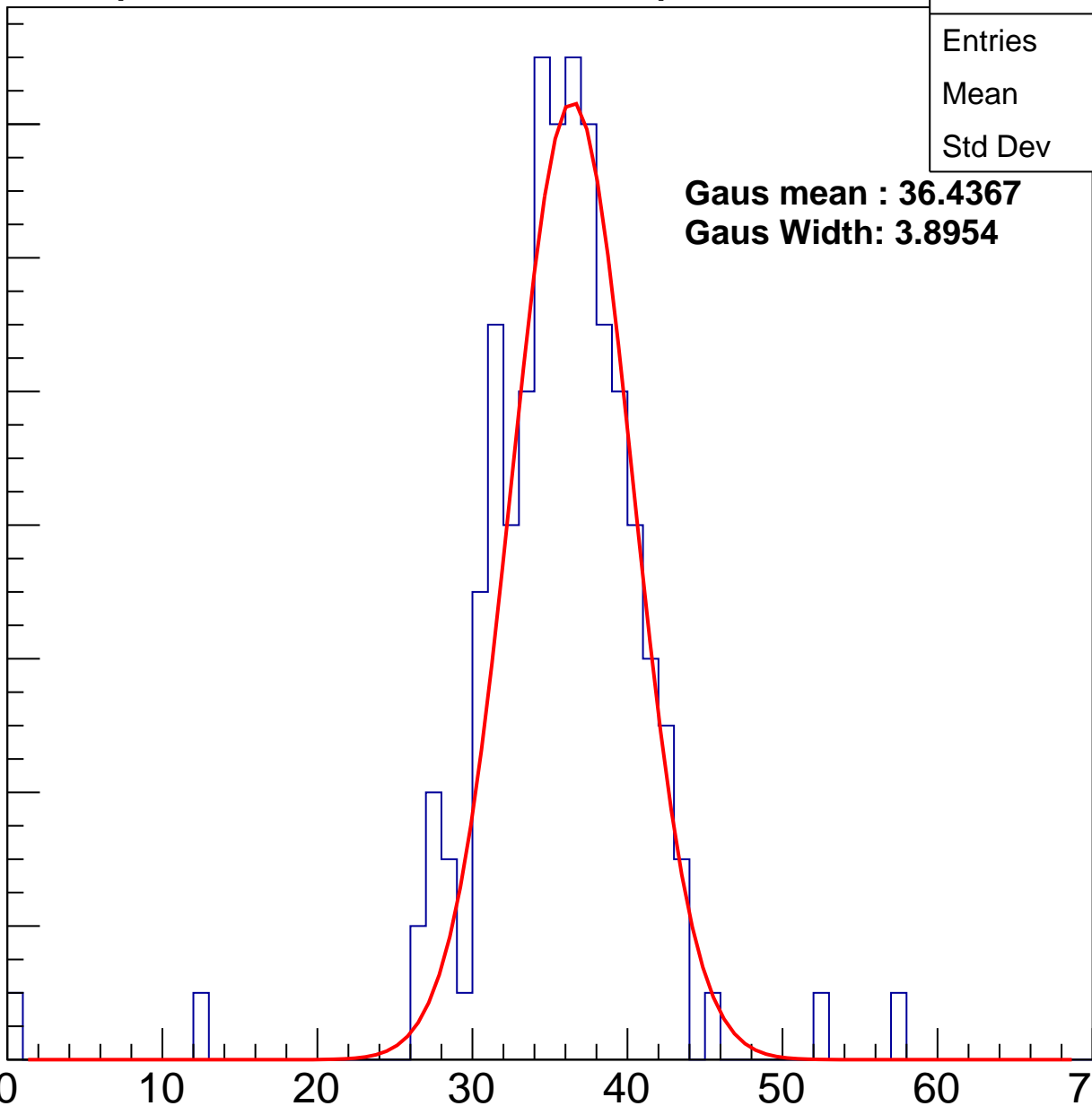
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	152
Mean	35.18
Std Dev	5.669

**Gaus mean : 36.4367**  
**Gaus Width: 3.8954**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U23-ch126, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

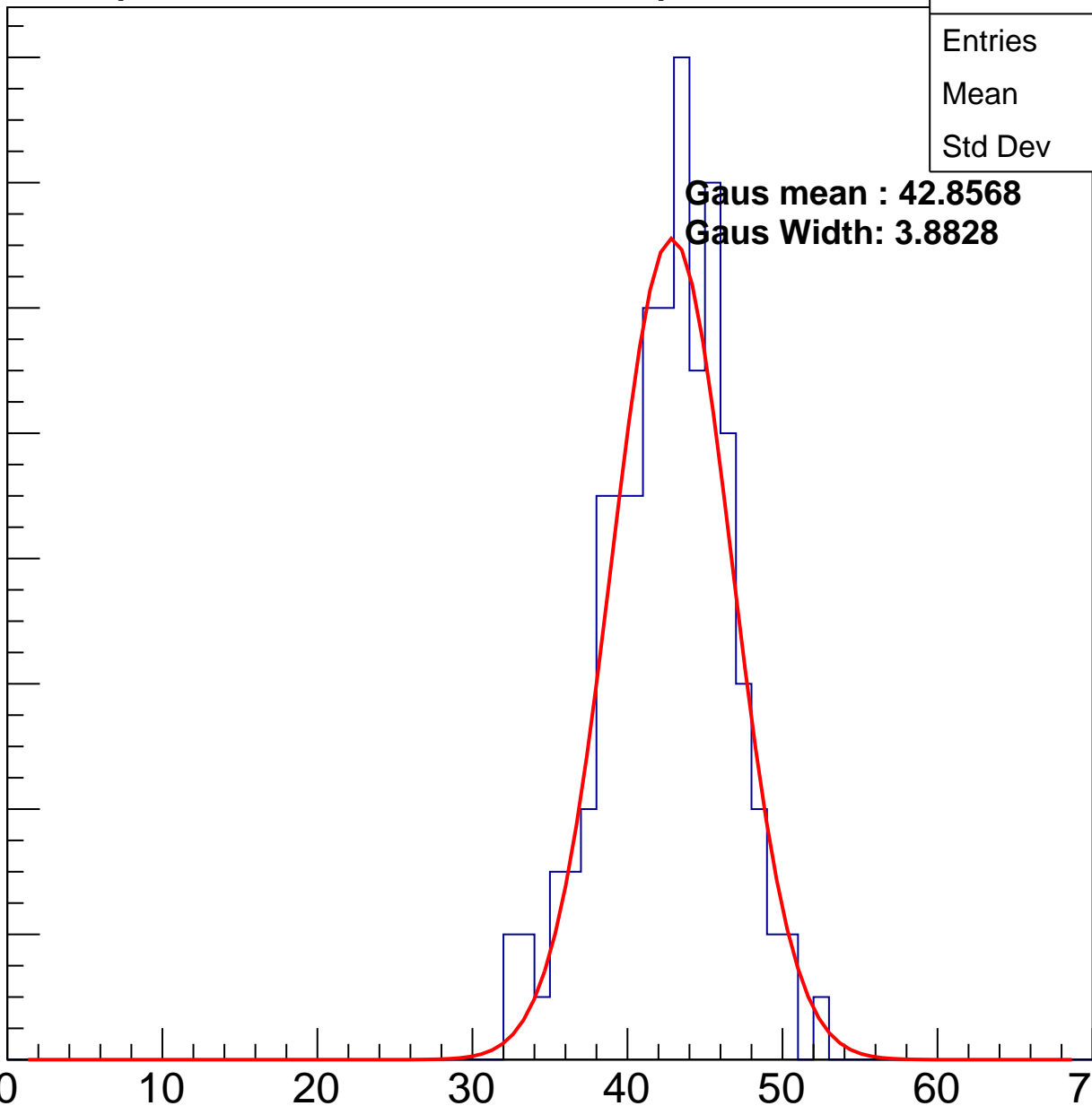
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	42.12
Std Dev	3.91

**Gaus mean : 42.8568**

**Gaus Width: 3.8828**

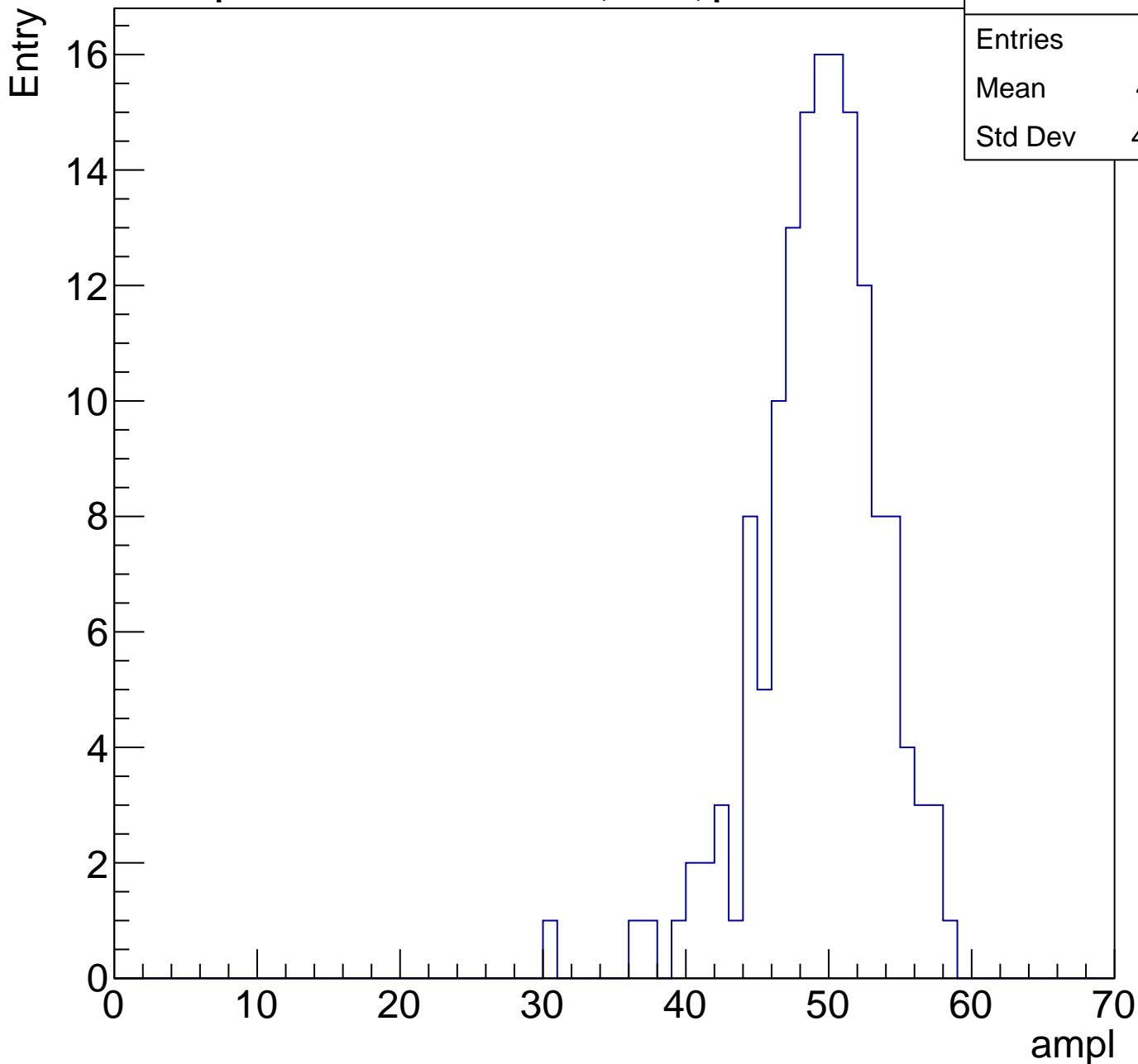
ampl



# B0L101S, U23-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	48.91
Std Dev	4.303



# B0L101S, U23-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	55.37
Std Dev	6.661

Entry

10

8

6

4

2

0

0

10

20

30

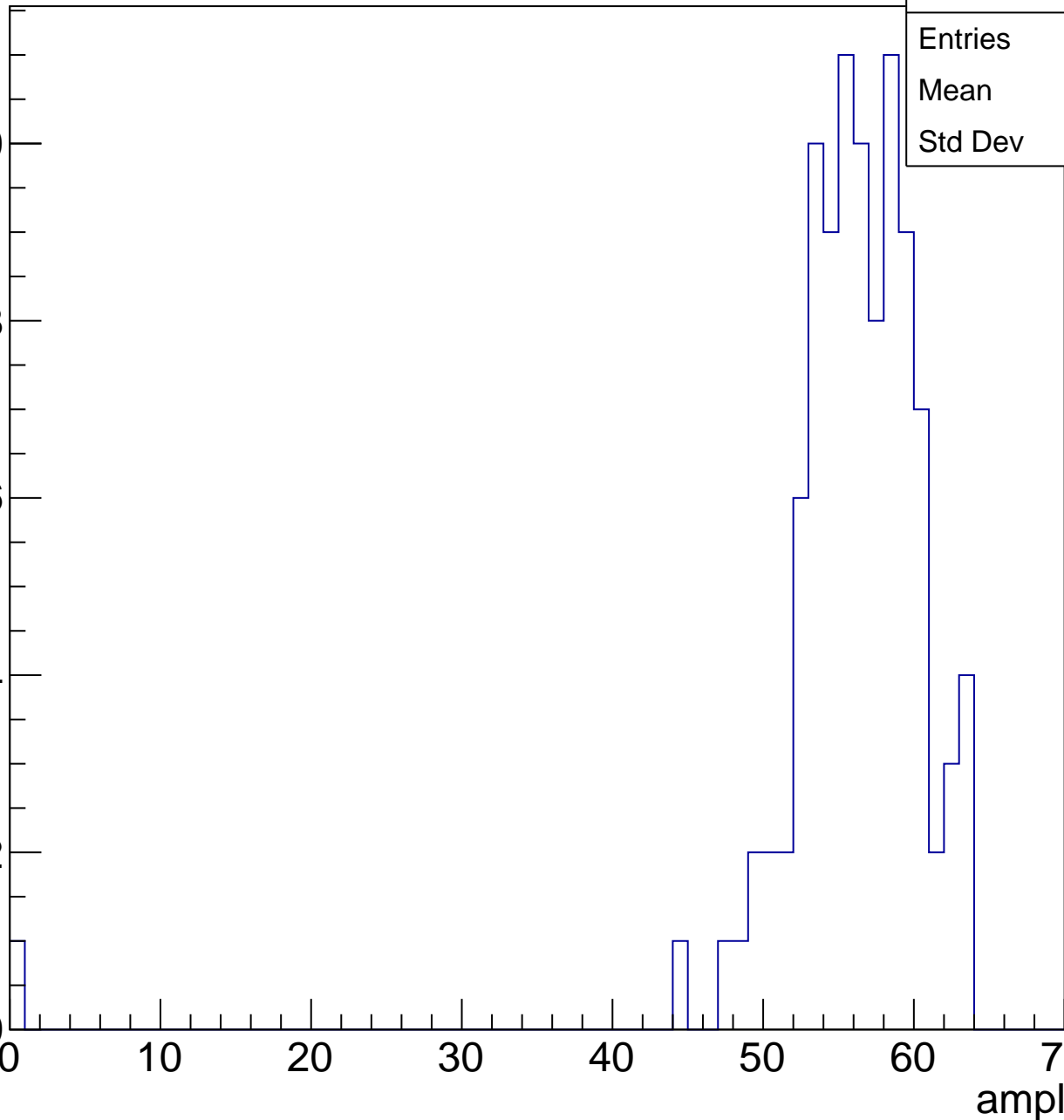
40

50

60

70

ampl



# B0L101S, U23-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	92
Mean	58.24
Std Dev	6.859

ampl

0

10

20

30

40

50

60

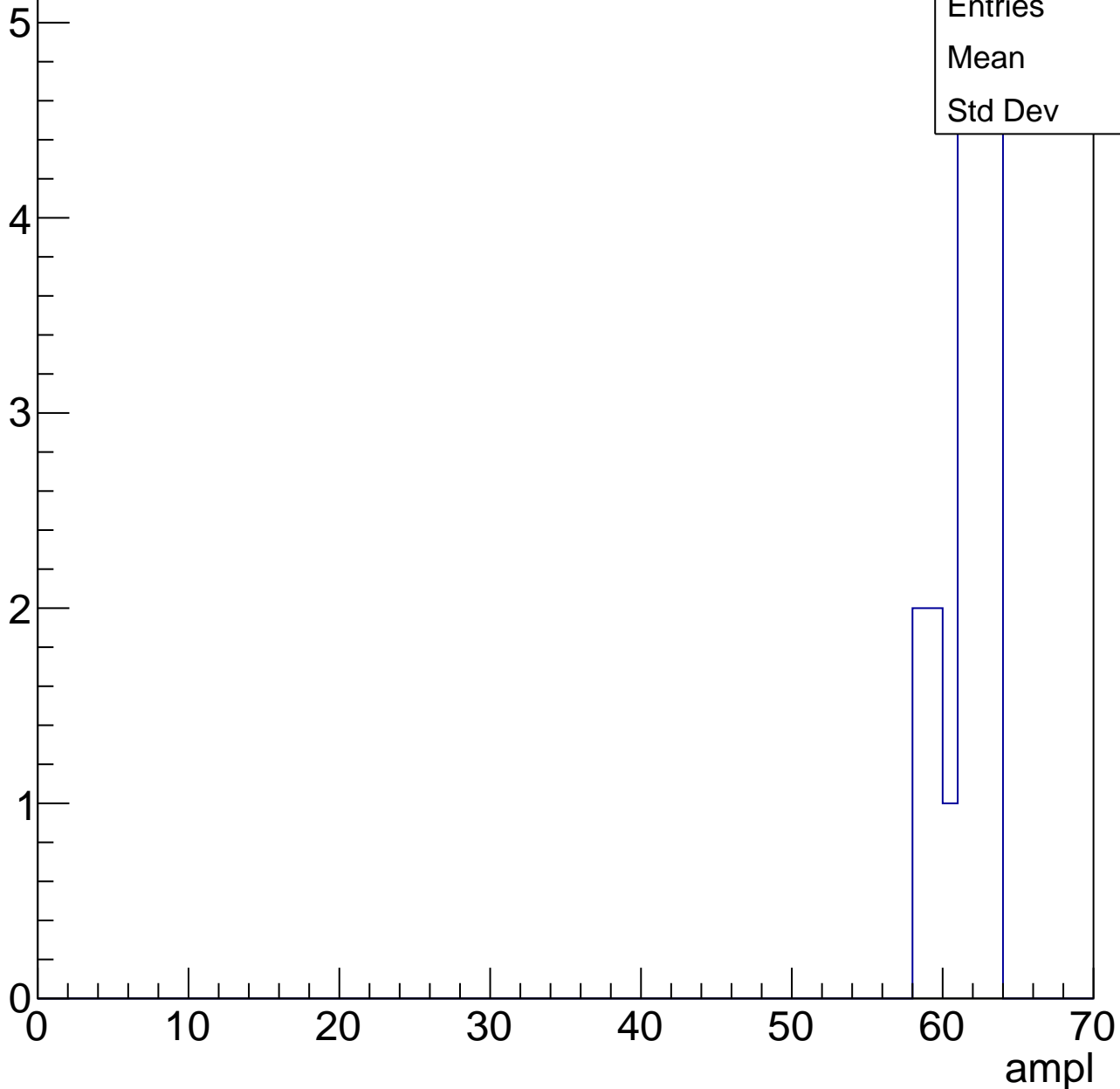
70

# B0L101S, U23-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.2
Std Dev	1.6

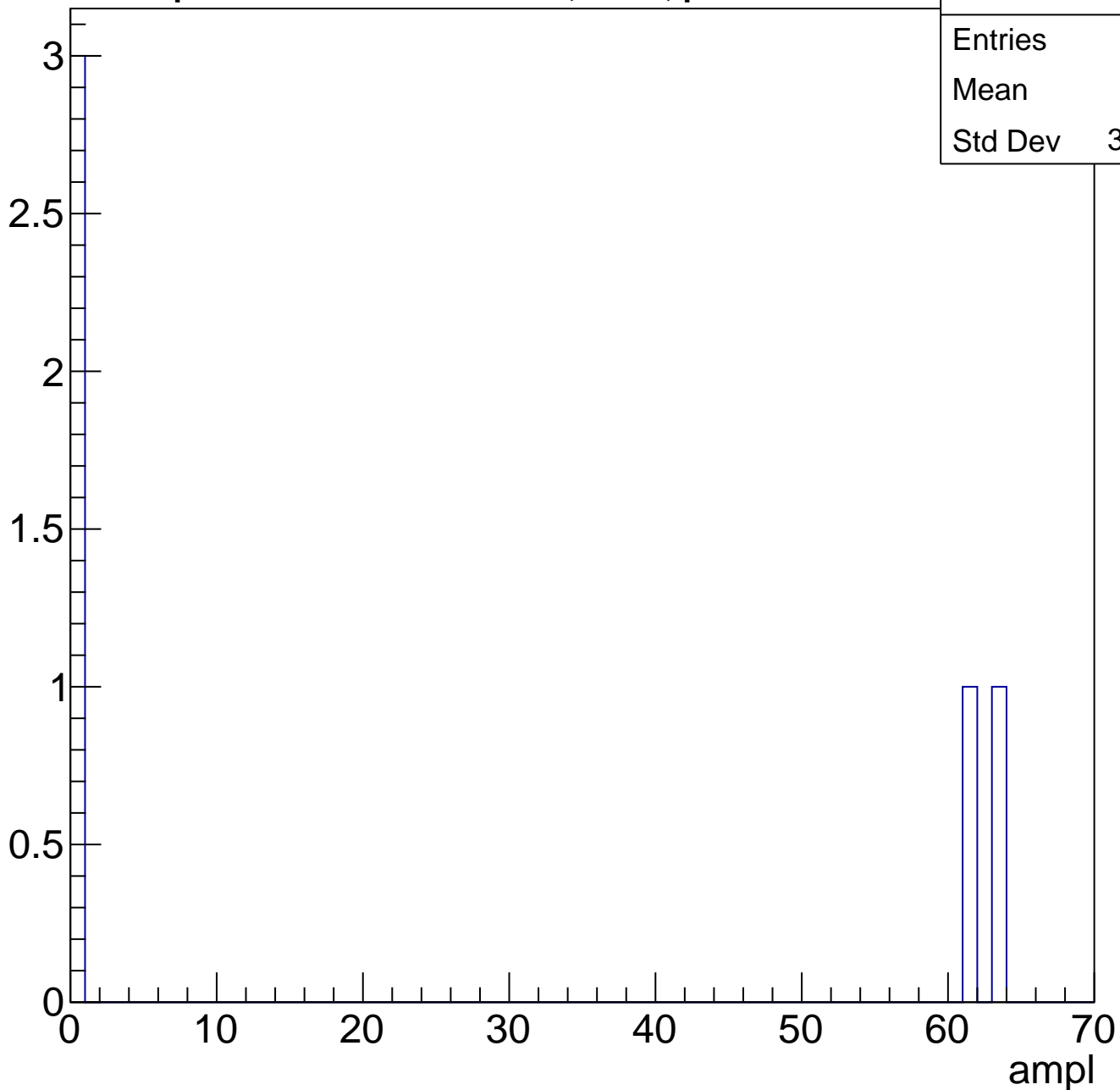




# B0L101S, U23-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U23-ch127, adc0

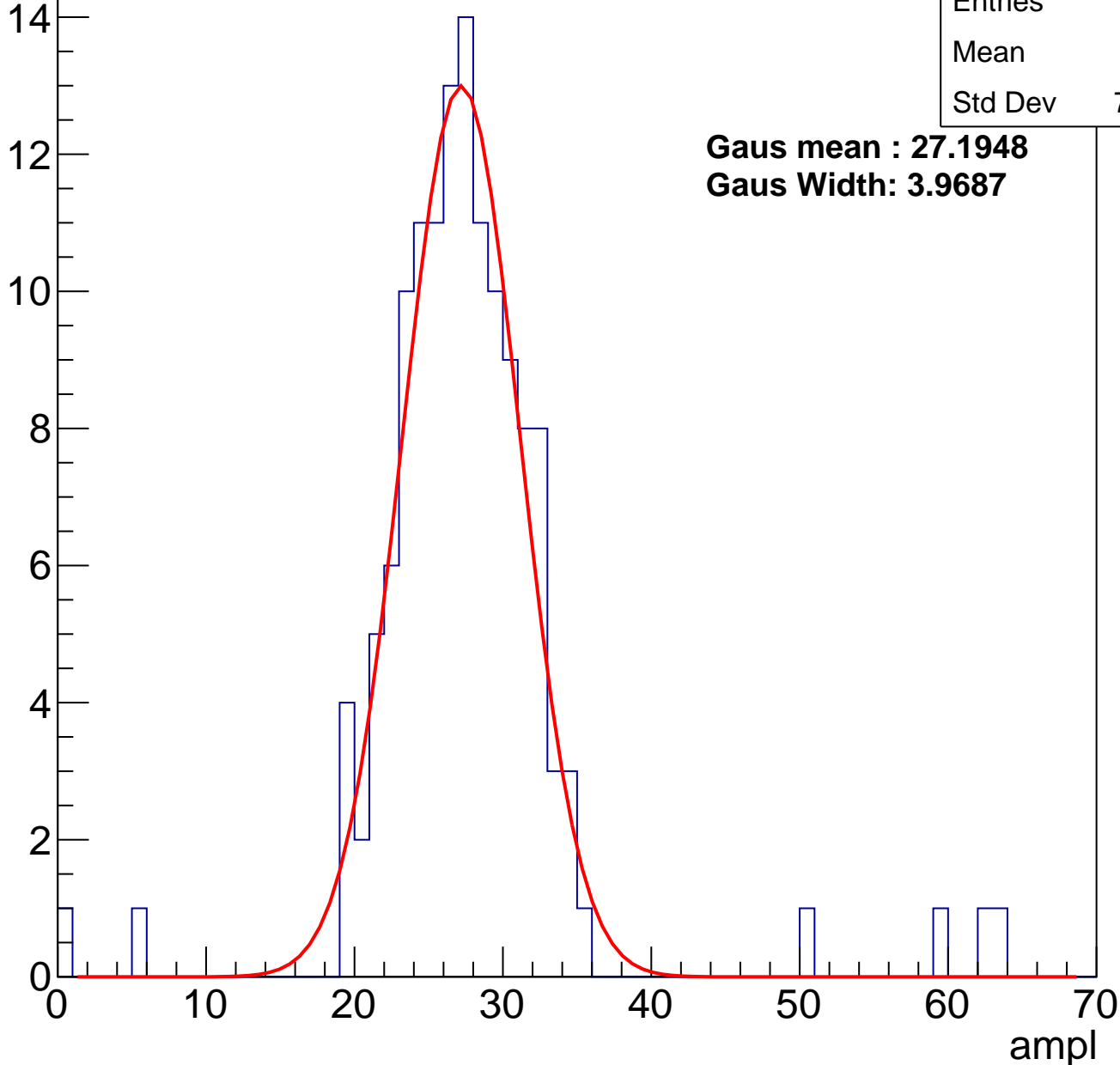
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	135
Mean	27.3
Std Dev	7.229

**Gaus mean : 27.1948**

**Gaus Width: 3.9687**



# B0L101S, U23-ch127, adc1

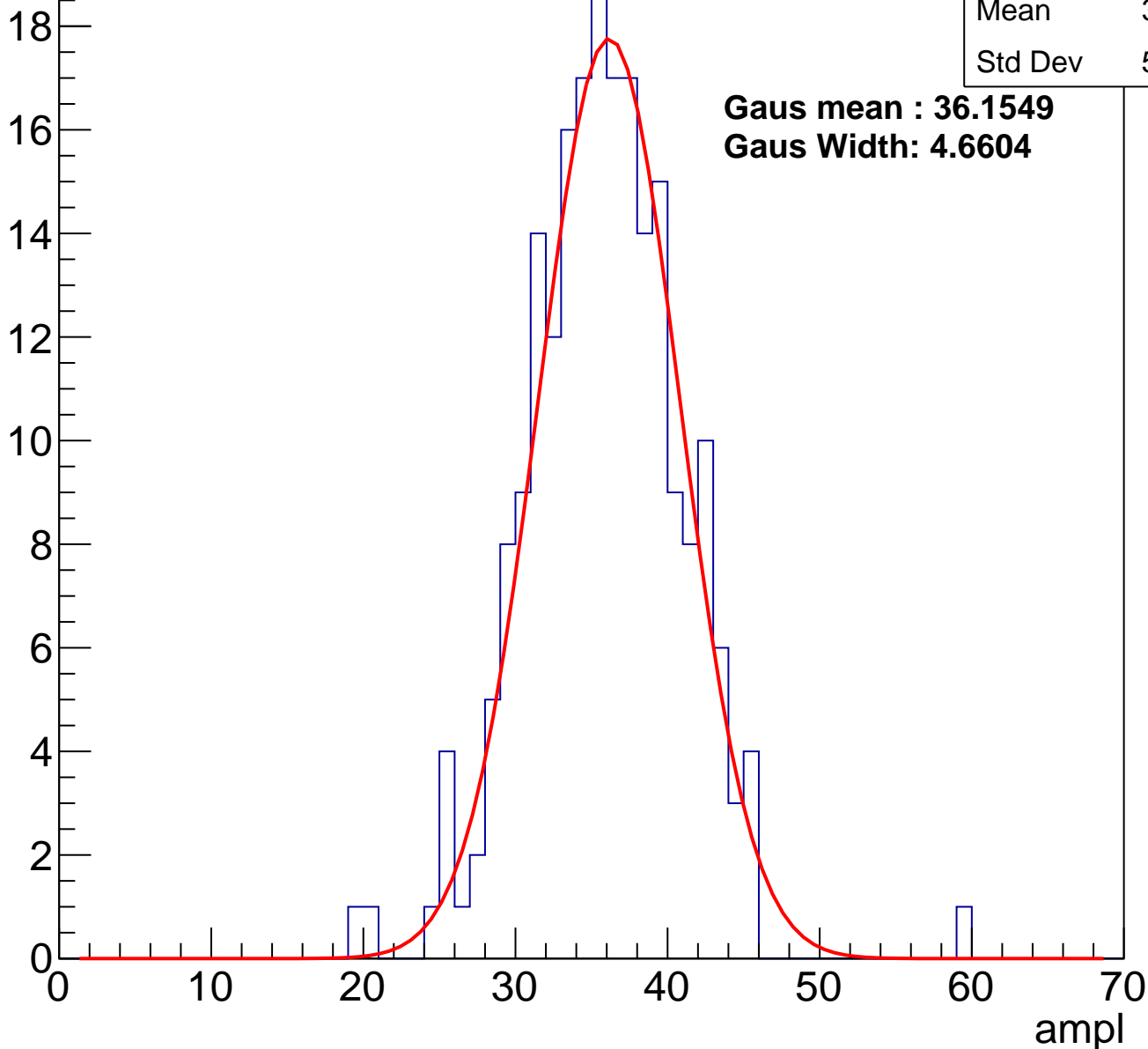
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	214
Mean	35.37
Std Dev	5.028

**Gaus mean : 36.1549**

**Gaus Width: 4.6604**

Entry



# B0L101S, U23-ch127, adc2

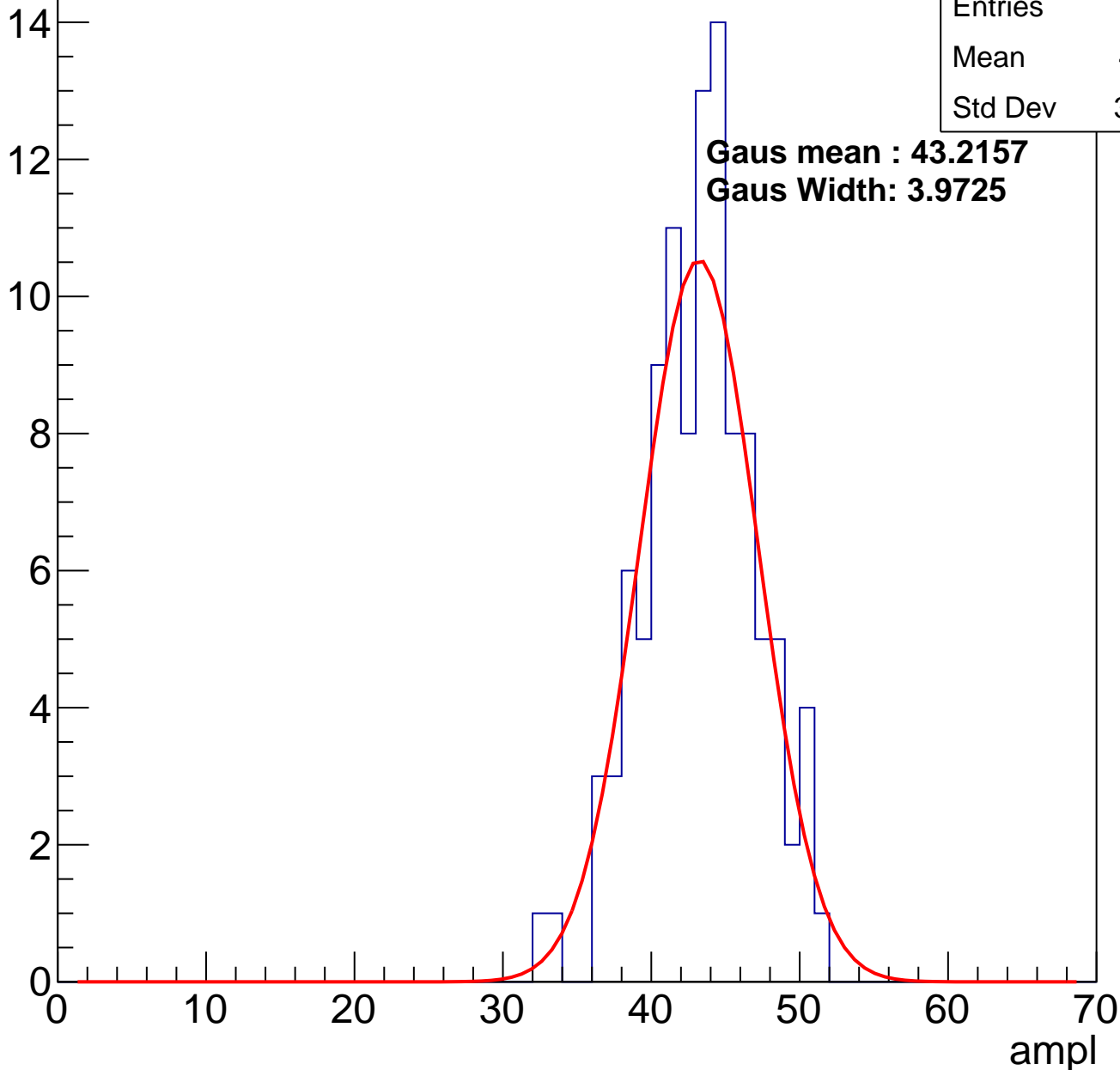
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	107
Mean	42.81
Std Dev	3.732

**Gaus mean : 43.2157**

**Gaus Width: 3.9725**



# B0L101S, U23-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

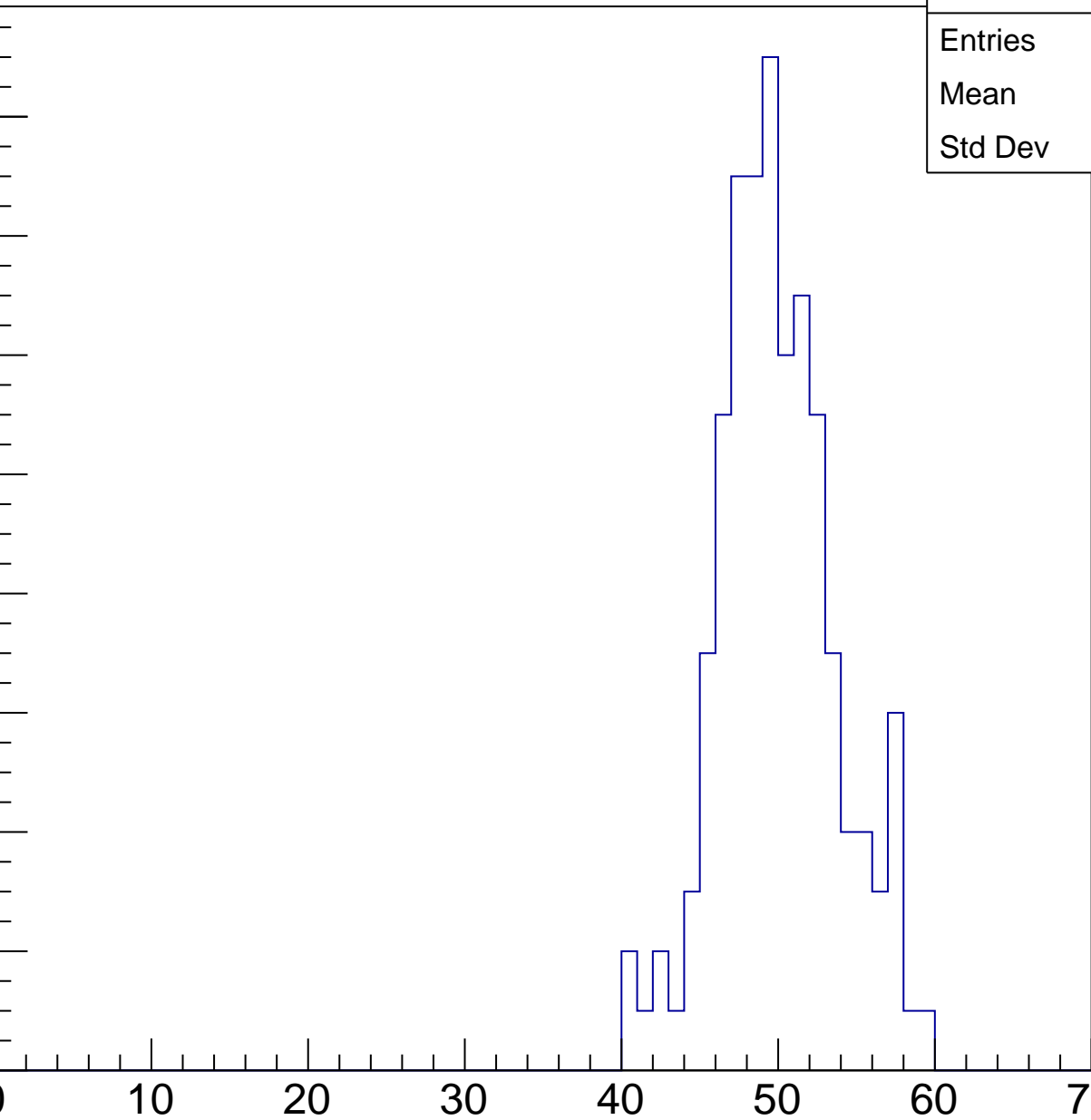
Entry

Entries	136
Mean	49.47
Std Dev	3.759

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U23-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

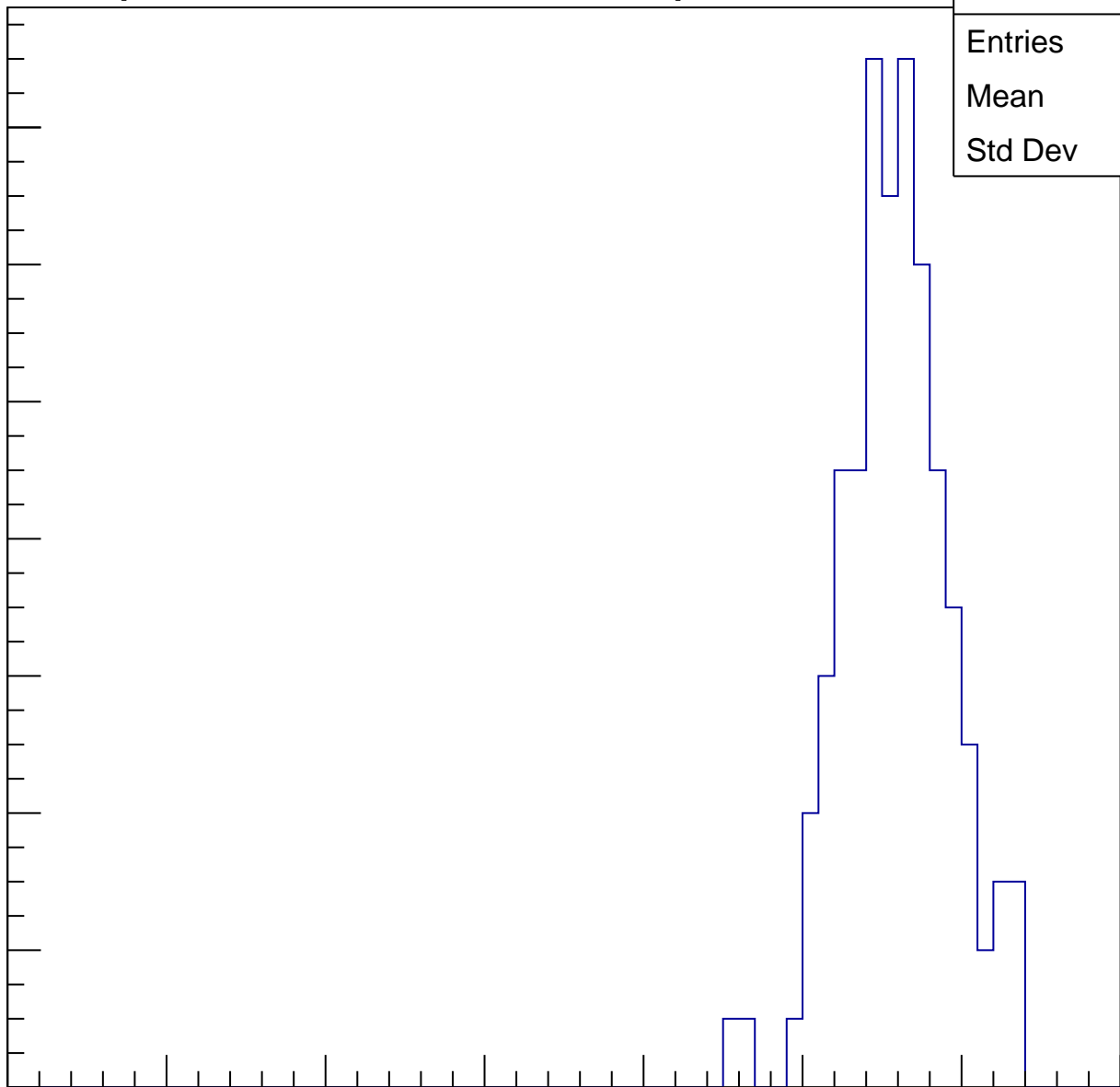
Entries	115
Mean	55.41
Std Dev	3.385

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

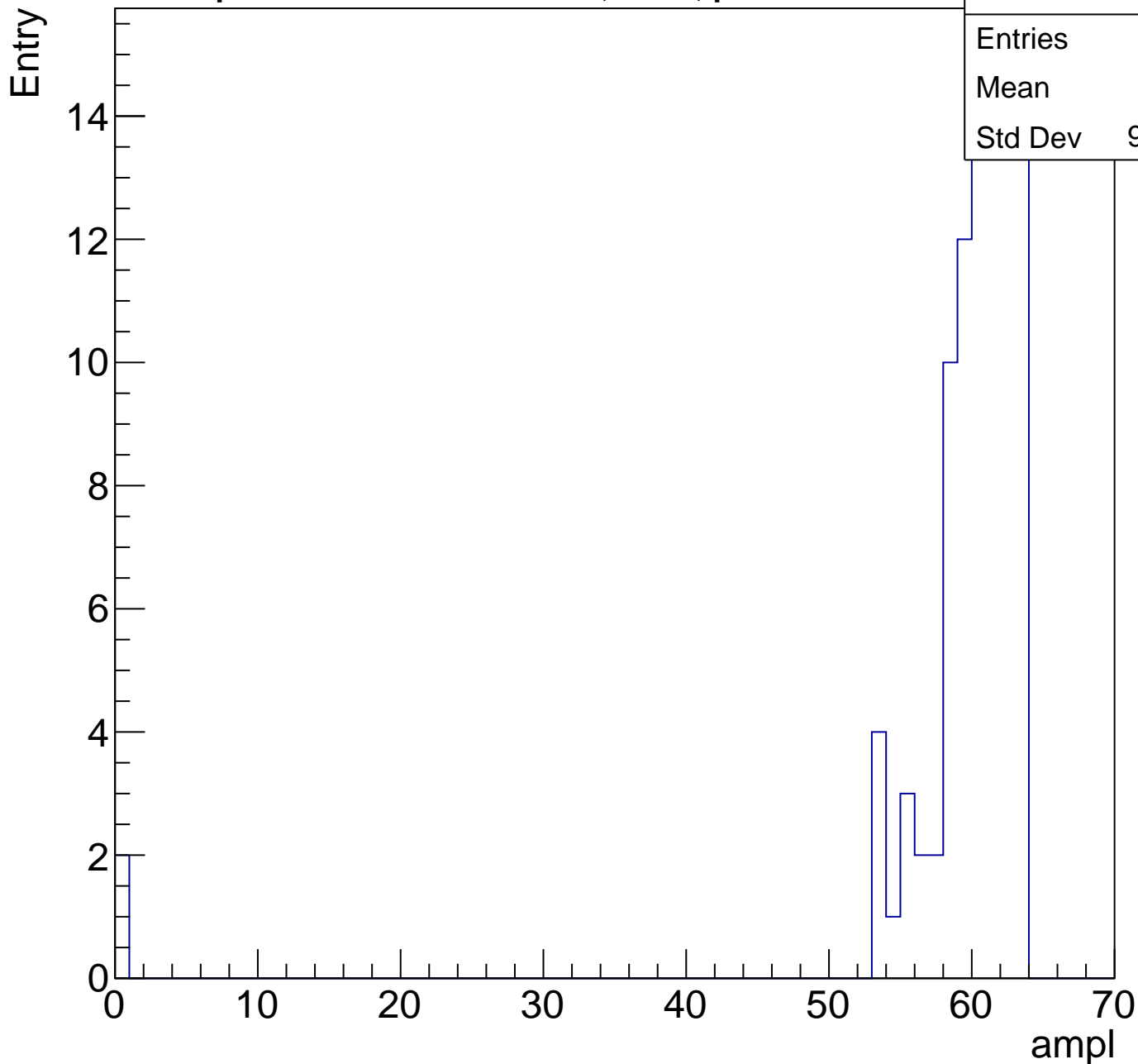
ampl



# B0L101S, U23-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

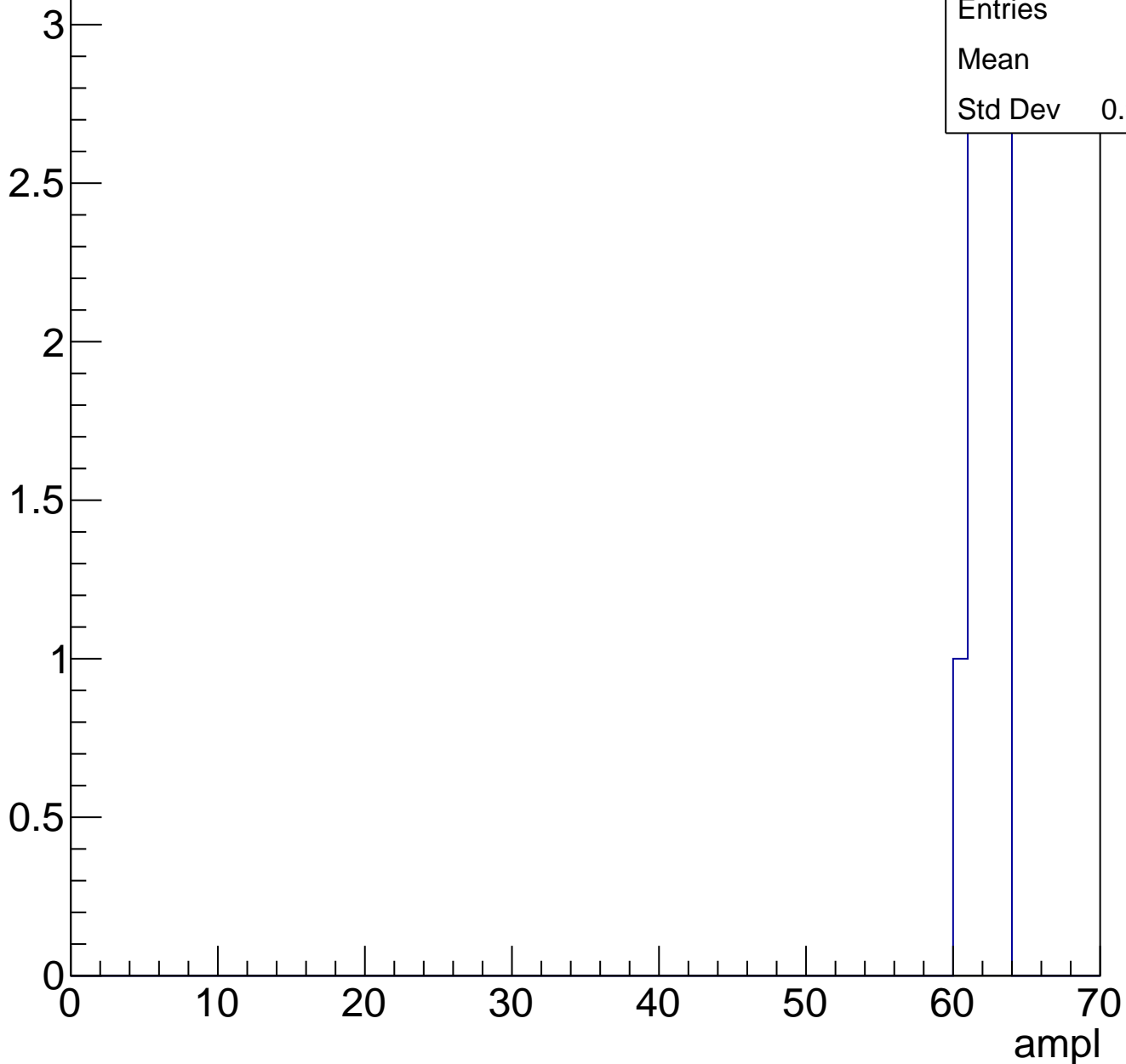
Entries	93
Mean	58.6
Std Dev	9.054



# B0L101S, U23-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

