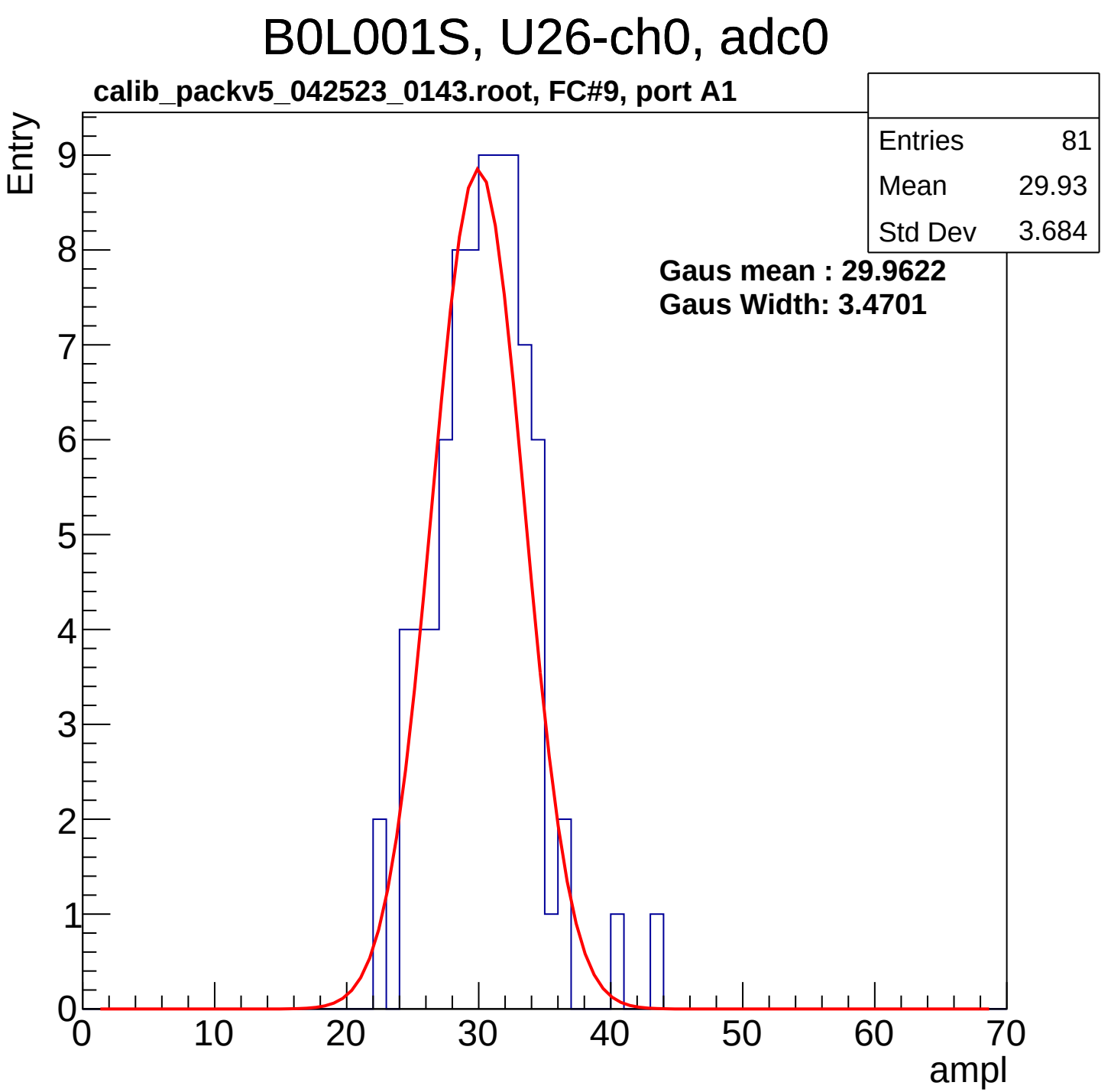
B0L001S, U26-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 29.93
Std Dev 3.684
Gaus mean : 29.9622
Gaus Width: 3.4701
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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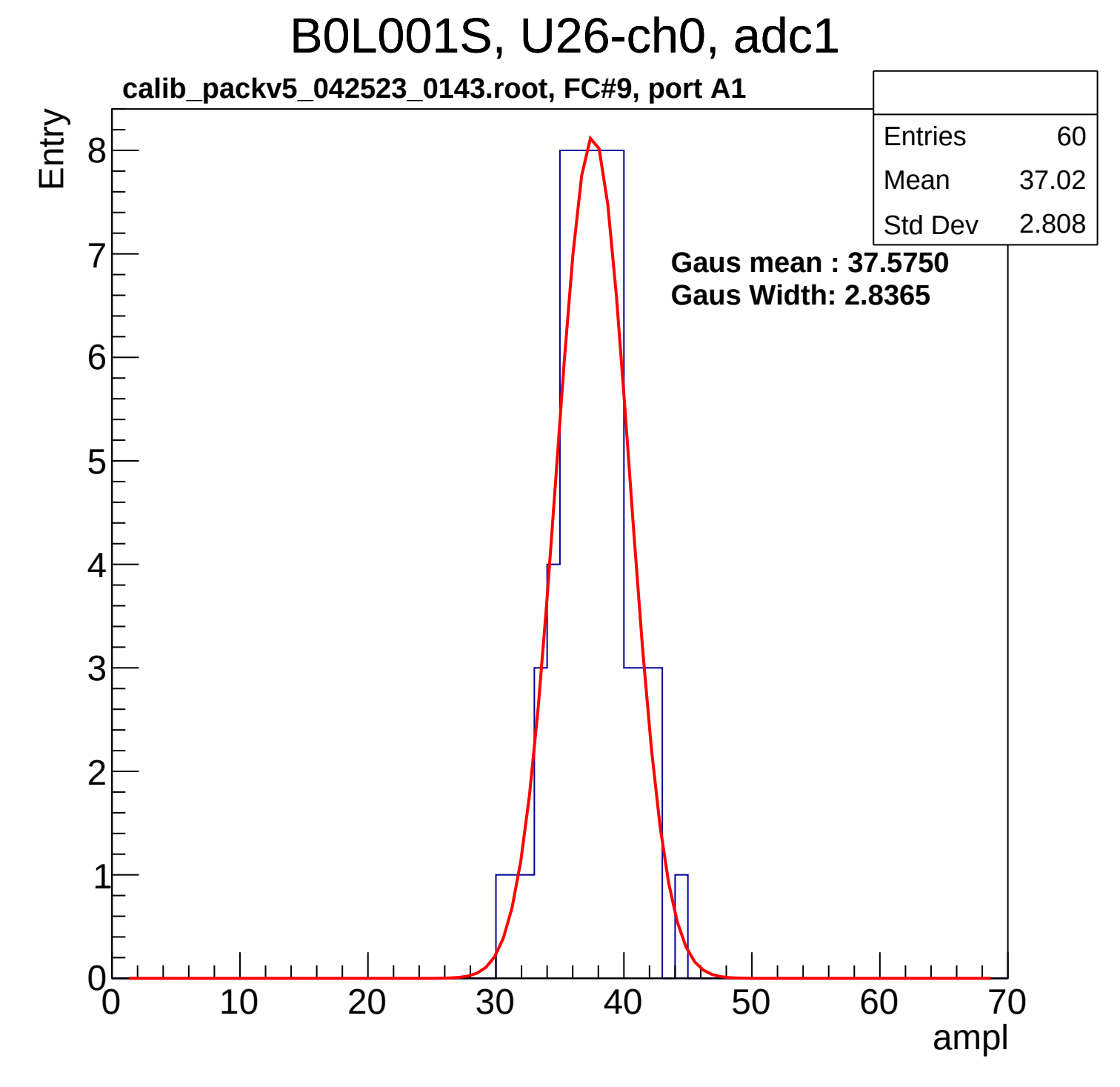
B0L001S, U26-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.02
Std Dev 2.808
Gaus mean : 37.5750
Gaus Width: 2.8365
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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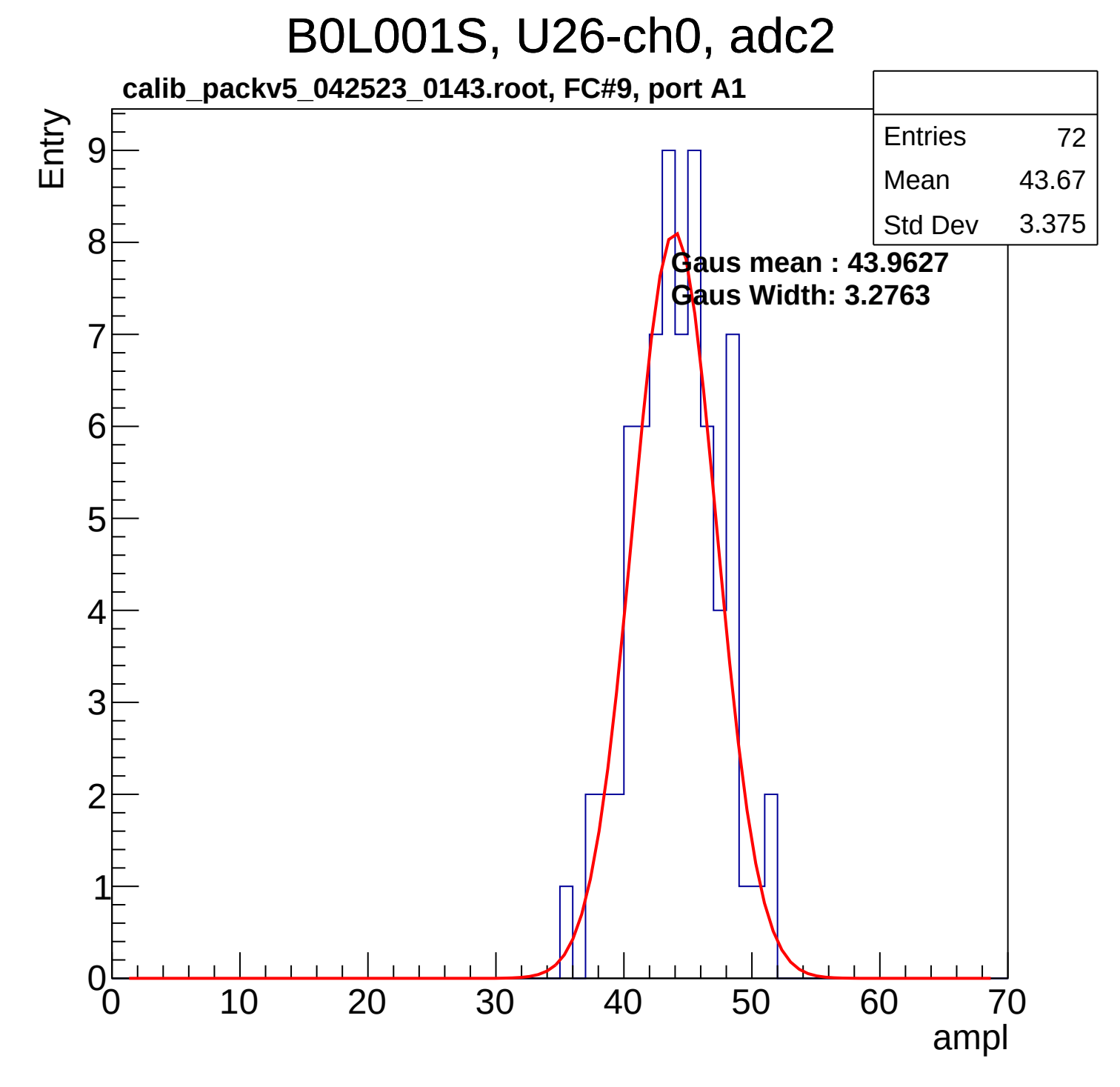
B0L001S, U26-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 43.67
Std Dev 3.375
Gaus mean : 43.9627
Gaus Width: 3.2763
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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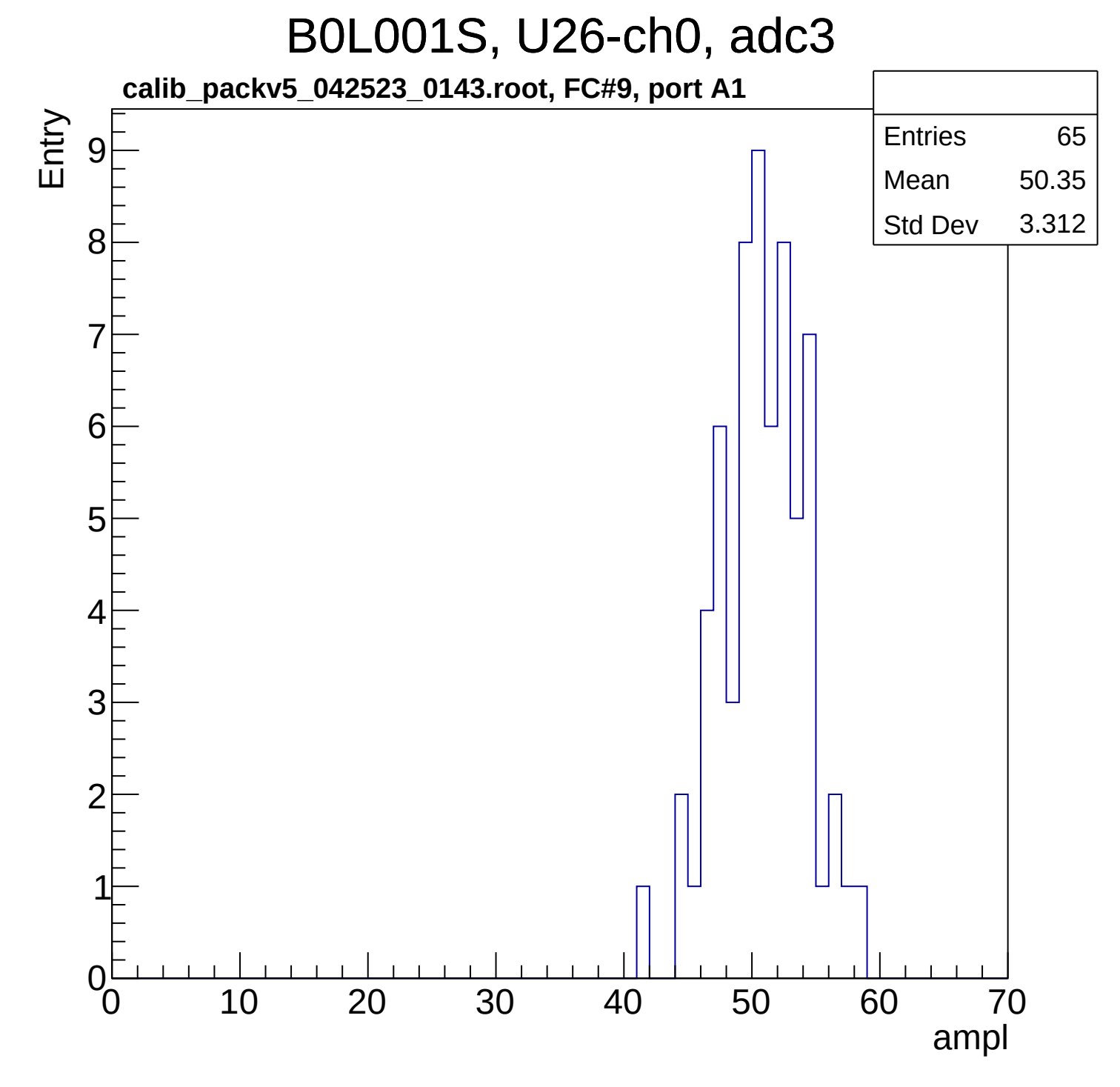
B0L001S, U26-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.35
Std Dev 3.312
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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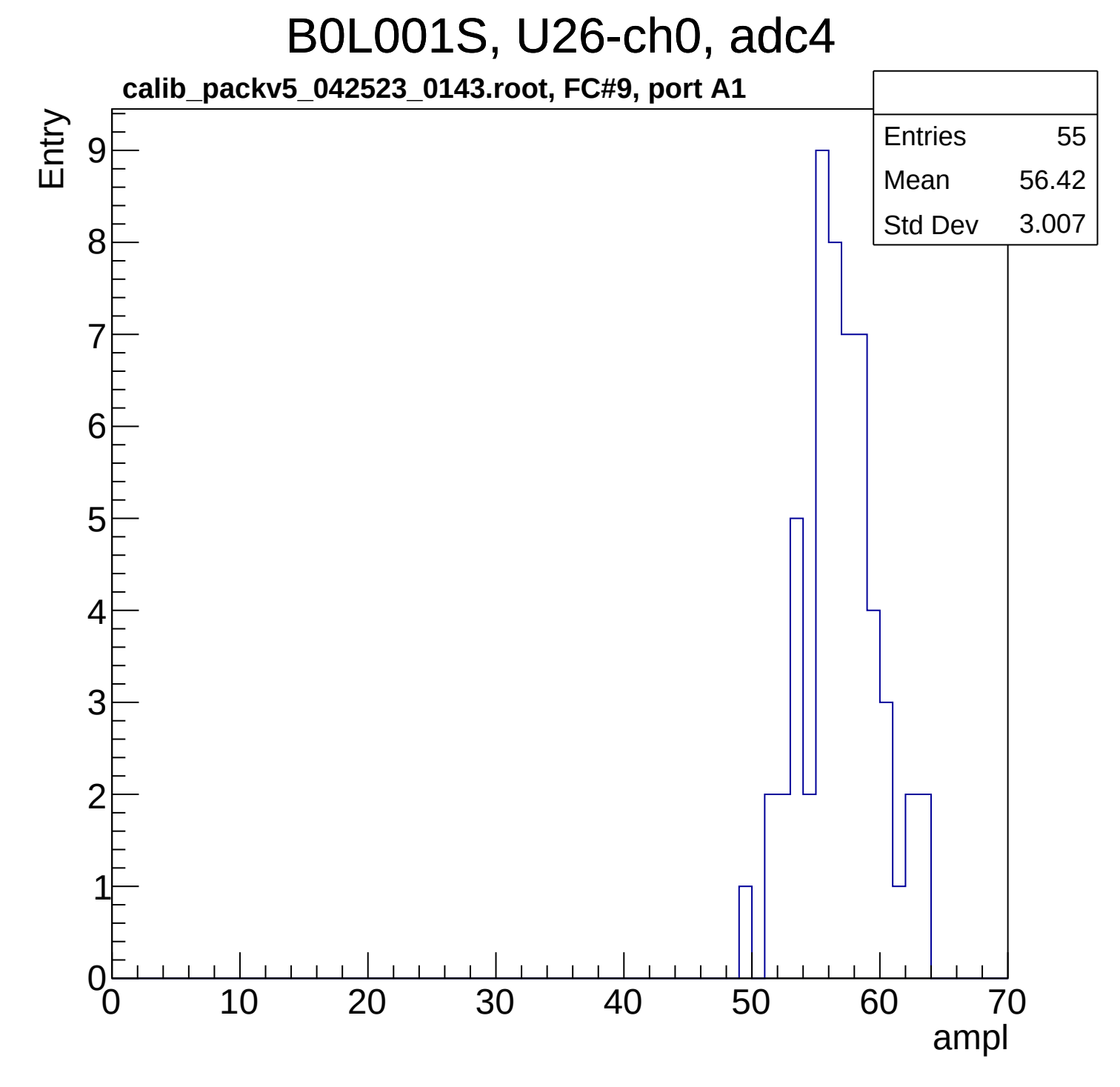
B0L001S, U26-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.42
Std Dev 3.007
Entry
ampl
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2
3
4
5
6
7
8
9
0
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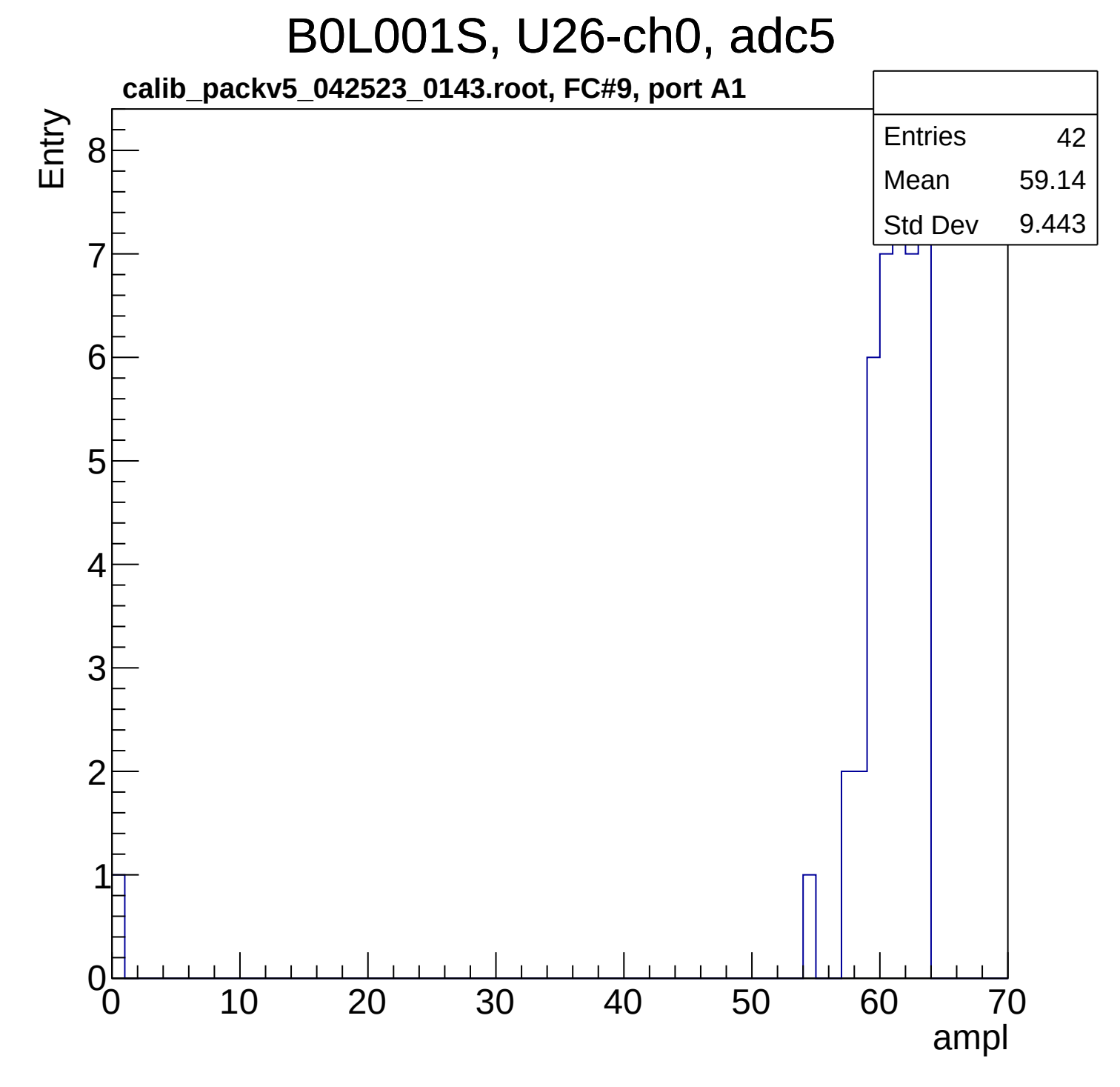
B0L001S, U26-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.14
Std Dev 9.443
Entry
ampl
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2
3
4
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0
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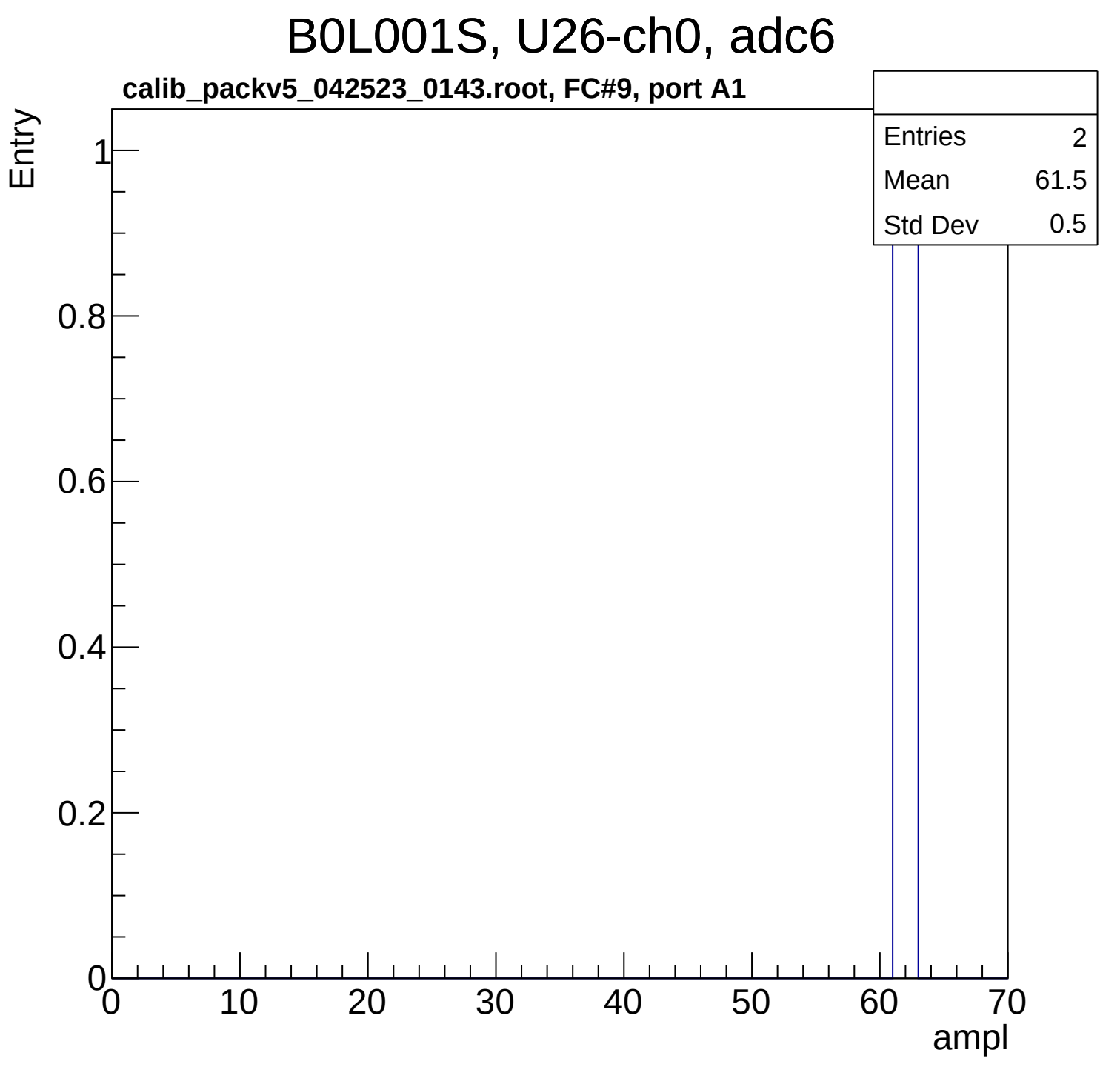
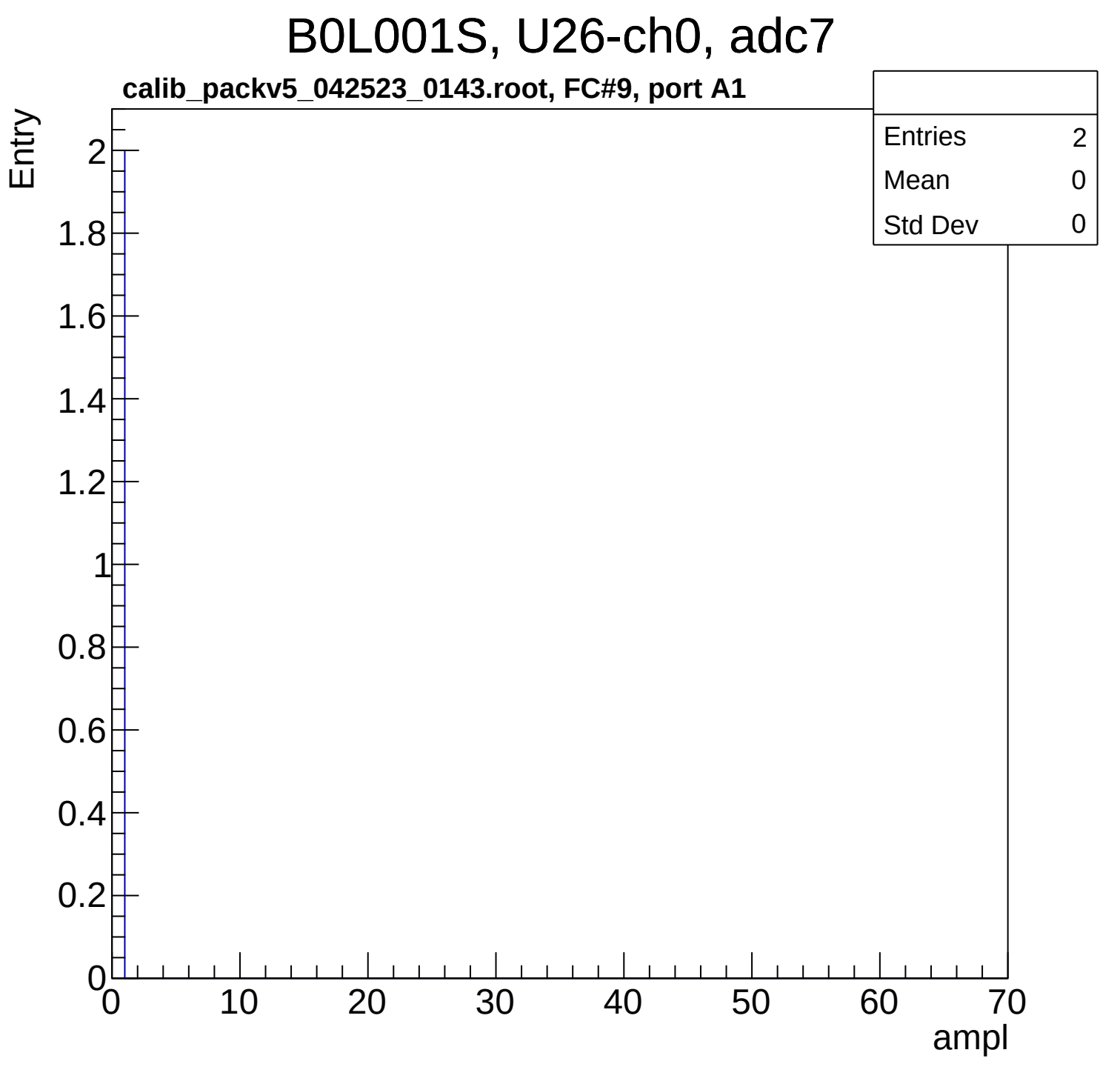
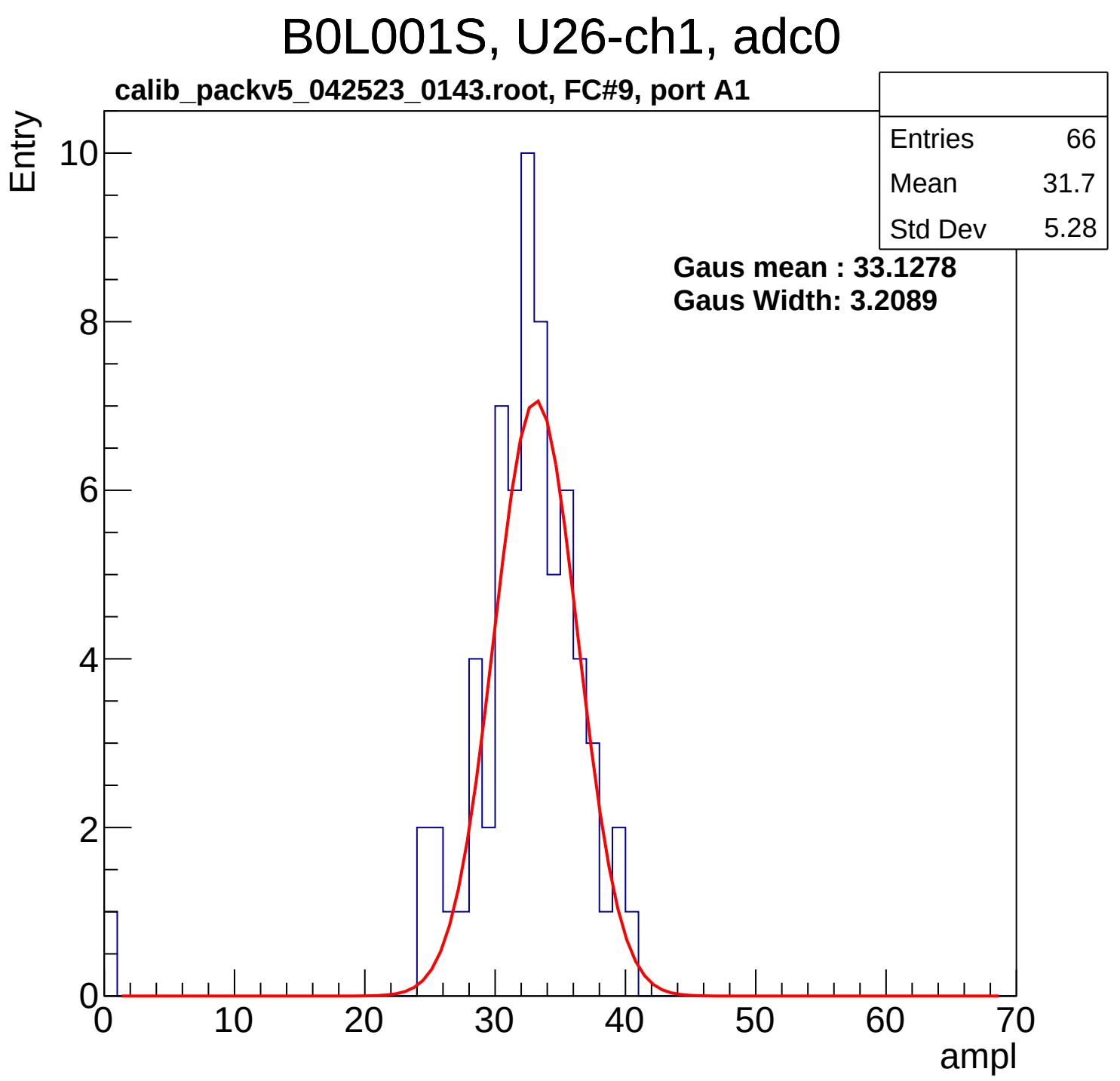
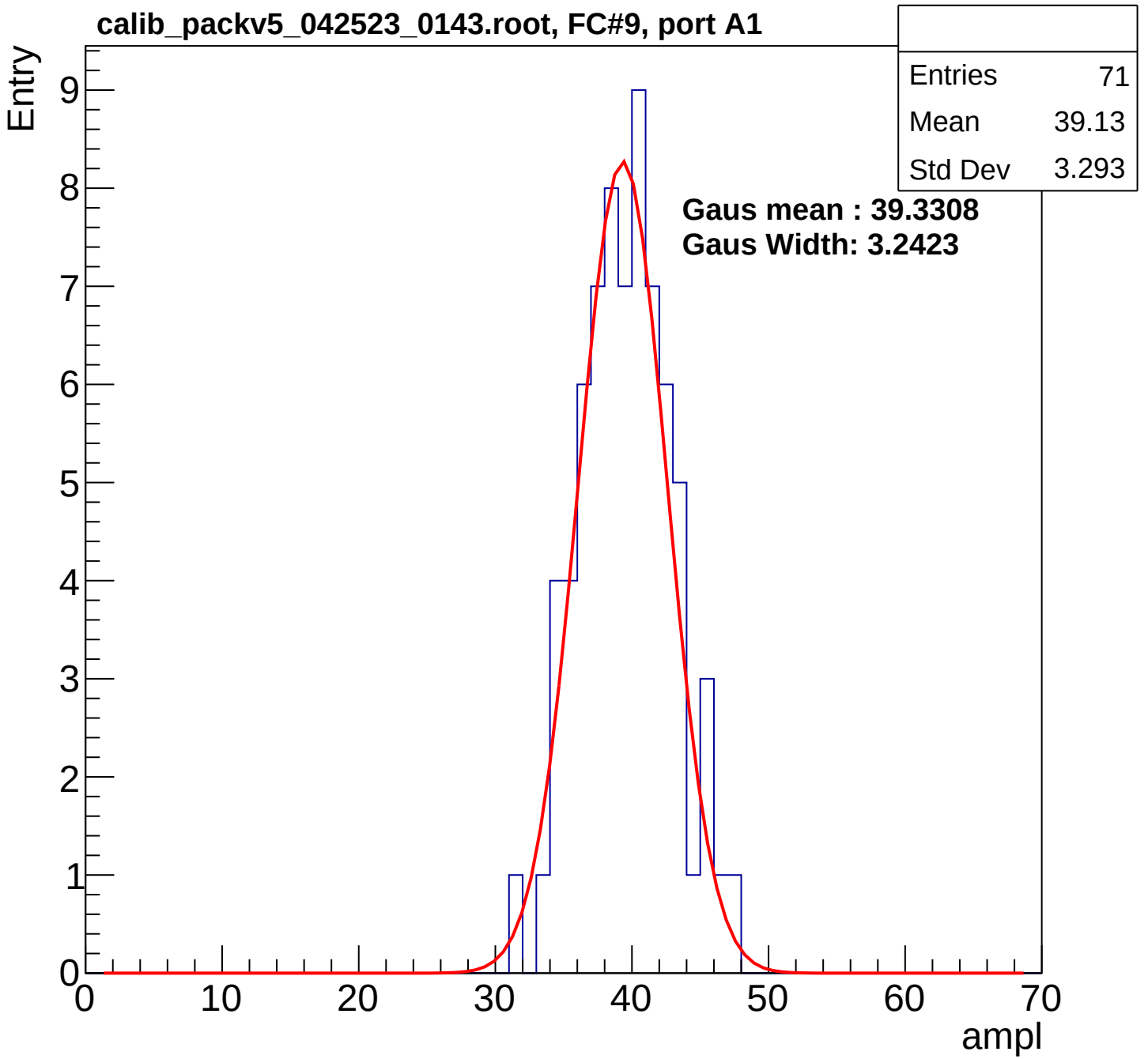

B0L001S, U26-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.7
Std Dev 5.28
Gaus mean : 33.1278
Gaus Width: 3.2089
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 39.13
Std Dev 3.293
Gaus mean : 39.3308
Gaus Width: 3.2423
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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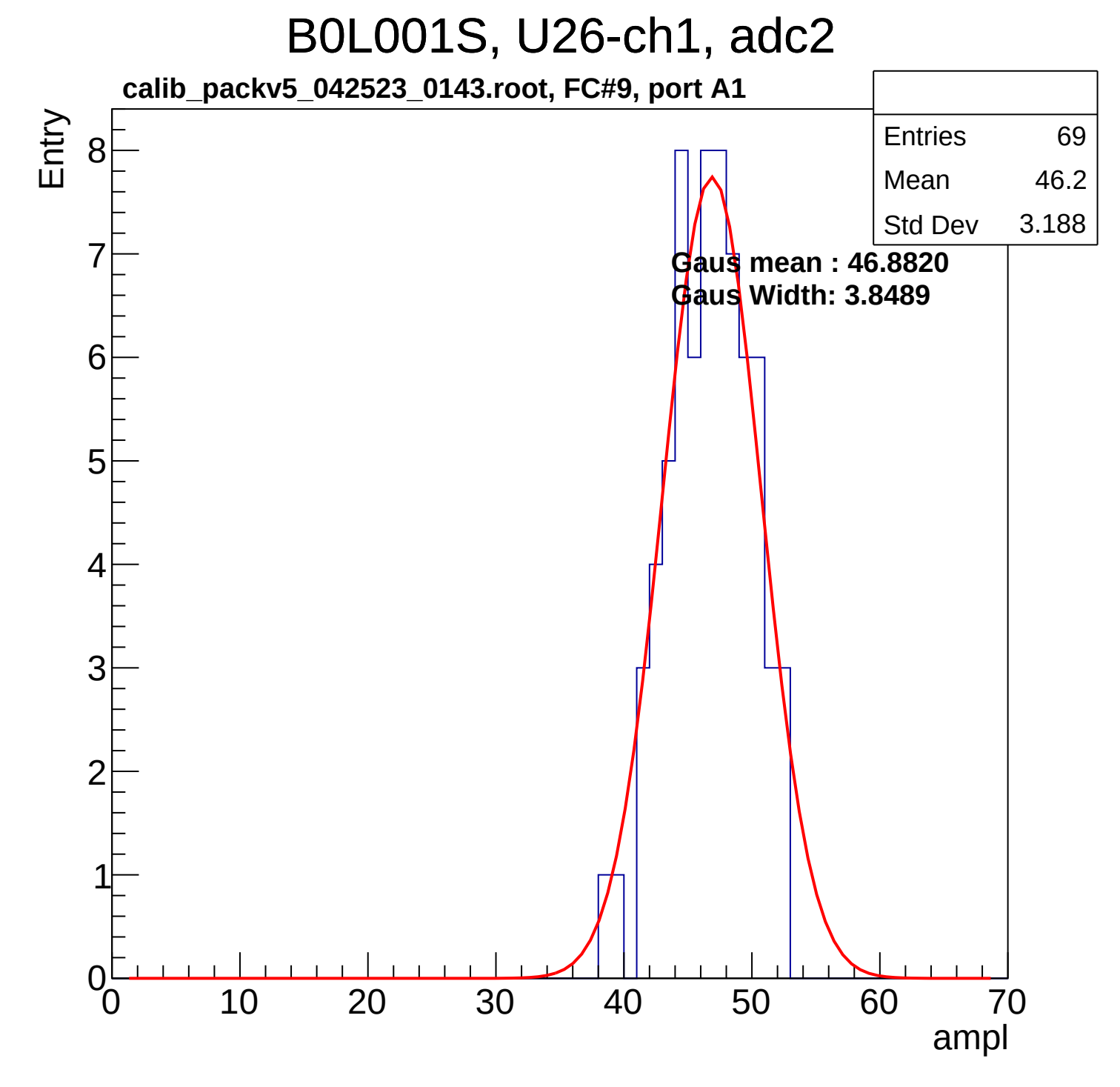
B0L001S, U26-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 46.2
Std Dev 3.188
Gaus mean : 46.8820
Gaus Width: 3.8489
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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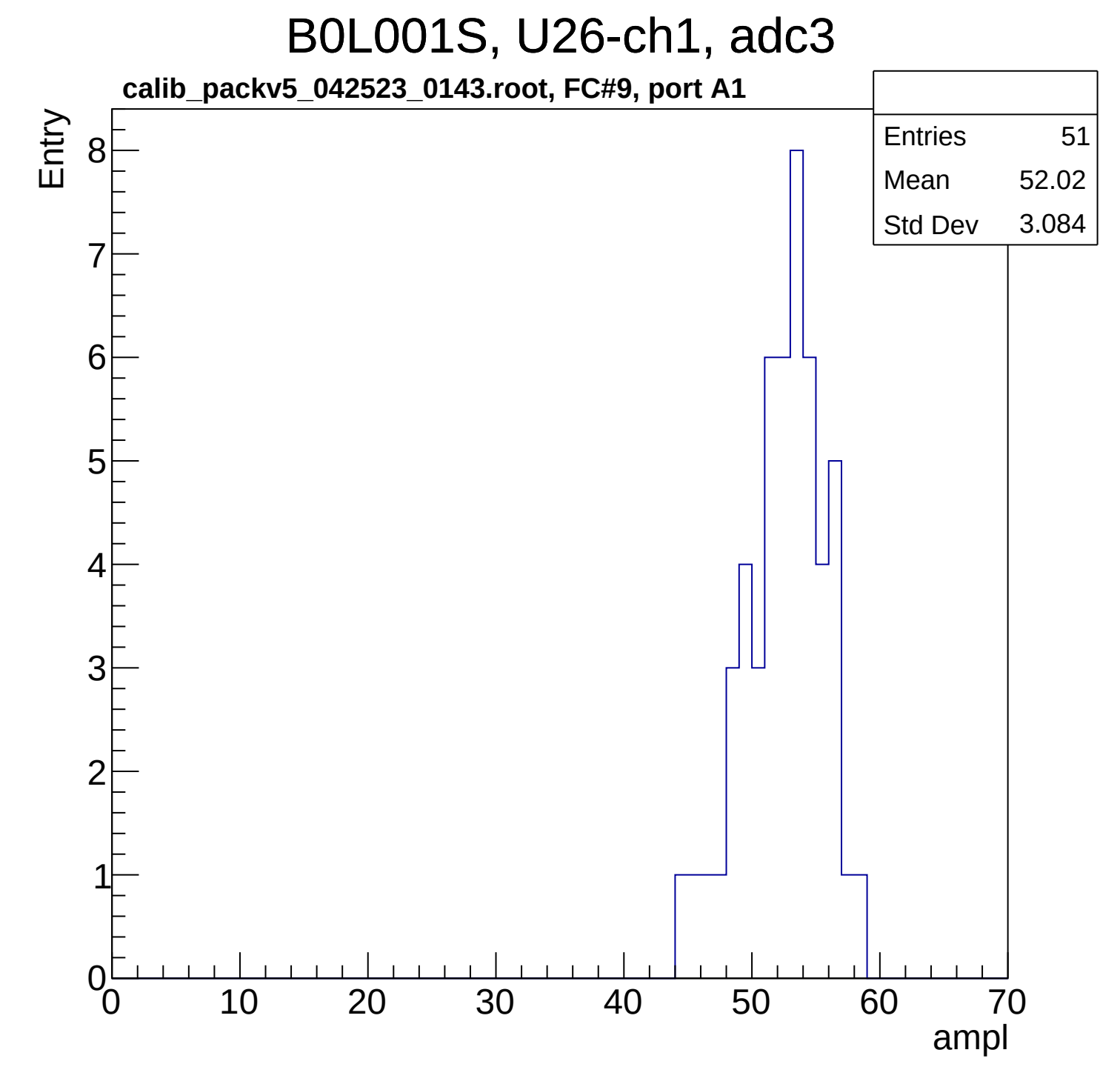
B0L001S, U26-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 52.02
Std Dev 3.084
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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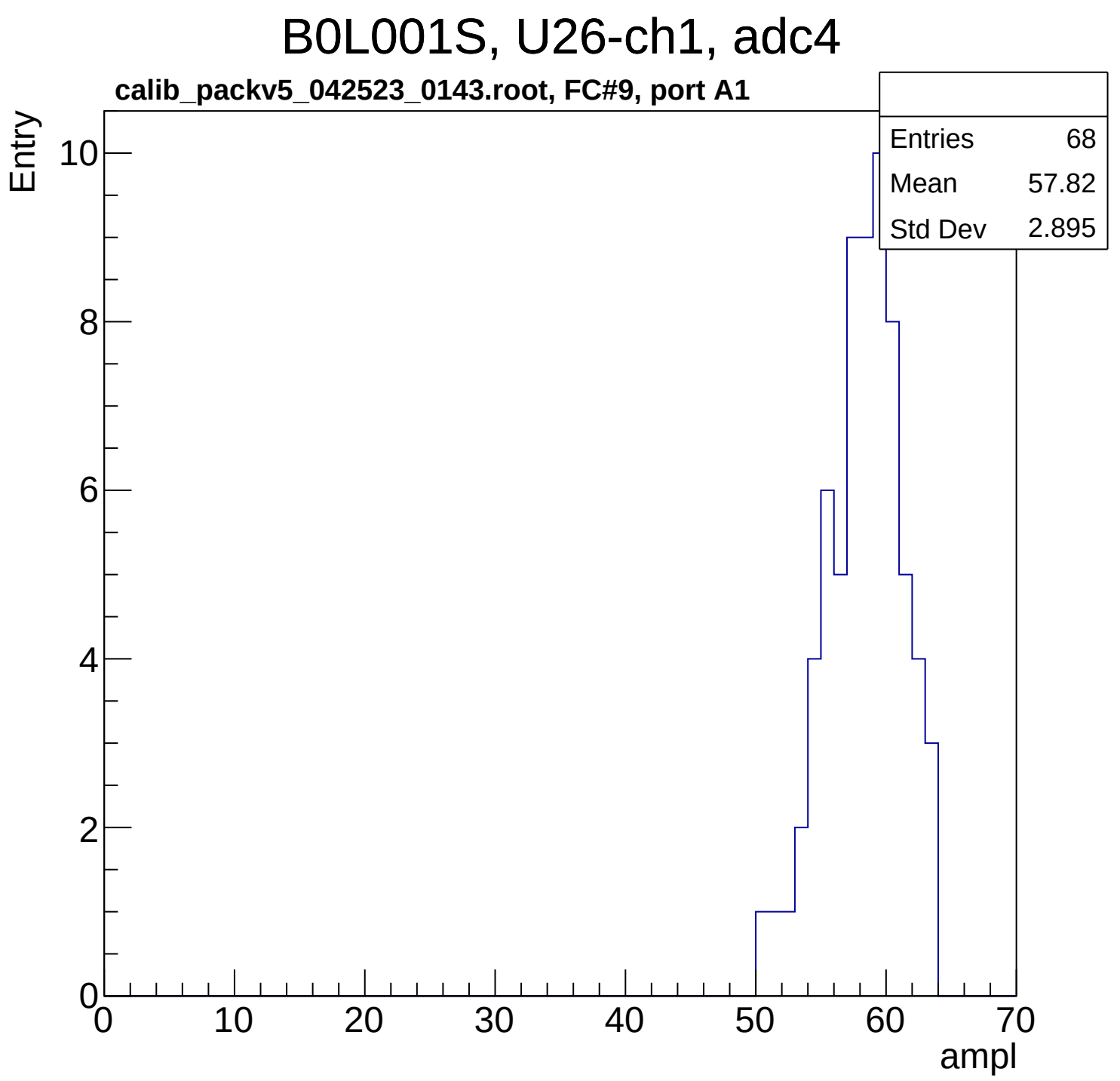
B0L001S, U26-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.82
Std Dev 2.895
Entry
0
2
4
6
8
10
0
10
20
30
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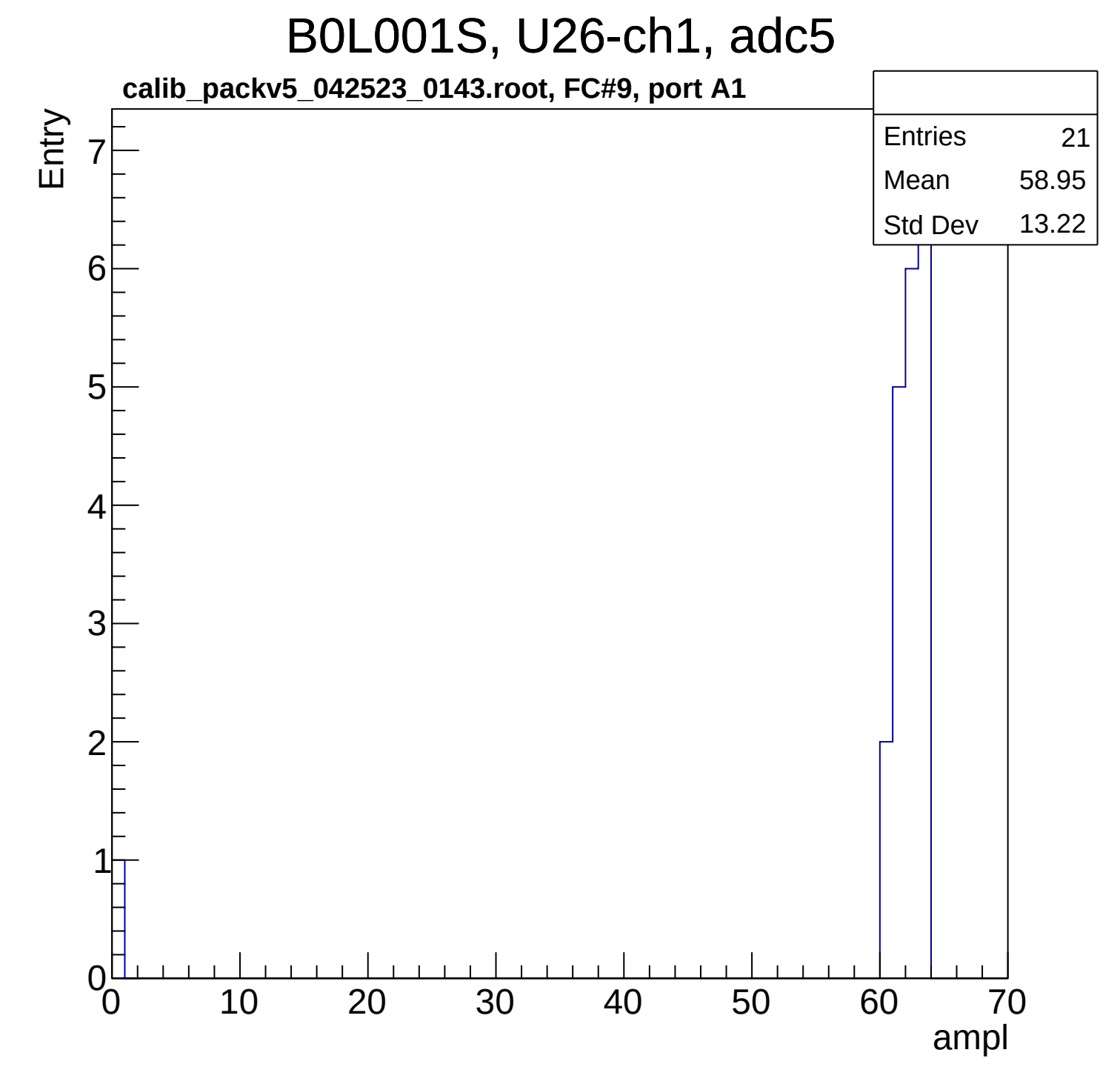
50
60
70
ampl


B0L001S, U26-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.95
Std Dev 13.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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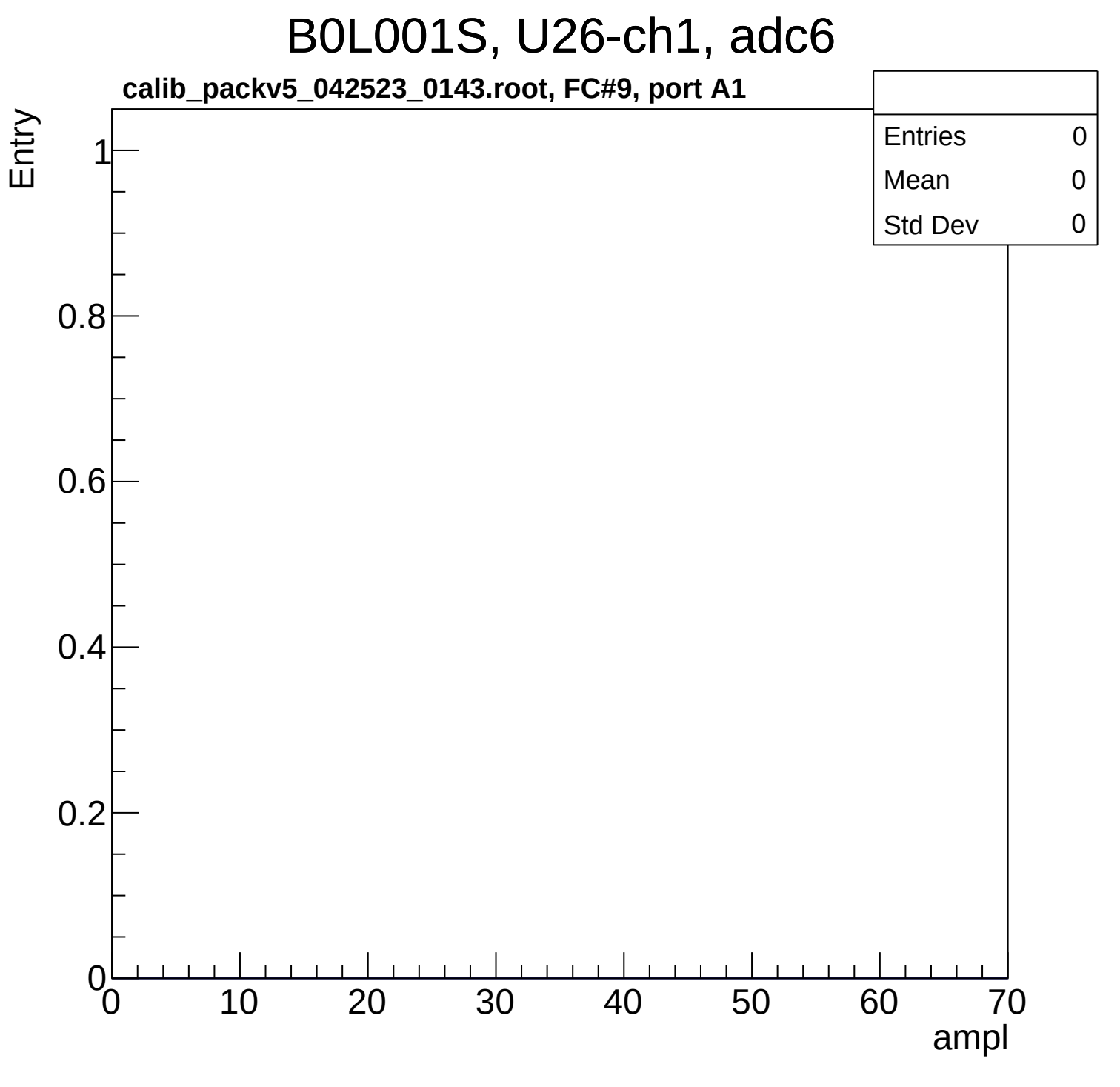
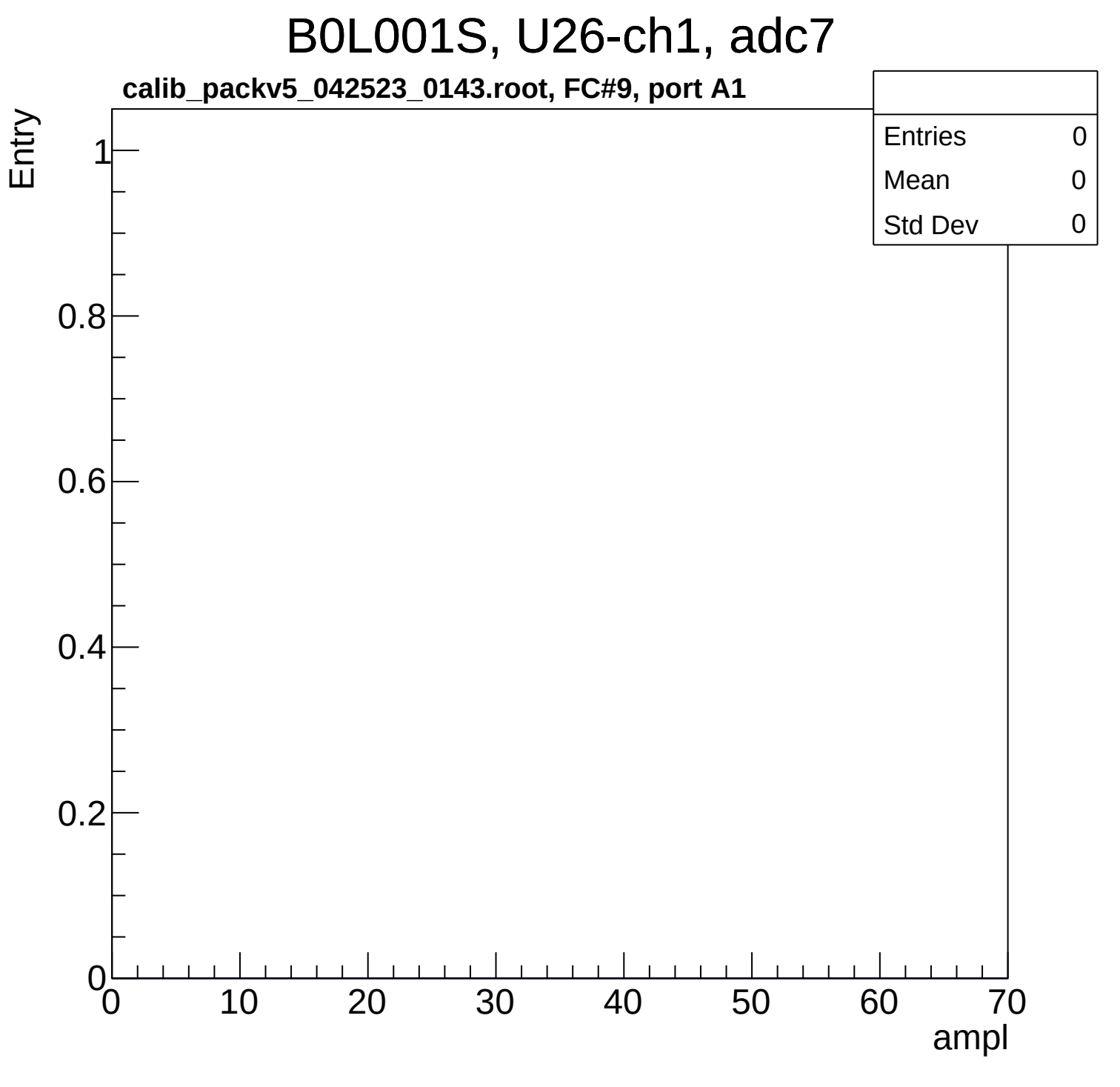

B0L001S, U26-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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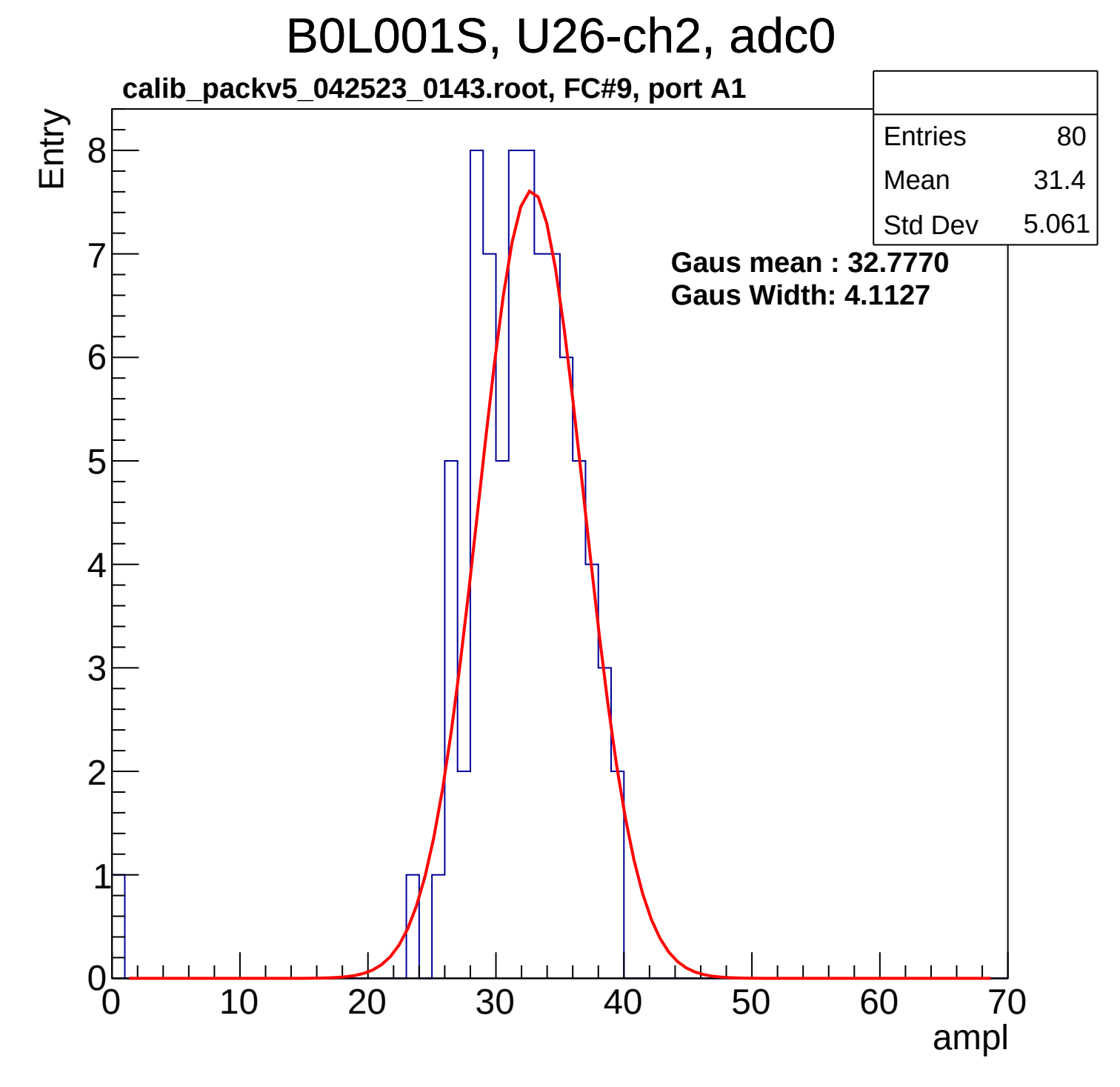
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 31.4
Std Dev 5.061
Gaus mean : 32.7770
Gaus Width: 4.1127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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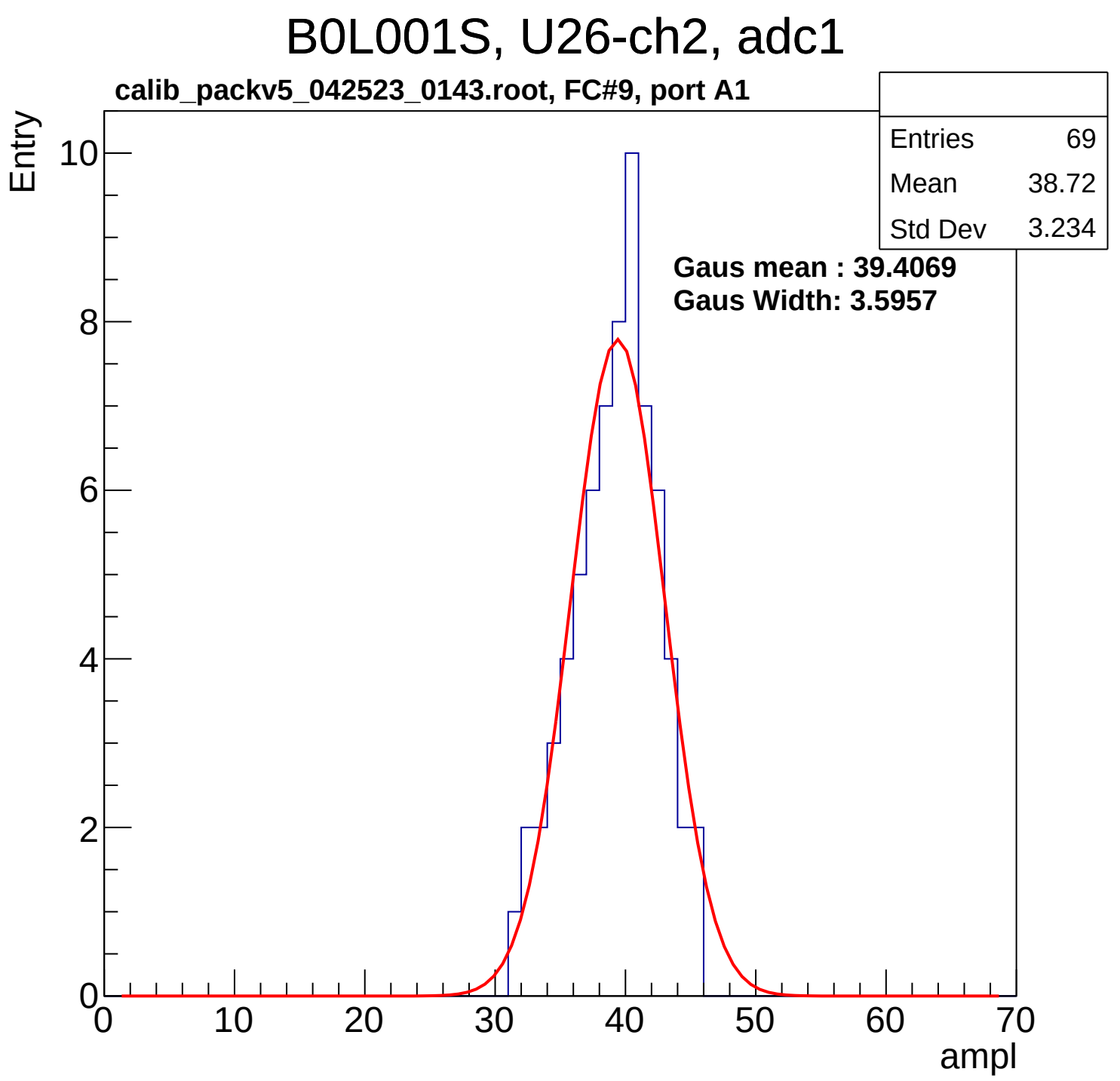
B0L001S, U26-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.72
Std Dev 3.234
Gaus mean : 39.4069
Gaus Width: 3.5957
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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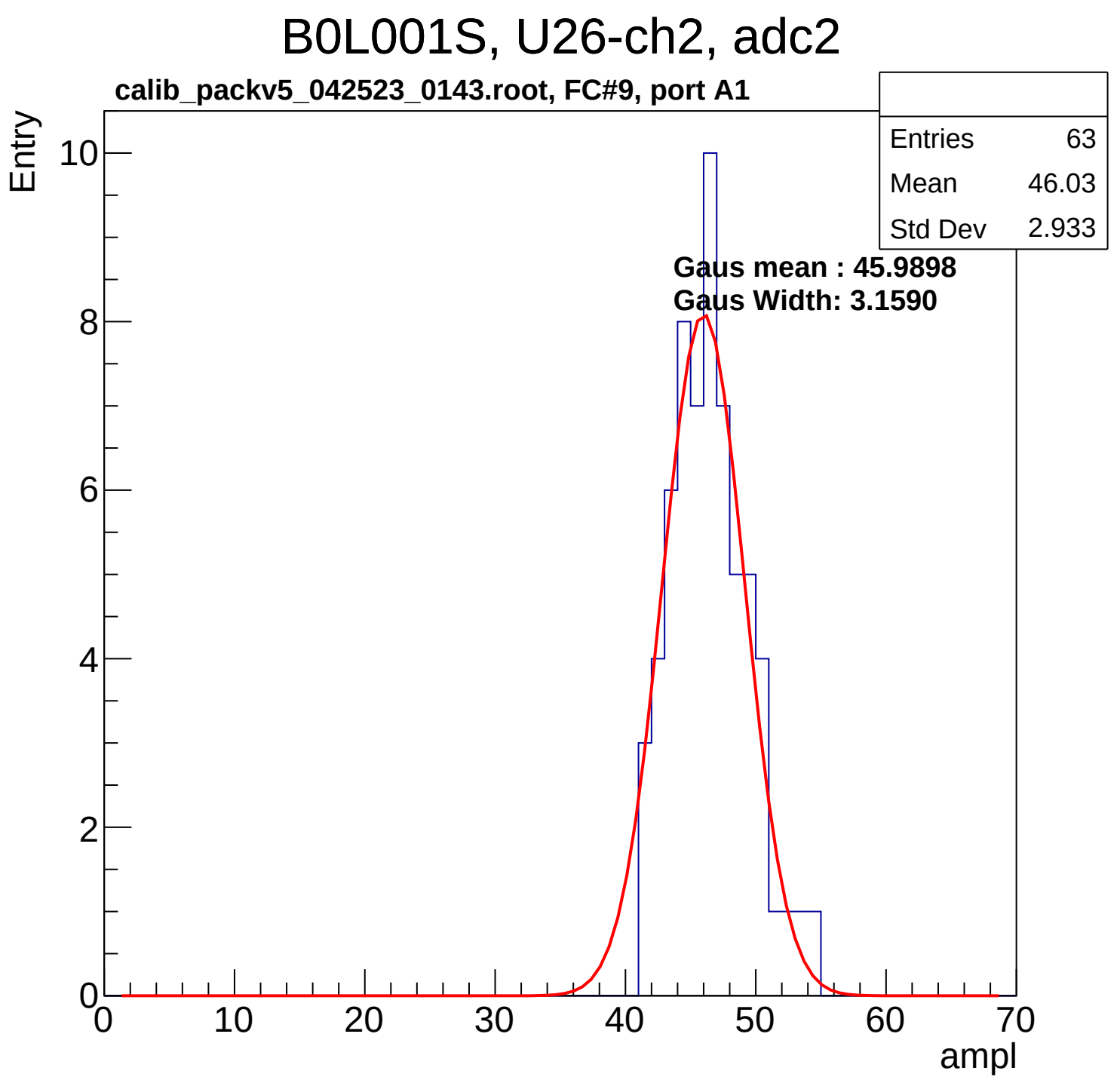
B0L001S, U26-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 46.03
Std Dev 2.933
Gaus mean : 45.9898
Gaus Width: 3.1590
Entry
ampl
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2
4
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0
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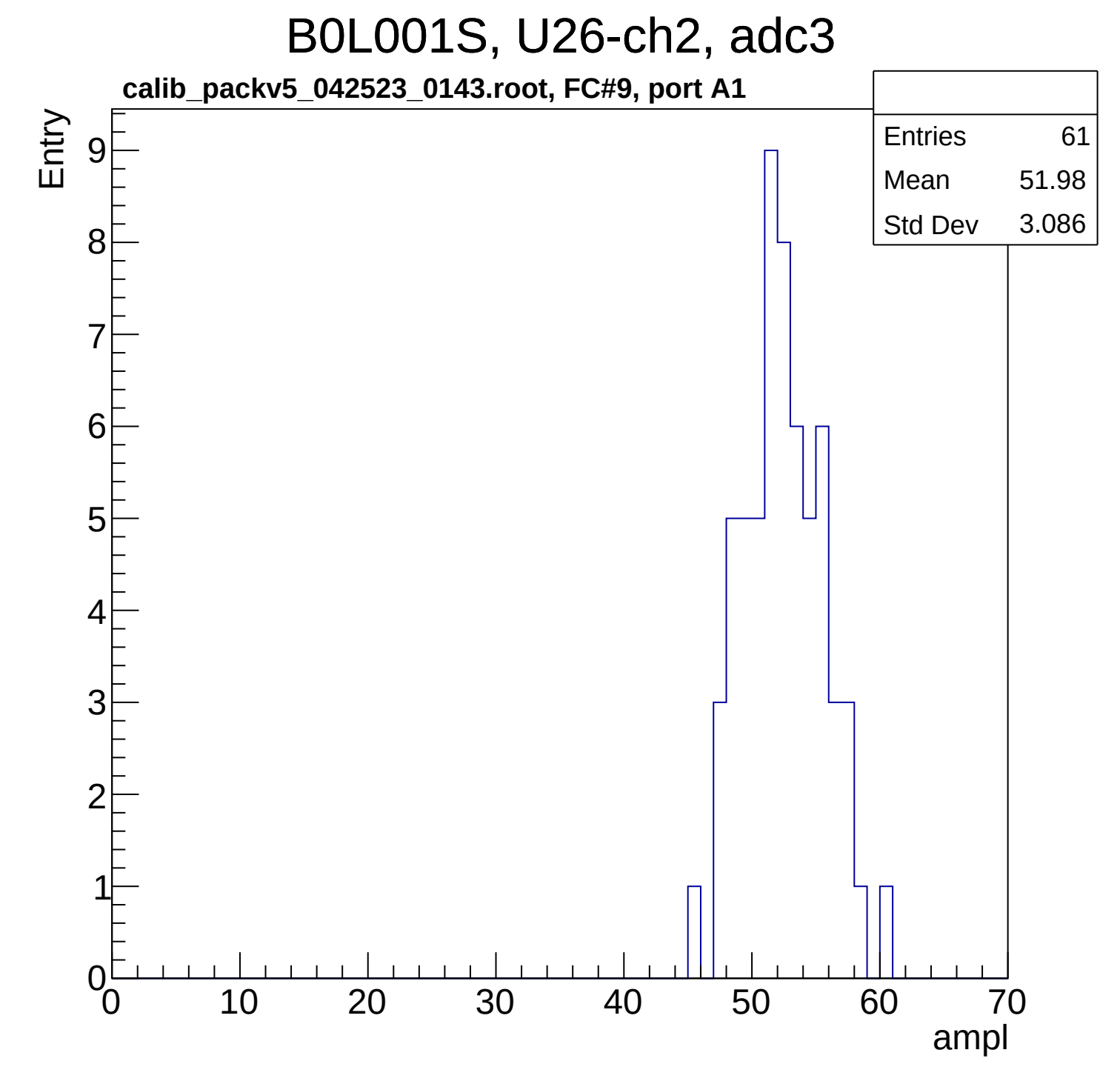
B0L001S, U26-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.98
Std Dev 3.086
Entry
ampl
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0
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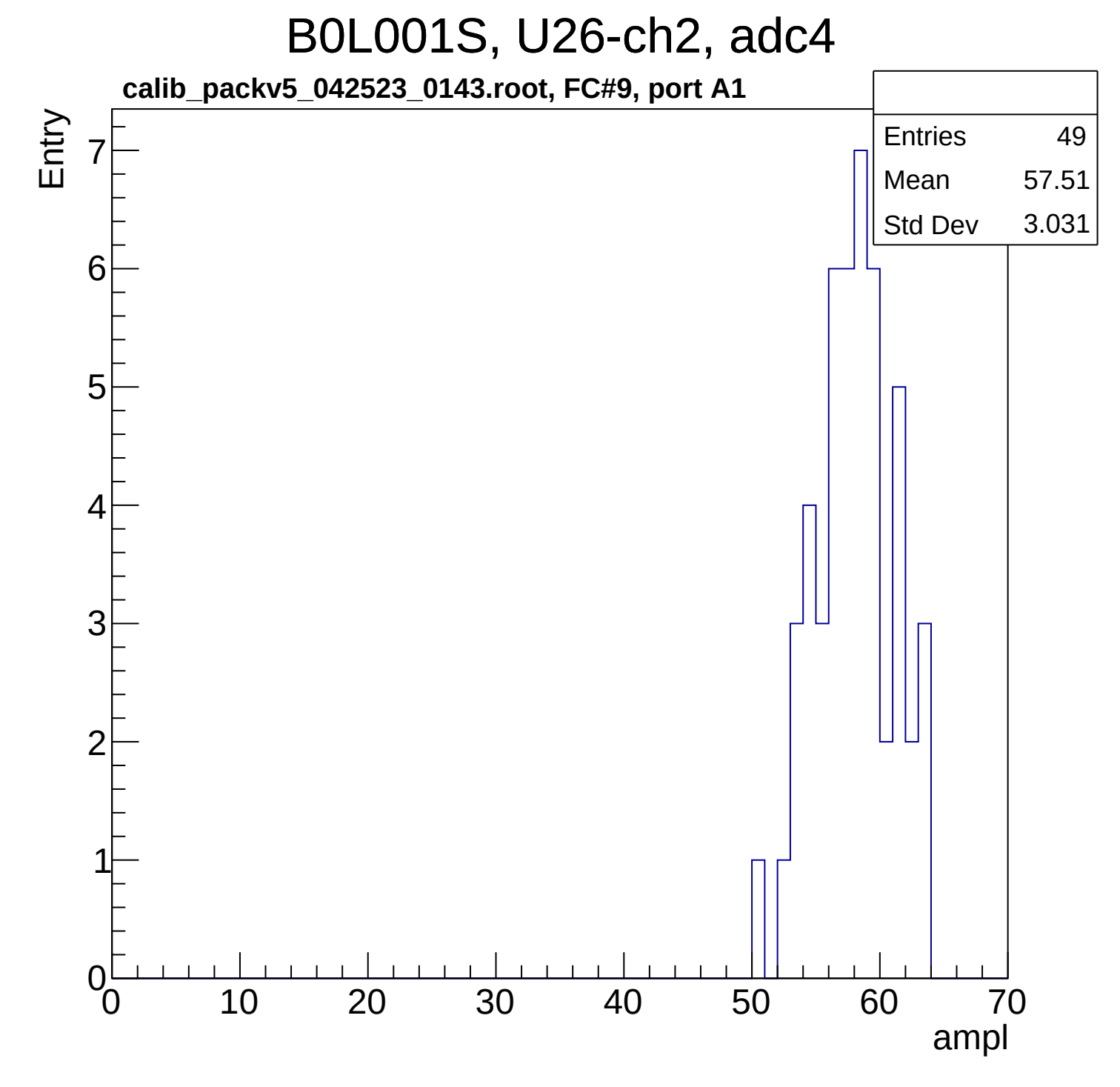
B0L001S, U26-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.51
Std Dev 3.031
Entry
ampl
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3
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0
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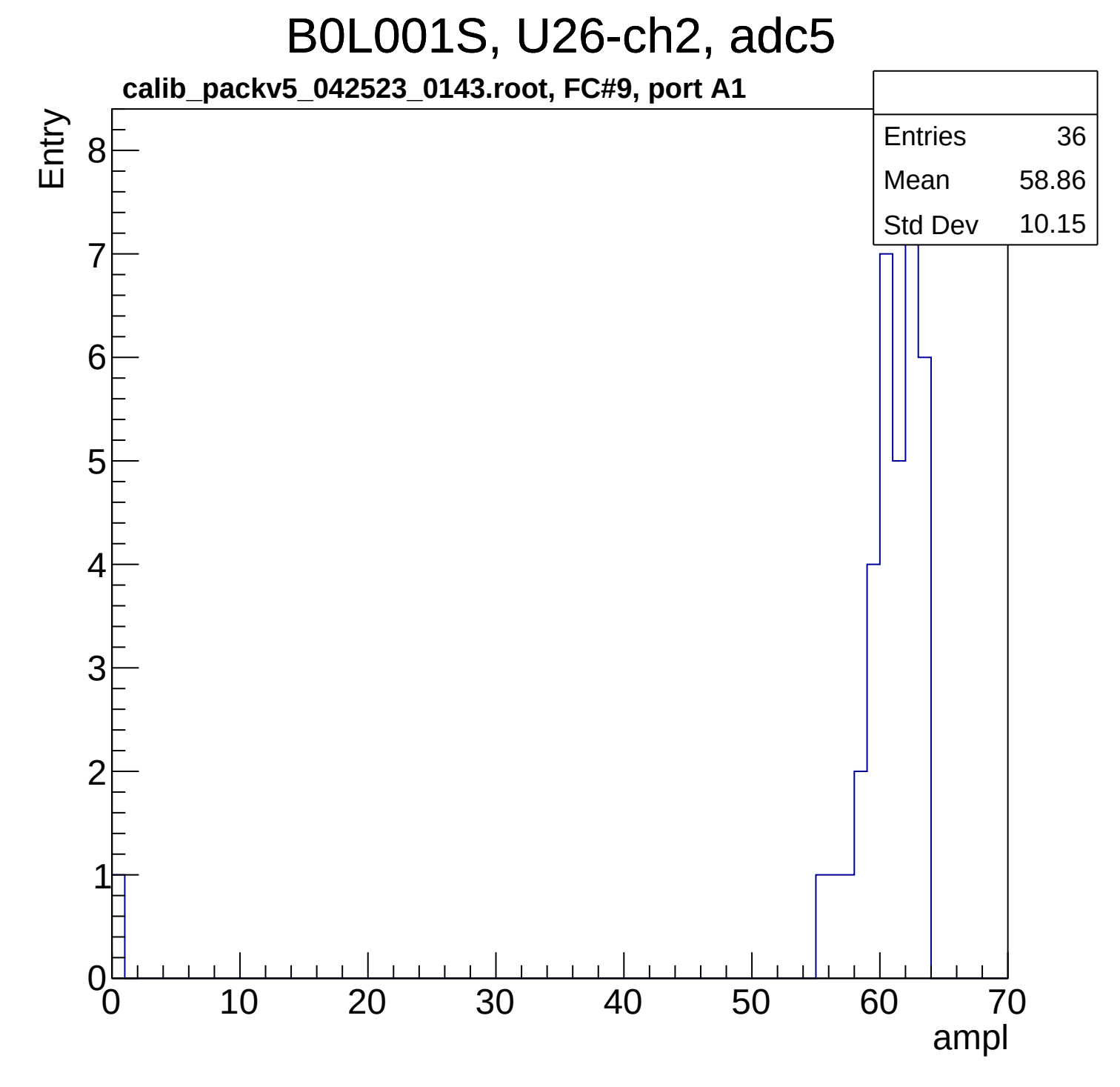
B0L001S, U26-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.86
Std Dev 10.15
Entry
ampl
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0
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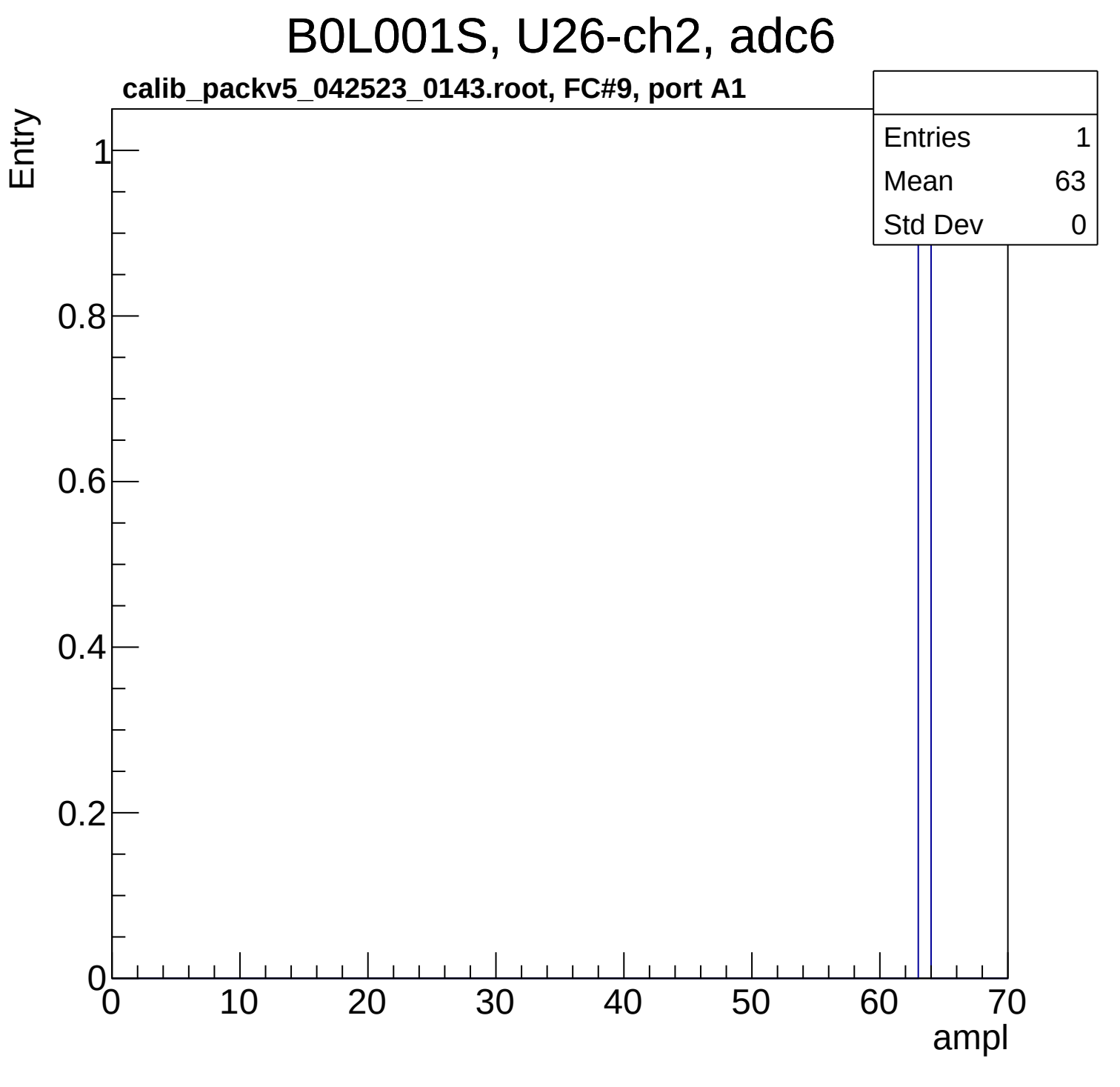
B0L001S, U26-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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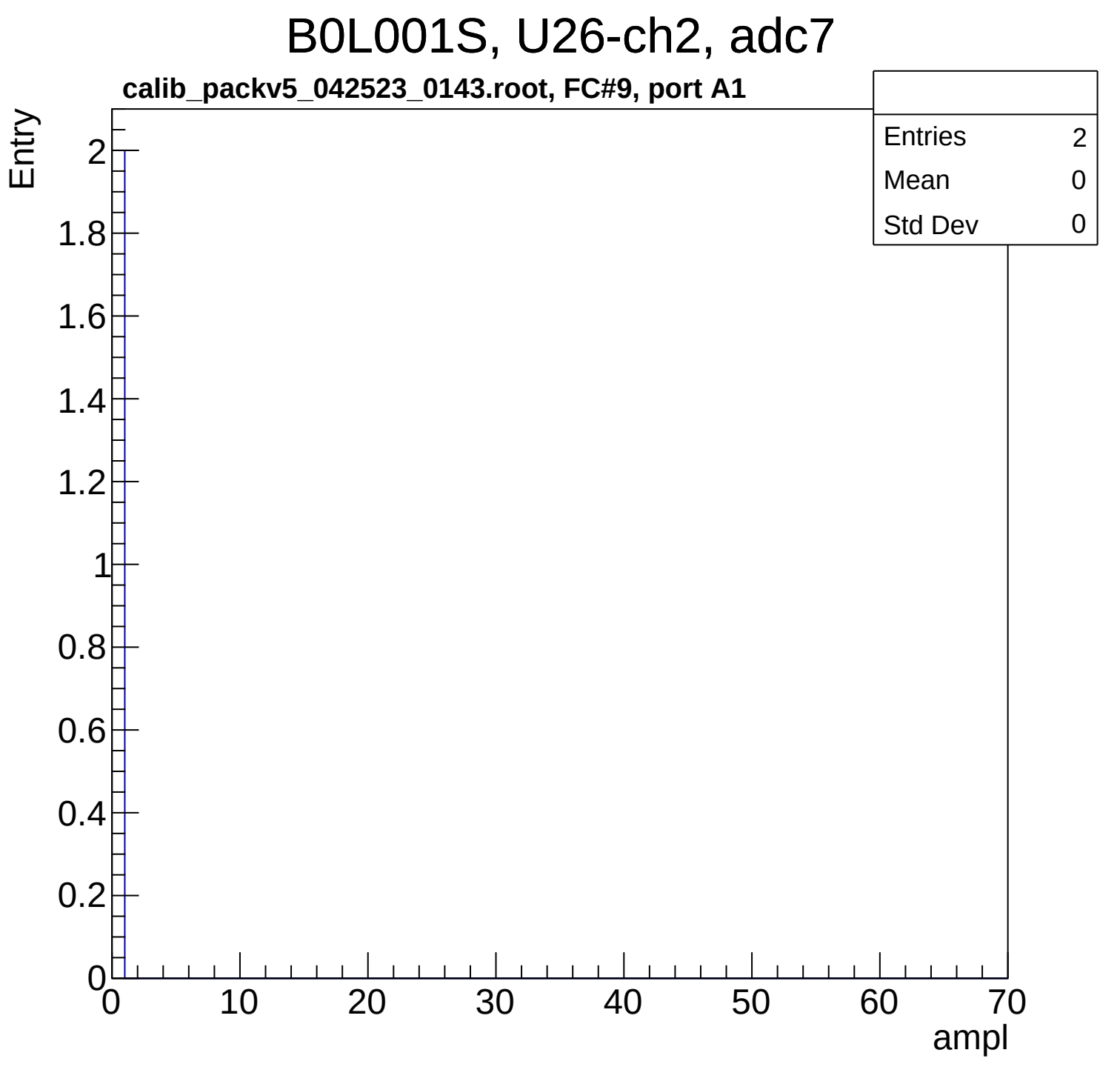
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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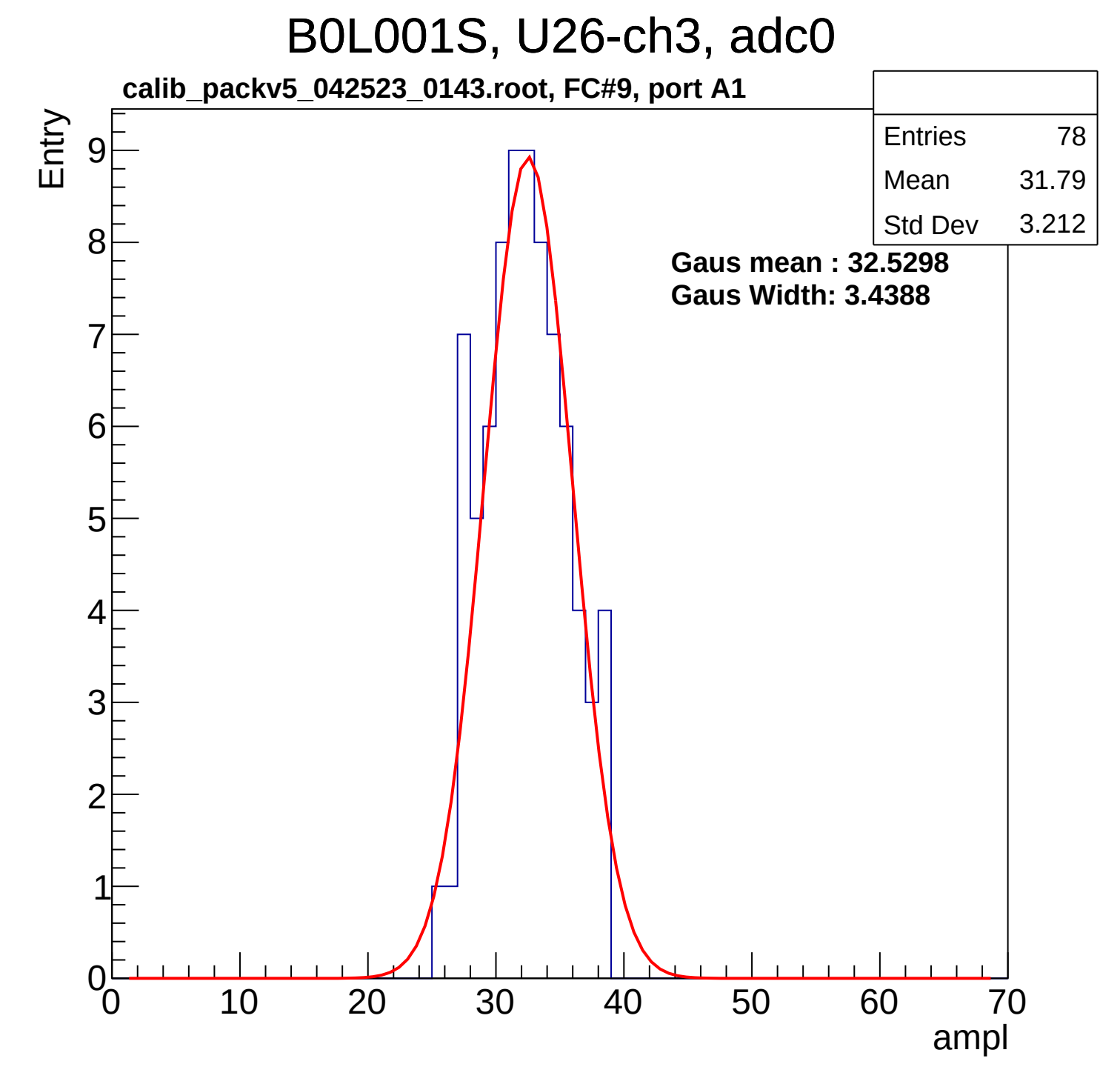
B0L001S, U26-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 31.79
Std Dev 3.212
Gaus mean : 32.5298
Gaus Width: 3.4388
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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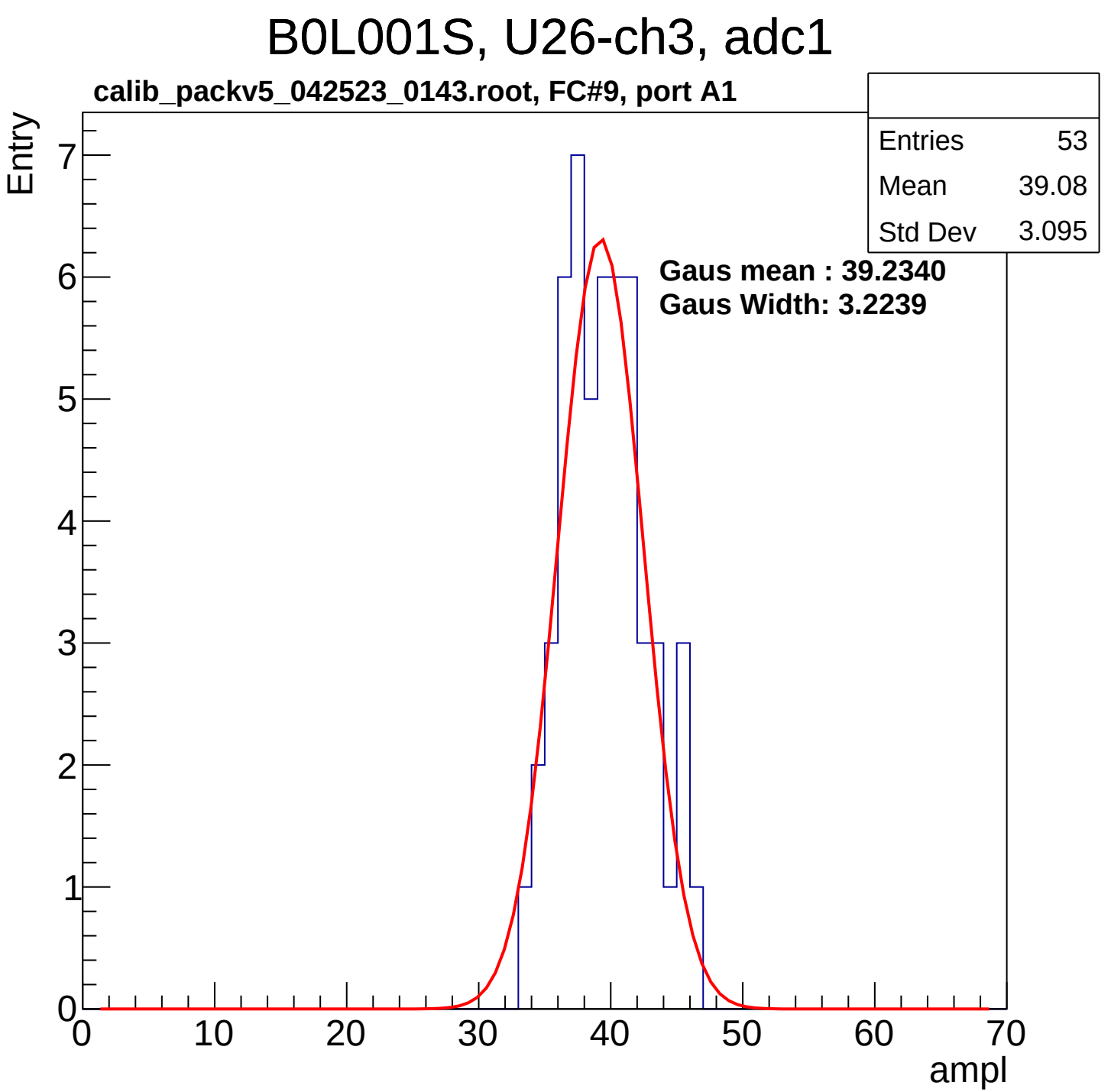
B0L001S, U26-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 39.08
Std Dev 3.095
Gaus mean : 39.2340
Gaus Width: 3.2239
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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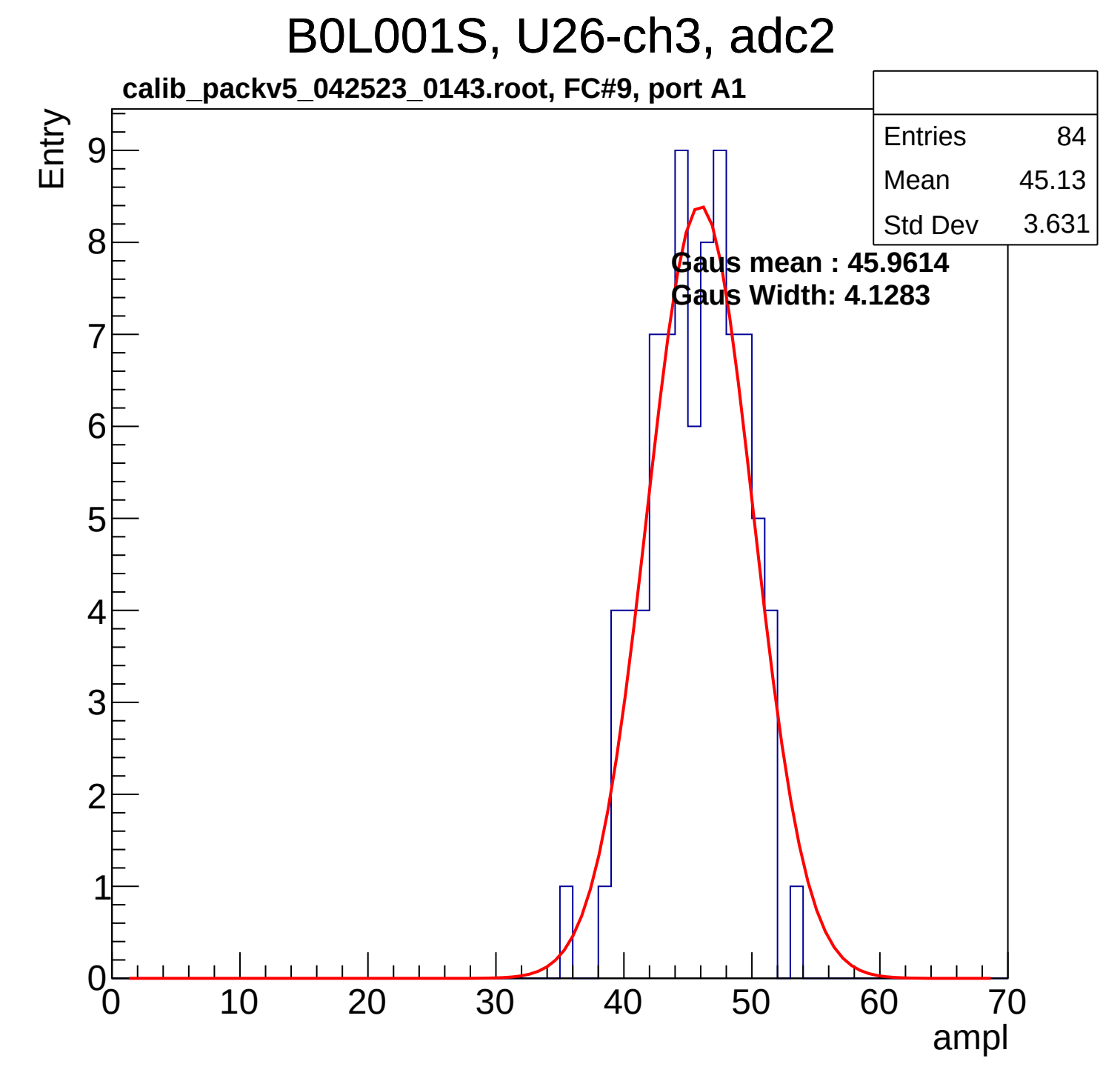
B0L001S, U26-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 45.13
Std Dev 3.631
Gaus mean : 45.9614
Gaus Width: 4.1283
Entry
ampl
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2
3
4
5
6
7
8
9
0
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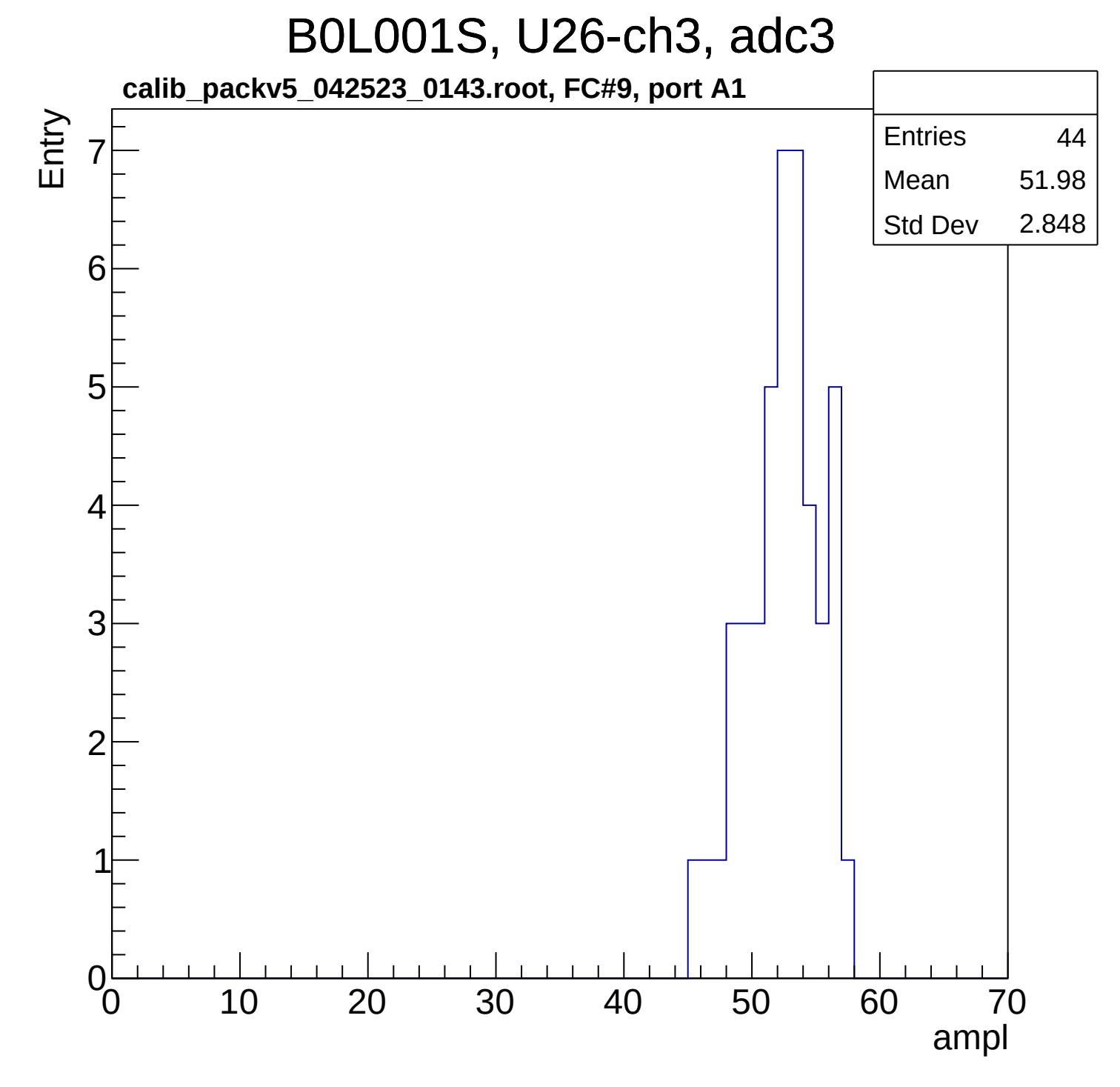
B0L001S, U26-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 51.98
Std Dev 2.848
Entry
ampl
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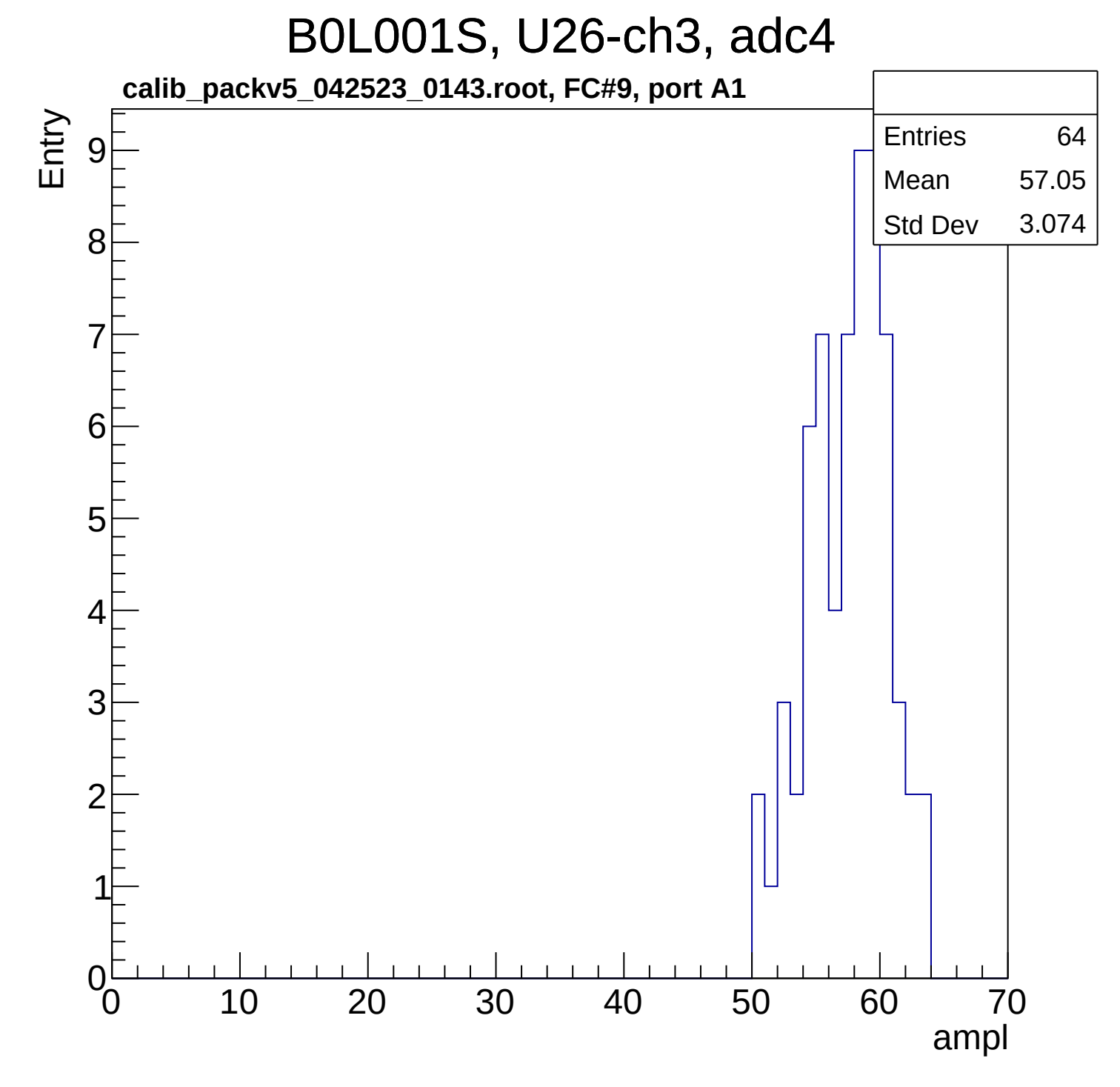
B0L001S, U26-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.05
Std Dev 3.074
Entry
ampl
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3
4
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7
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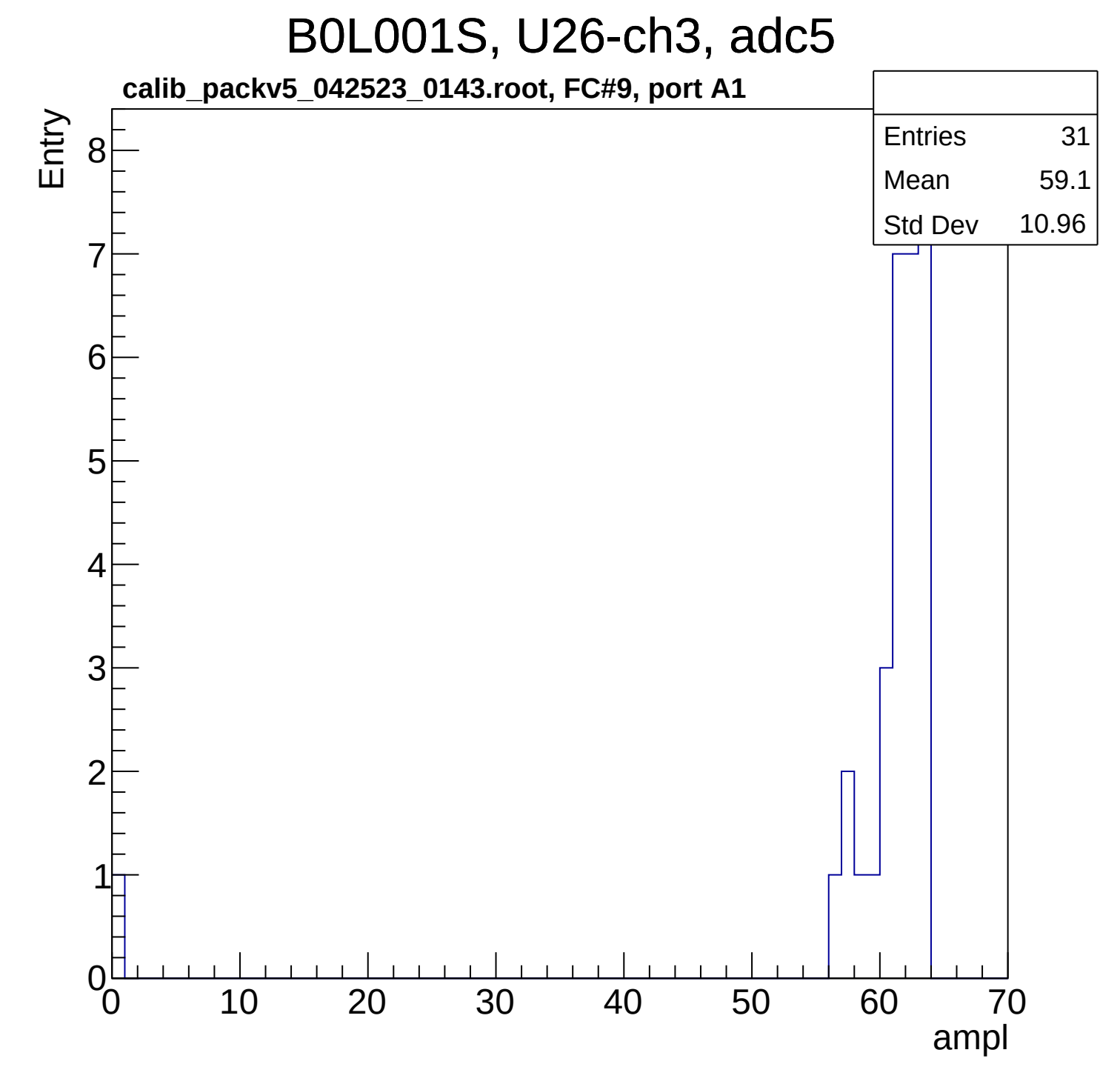
B0L001S, U26-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.1
Std Dev 10.96
Entry
ampl
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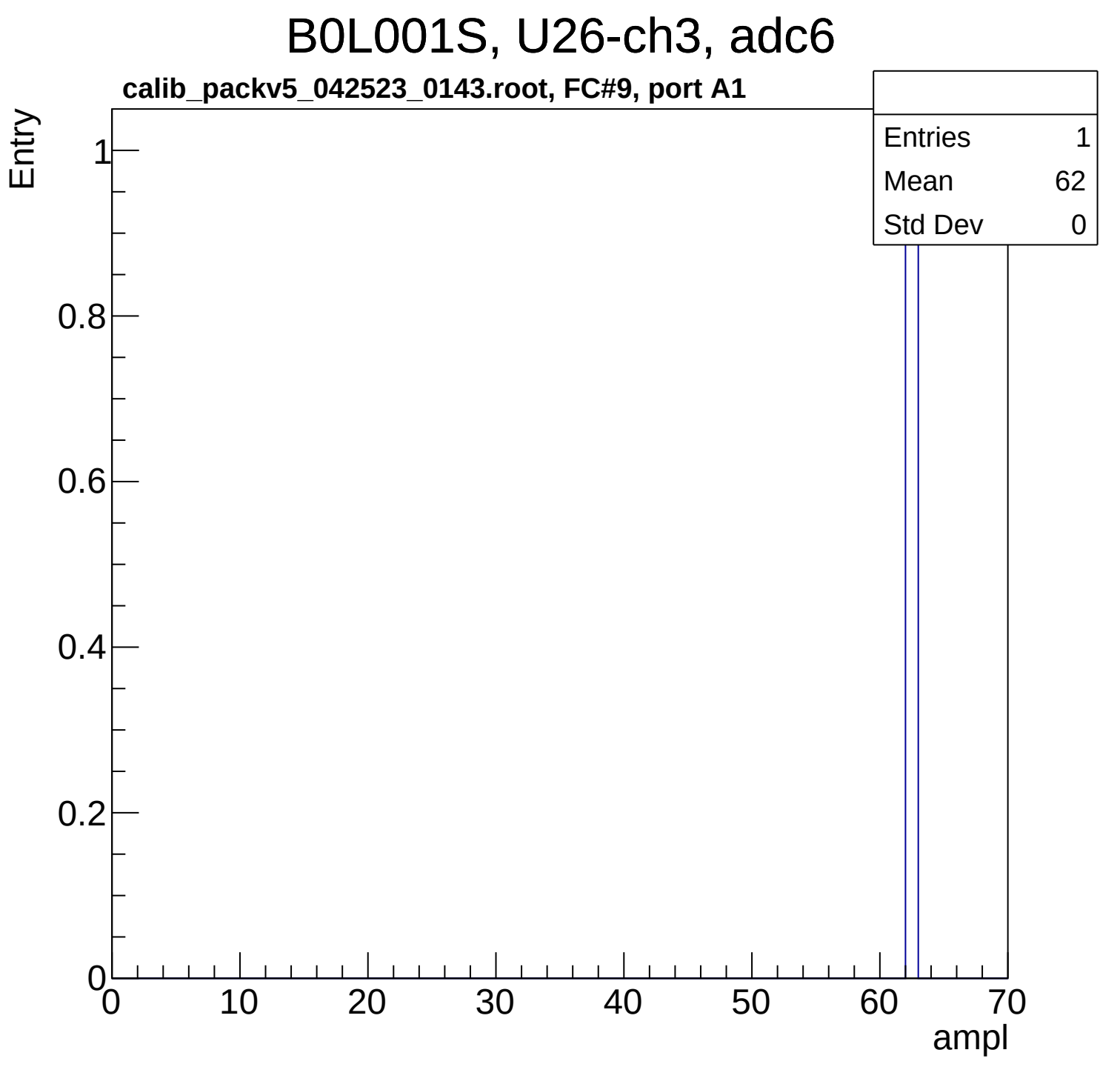
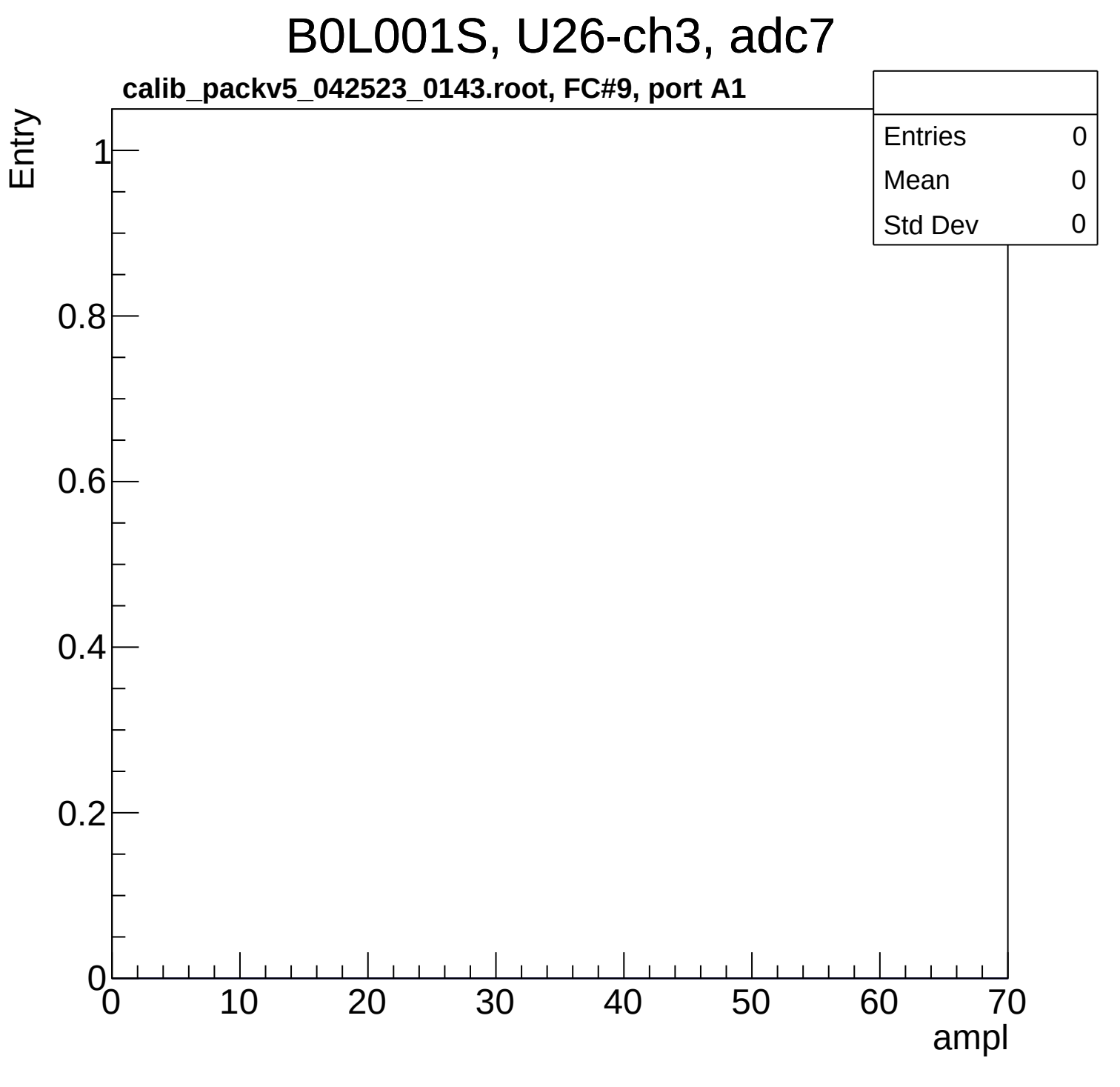
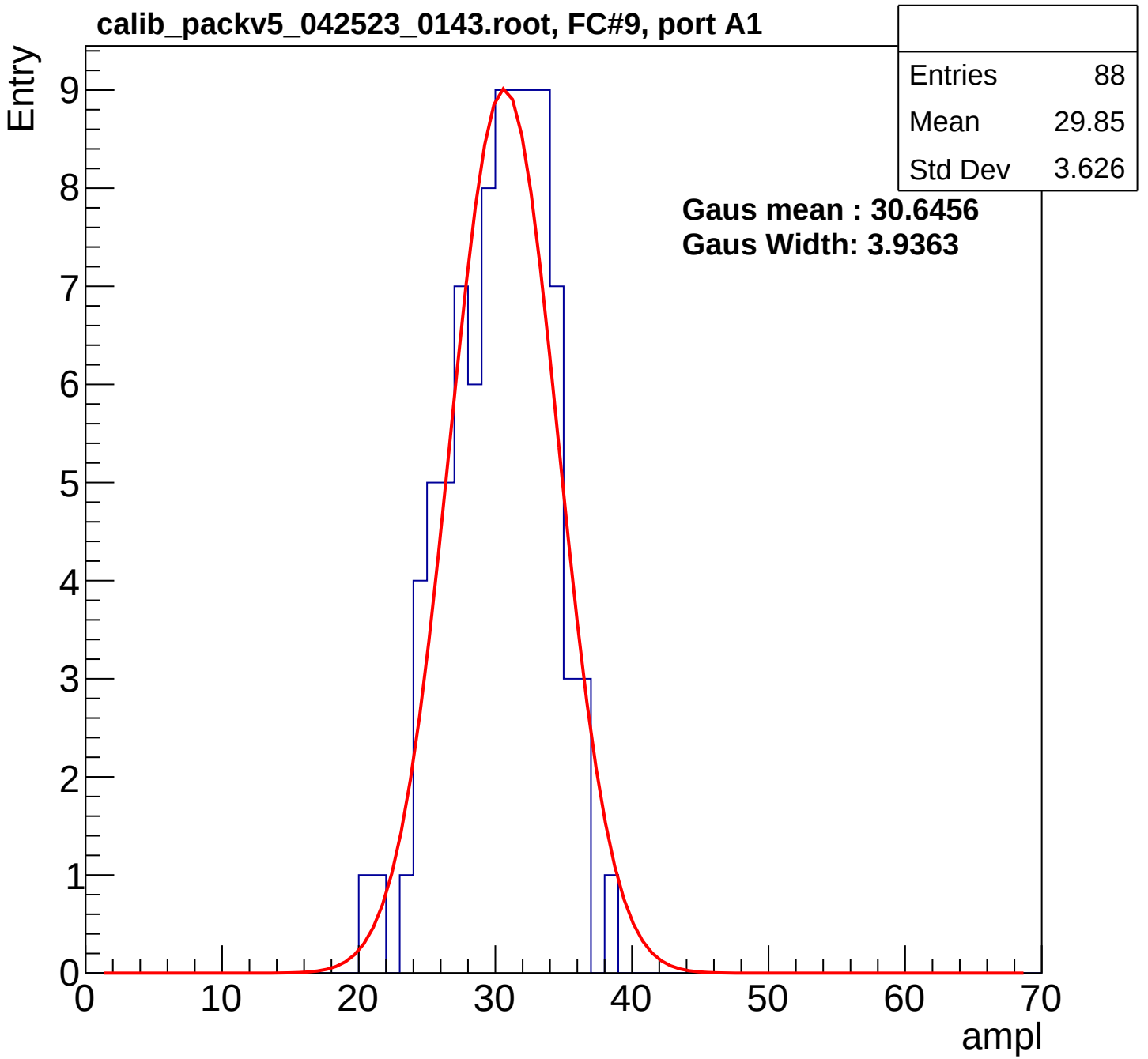
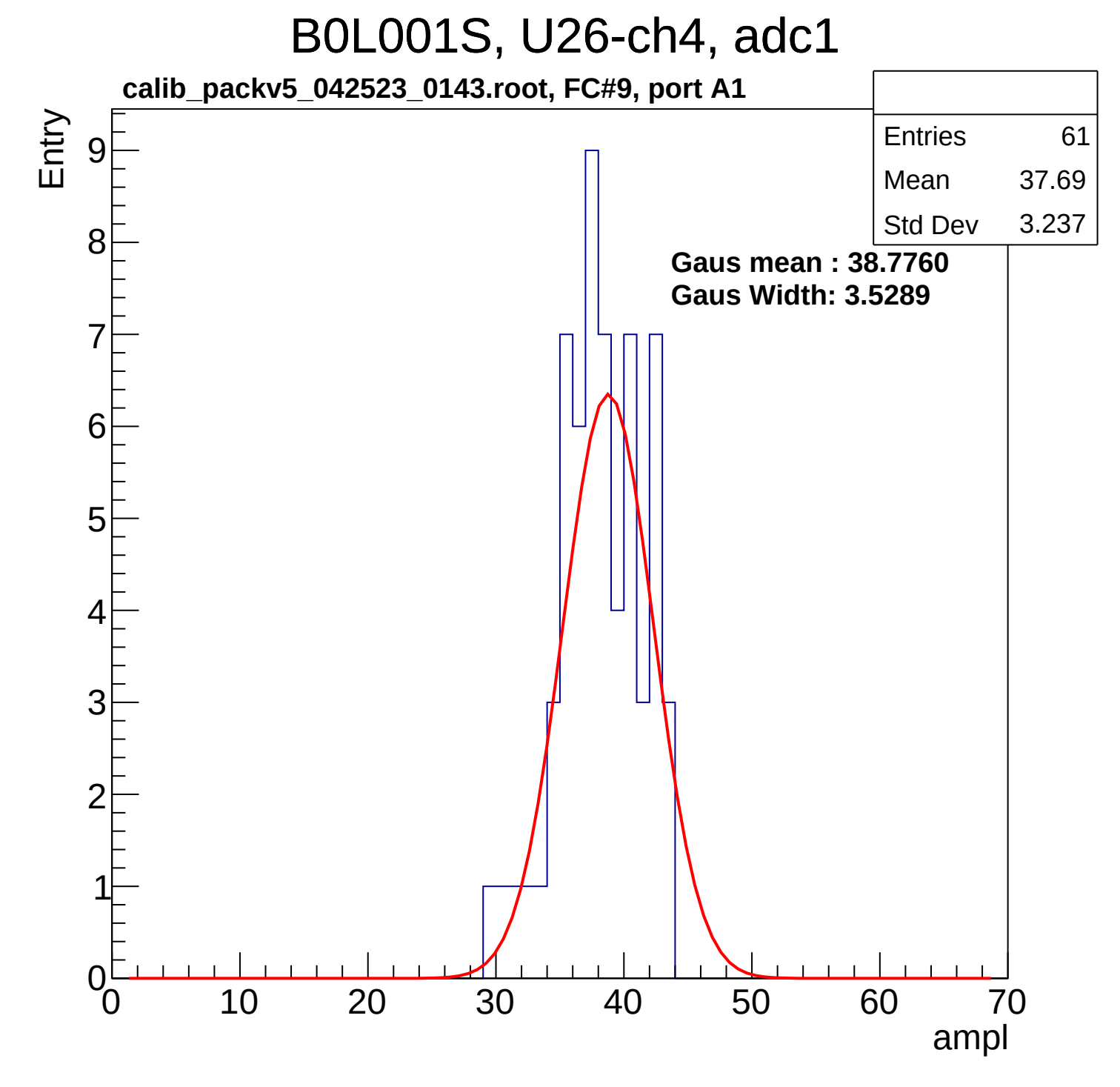

B0L001S, U26-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 29.85
Std Dev 3.626
Gaus mean : 30.6456
Gaus Width: 3.9363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.69
Std Dev 3.237
Gaus mean : 38.7760
Gaus Width: 3.5289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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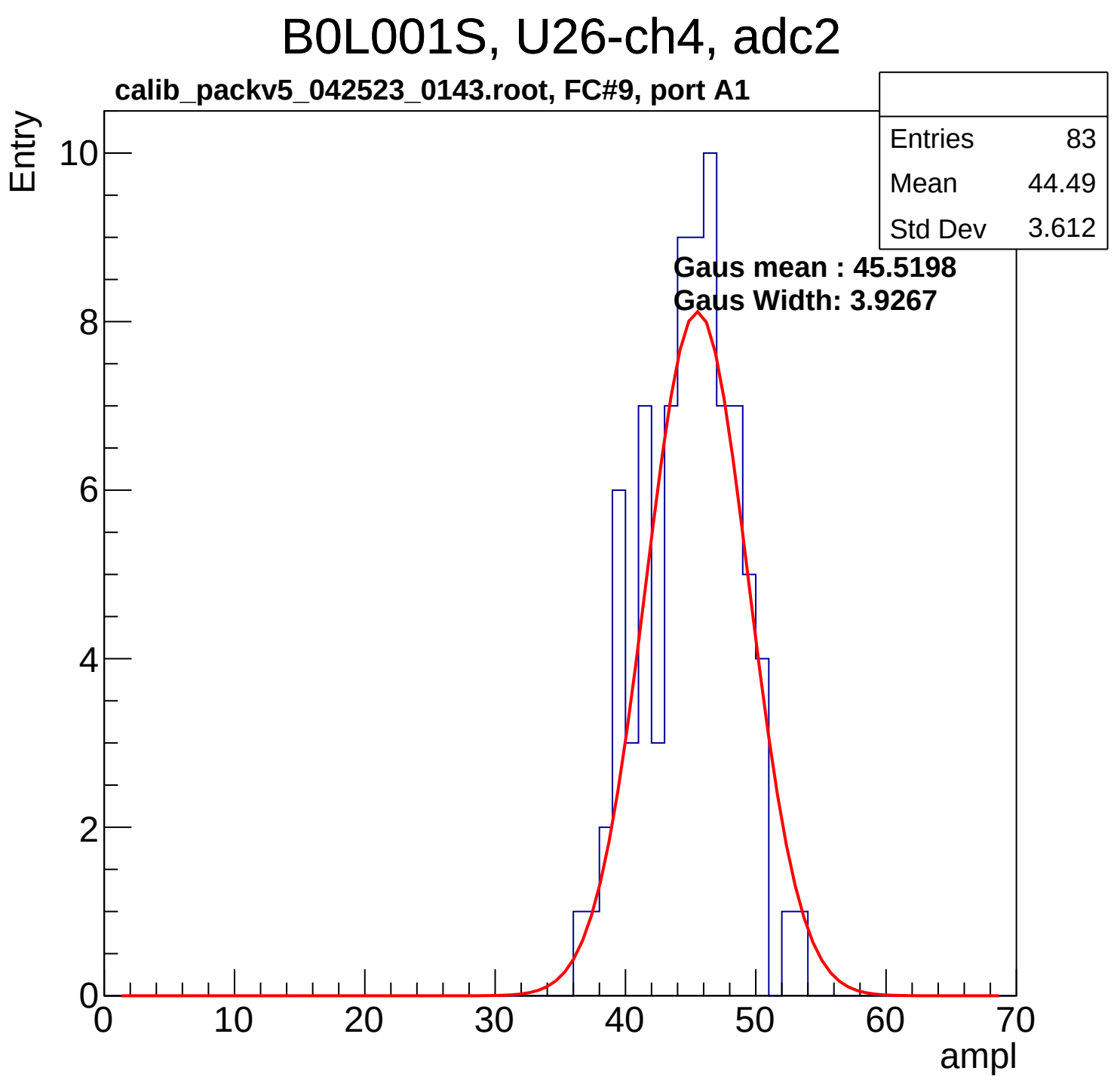
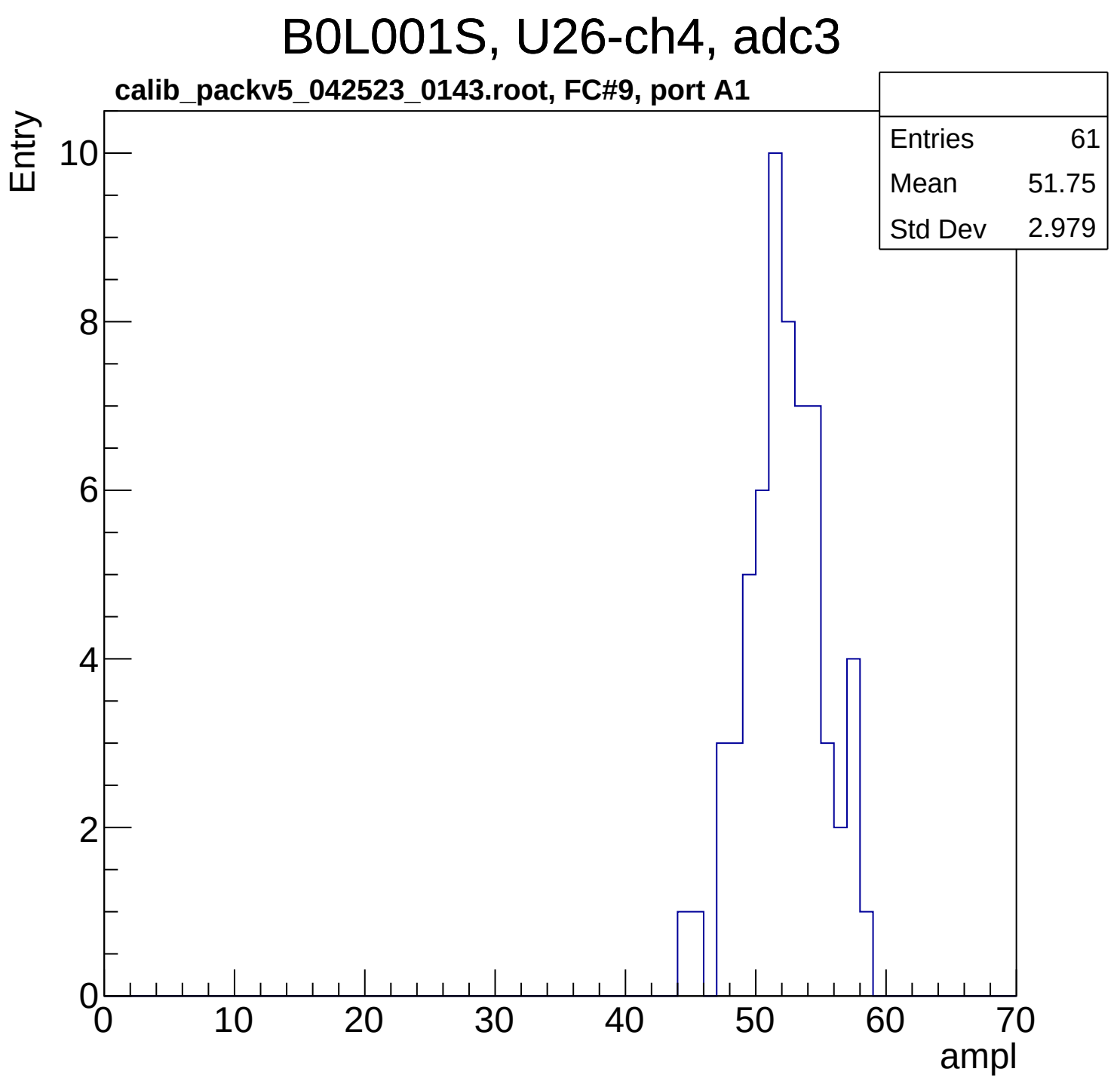
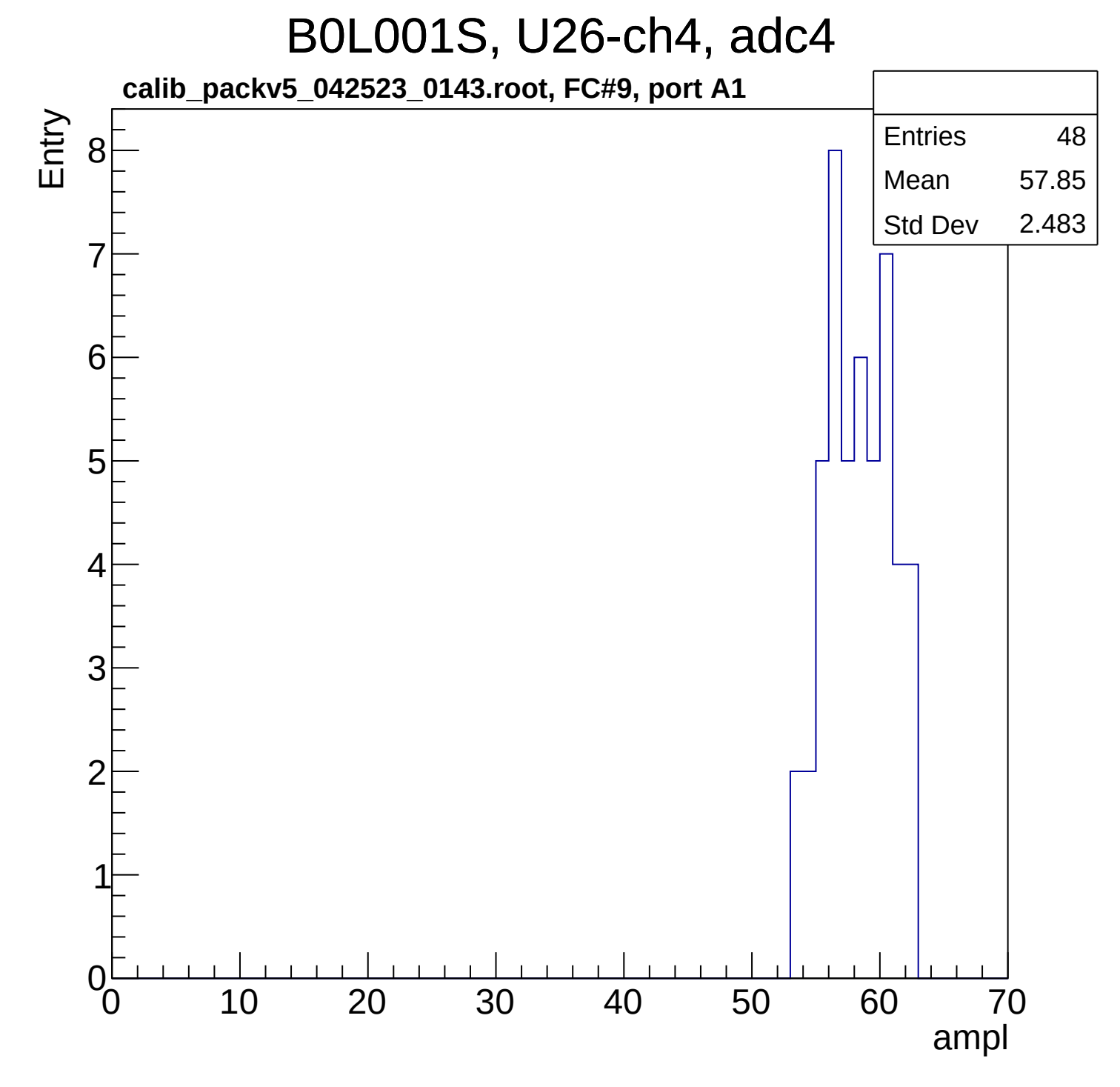

B0L001S, U26-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 44.49
Std Dev 3.612
Gaus mean : 45.5198
Gaus Width: 3.9267
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.75
Std Dev 2.979
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.85
Std Dev 2.483
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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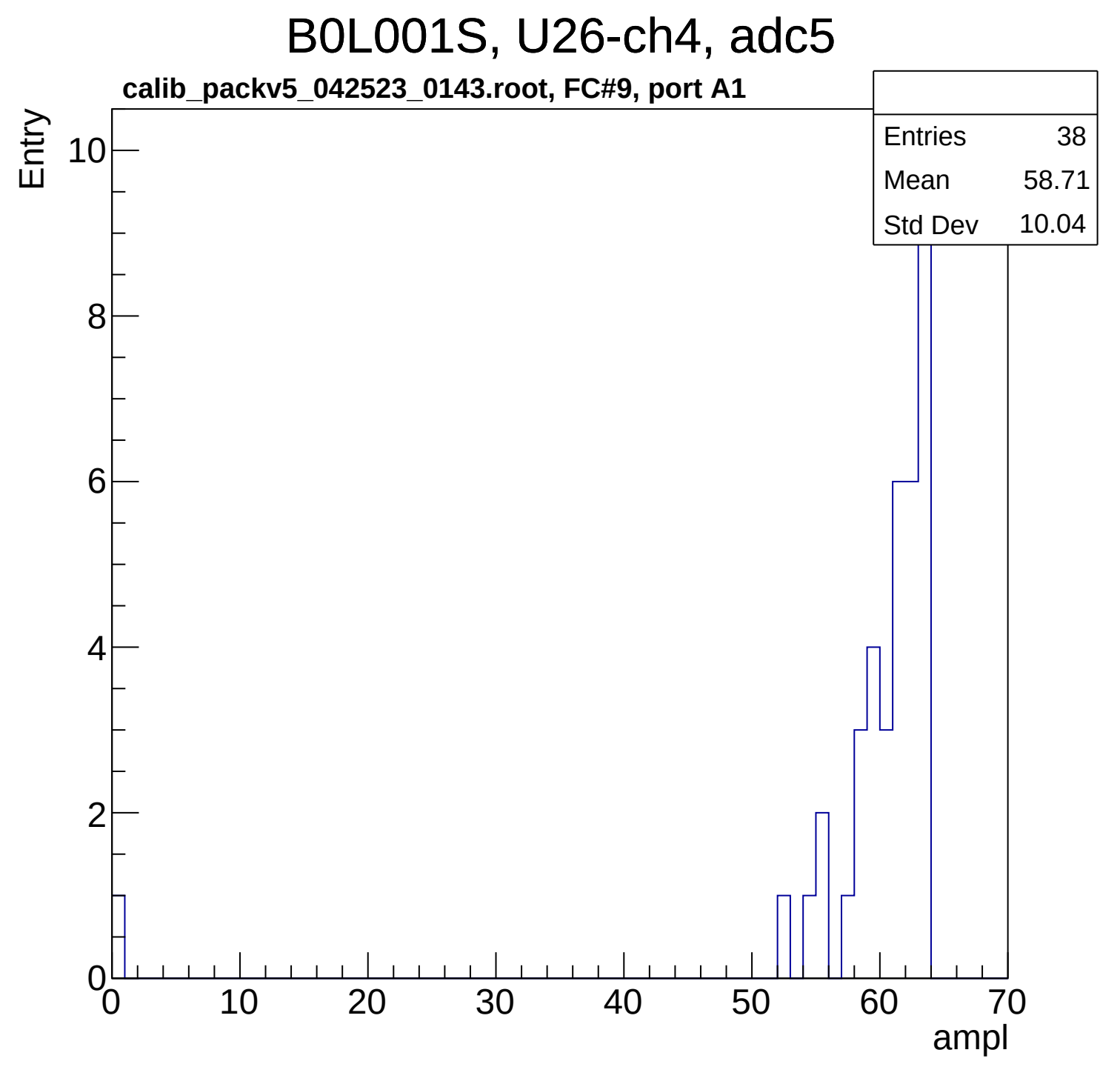
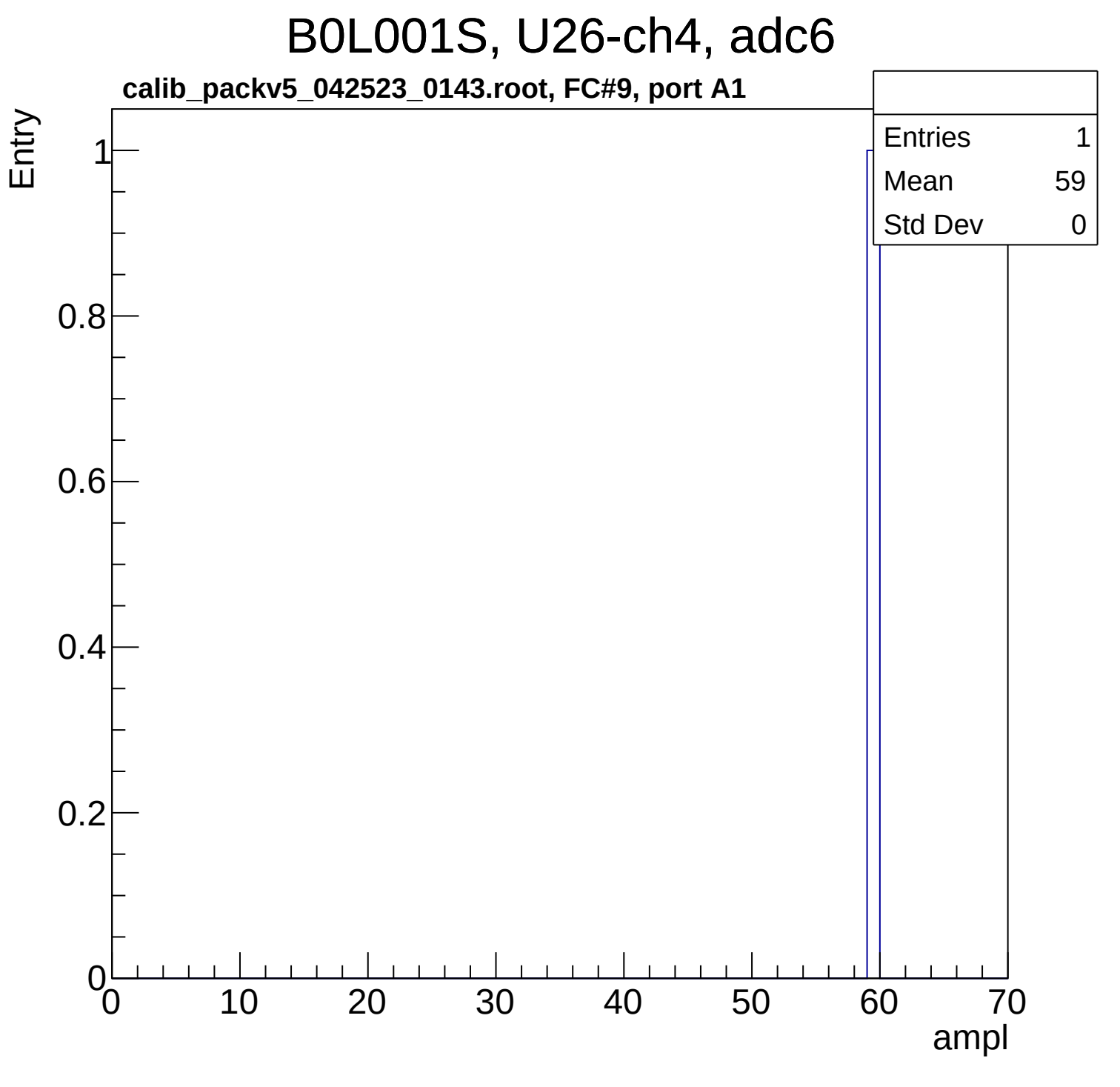
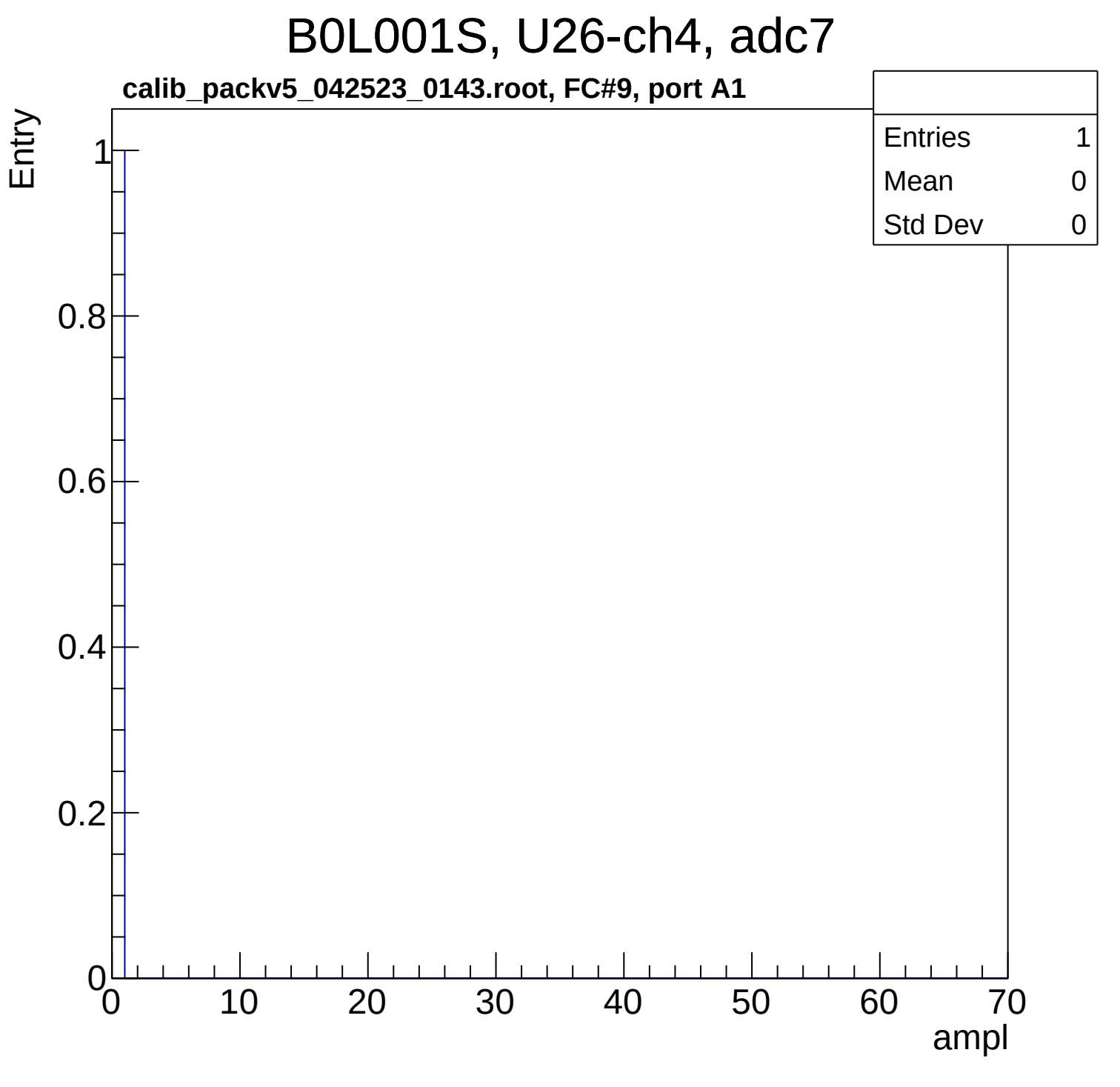
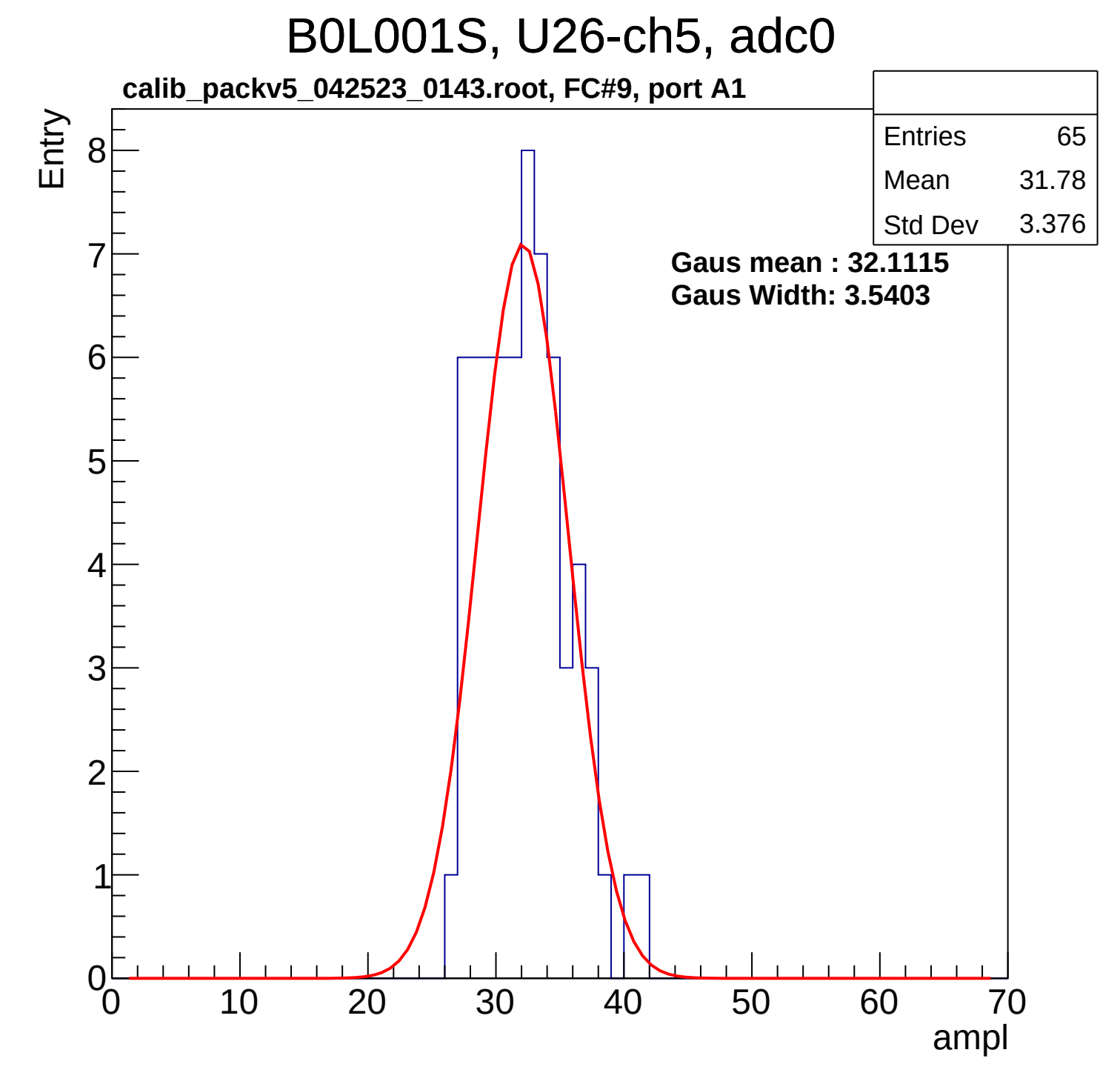
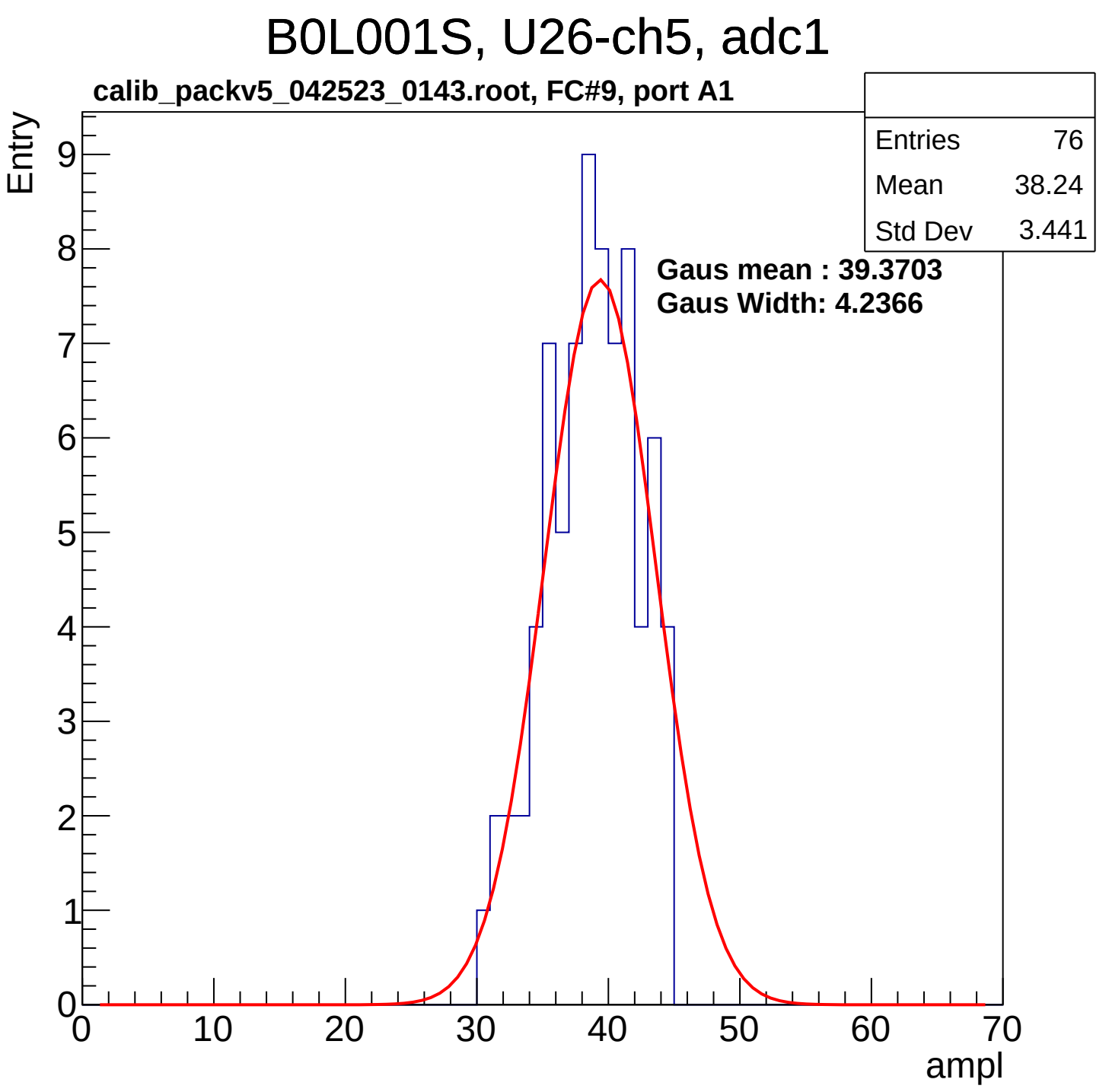
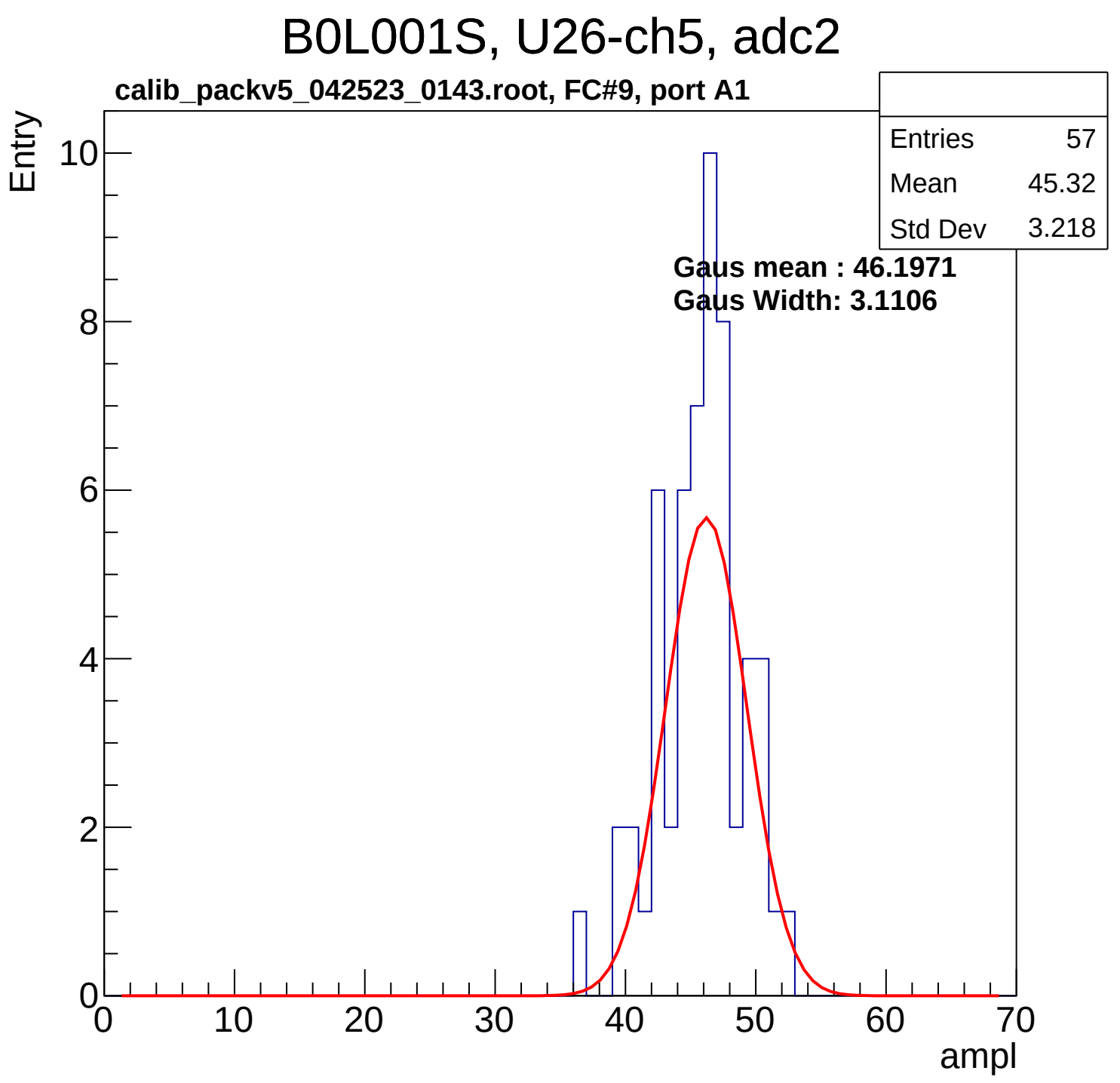

B0L001S, U26-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.71
Std Dev 10.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

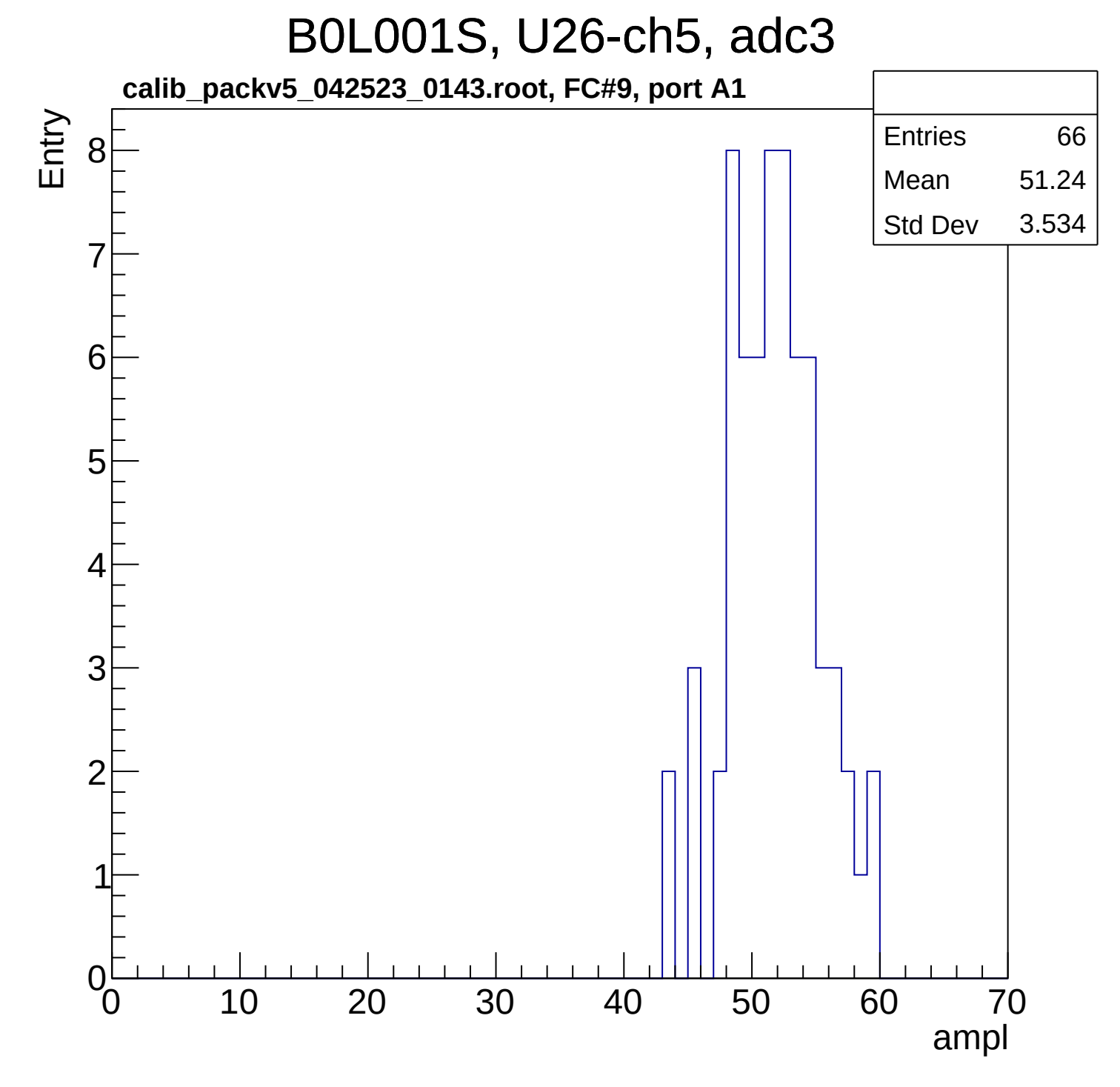

B0L001S, U26-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

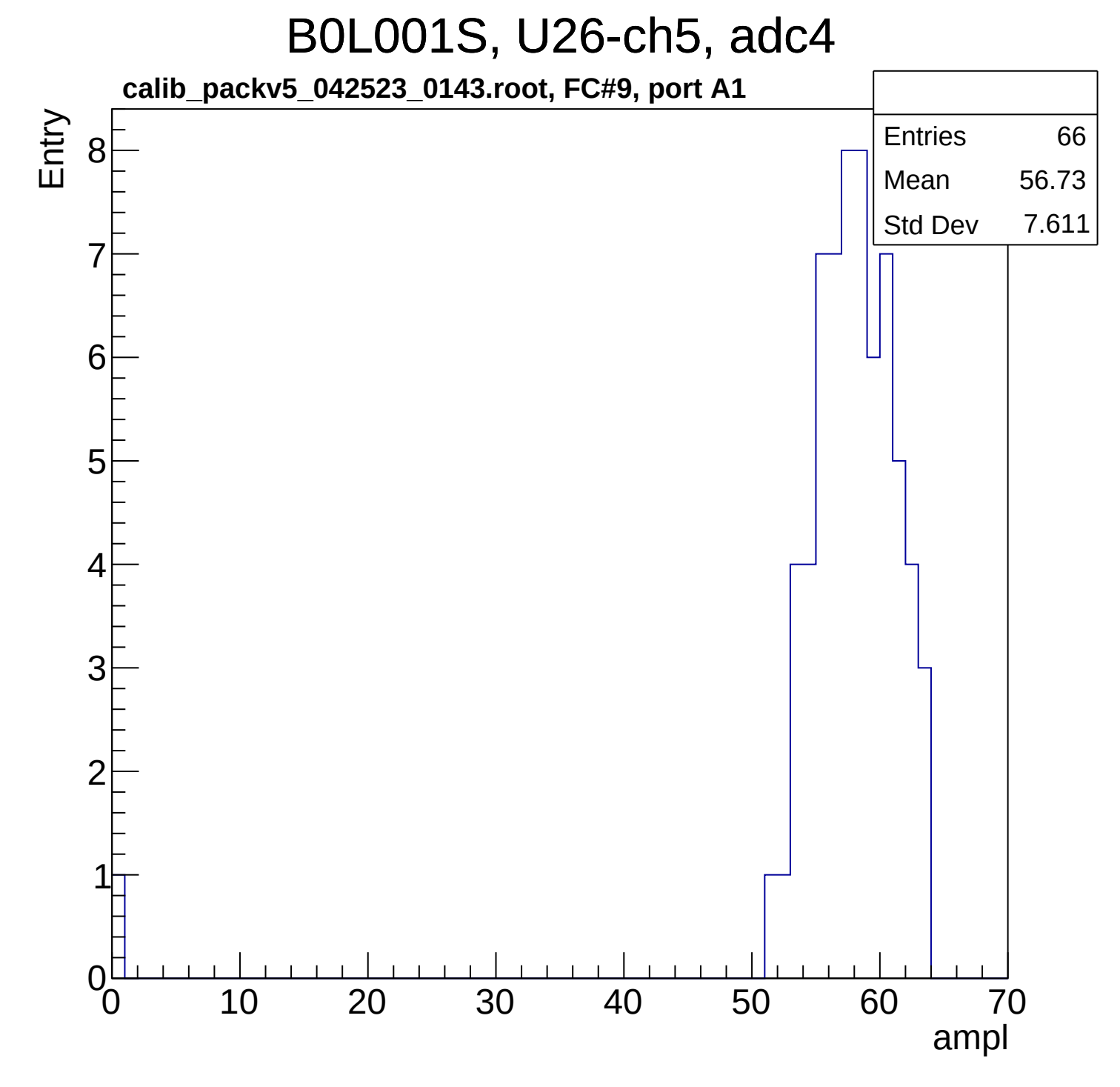

B0L001S, U26-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

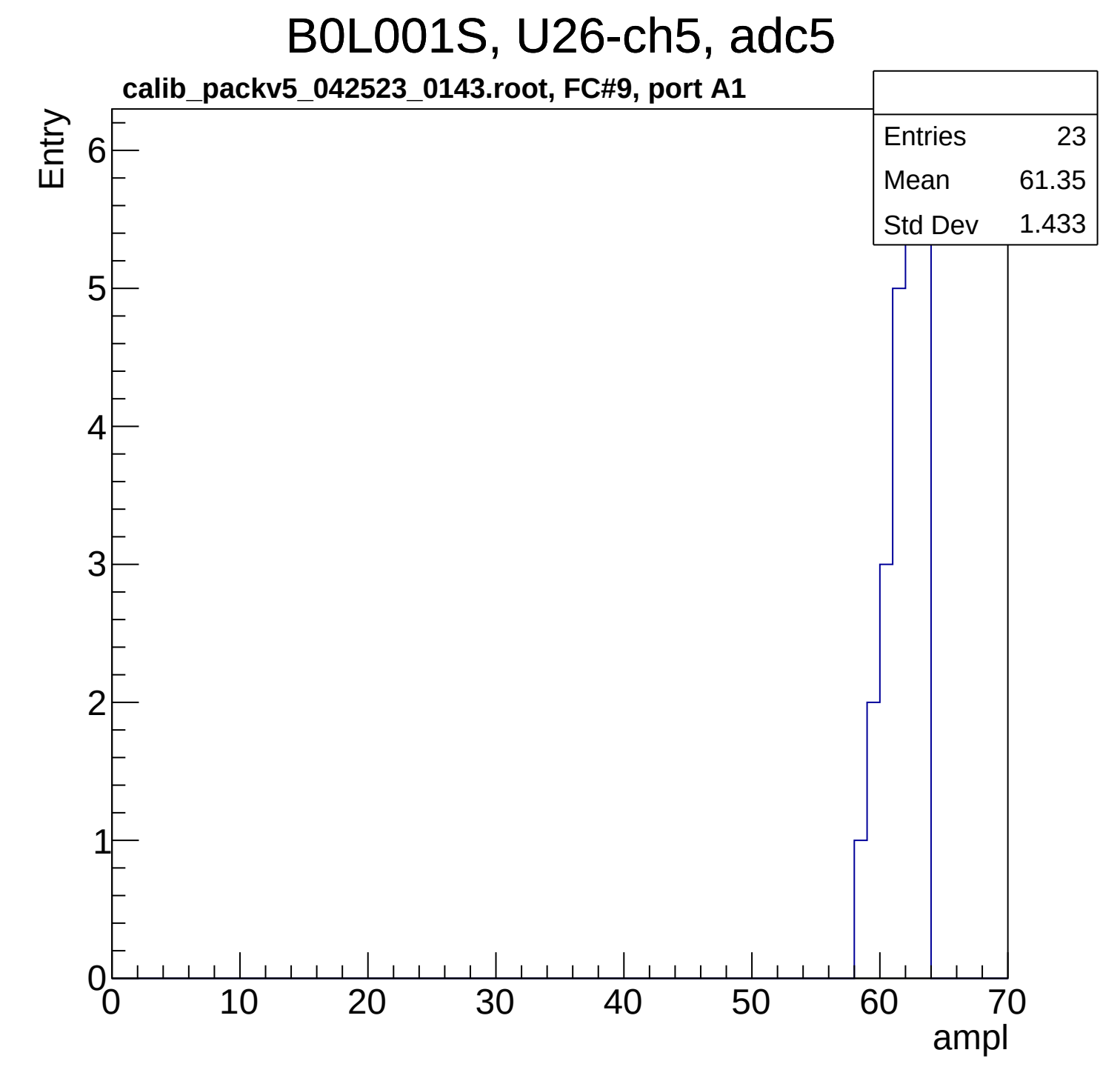

B0L001S, U26-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 31.78
Std Dev 3.376
Gaus mean : 32.1115
Gaus Width: 3.5403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

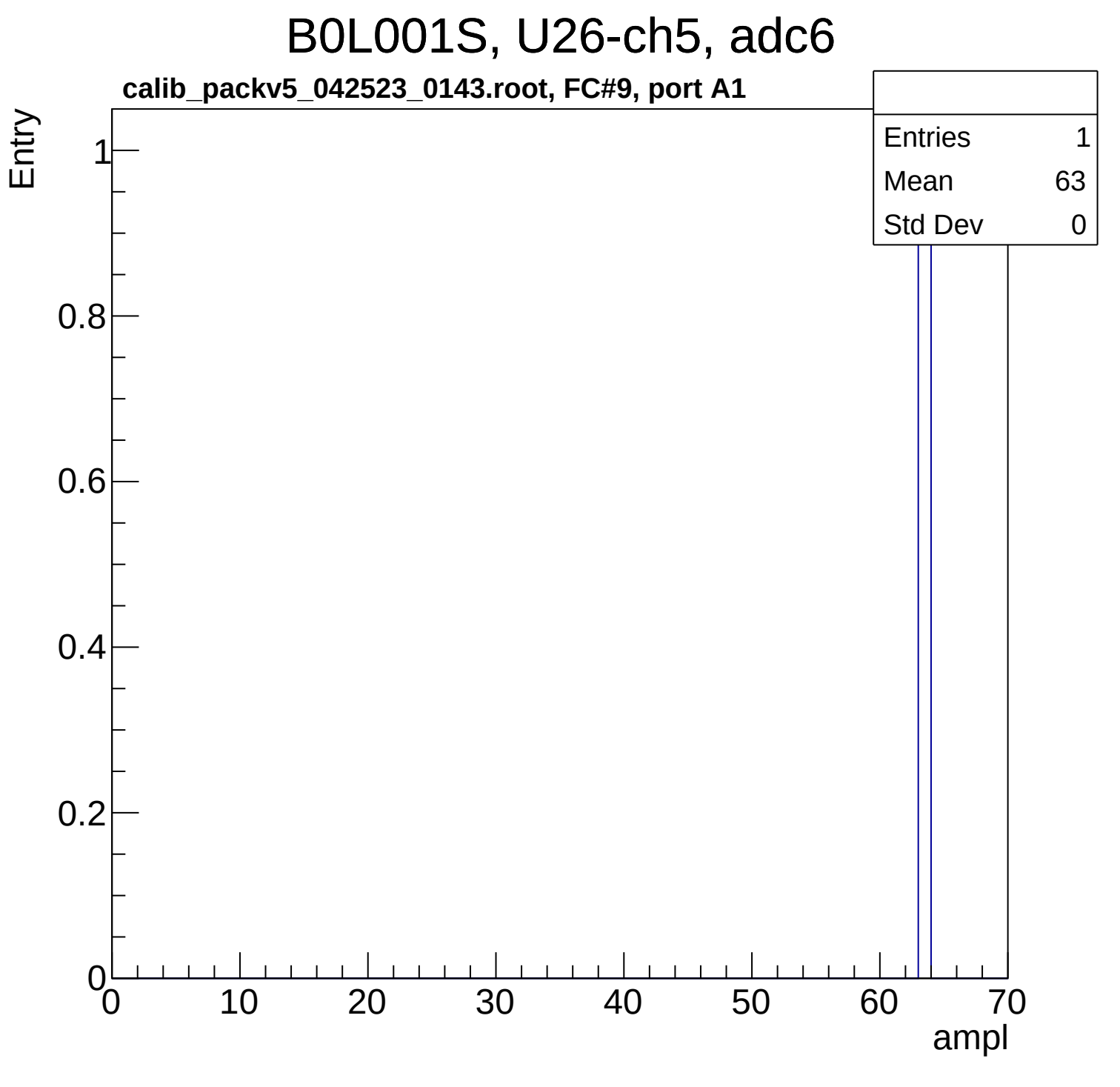

B0L001S, U26-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.24
Std Dev 3.441
Gaus mean : 39.3703
Gaus Width: 4.2366
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

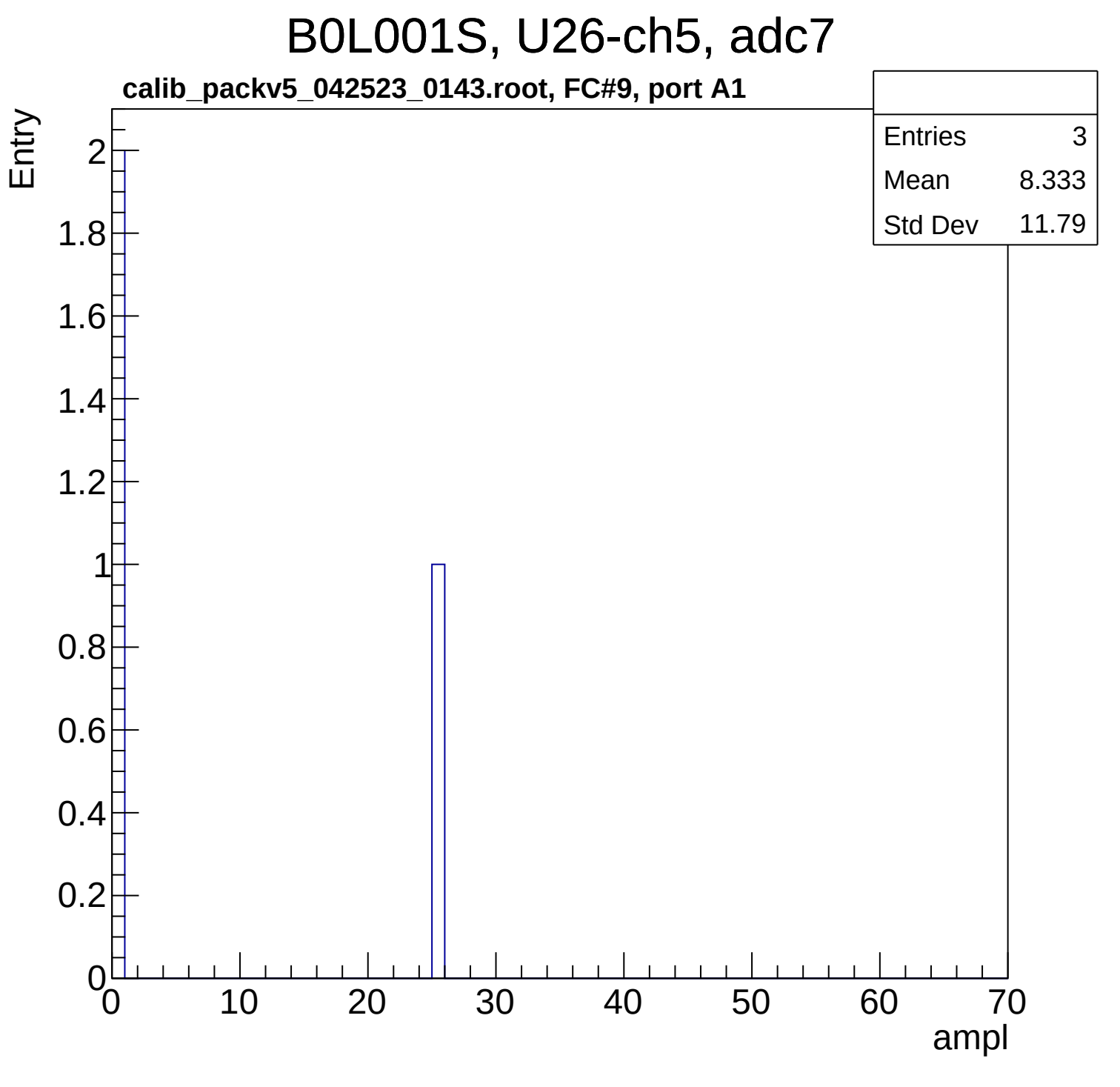

B0L001S, U26-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 45.32
Std Dev 3.218
Gaus mean : 46.1971
Gaus Width: 3.1106
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

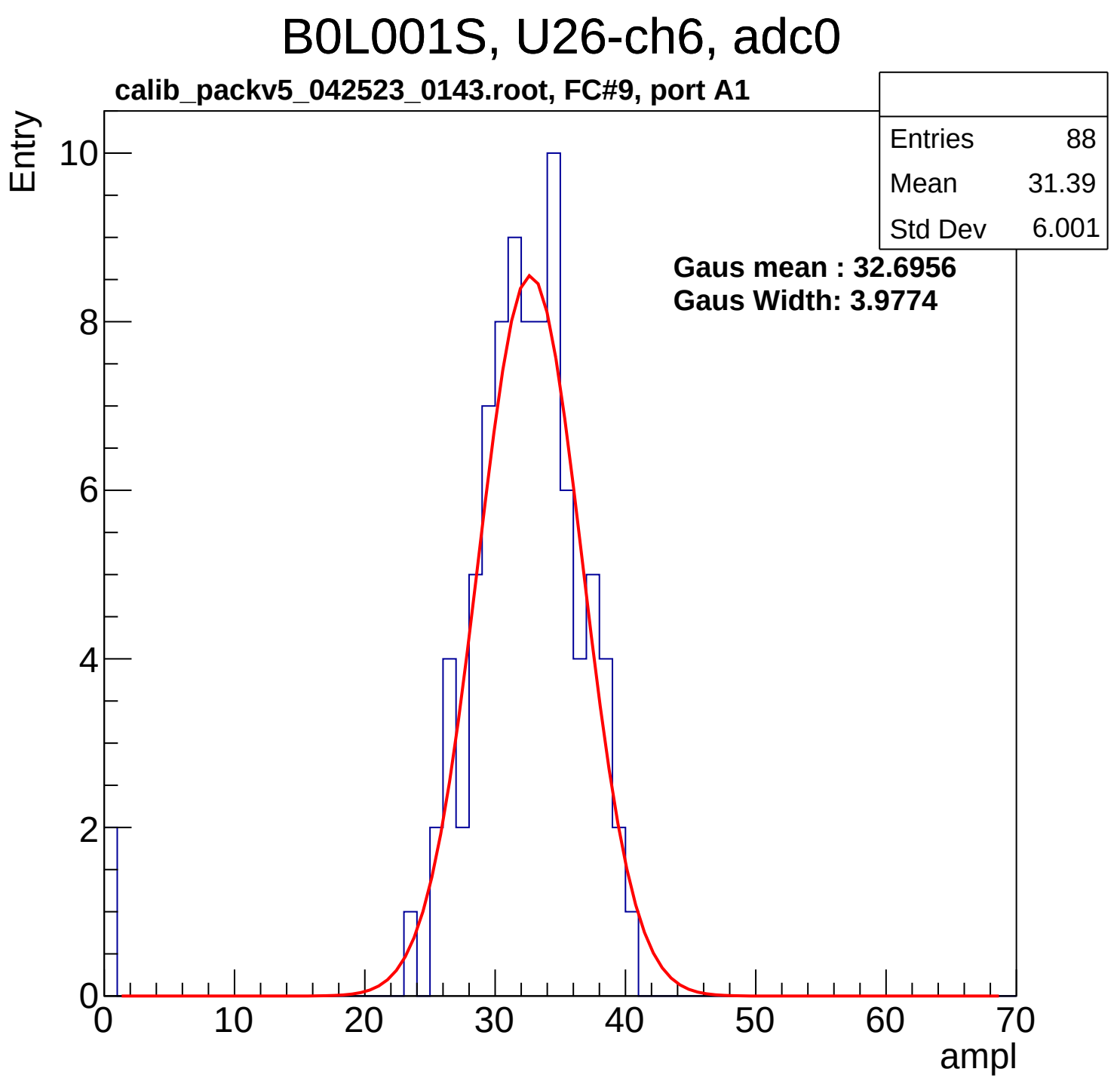

B0L001S, U26-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.24
Std Dev 3.534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

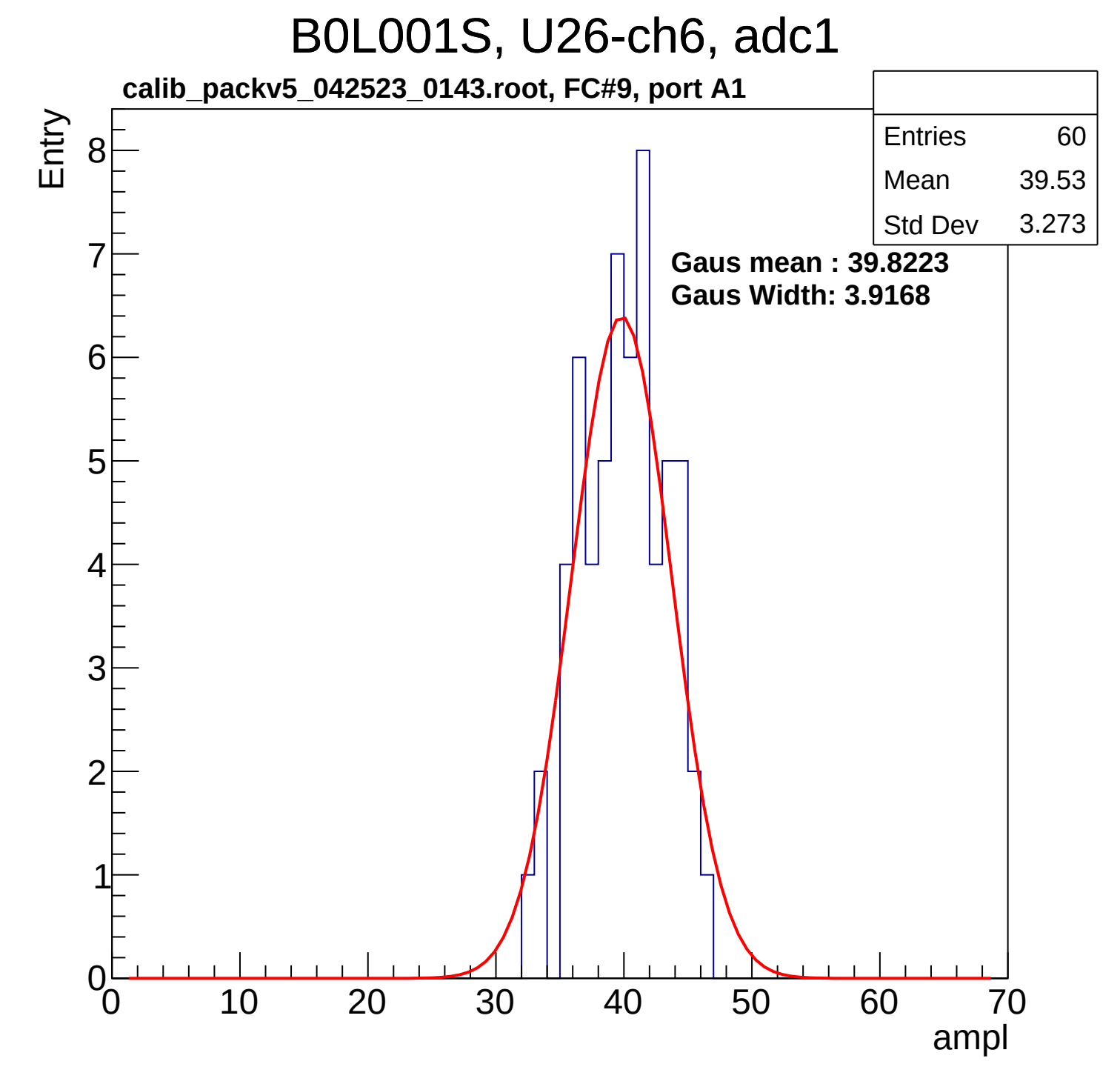

B0L001S, U26-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.73
Std Dev 7.611
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

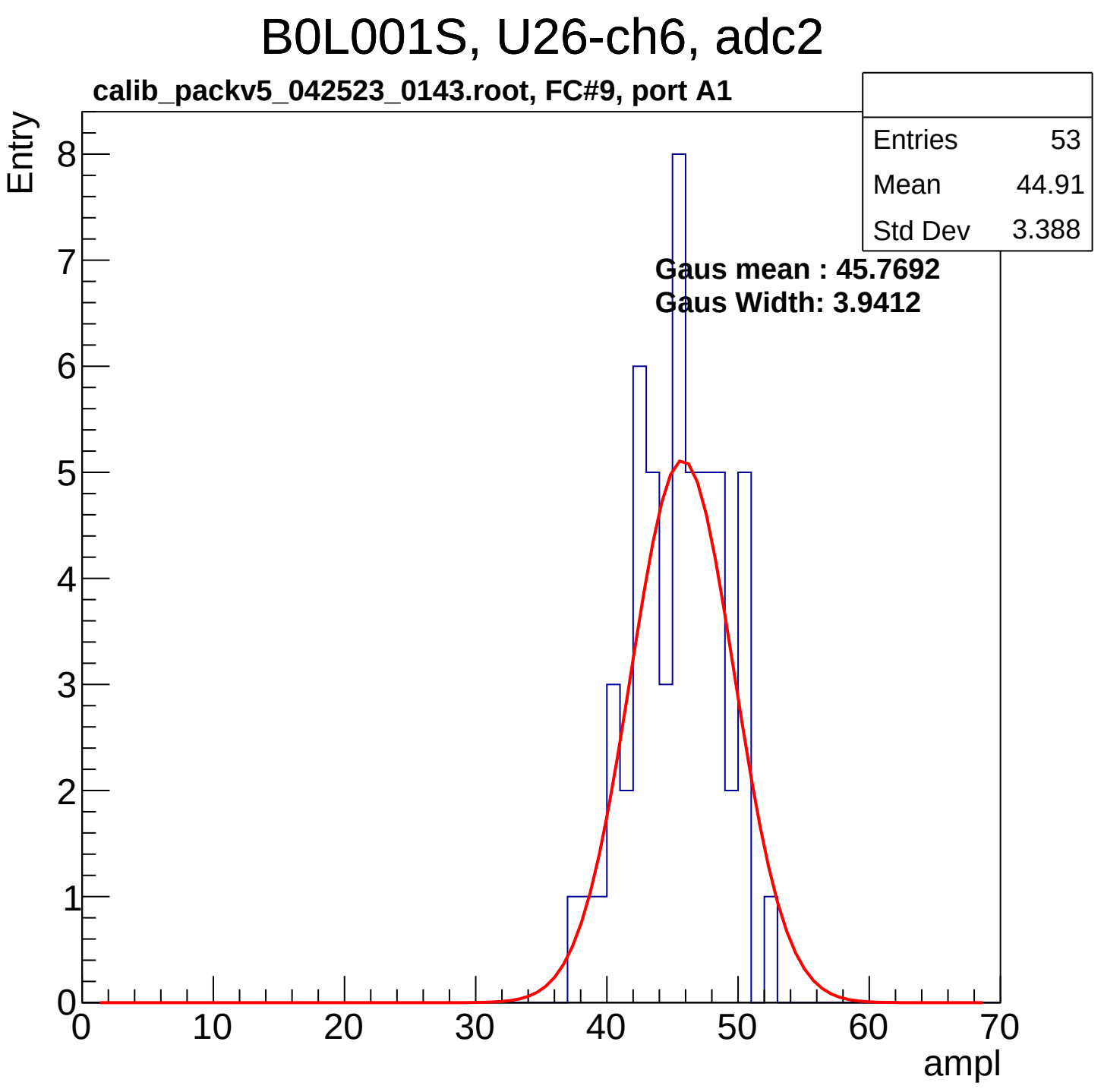

B0L001S, U26-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.35
Std Dev 1.433
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

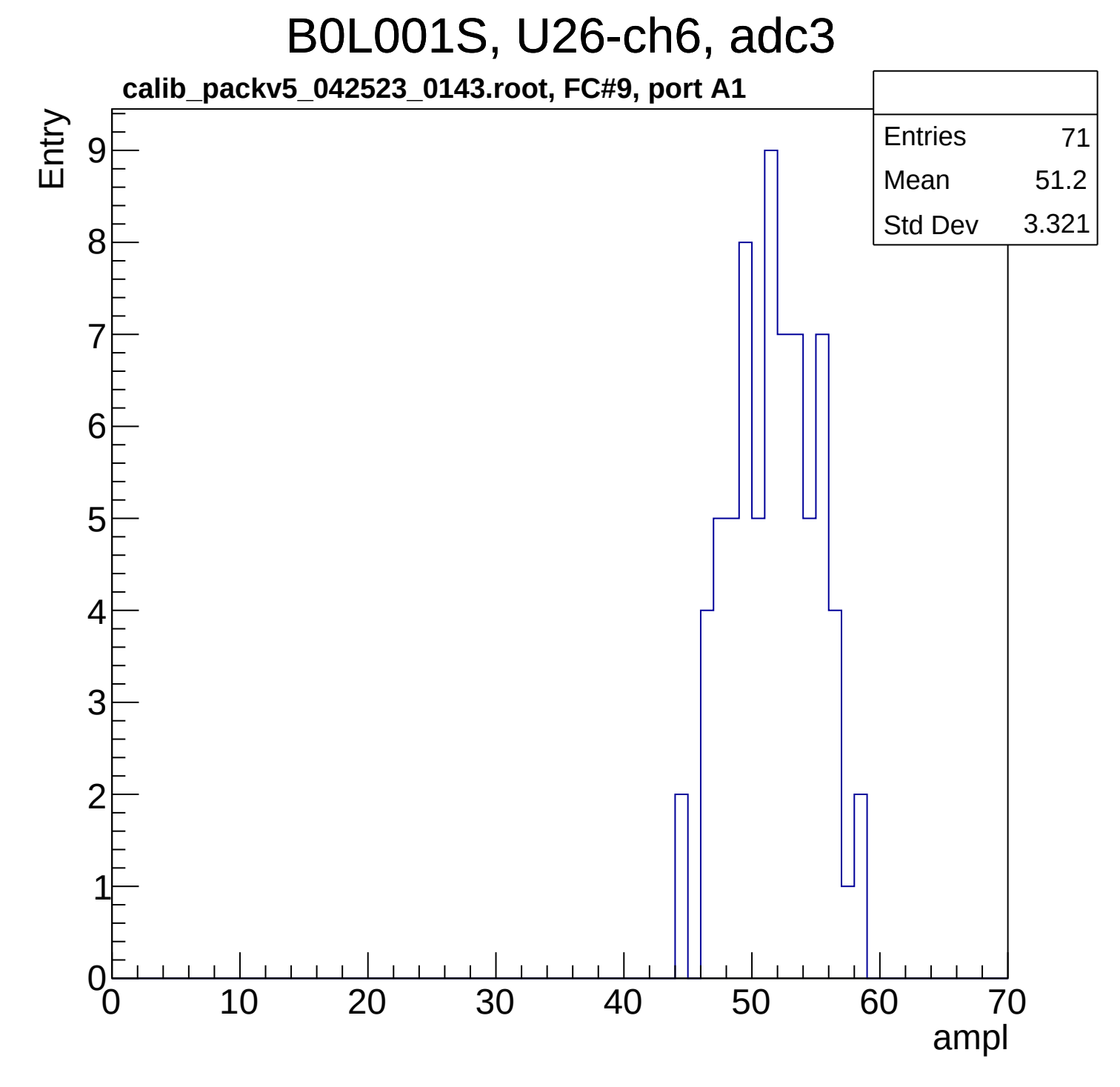

B0L001S, U26-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

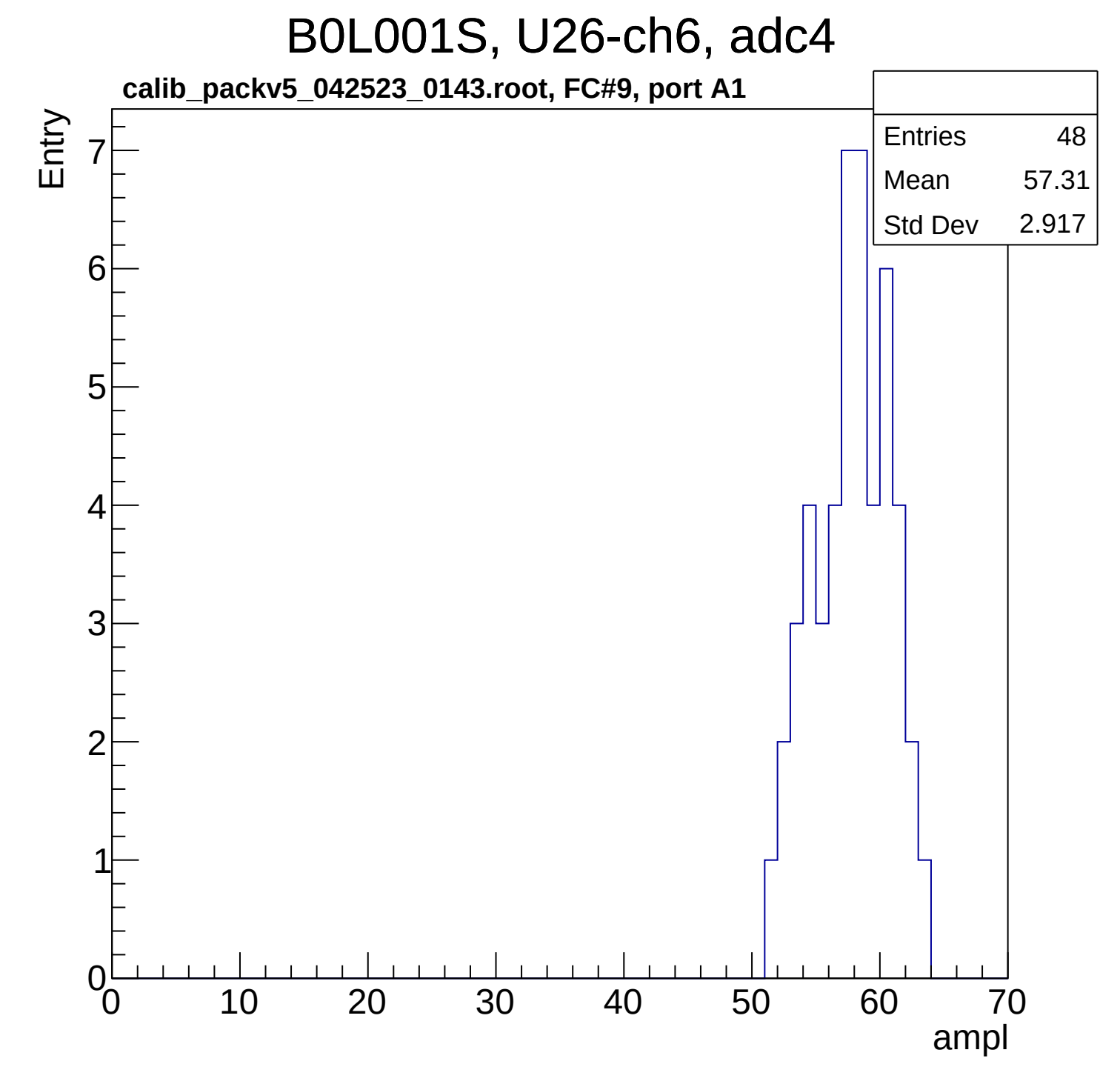

B0L001S, U26-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

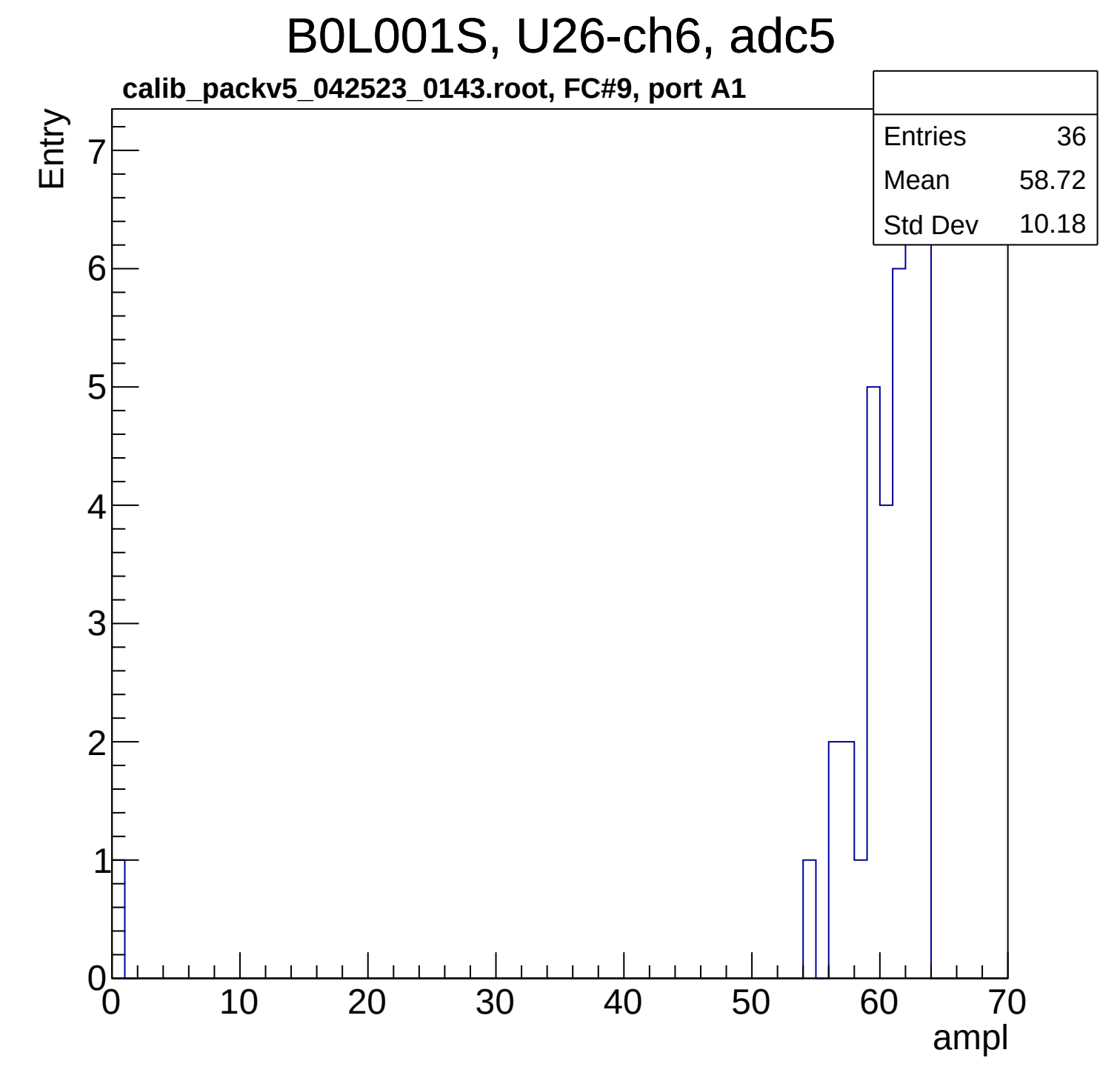

B0L001S, U26-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 31.39
Std Dev 6.001
Gaus mean : 32.6956
Gaus Width: 3.9774
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

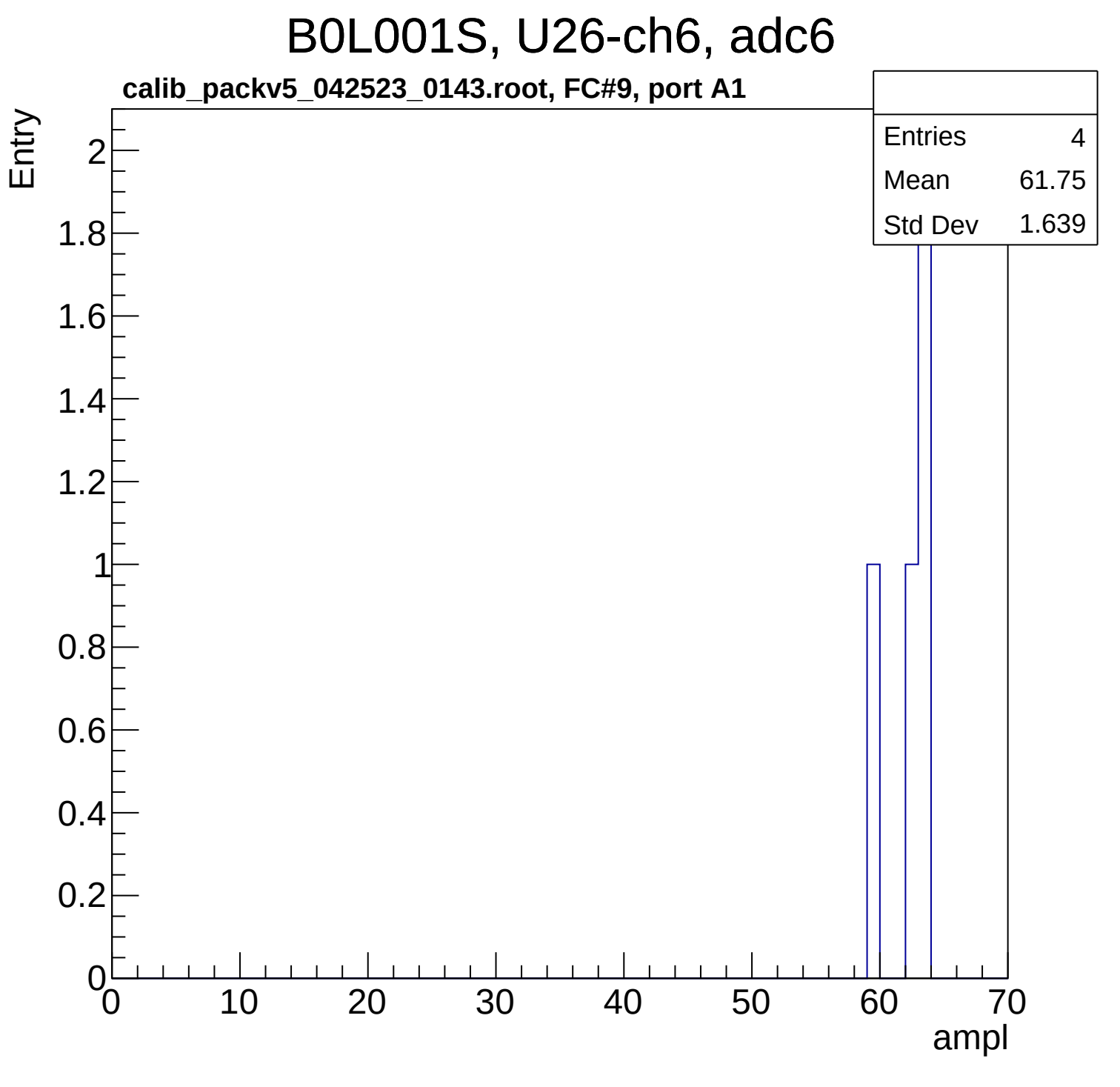

B0L001S, U26-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 39.53
Std Dev 3.273
Gaus mean : 39.8223
Gaus Width: 3.9168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

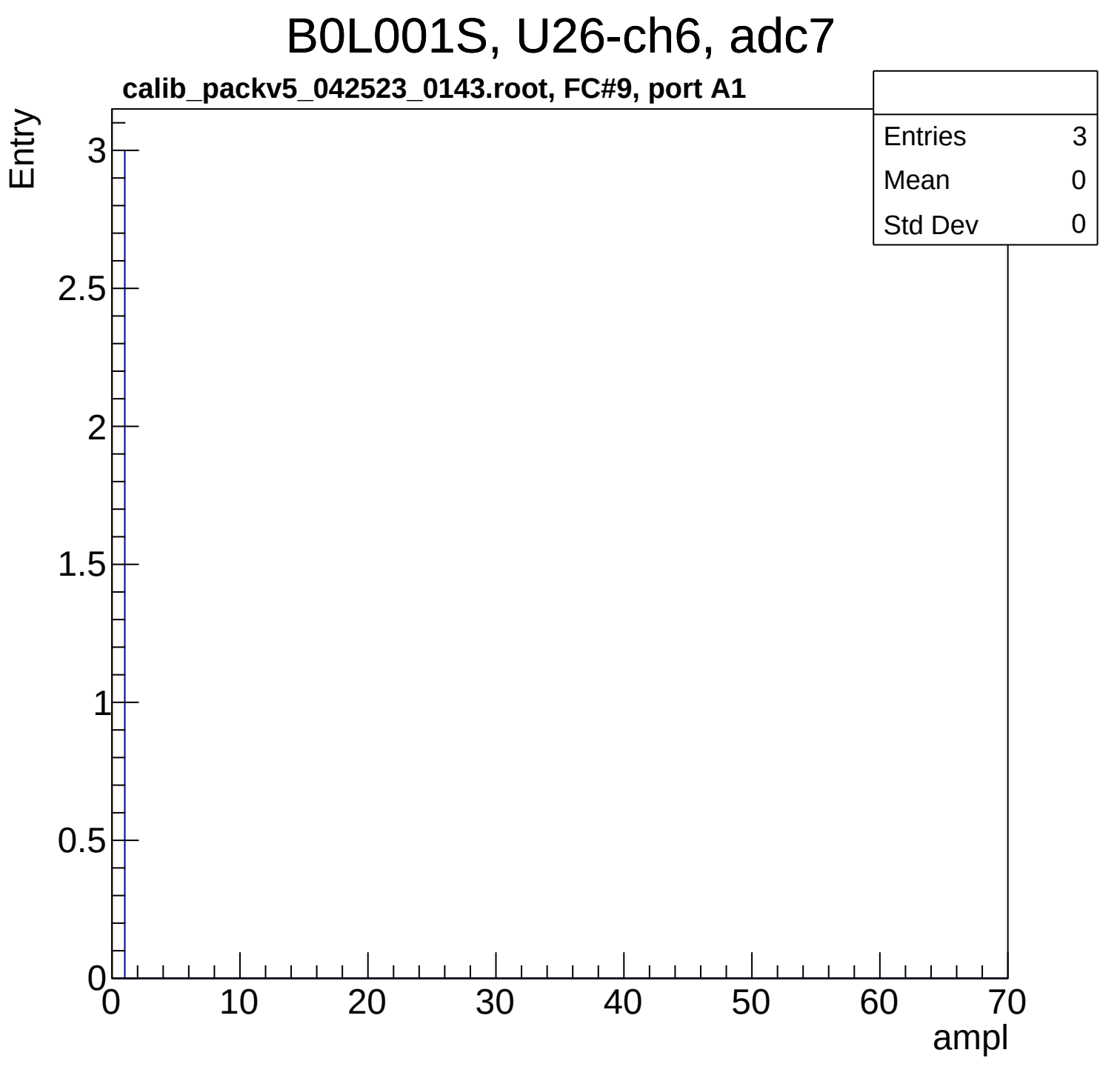

B0L001S, U26-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.91
Std Dev 3.388
Gaus mean : 45.7692
Gaus Width: 3.9412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.2
Std Dev 3.321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.31
Std Dev 2.917
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.72
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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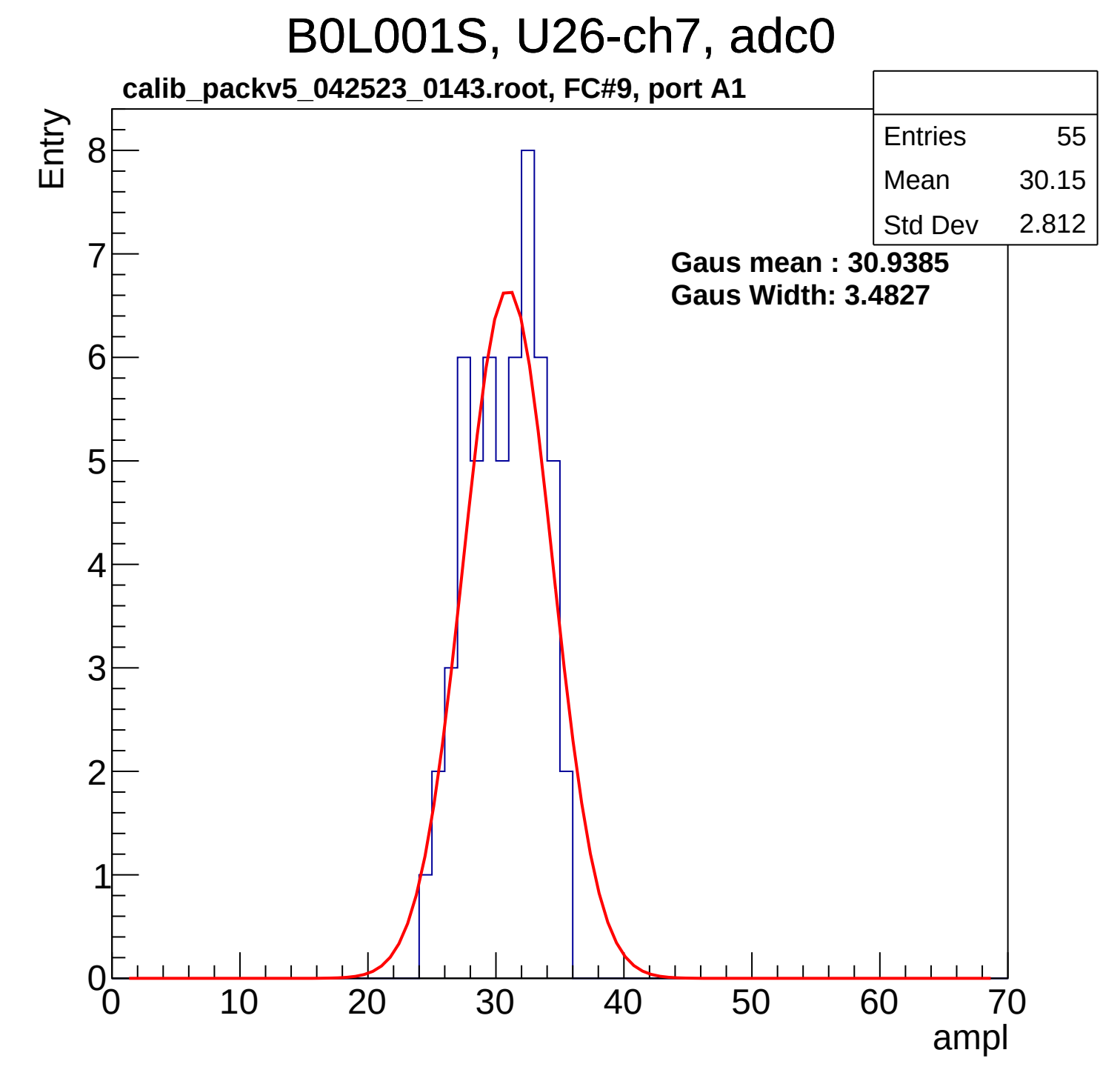
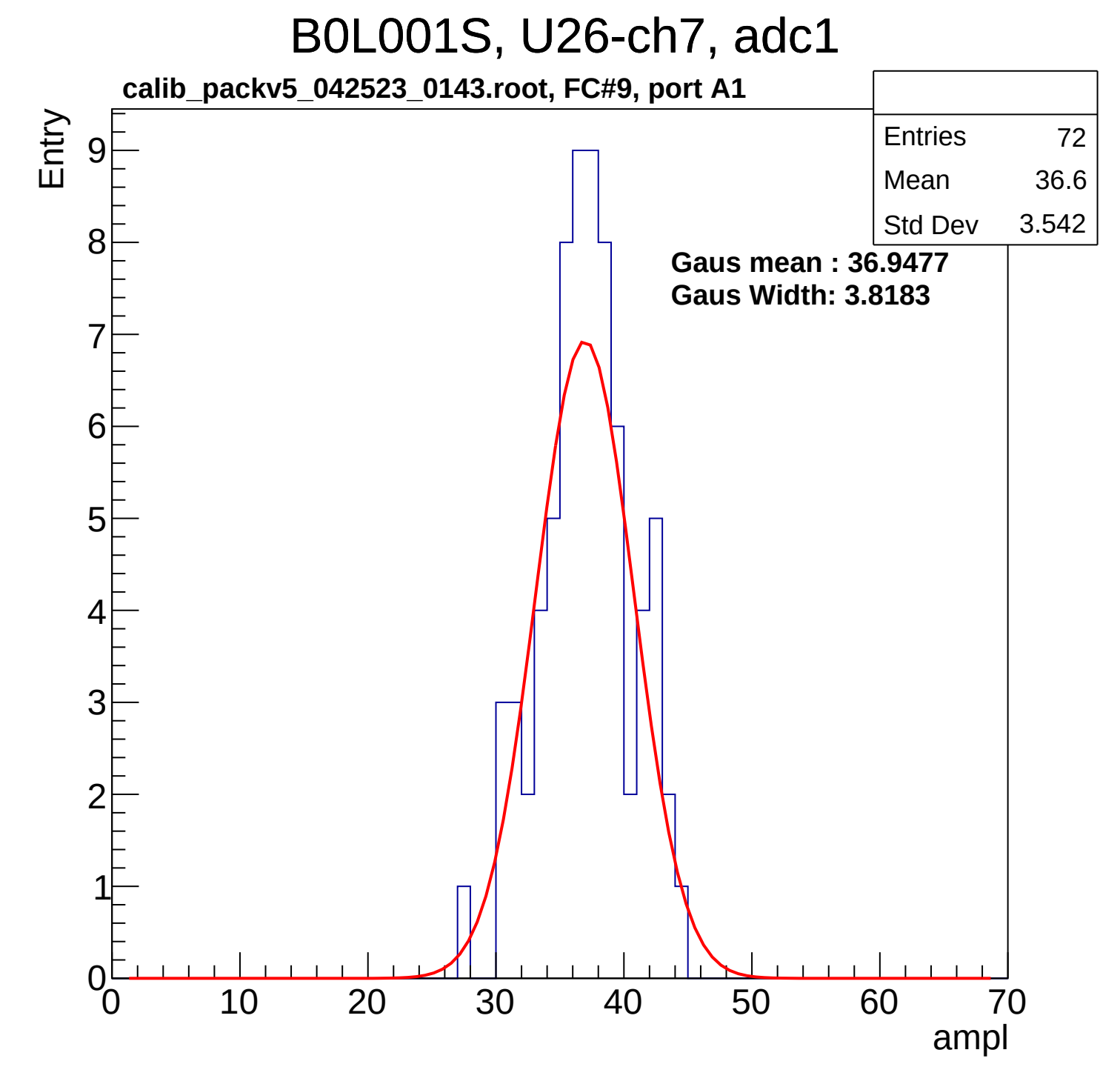
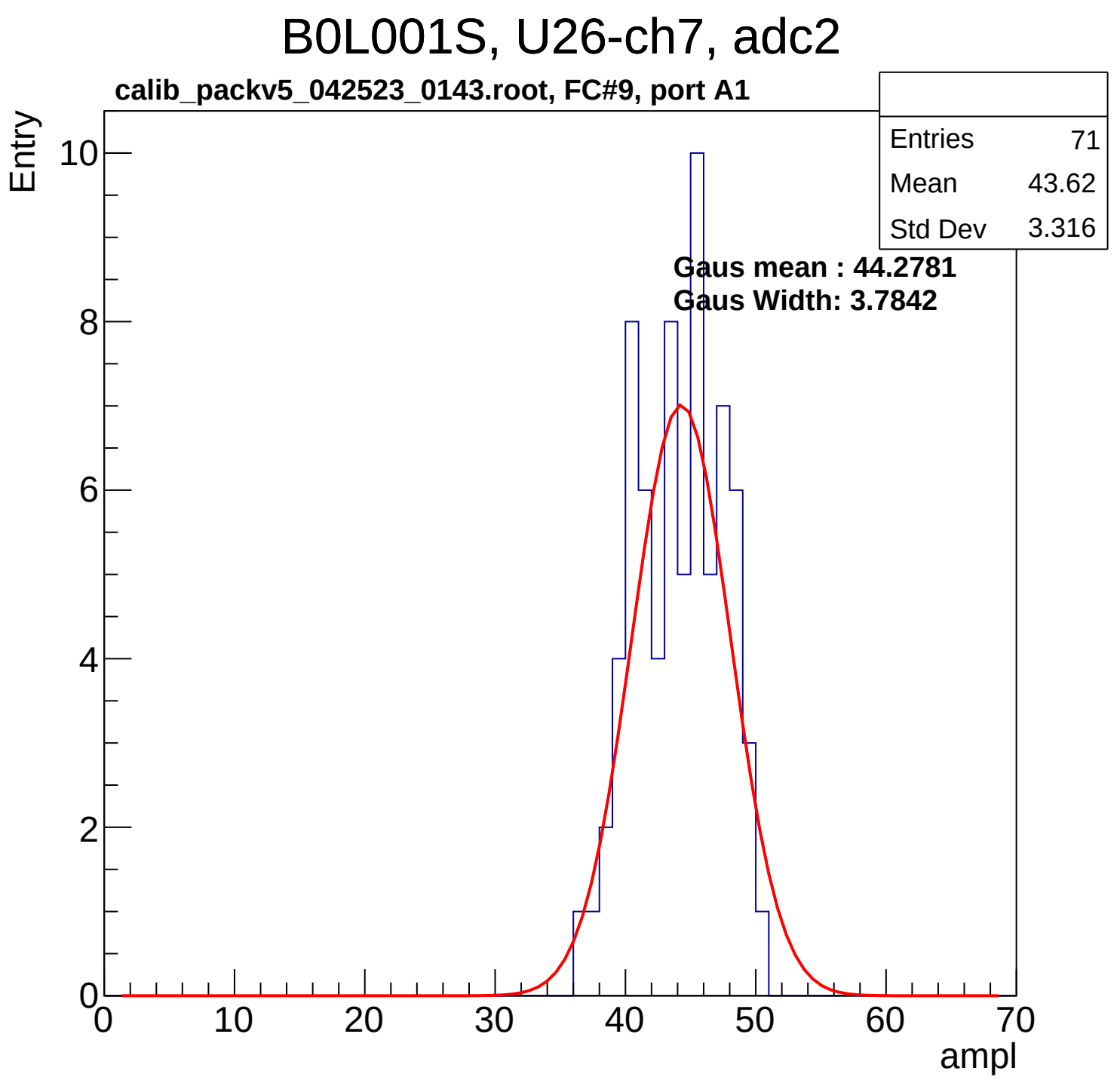
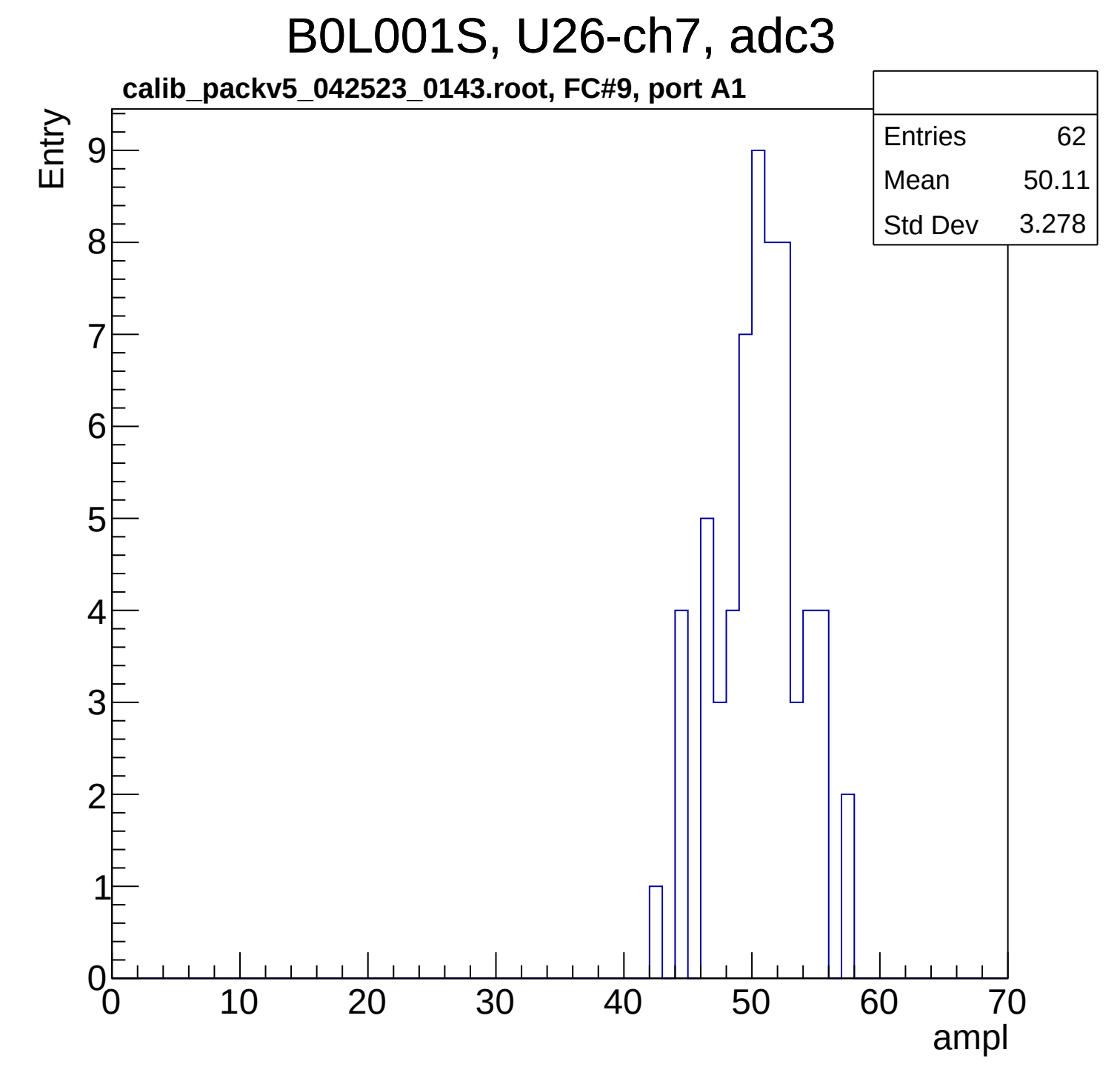
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.15
Std Dev 2.812
Gaus mean : 30.9385
Gaus Width: 3.4827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.6
Std Dev 3.542
Gaus mean : 36.9477
Gaus Width: 3.8183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.62
Std Dev 3.316
Gaus mean : 44.2781
Gaus Width: 3.7842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.11
Std Dev 3.278
Entry
ampl
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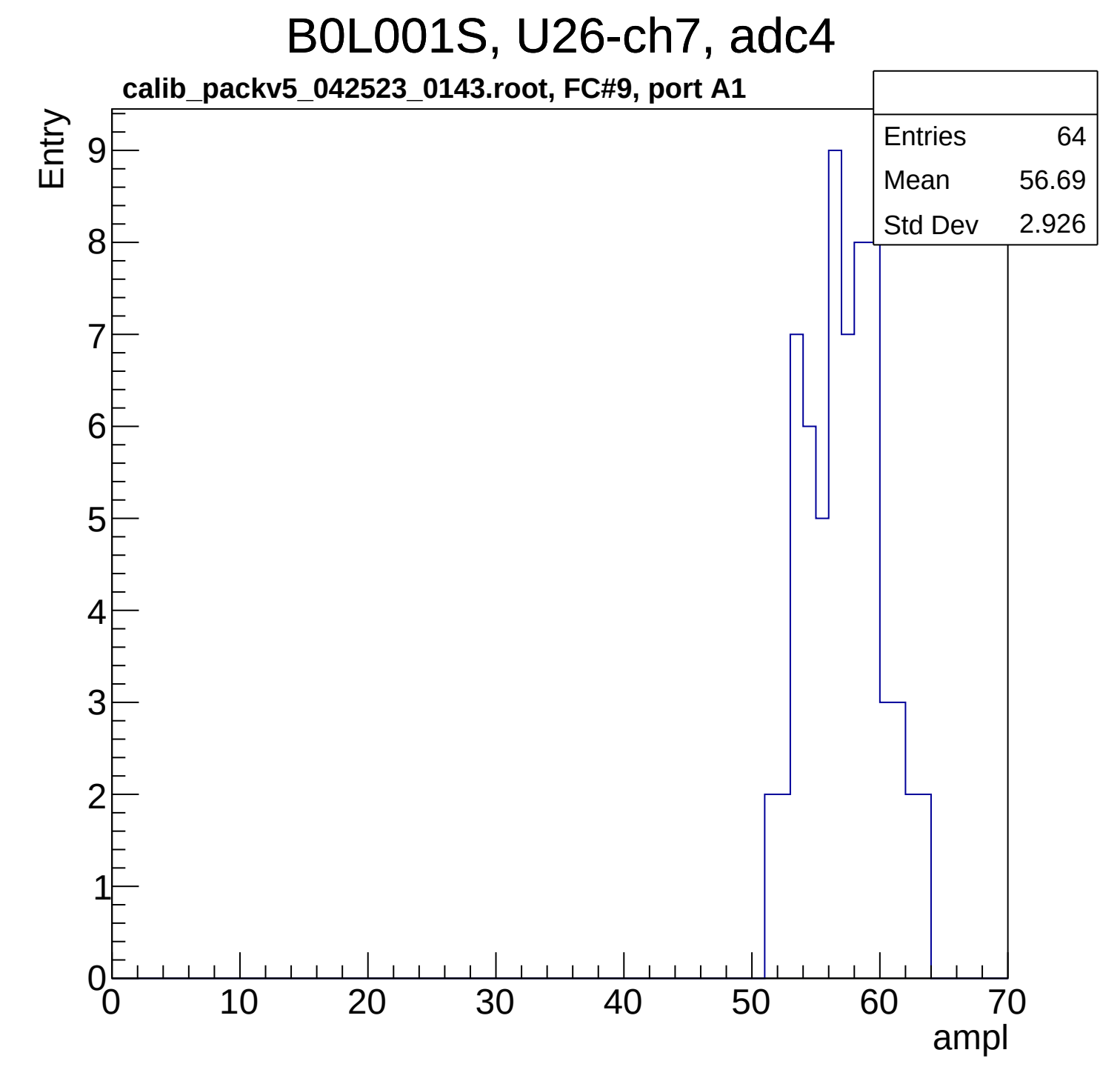
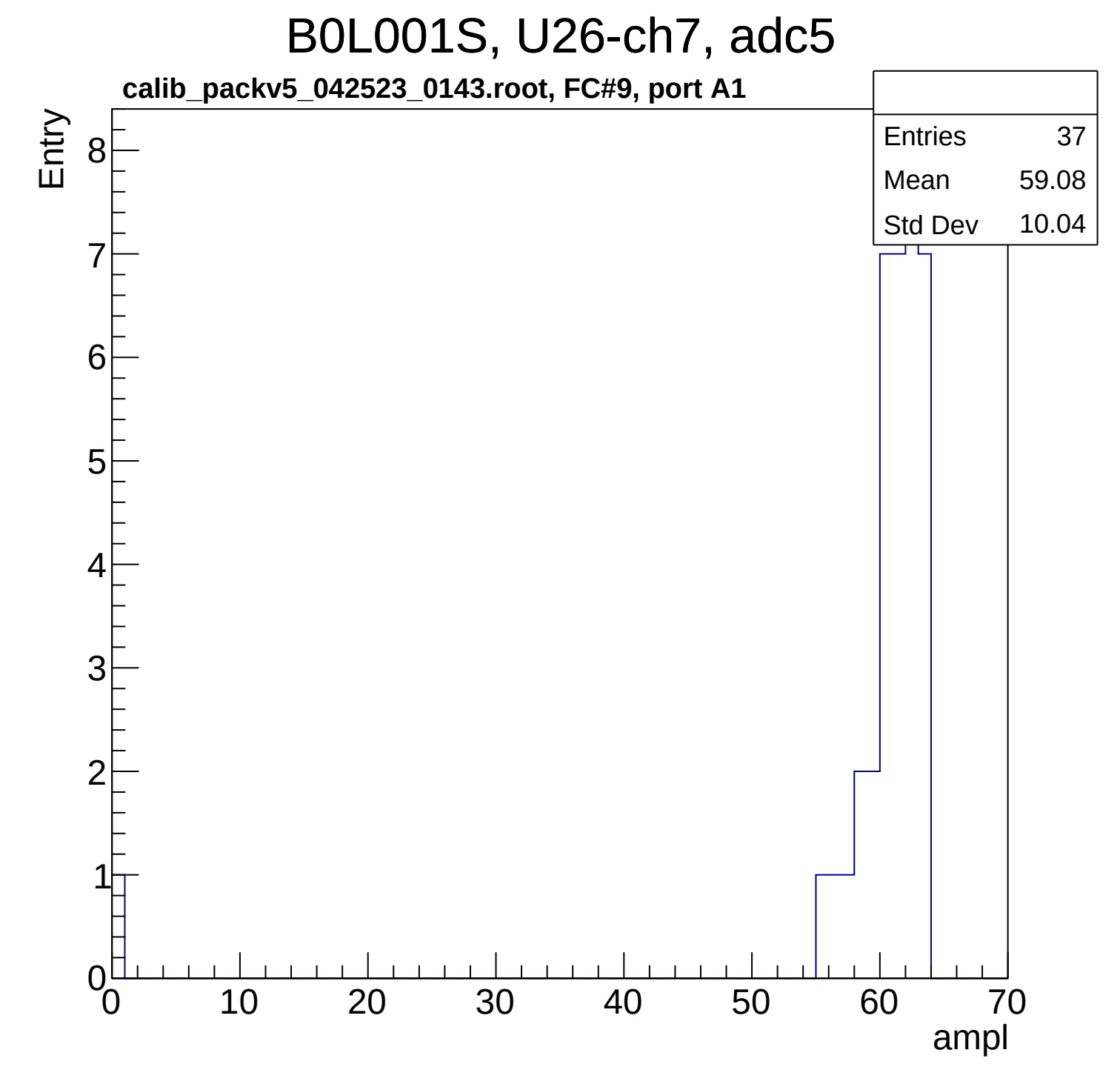
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.69
Std Dev 2.926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.08
Std Dev 10.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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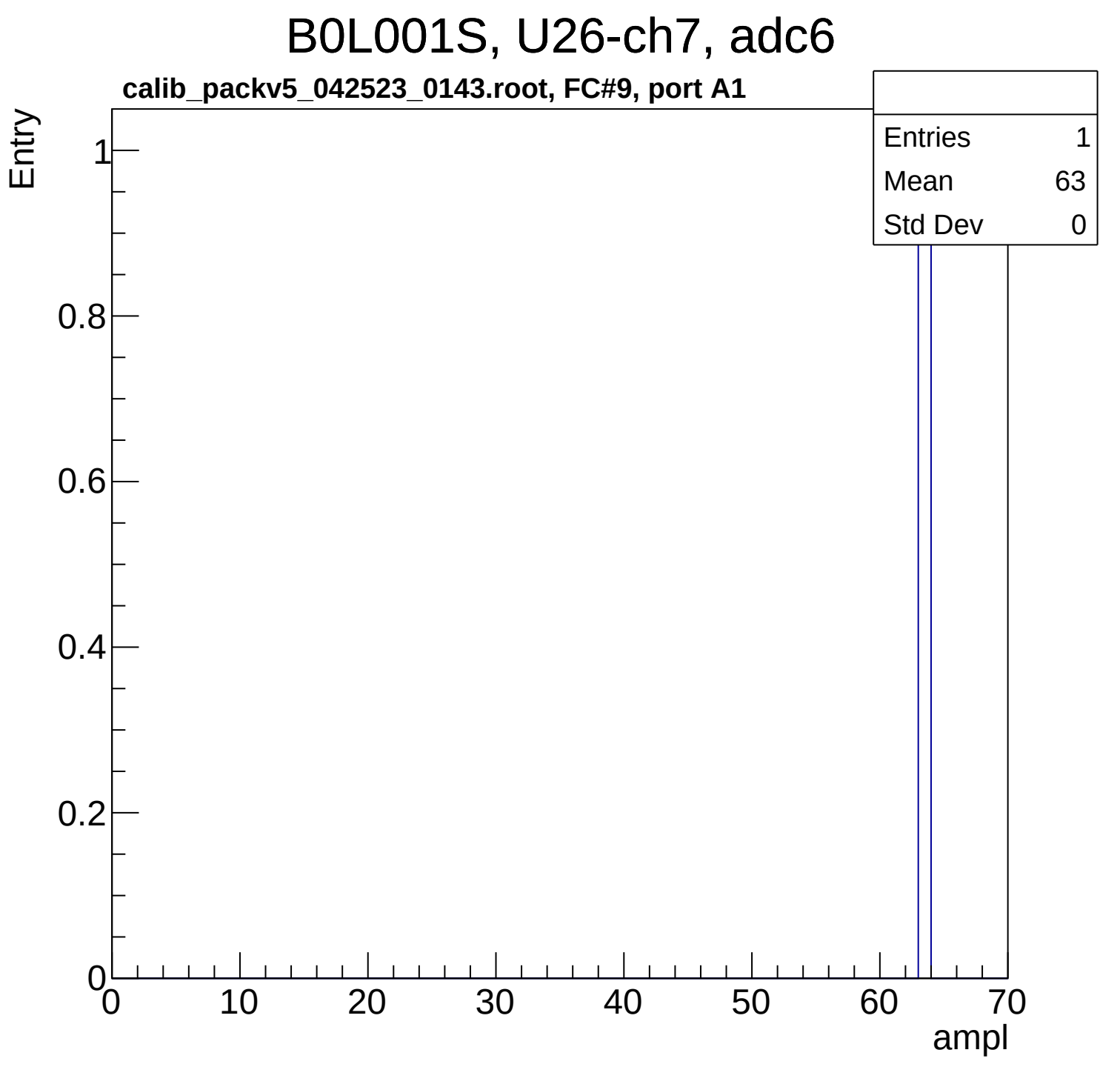
B0L001S, U26-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
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0.6
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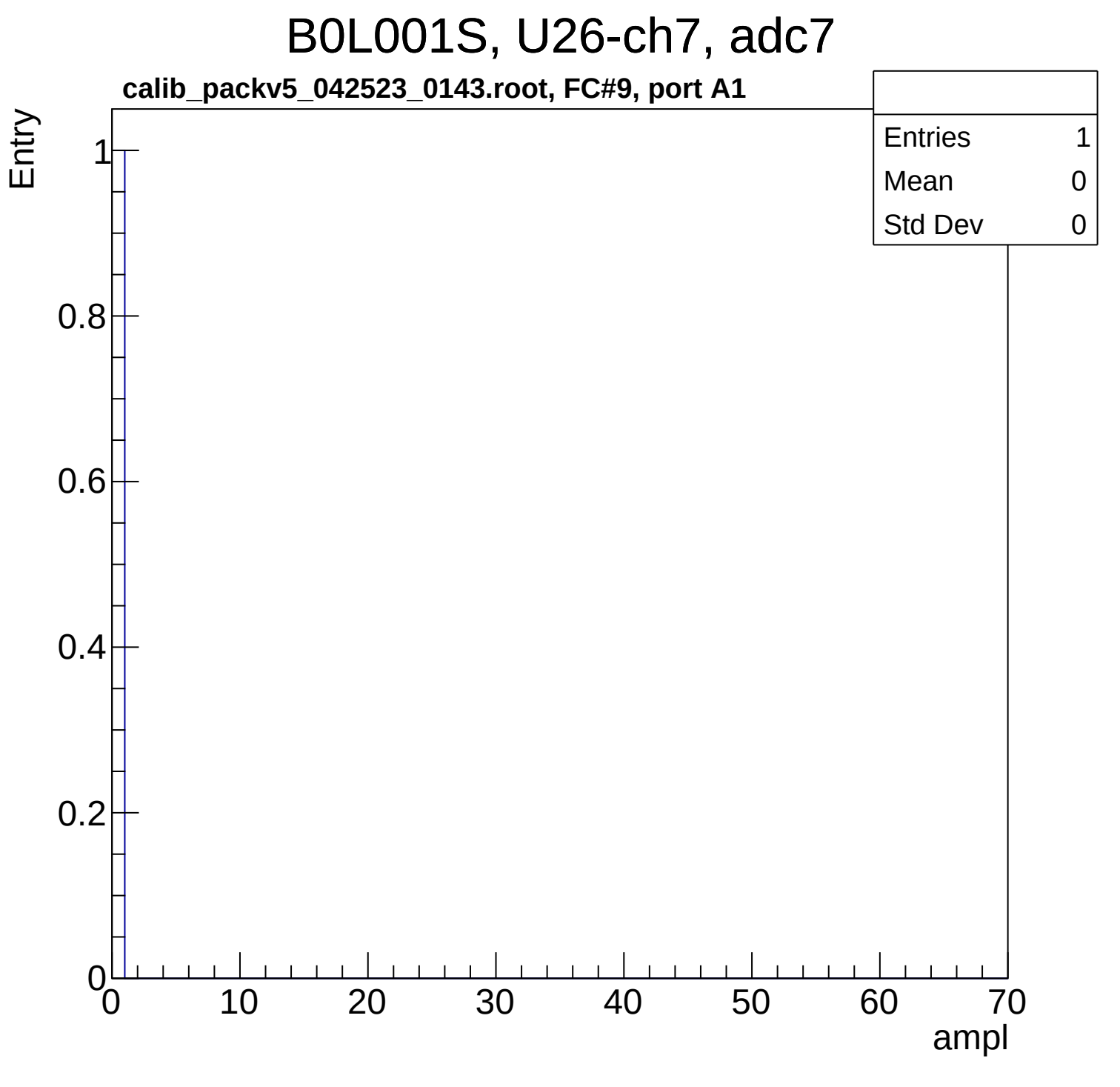
ampl


B0L001S, U26-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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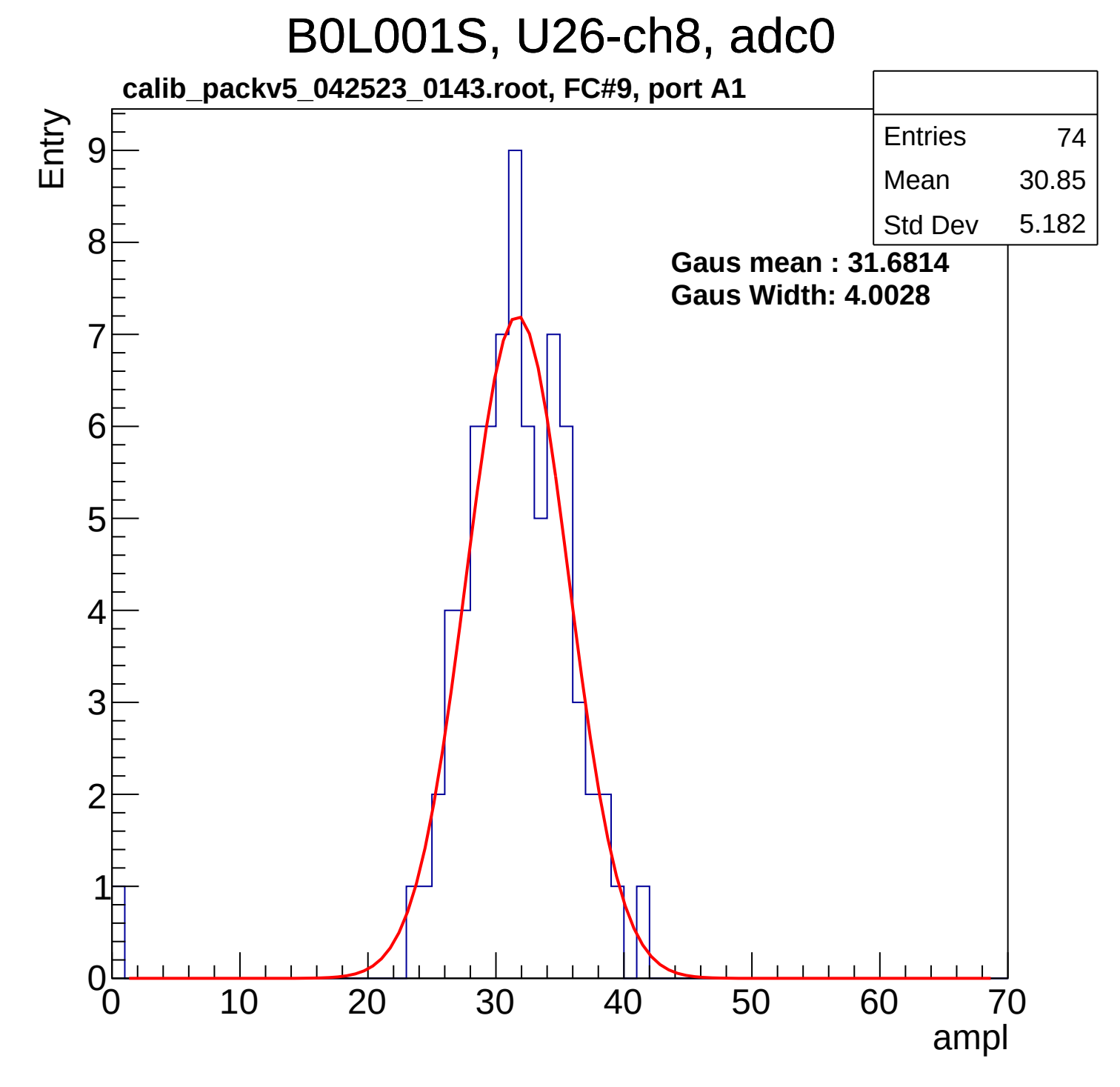
ampl


B0L001S, U26-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.85
Std Dev 5.182
Gaus mean : 31.6814
Gaus Width: 4.0028
Entry
ampl
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2
3
4
5
6
7
8
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0
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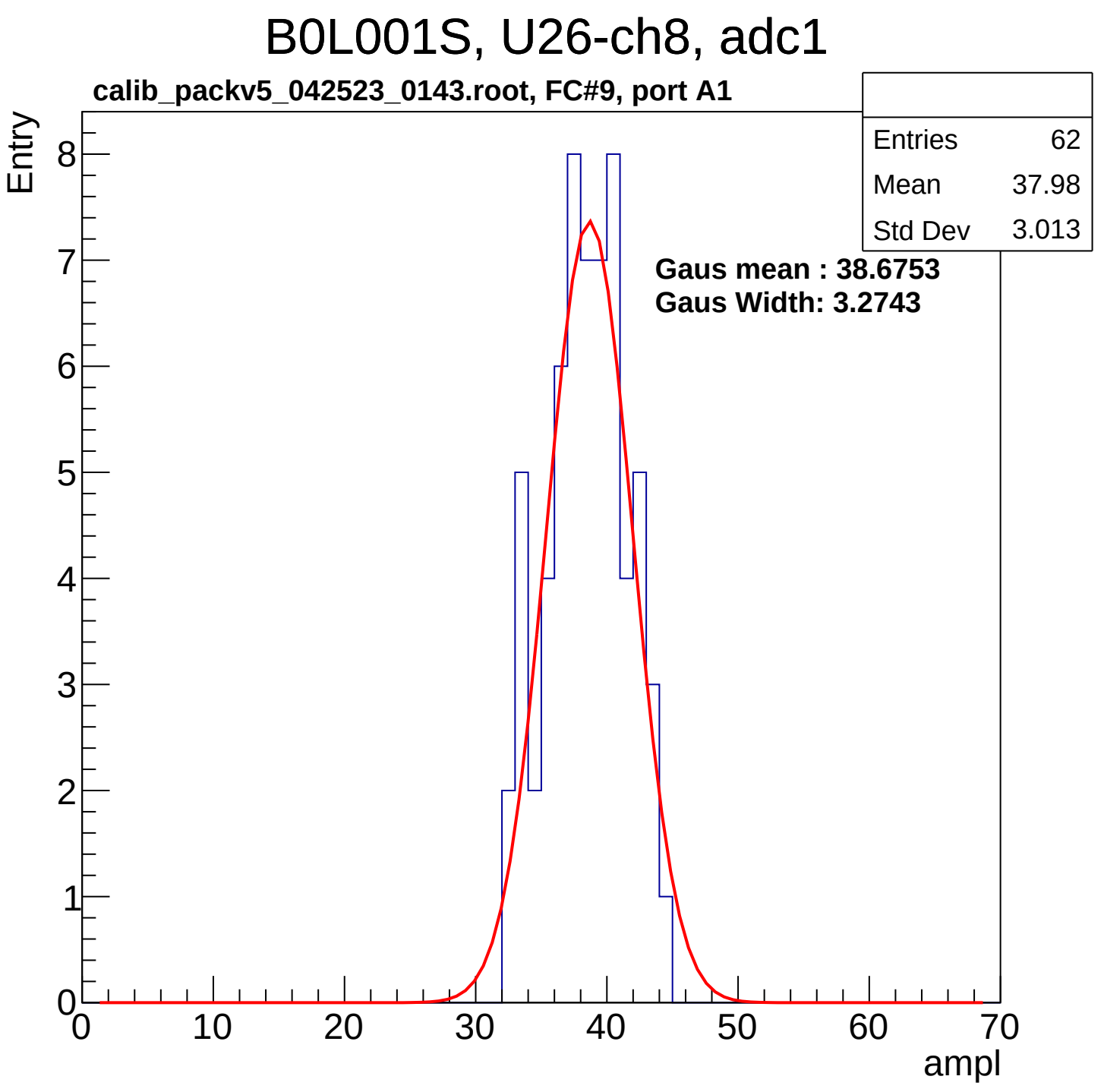
B0L001S, U26-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.98
Std Dev 3.013
Gaus mean : 38.6753
Gaus Width: 3.2743
Entry
ampl
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2
3
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0
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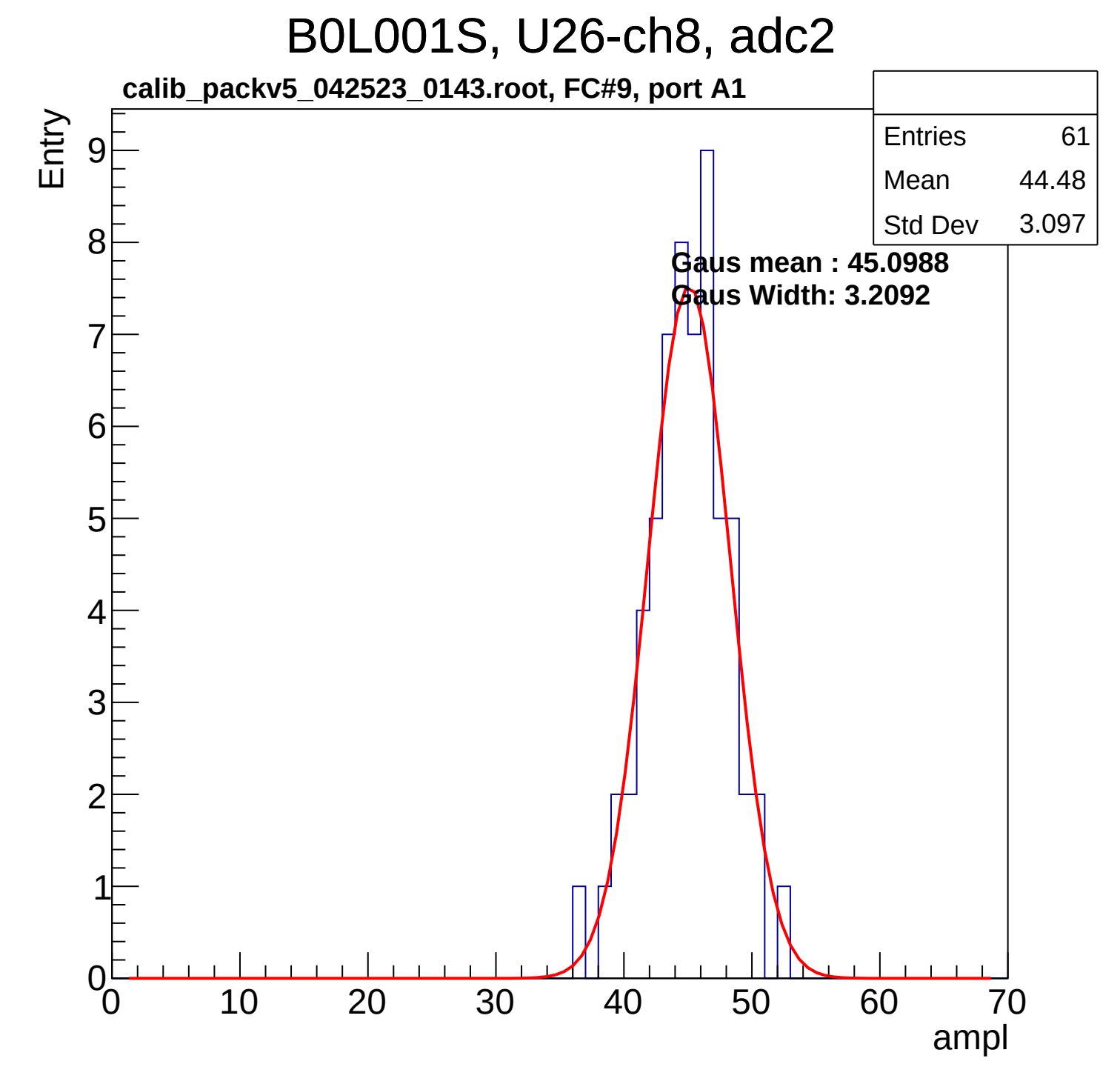
B0L001S, U26-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.48
Std Dev 3.097
Gaus mean : 45.0988
Gaus Width: 3.2092
Entry
ampl
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3
4
5
6
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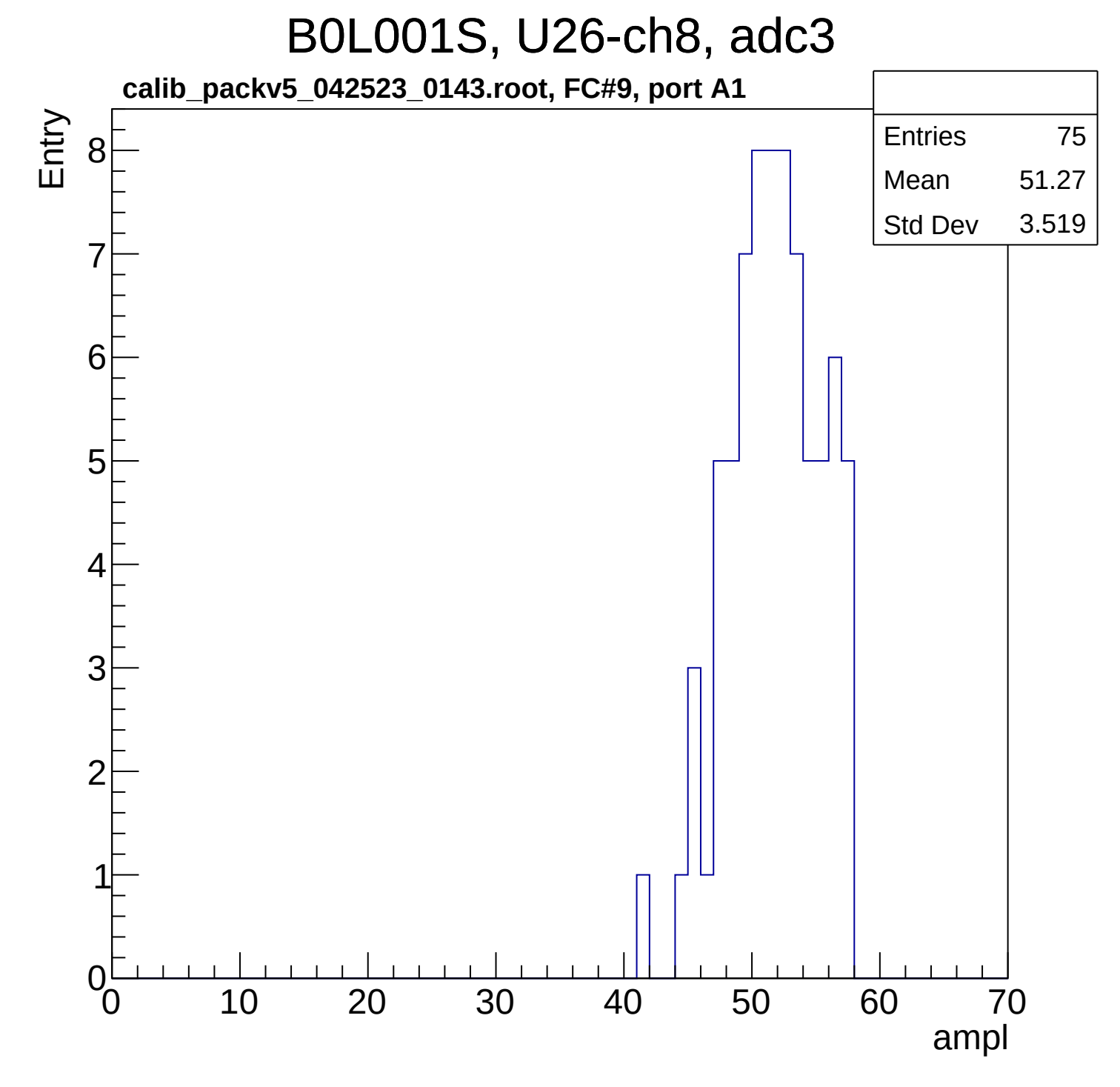
B0L001S, U26-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.27
Std Dev 3.519
Entry
ampl
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0
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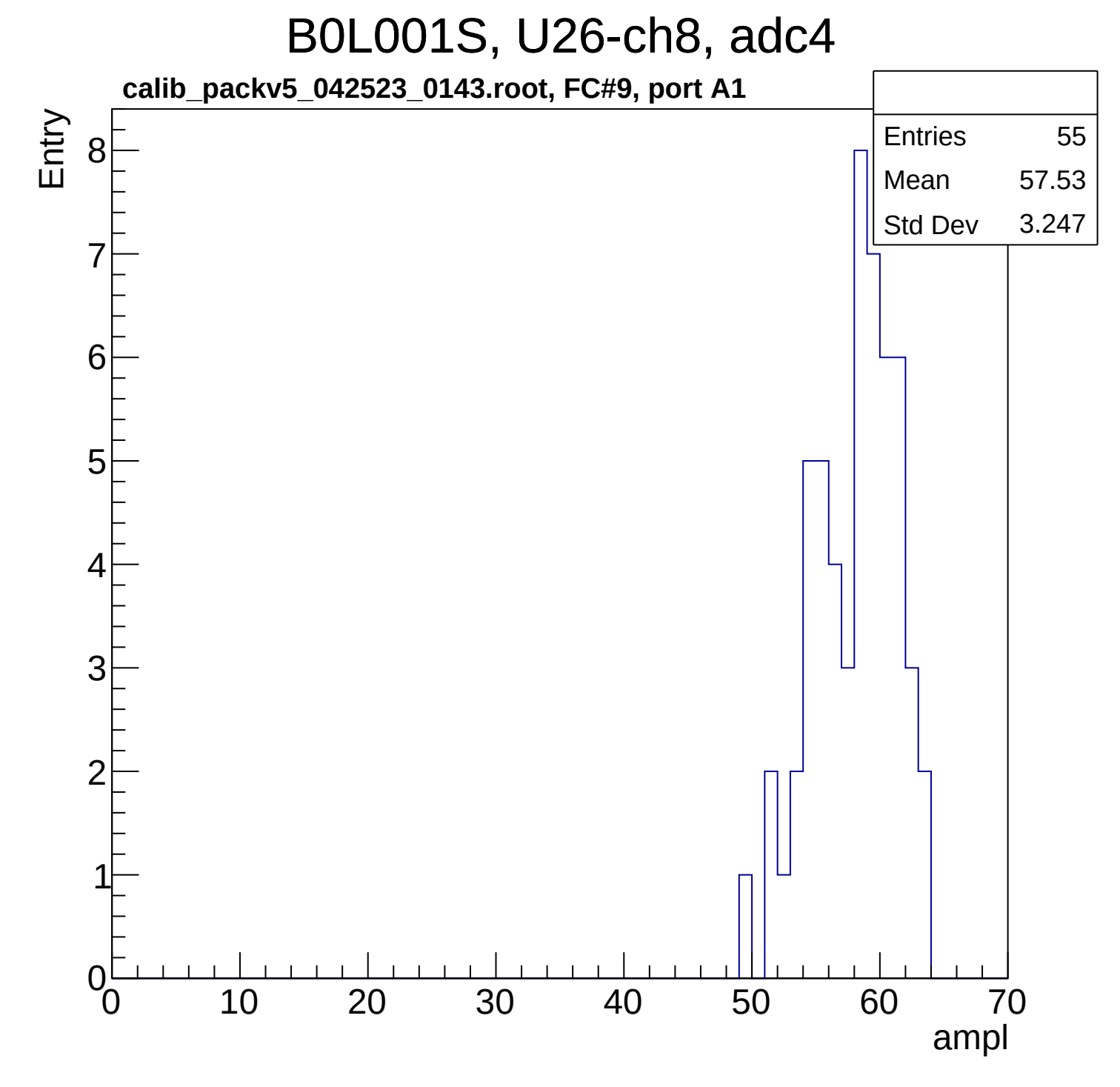
B0L001S, U26-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.53
Std Dev 3.247
Entry
ampl
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2
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4
5
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7
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0
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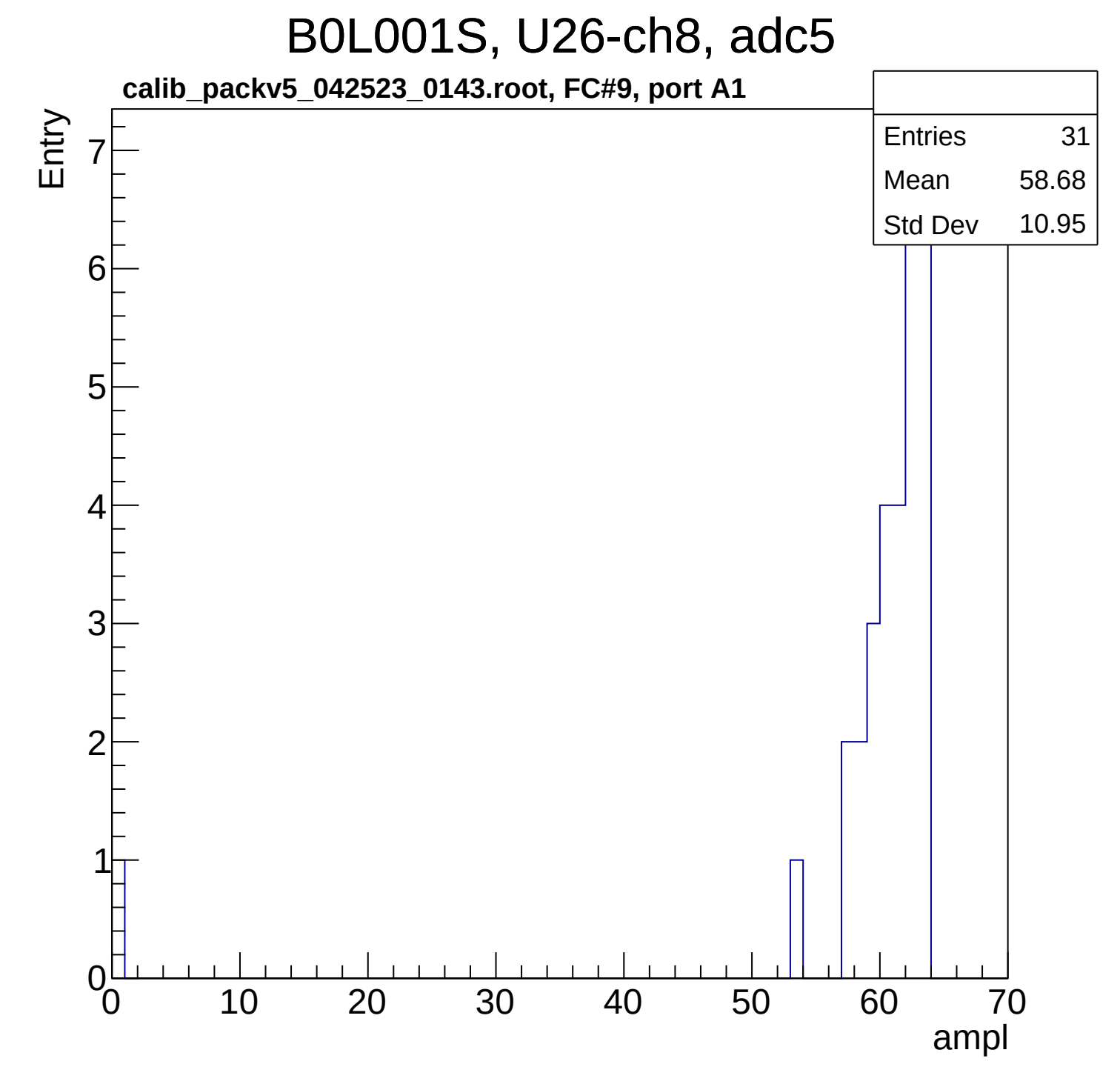
B0L001S, U26-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.68
Std Dev 10.95
Entry
ampl
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4
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0
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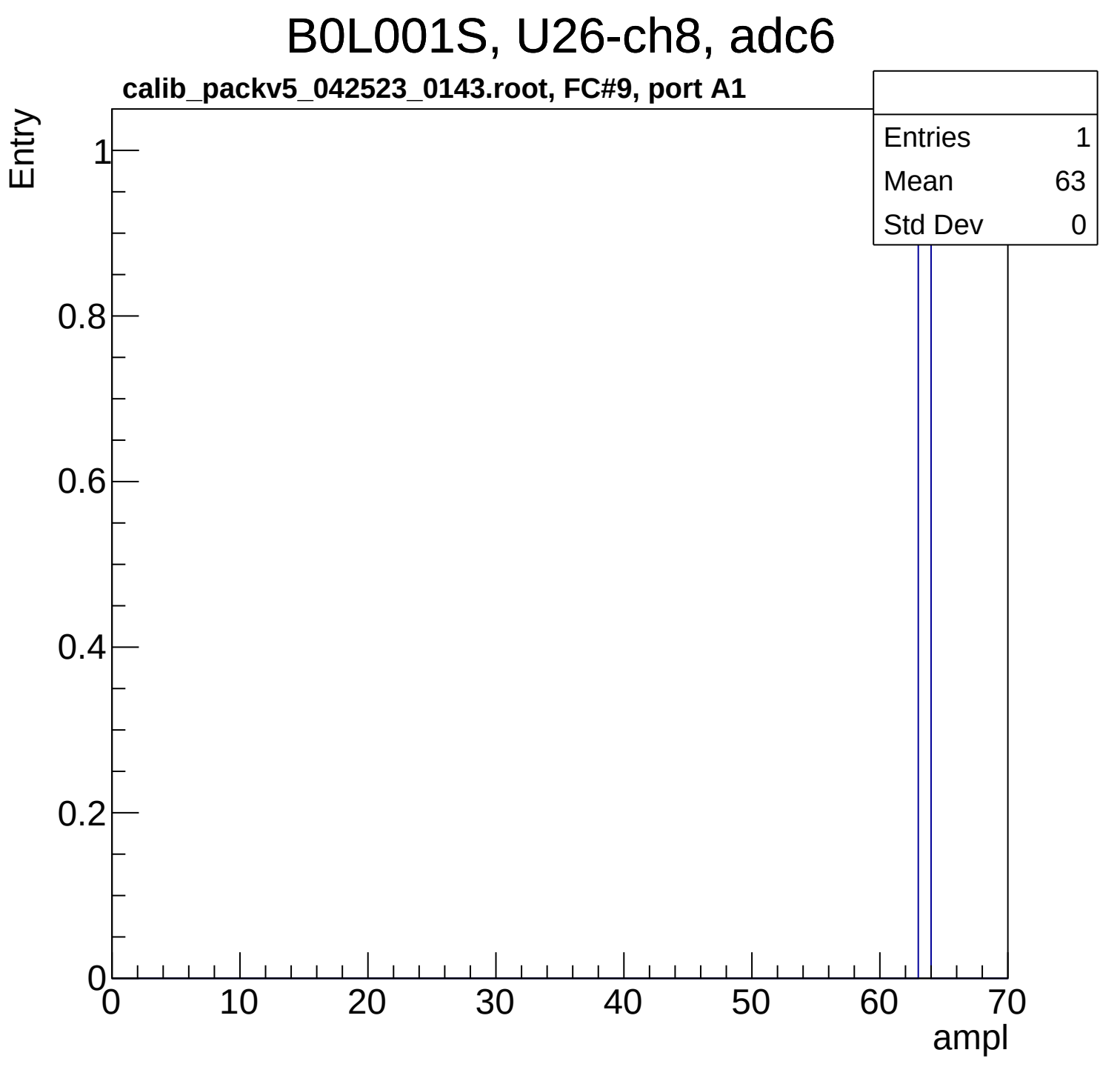
B0L001S, U26-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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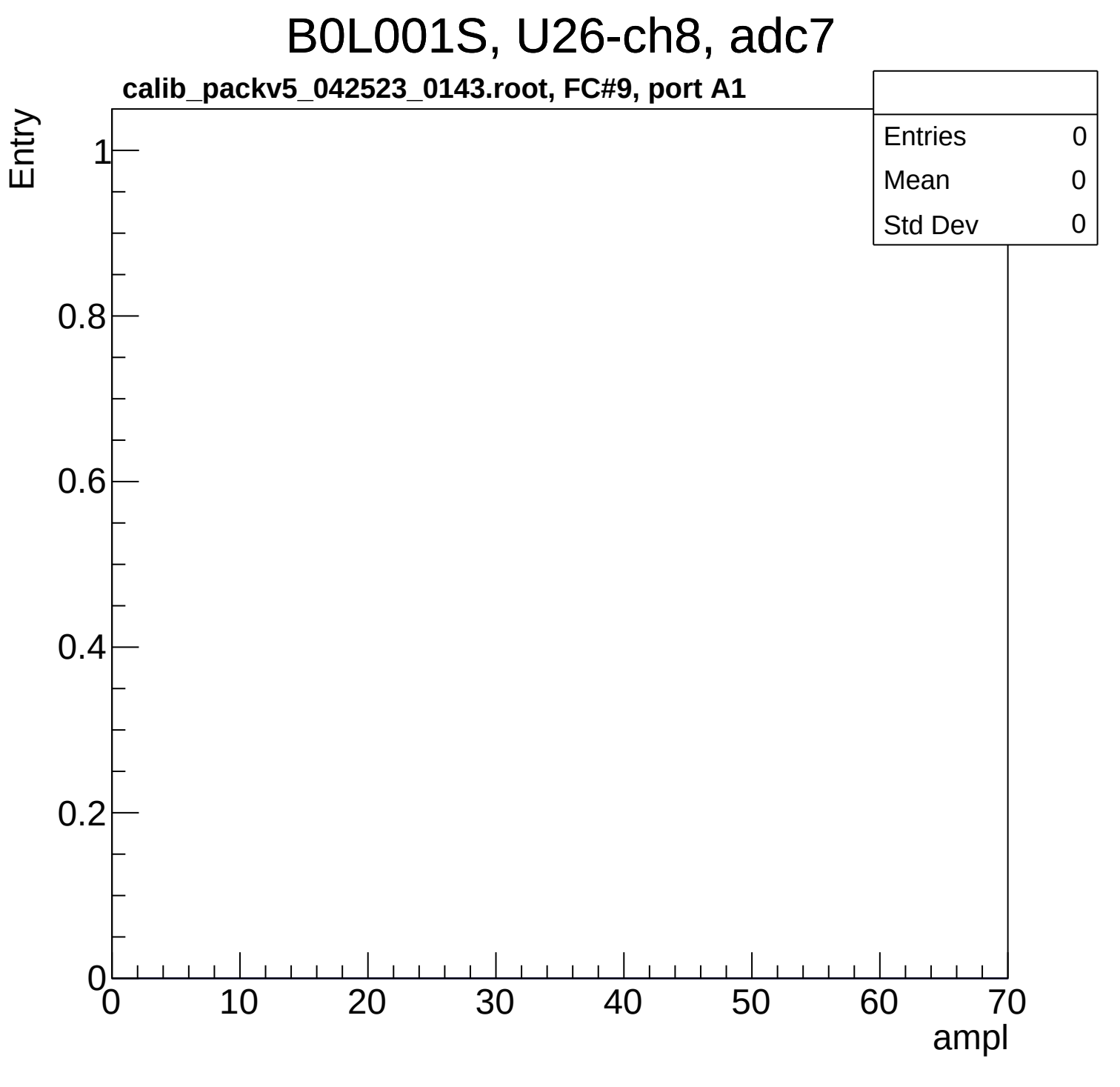
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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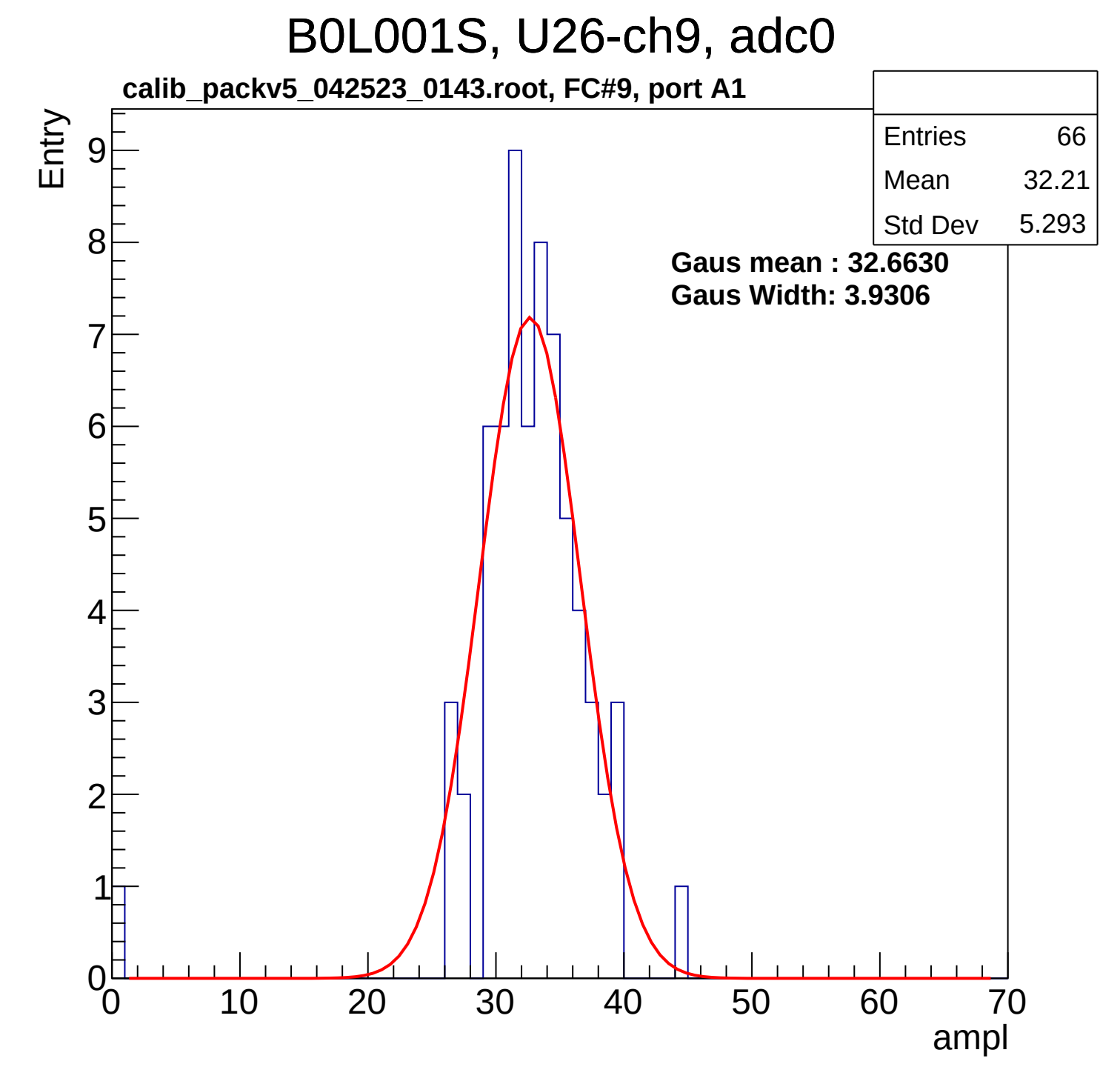
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 32.21
Std Dev 5.293
Gaus mean : 32.6630
Gaus Width: 3.9306
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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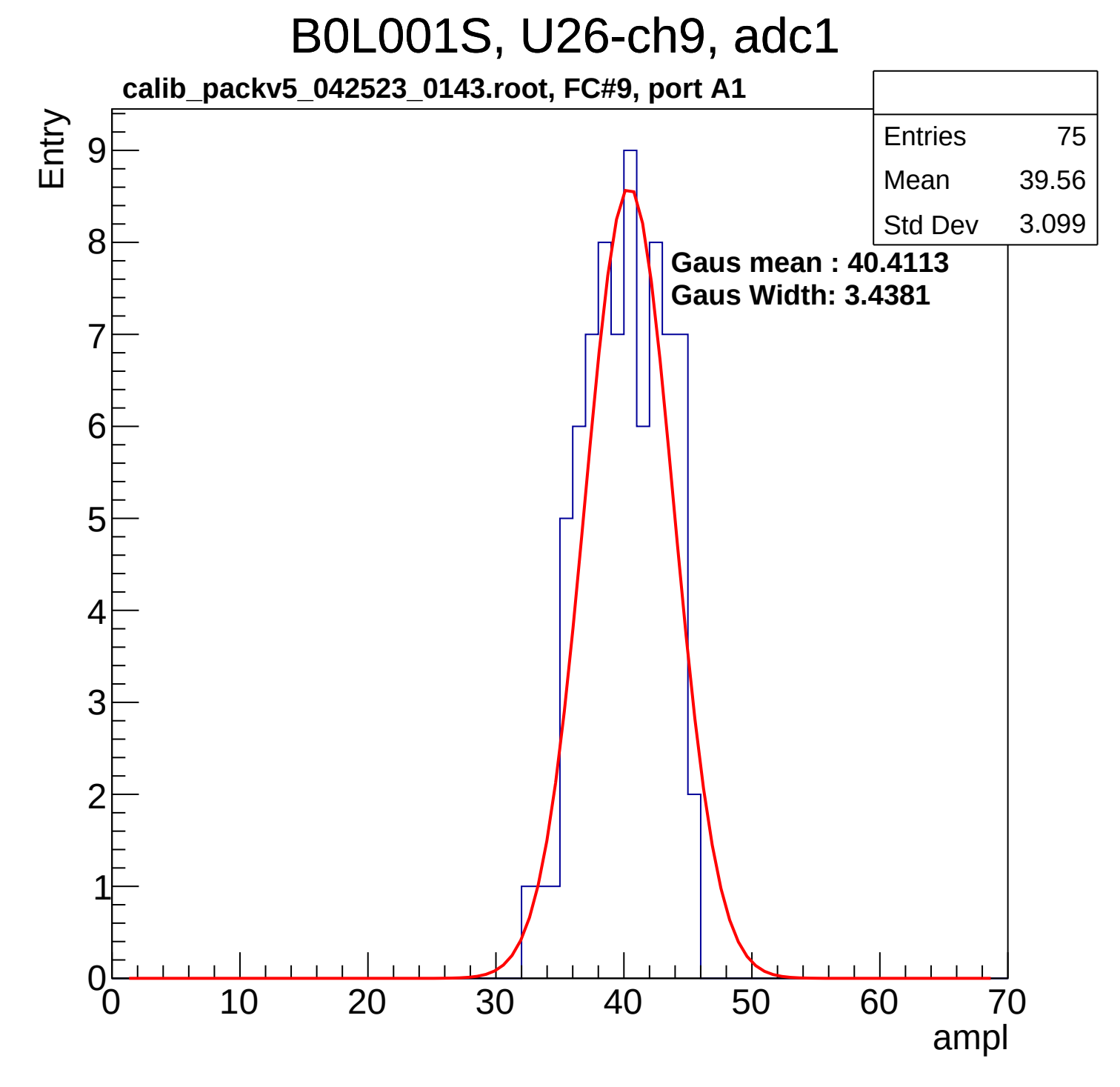
B0L001S, U26-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 39.56
Std Dev 3.099
Gaus mean : 40.4113
Gaus Width: 3.4381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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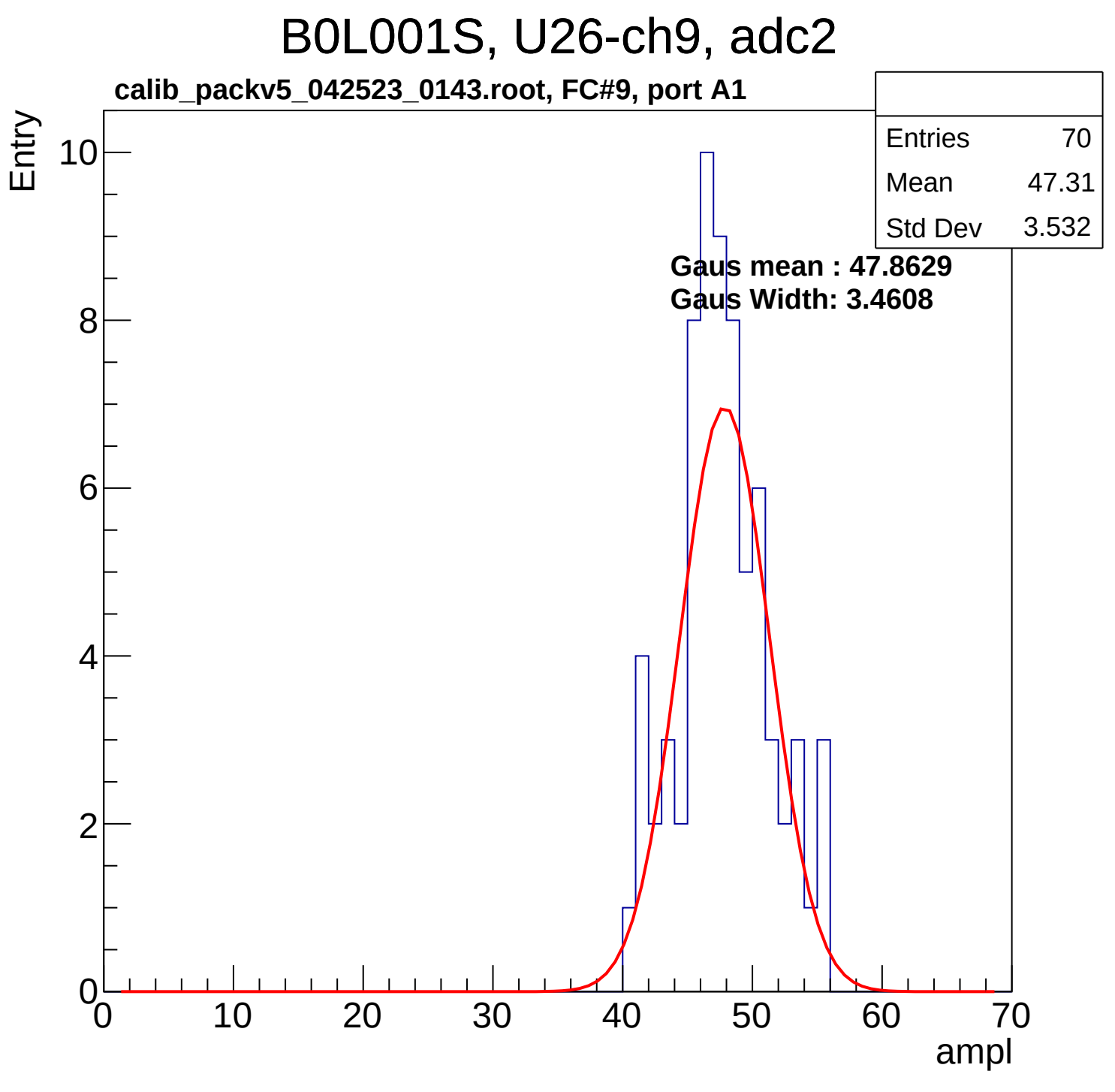
B0L001S, U26-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 47.31
Std Dev 3.532
Gaus mean : 47.8629
Gaus Width: 3.4608
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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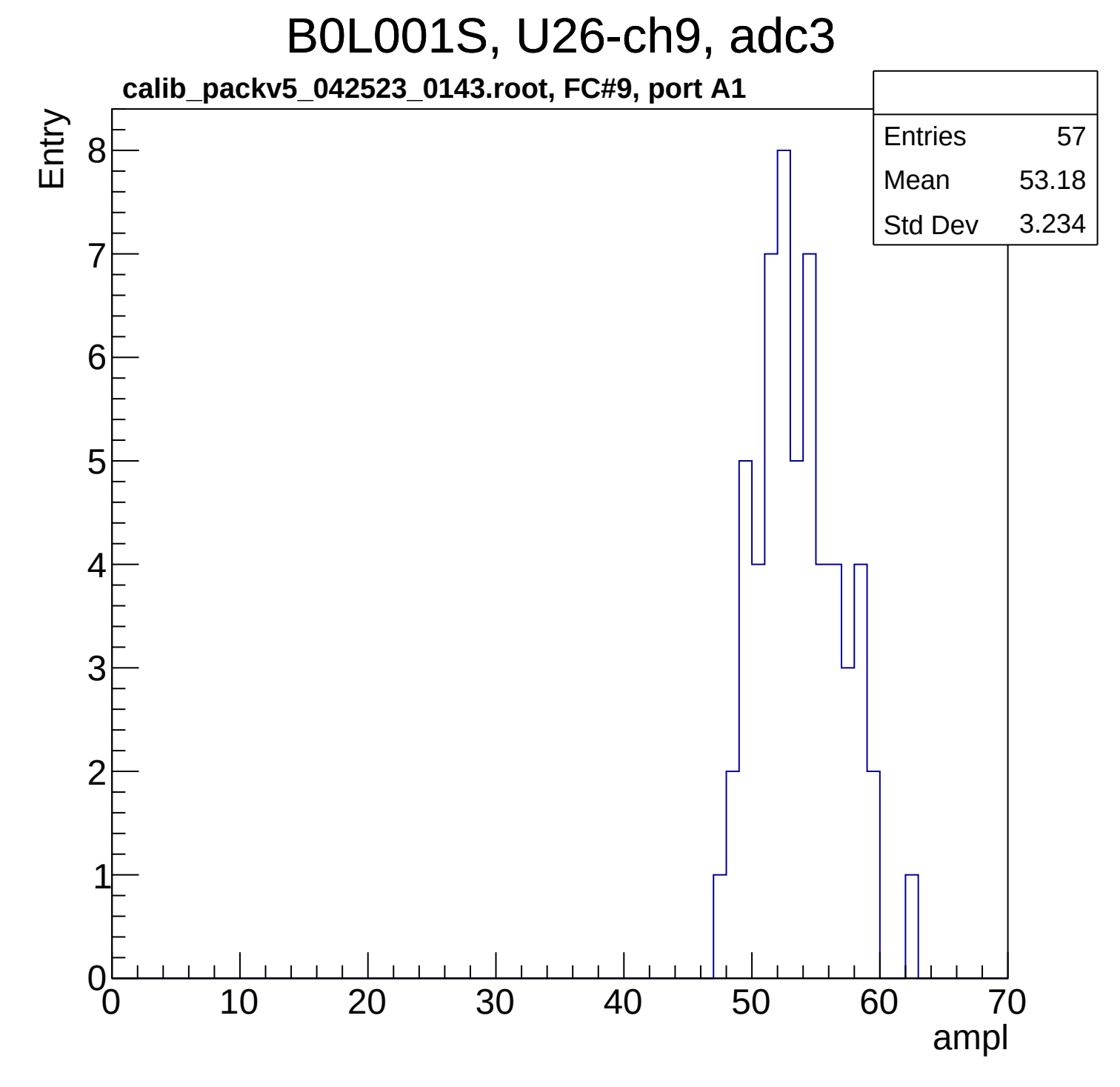
B0L001S, U26-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 53.18
Std Dev 3.234
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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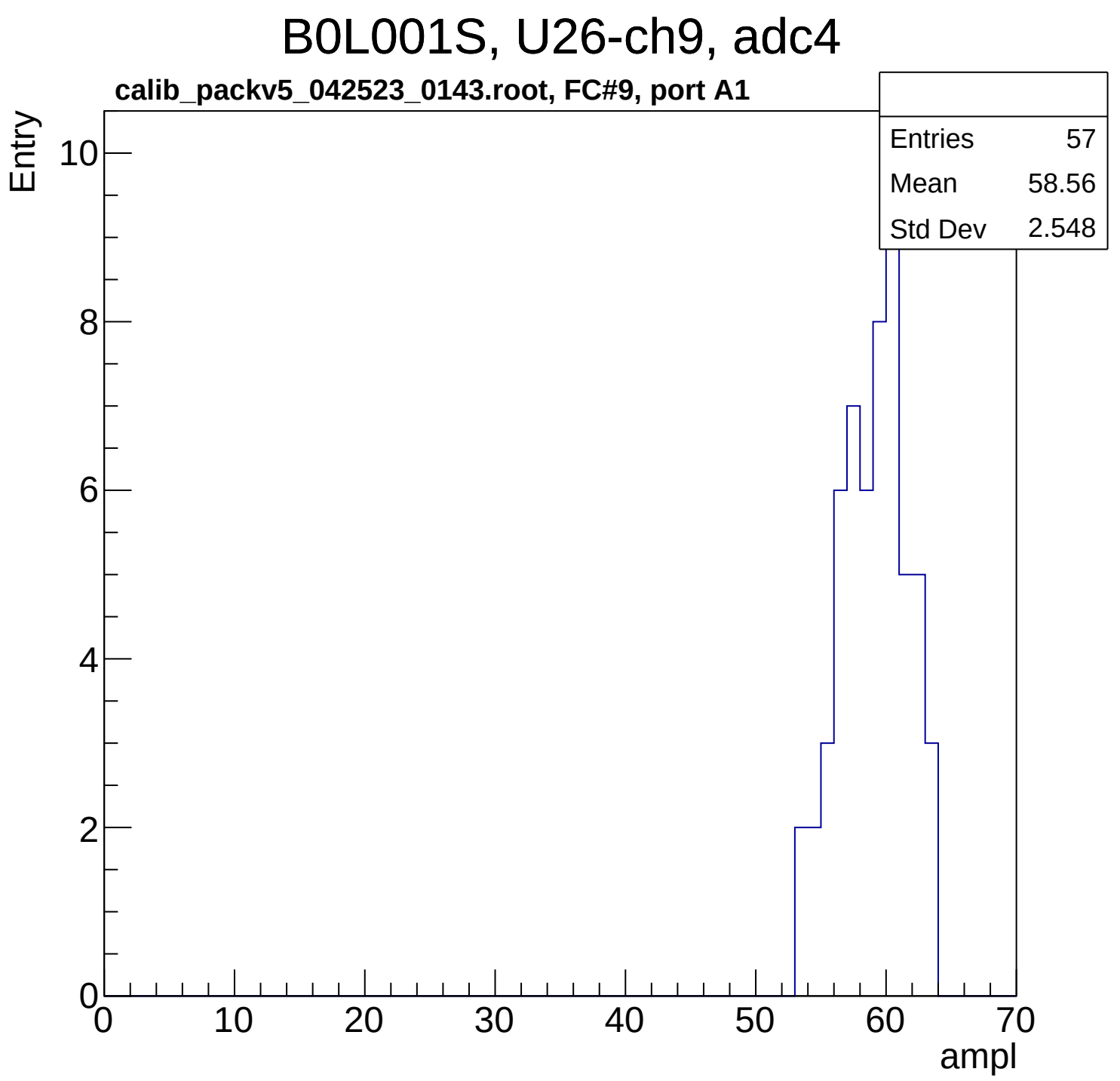
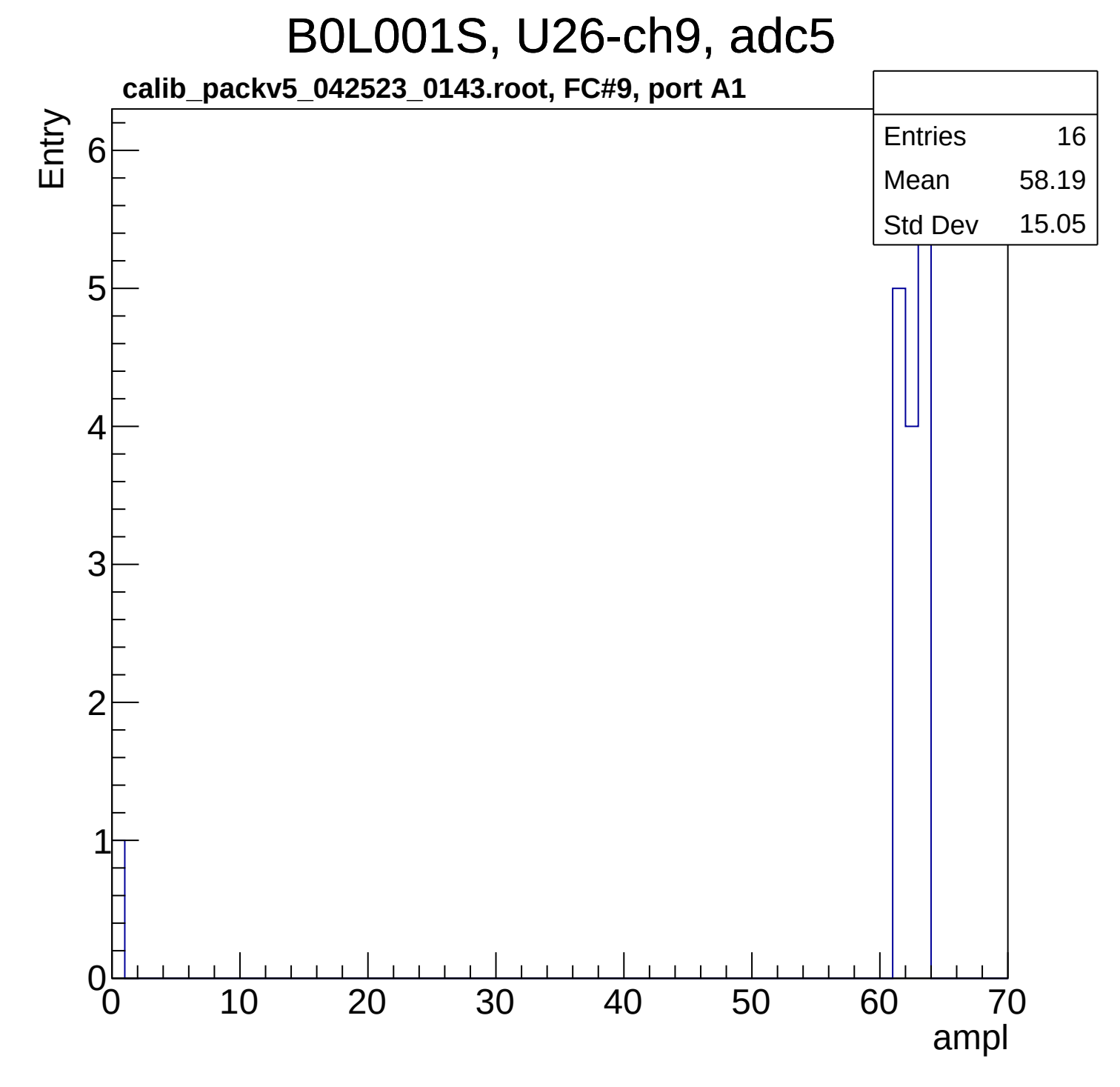

B0L001S, U26-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.56
Std Dev 2.548
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 58.19
Std Dev 15.05
Entry
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2
3
4
5
6
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10
20
30
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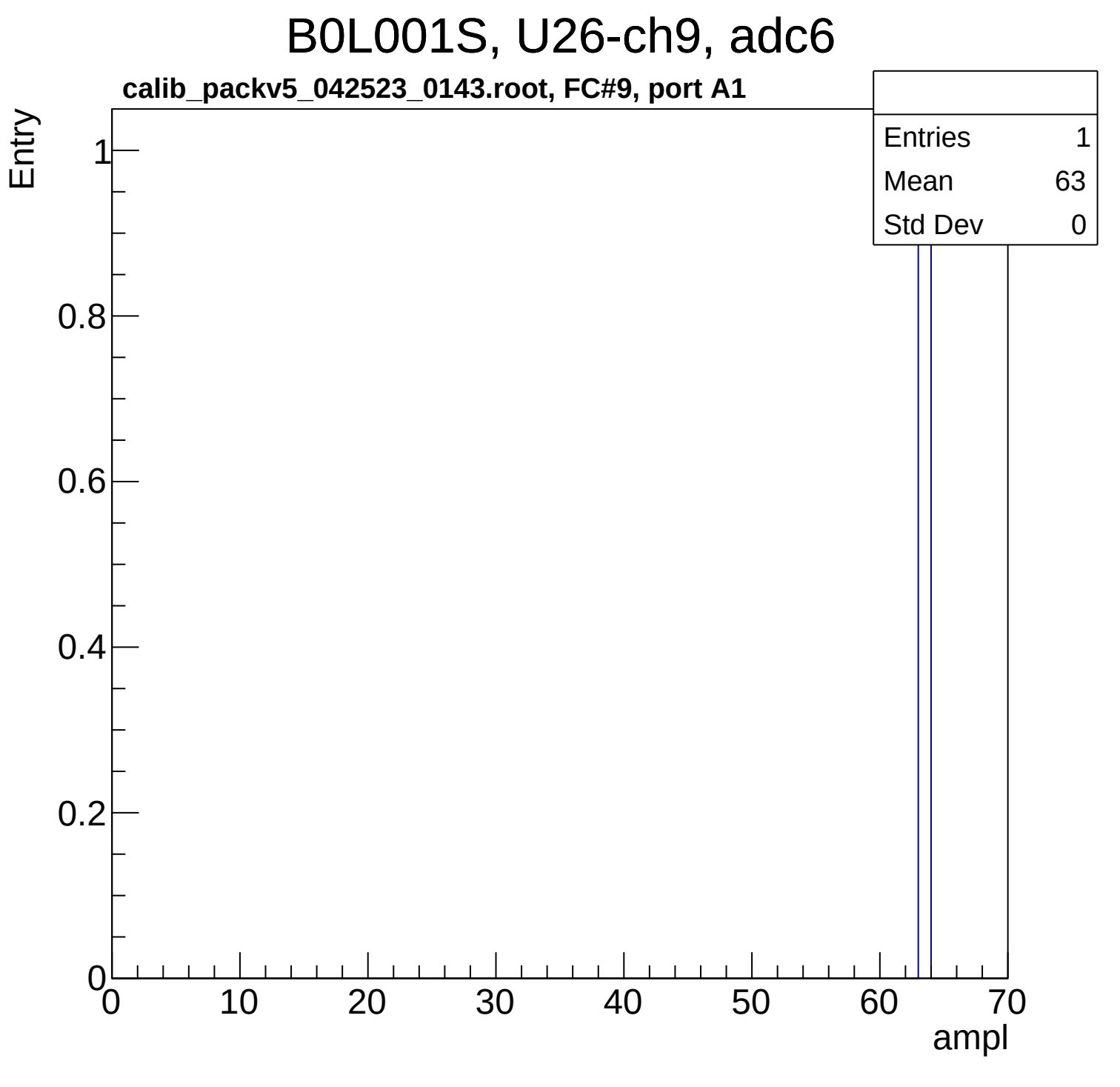
50
60
70
ampl


B0L001S, U26-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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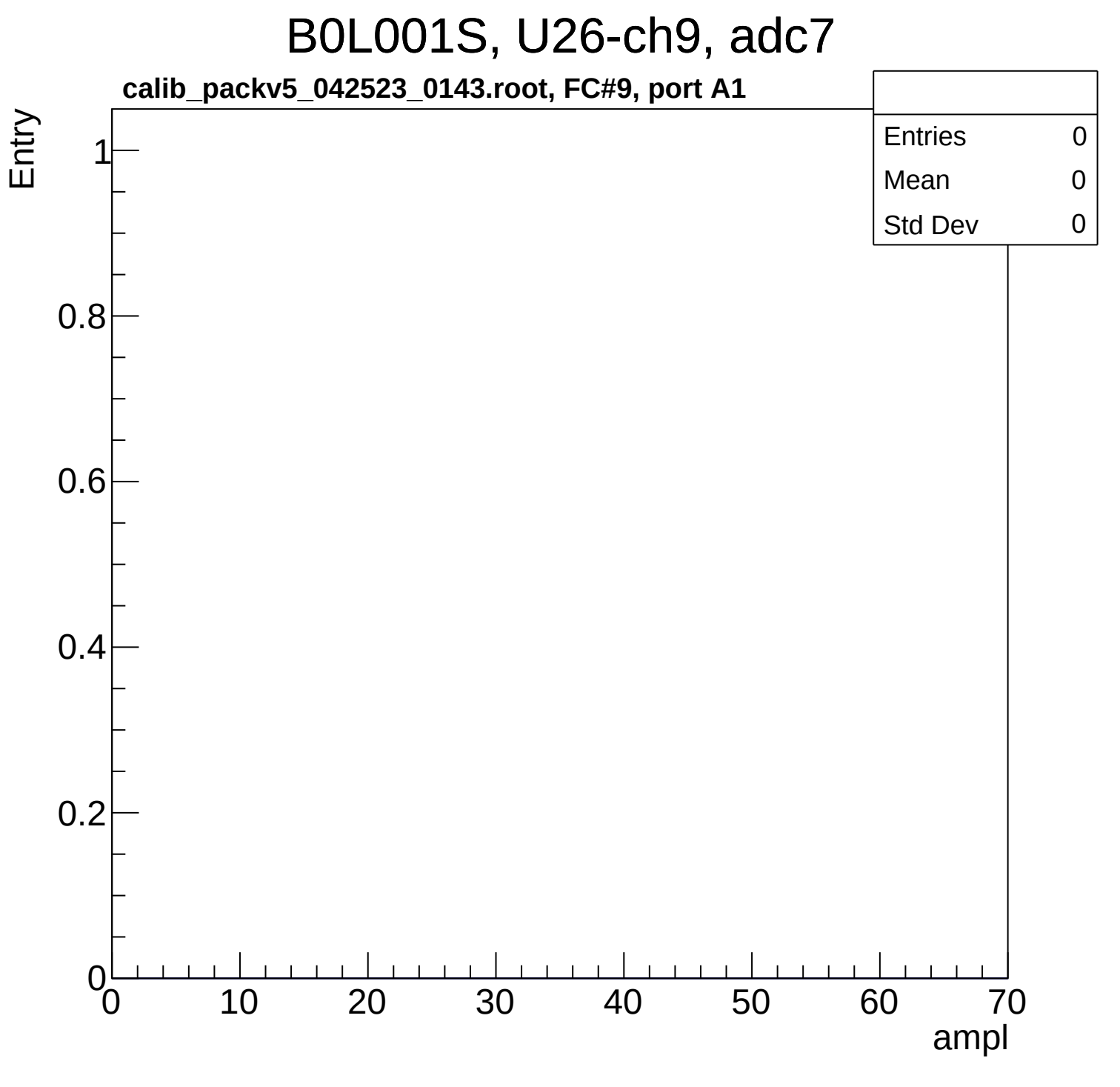
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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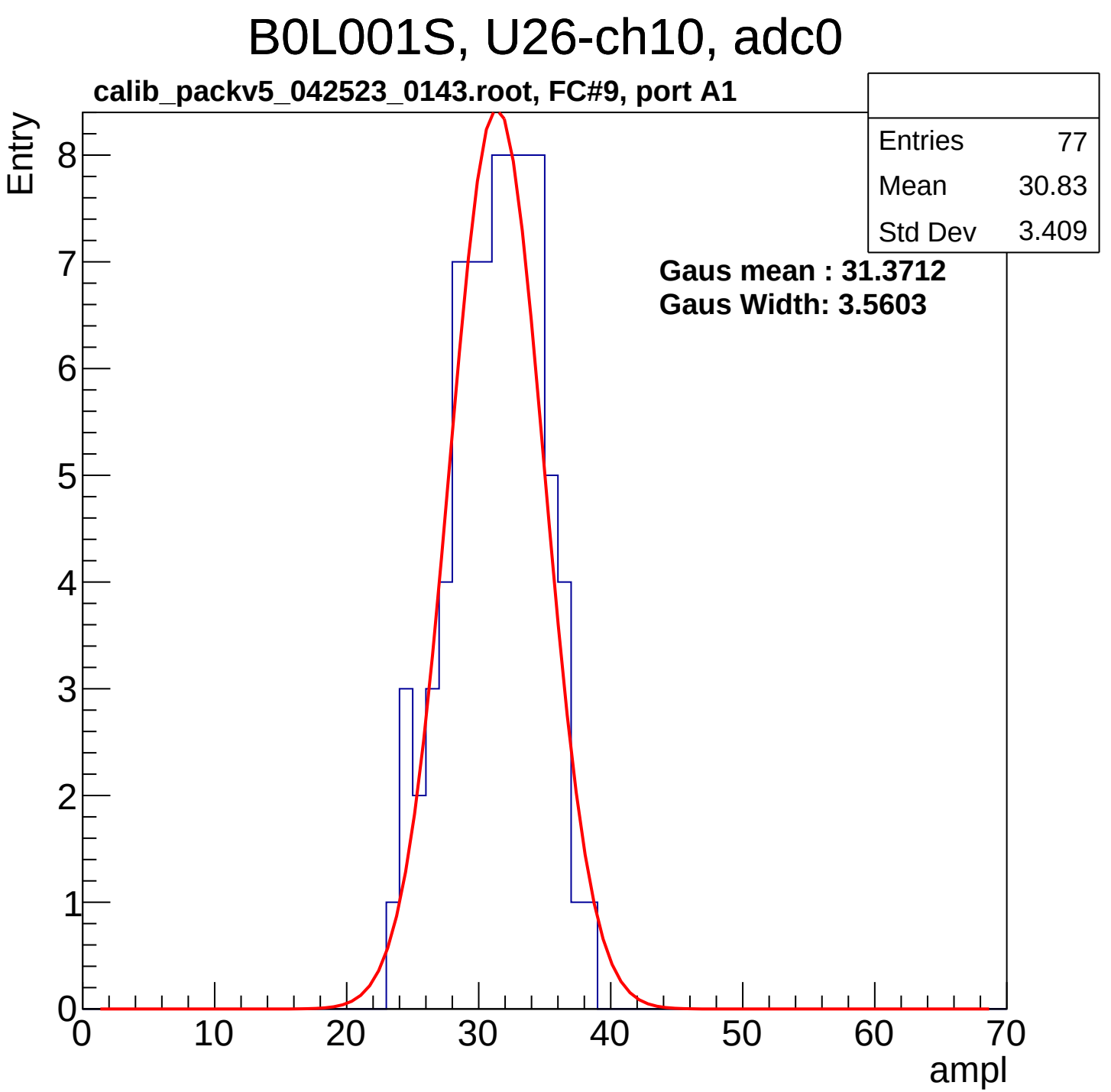
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.83
Std Dev 3.409
Gaus mean : 31.3712
Gaus Width: 3.5603
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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60
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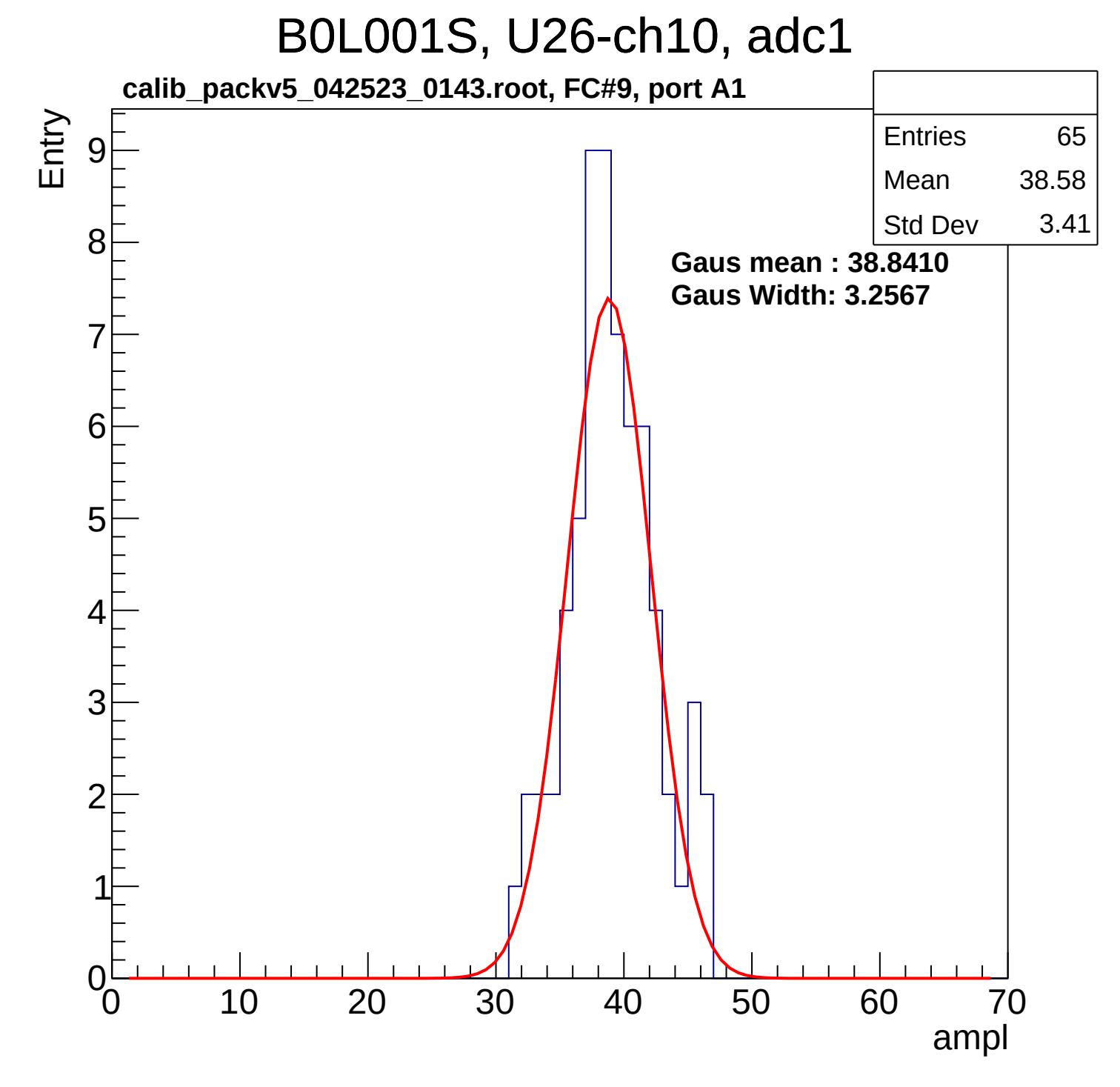
B0L001S, U26-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.58
Std Dev 3.41
Gaus mean : 38.8410
Gaus Width: 3.2567
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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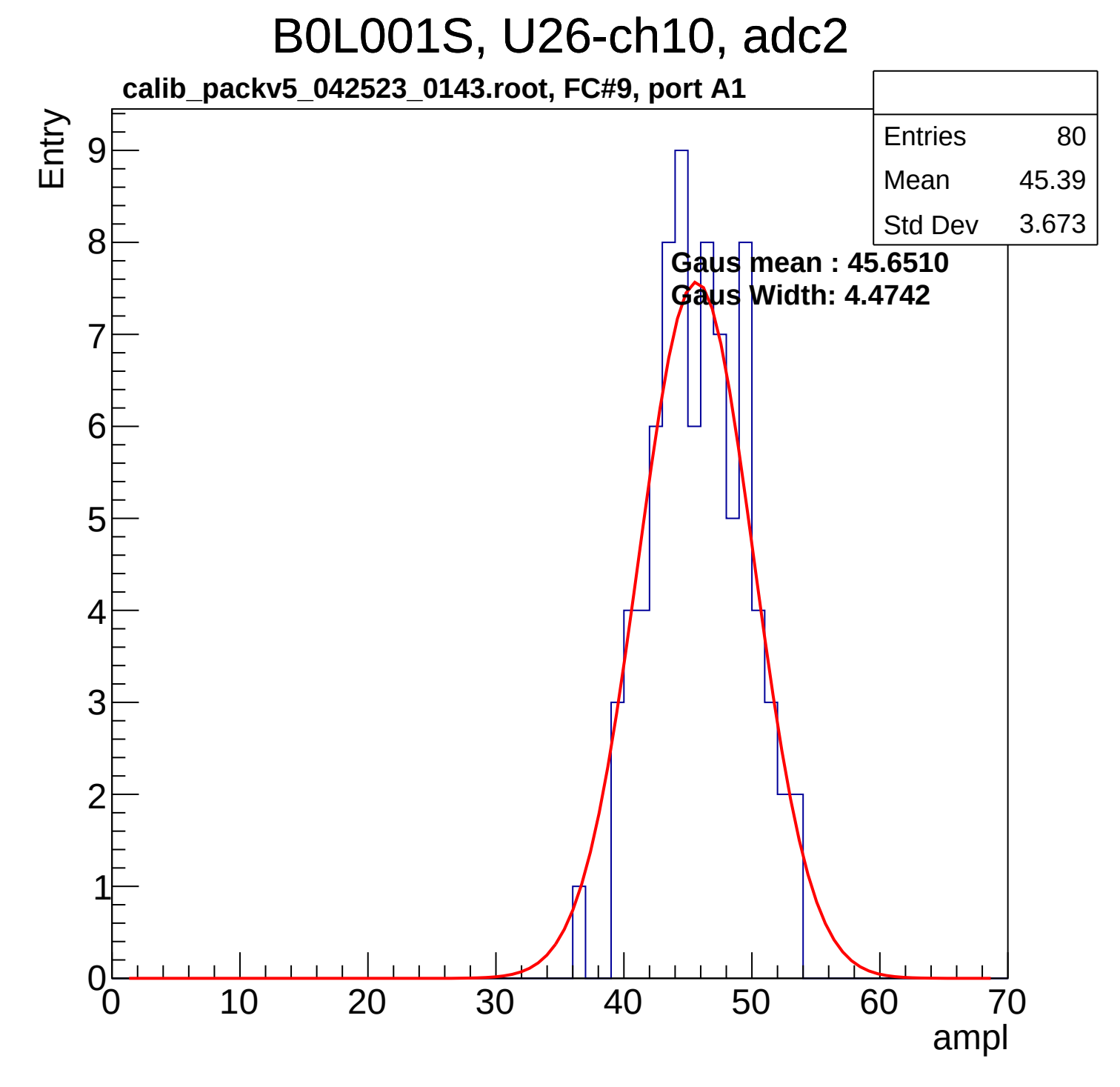
B0L001S, U26-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 45.39
Std Dev 3.673
Gaus mean : 45.6510
Gaus Width: 4.4742
Entry
ampl
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3
4
5
6
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8
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0
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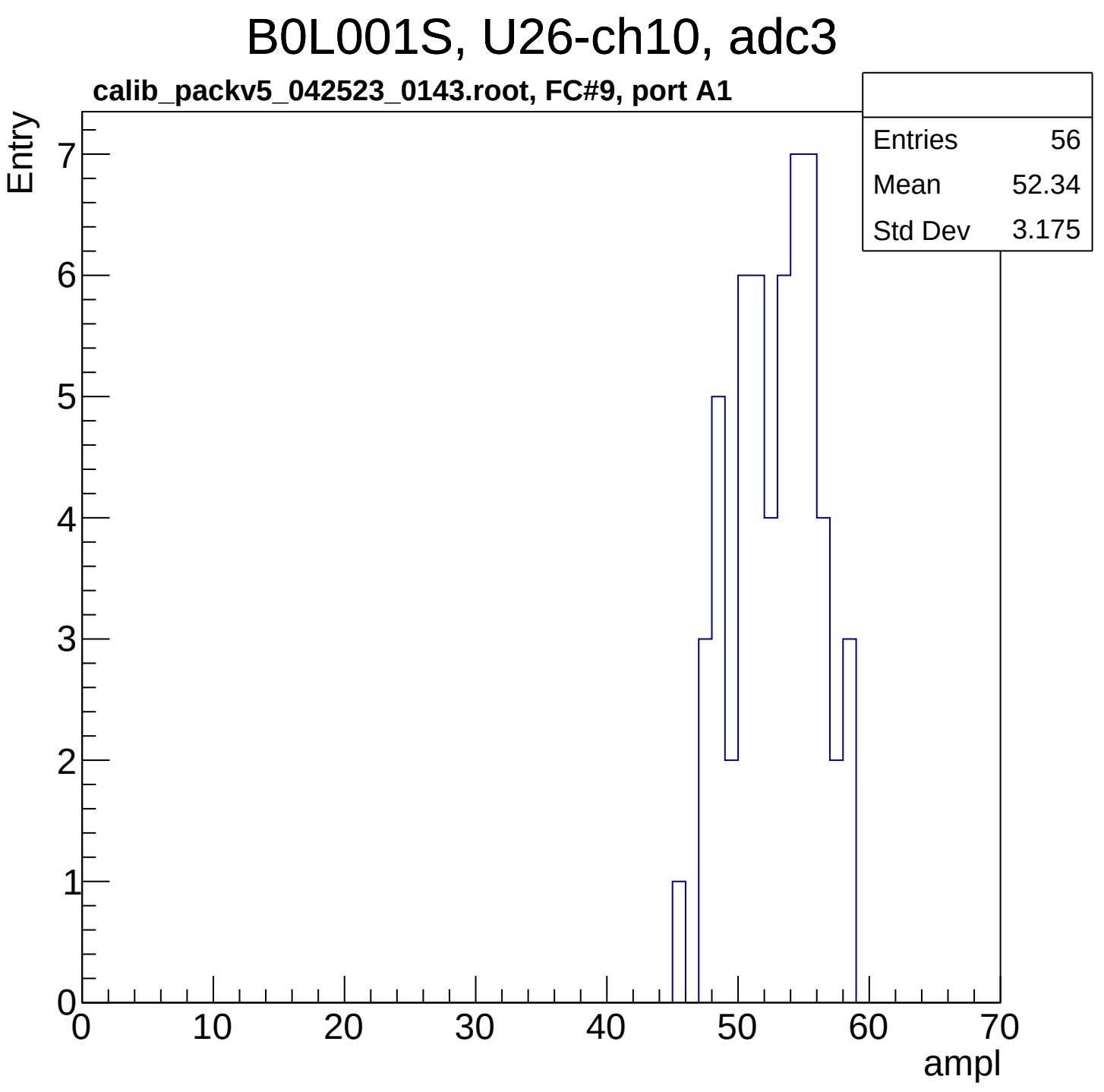
B0L001S, U26-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 52.34
Std Dev 3.175
Entry
ampl
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4
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6
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0
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40
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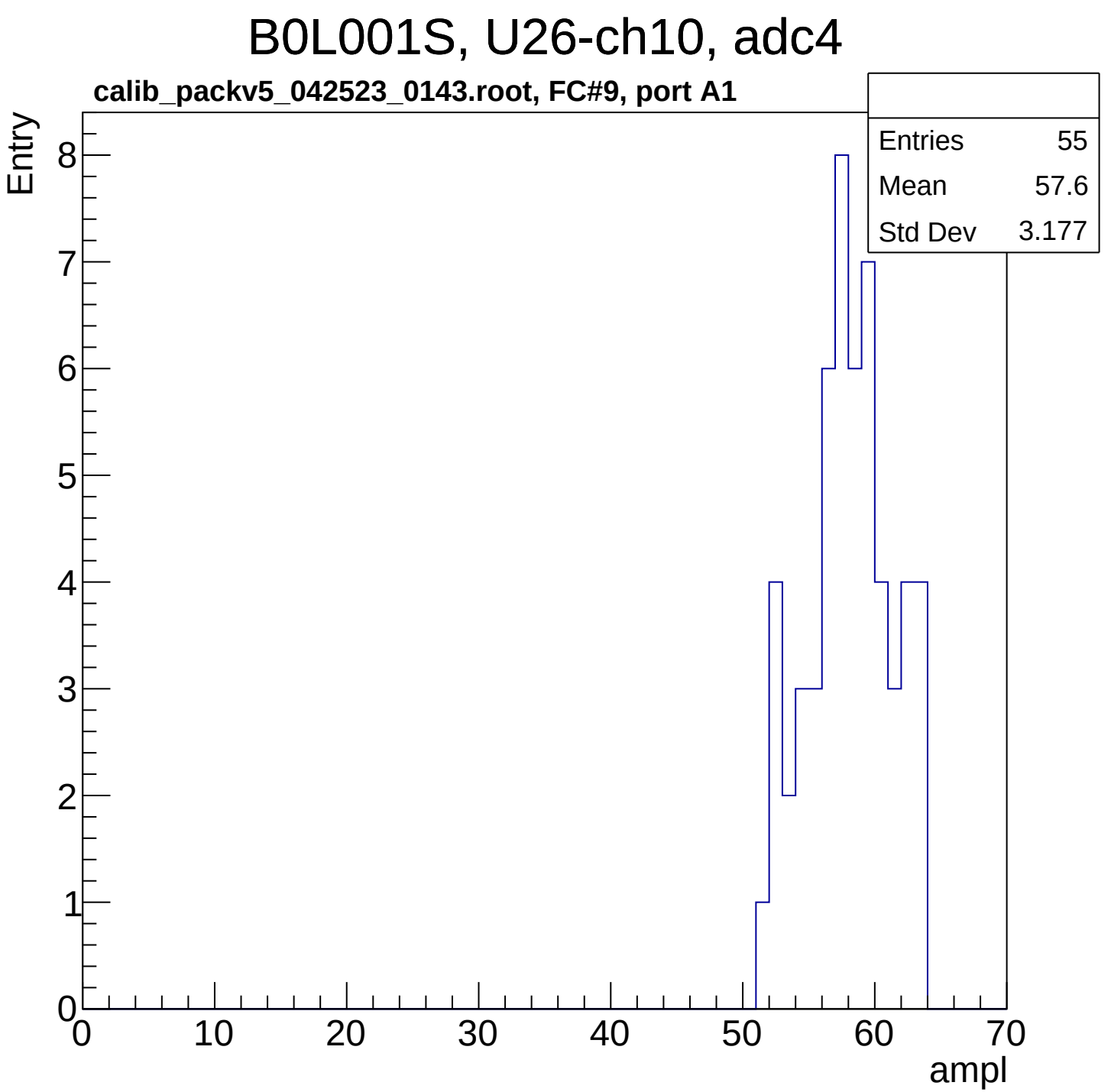
B0L001S, U26-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.6
Std Dev 3.177
Entry
ampl
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7
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0
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60
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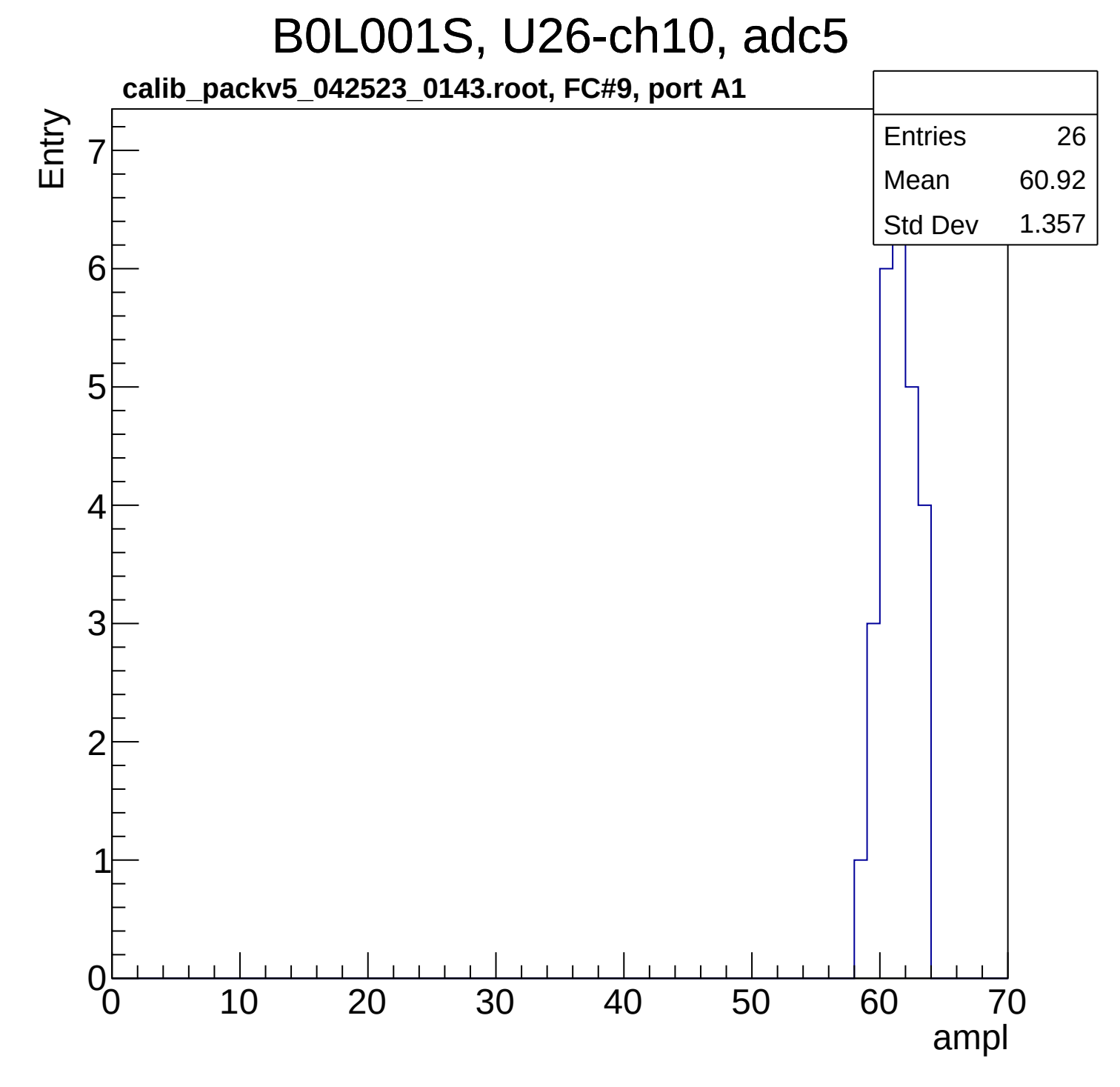
B0L001S, U26-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.92
Std Dev 1.357
Entry
ampl
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4
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0
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60
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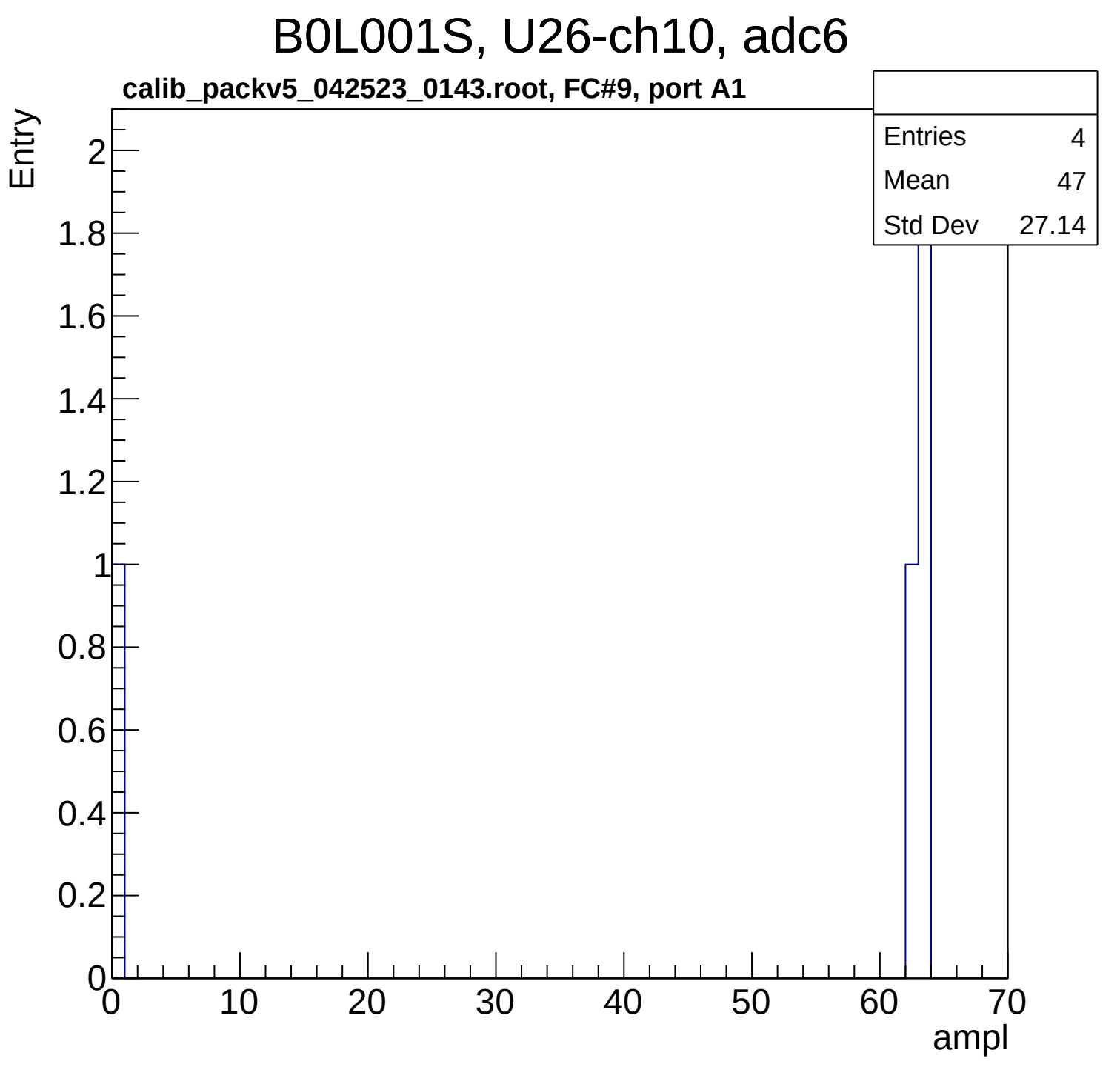
B0L001S, U26-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
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0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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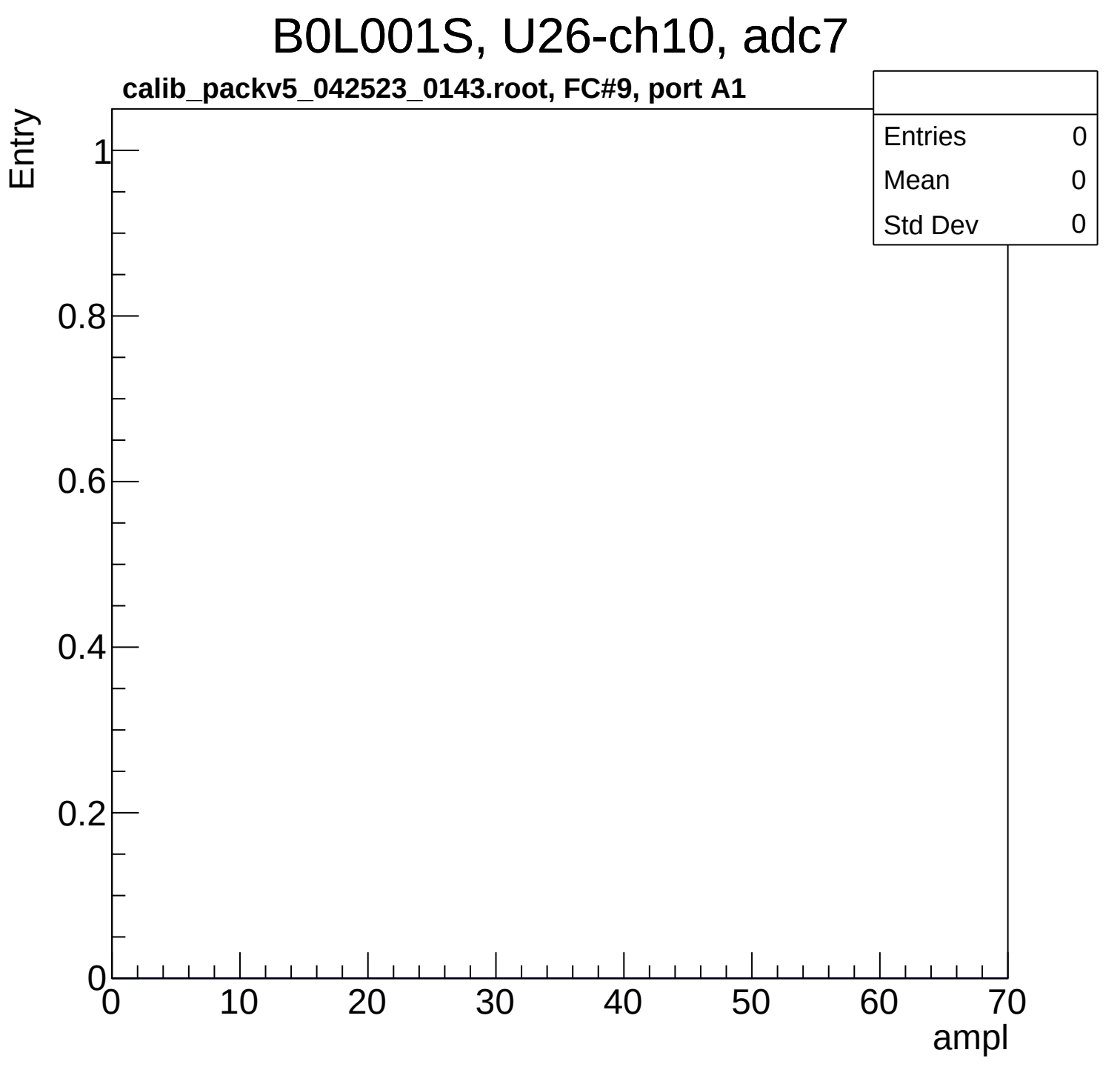
B0L001S, U26-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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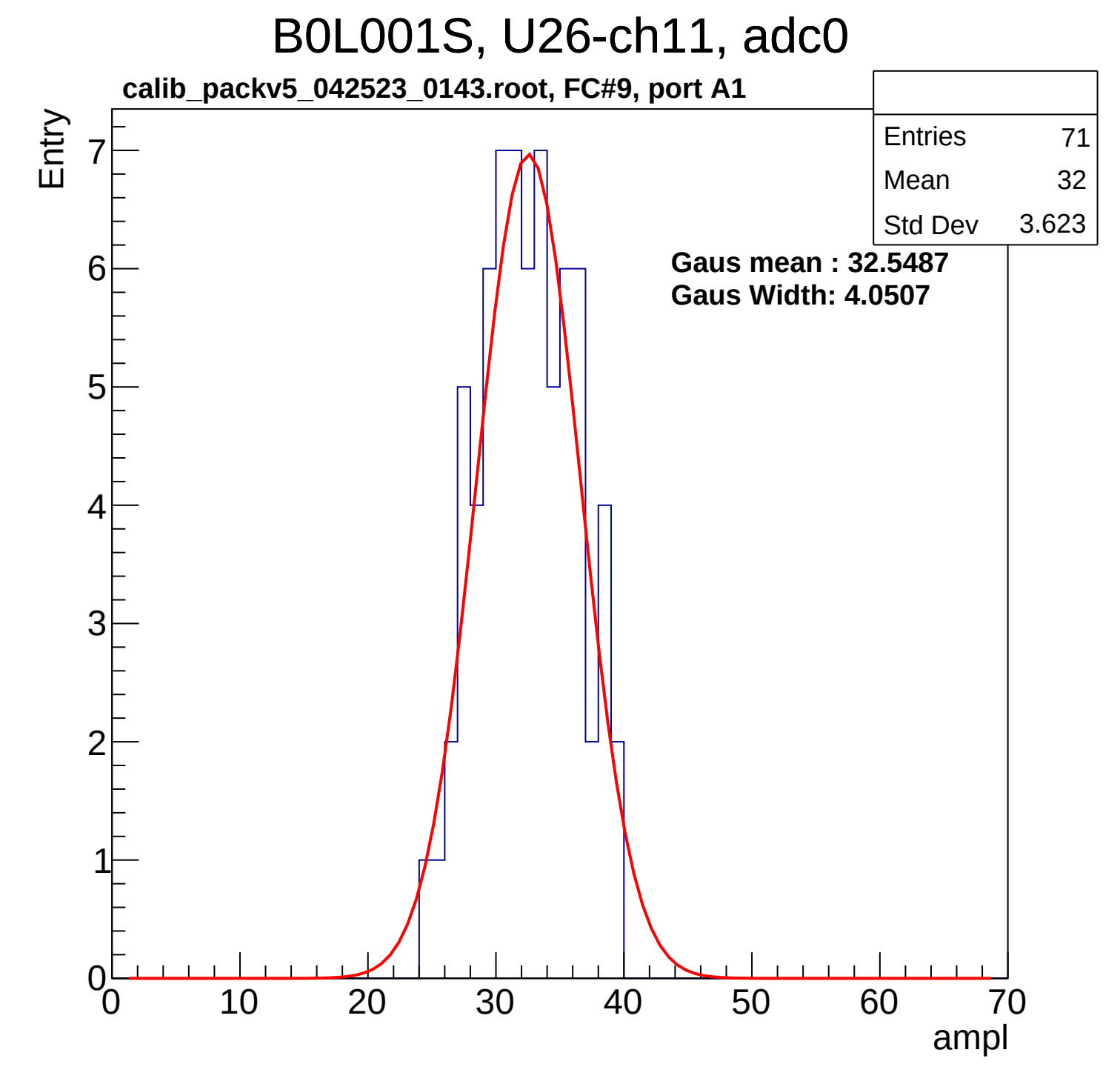
60
70
ampl


B0L001S, U26-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32
Std Dev 3.623
Gaus mean : 32.5487
Gaus Width: 4.0507
Entry
ampl
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6
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0
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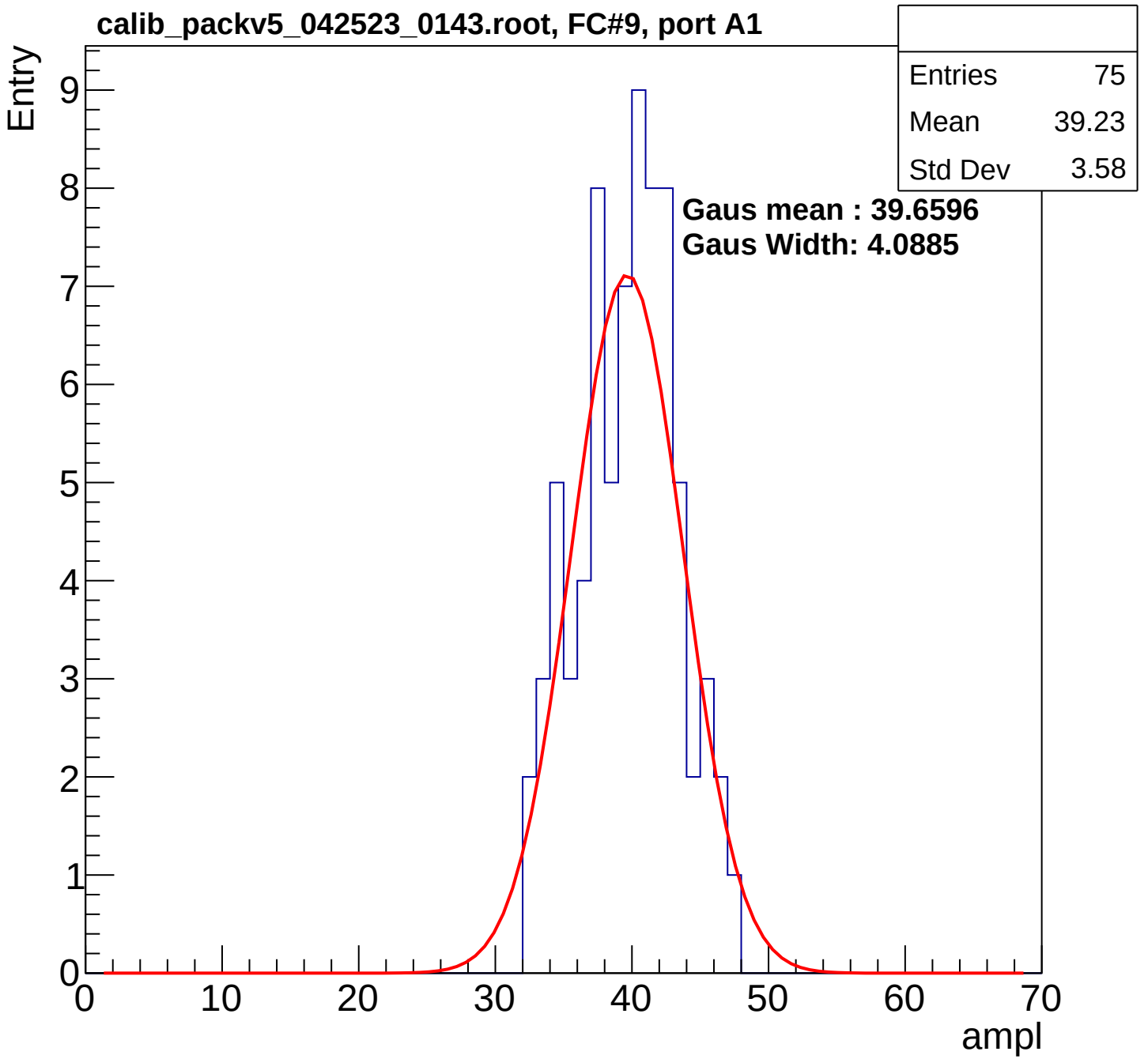
B0L001S, U26-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 39.23
Std Dev 3.58
Gaus mean : 39.6596
Gaus Width: 4.0885
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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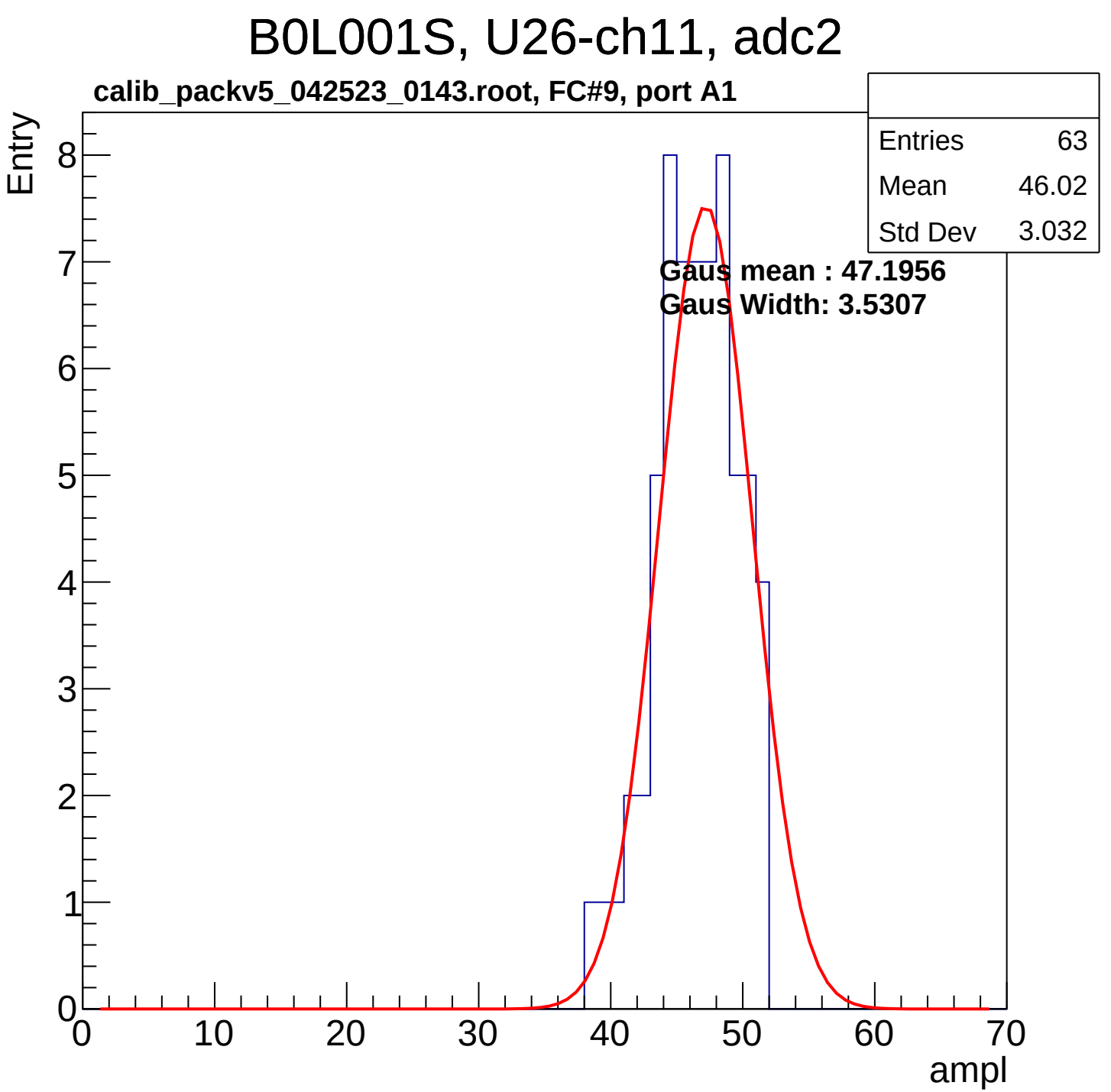
B0L001S, U26-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 46.02
Std Dev 3.032
Gaus mean : 47.1956
Gaus Width: 3.5307
Entry
ampl
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4
5
6
7
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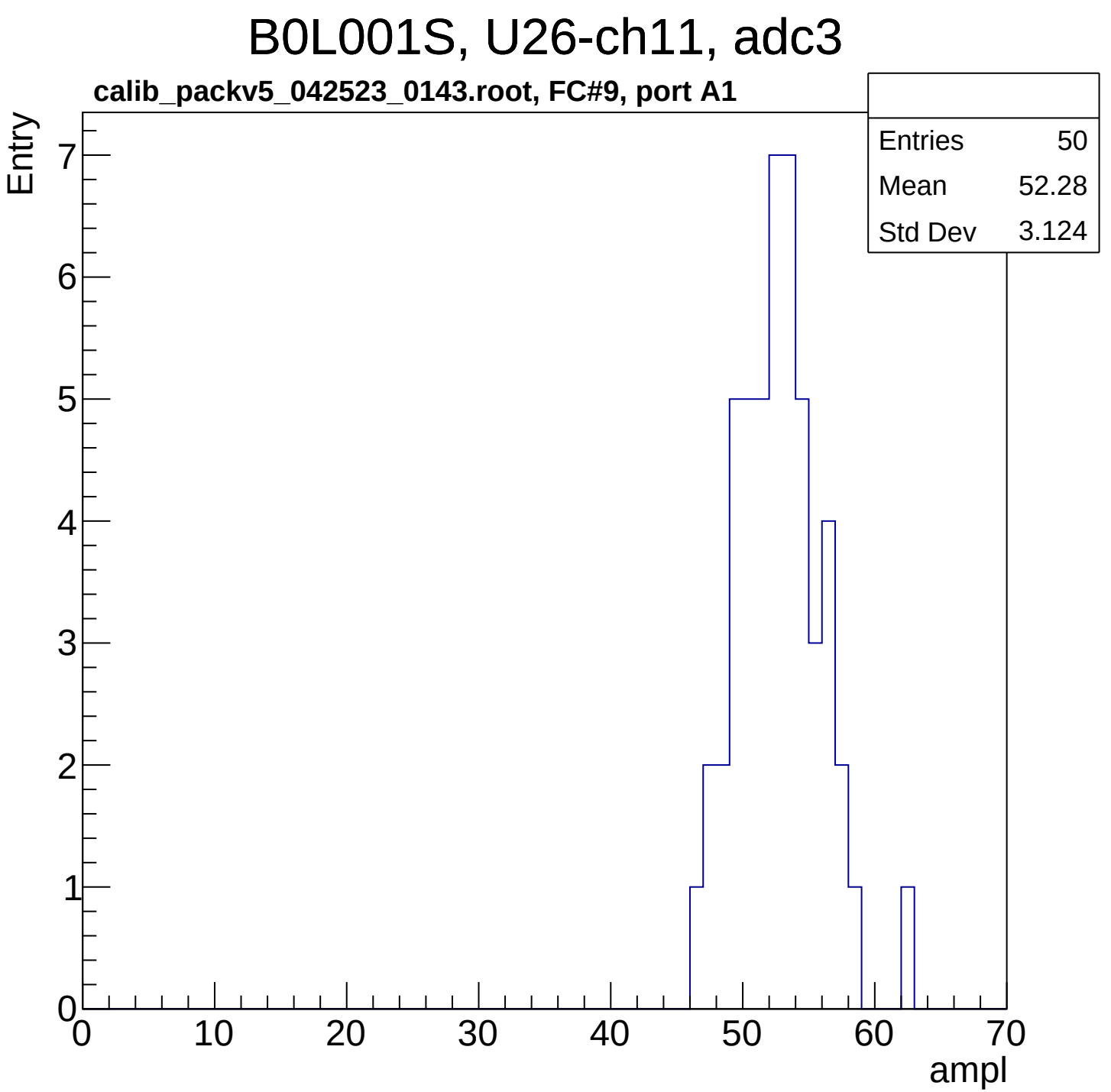
B0L001S, U26-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 52.28
Std Dev 3.124
Entry
ampl
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5
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7
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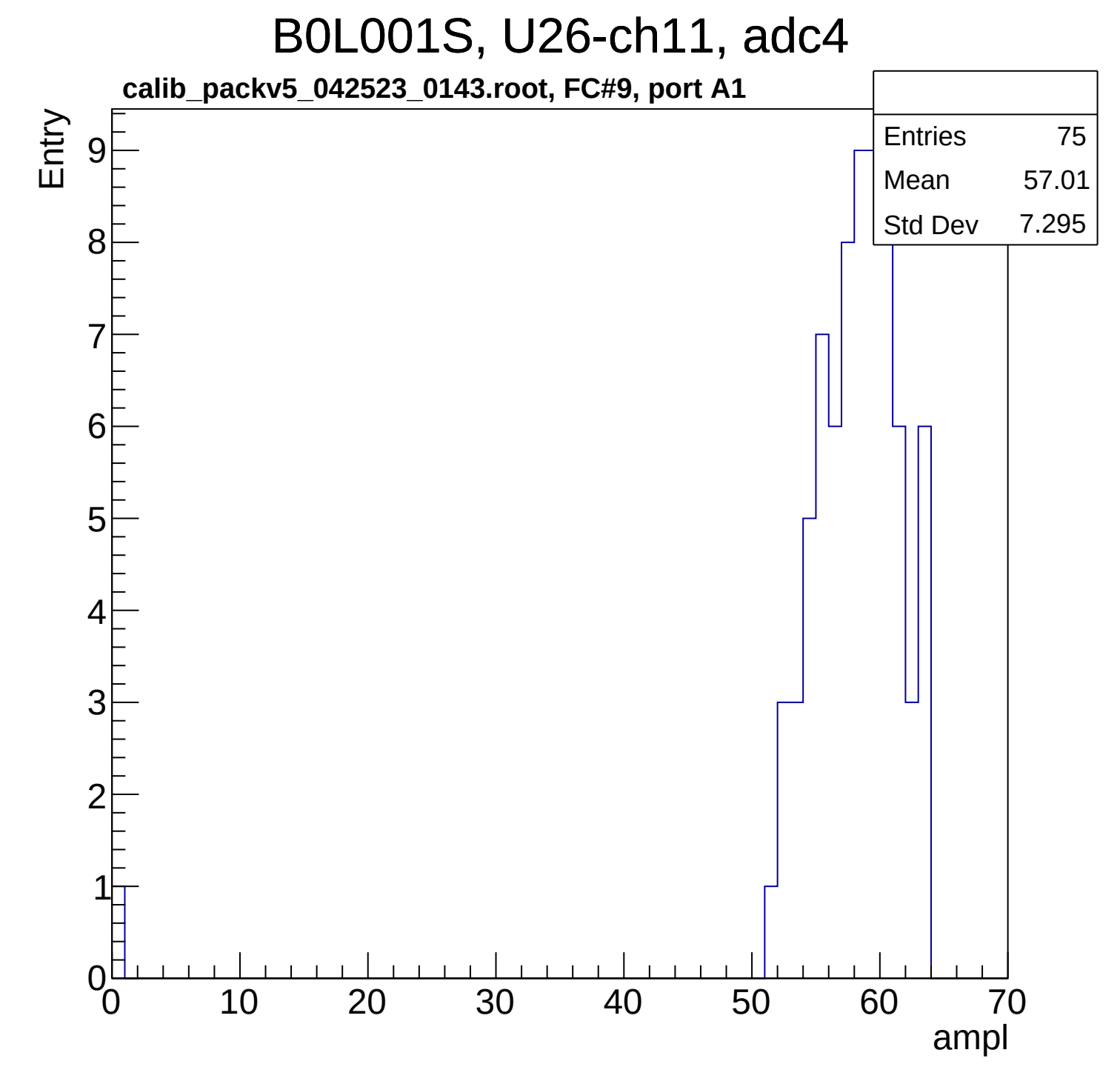
B0L001S, U26-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 57.01
Std Dev 7.295
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
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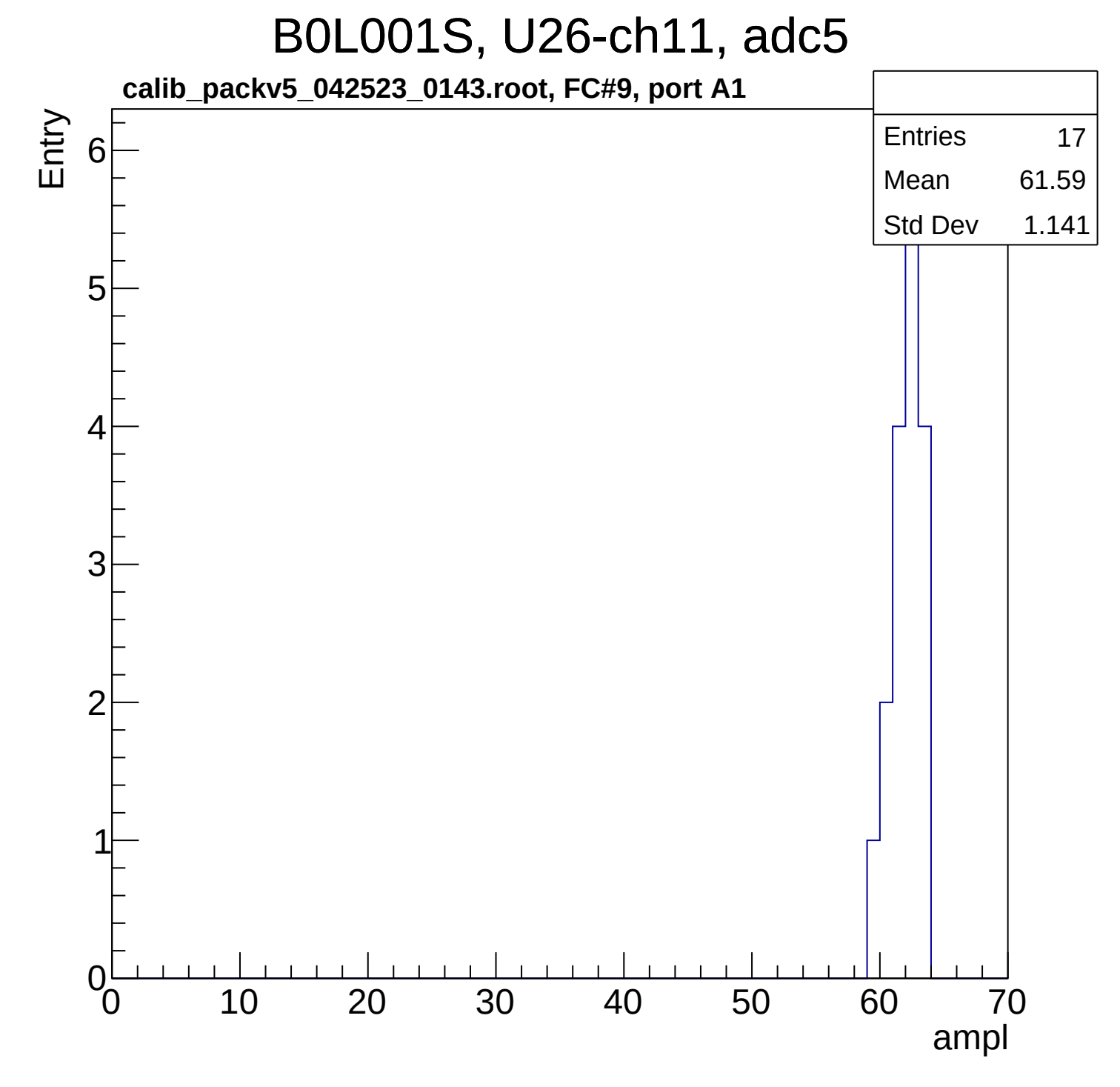
60
70
ampl


B0L001S, U26-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 61.59
Std Dev 1.141
Entry
0
1
2
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4
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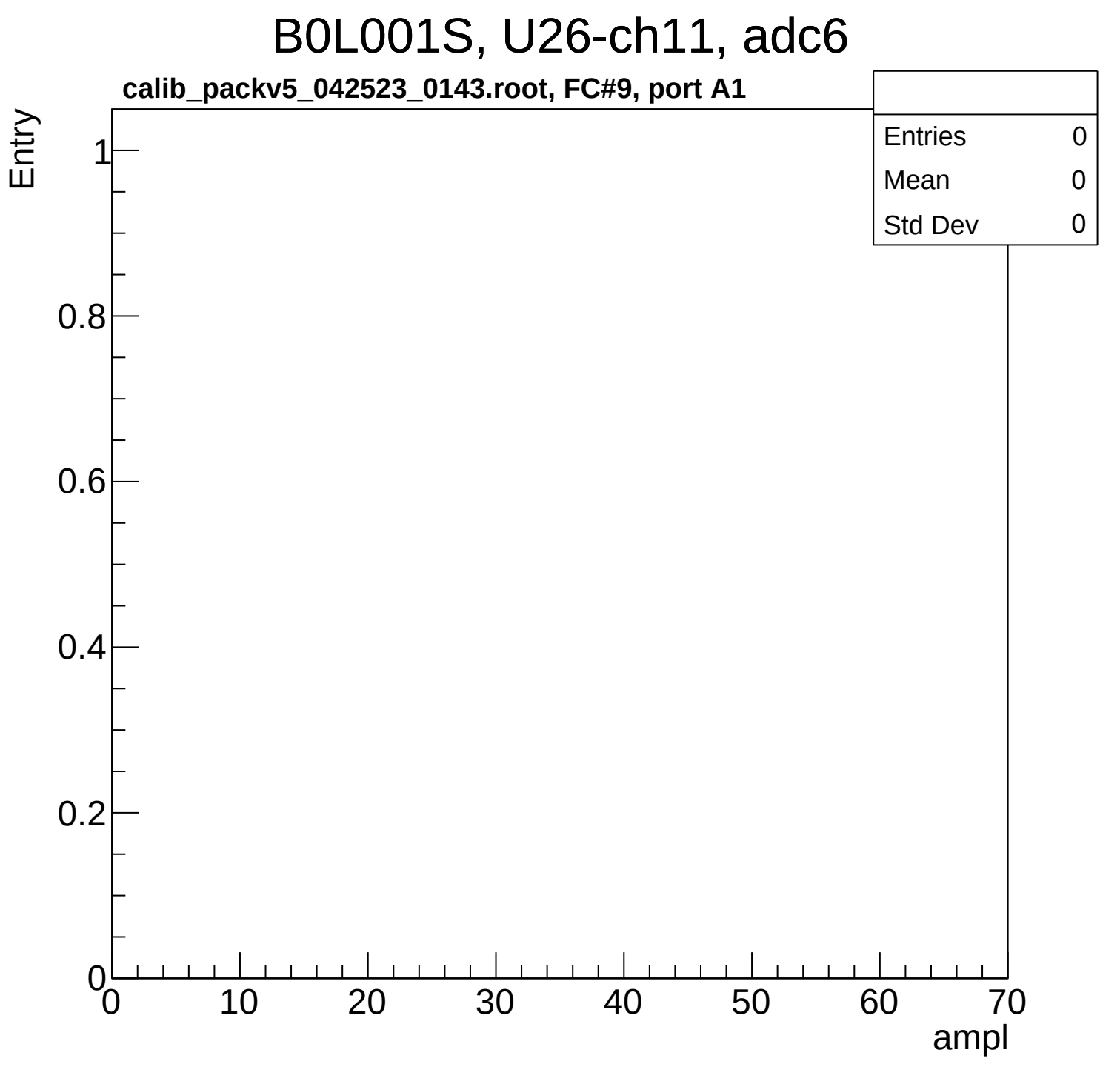
ampl


B0L001S, U26-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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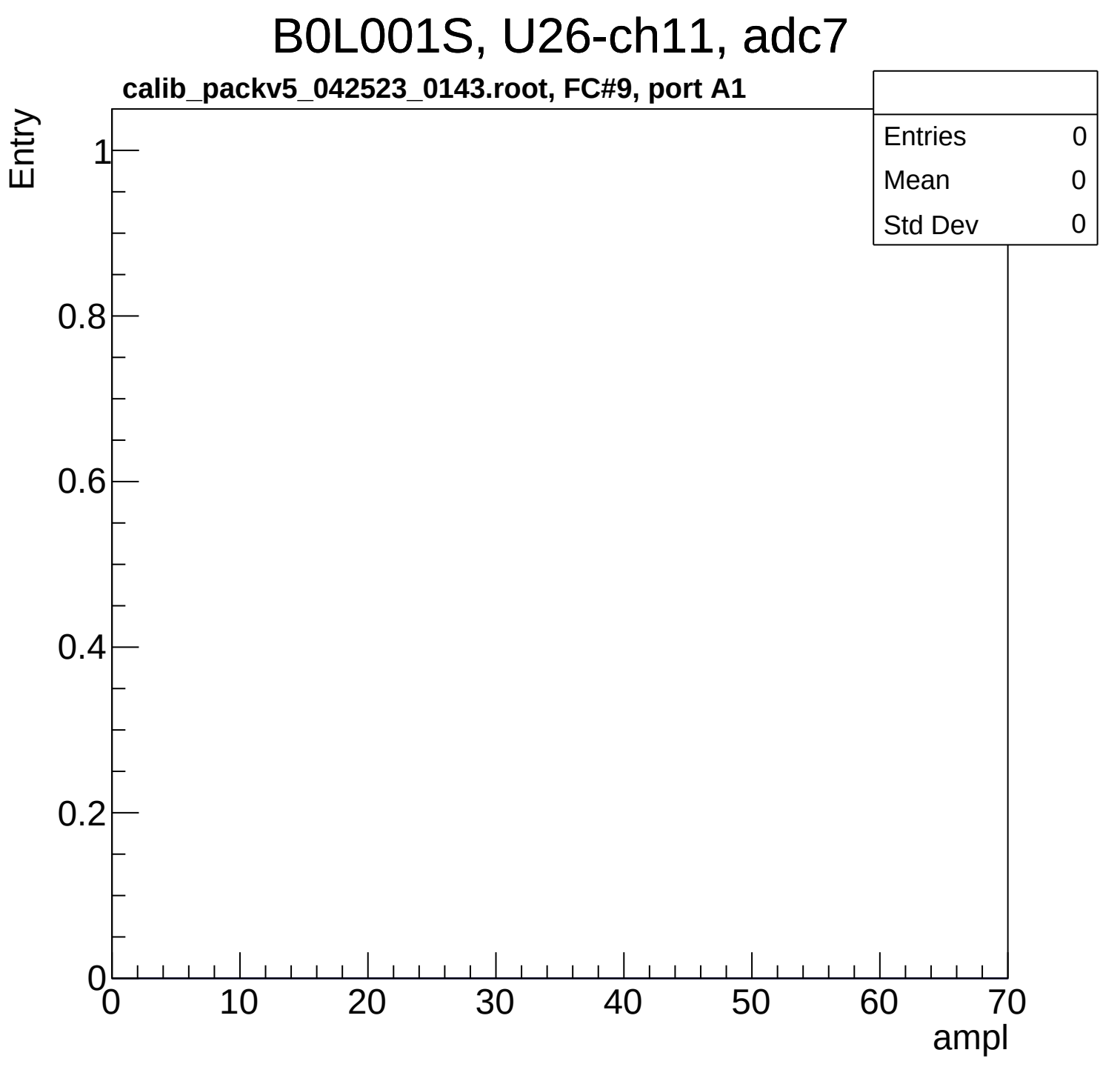
50
60
70
ampl


B0L001S, U26-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
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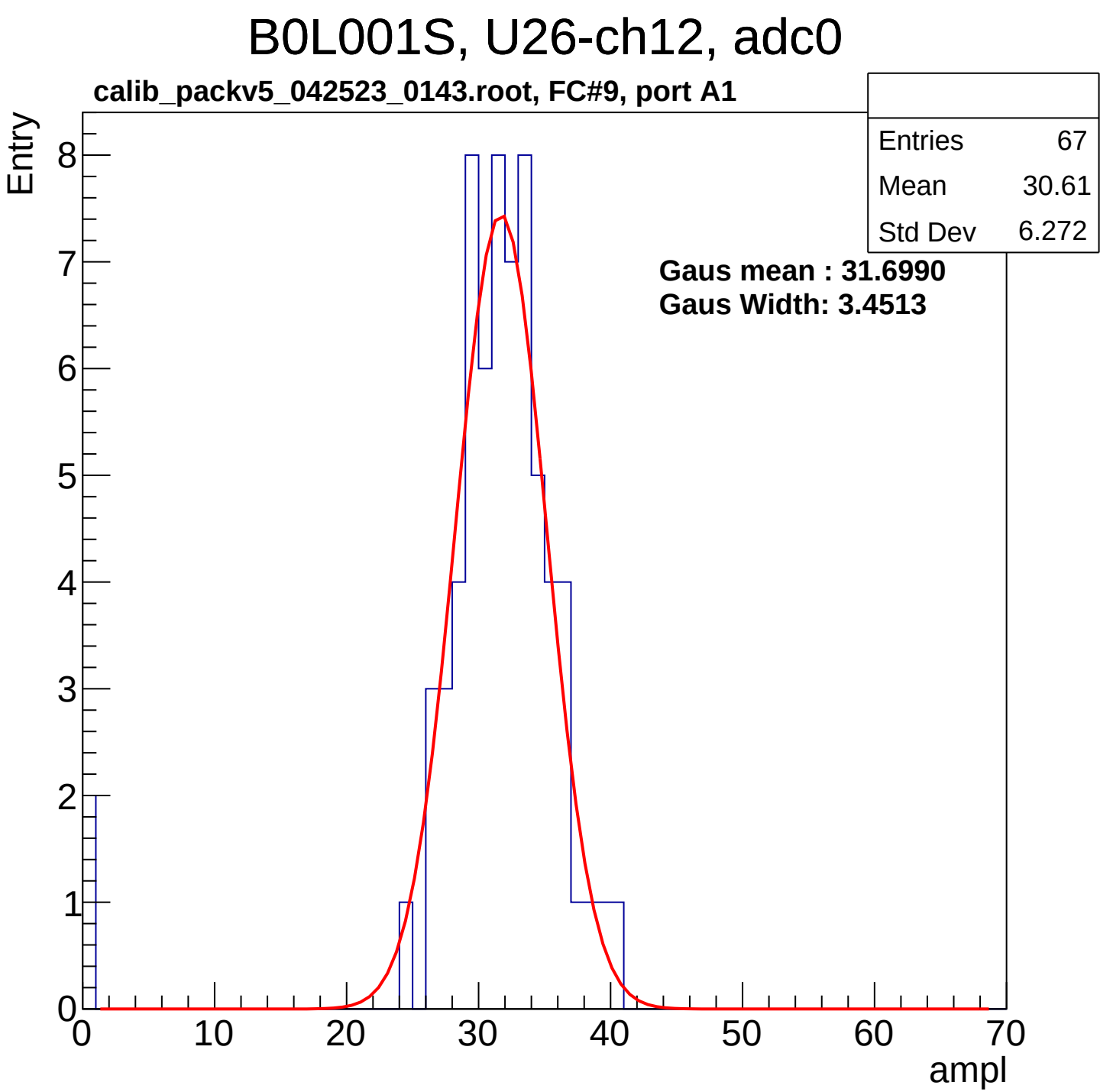
50
60
70
ampl


B0L001S, U26-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.61
Std Dev 6.272
Gaus mean : 31.6990
Gaus Width: 3.4513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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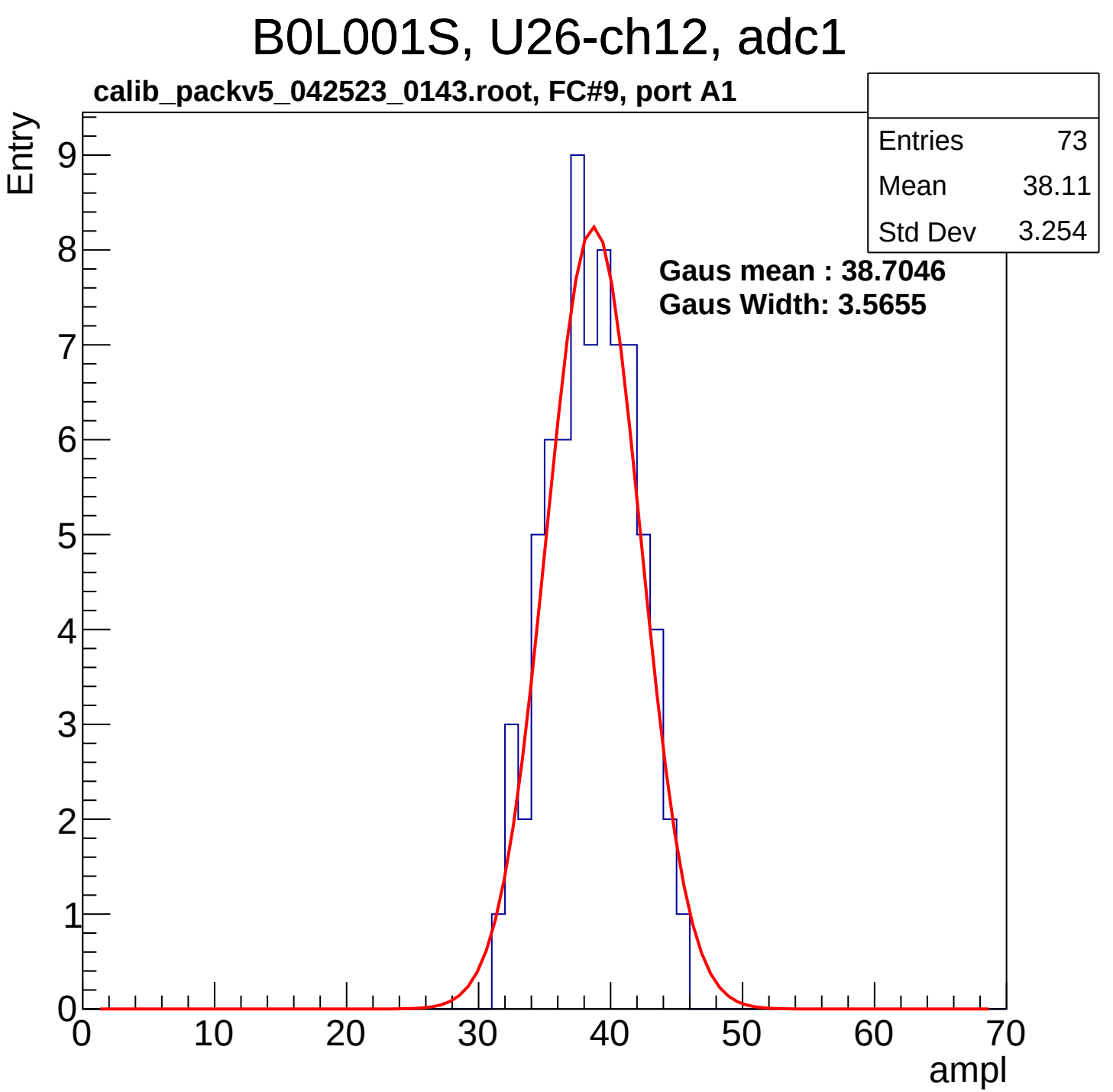
B0L001S, U26-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.11
Std Dev 3.254
Gaus mean : 38.7046
Gaus Width: 3.5655
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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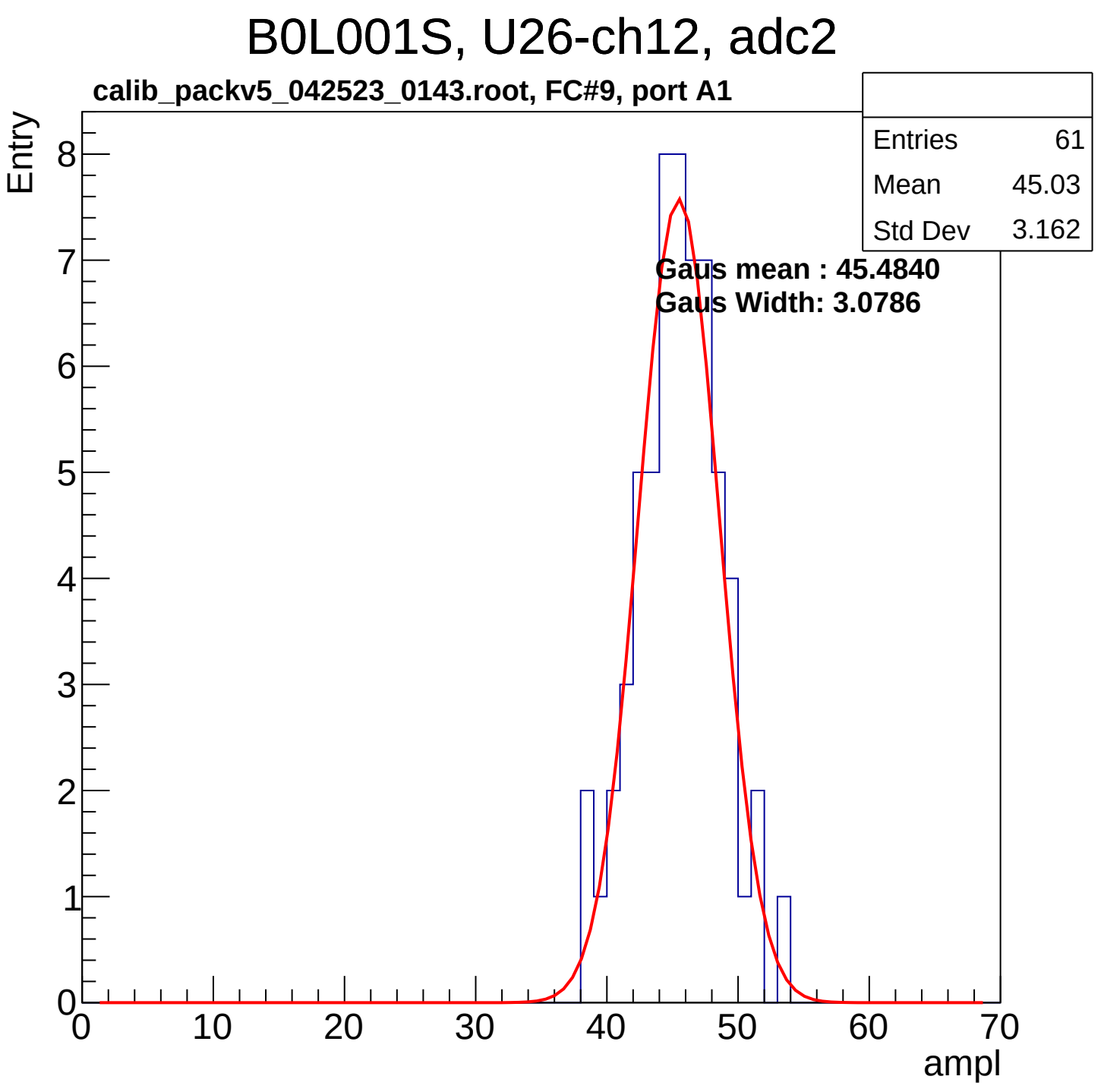
B0L001S, U26-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.03
Std Dev 3.162
Gaus mean : 45.4840
Gaus Width: 3.0786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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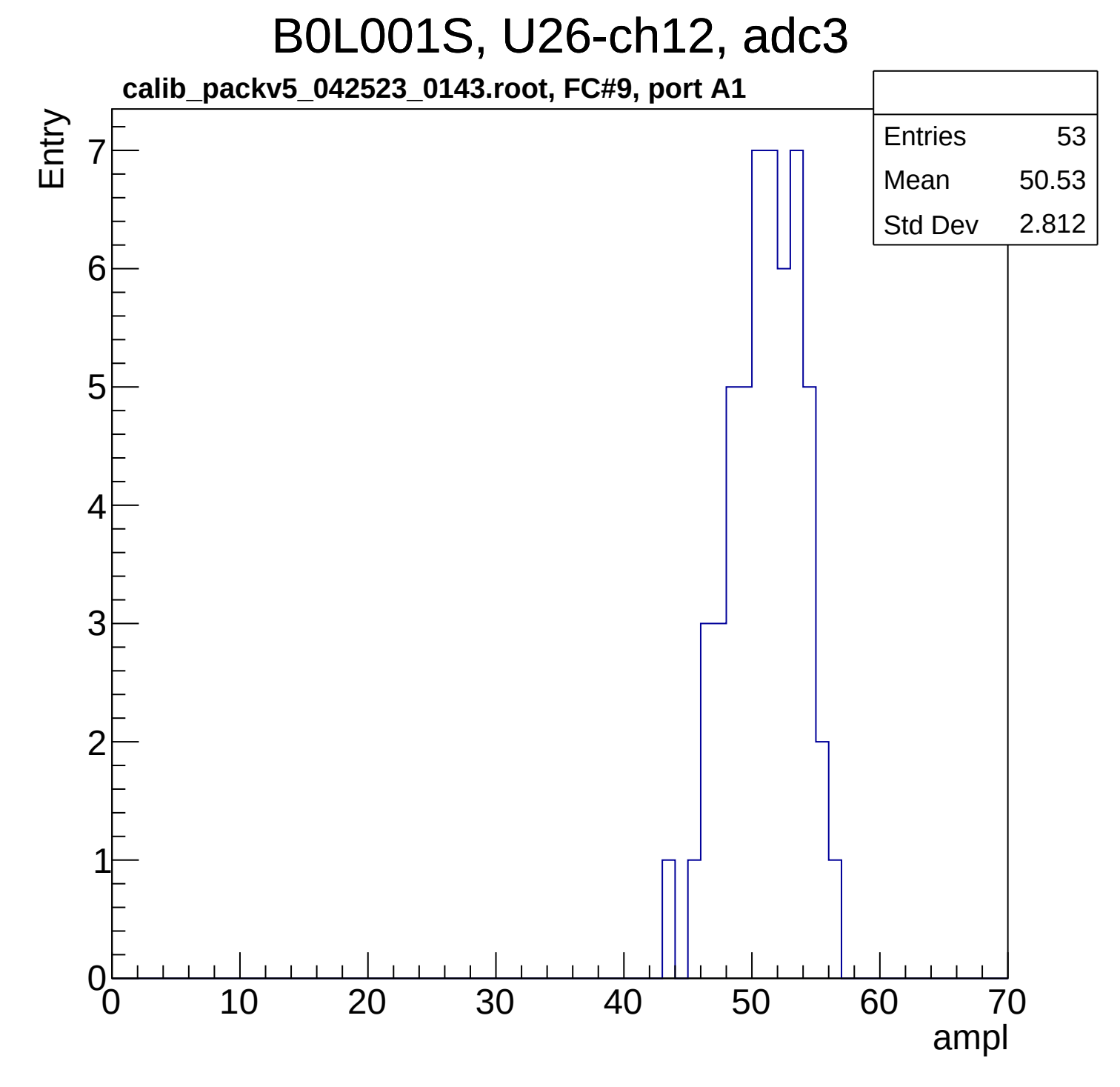
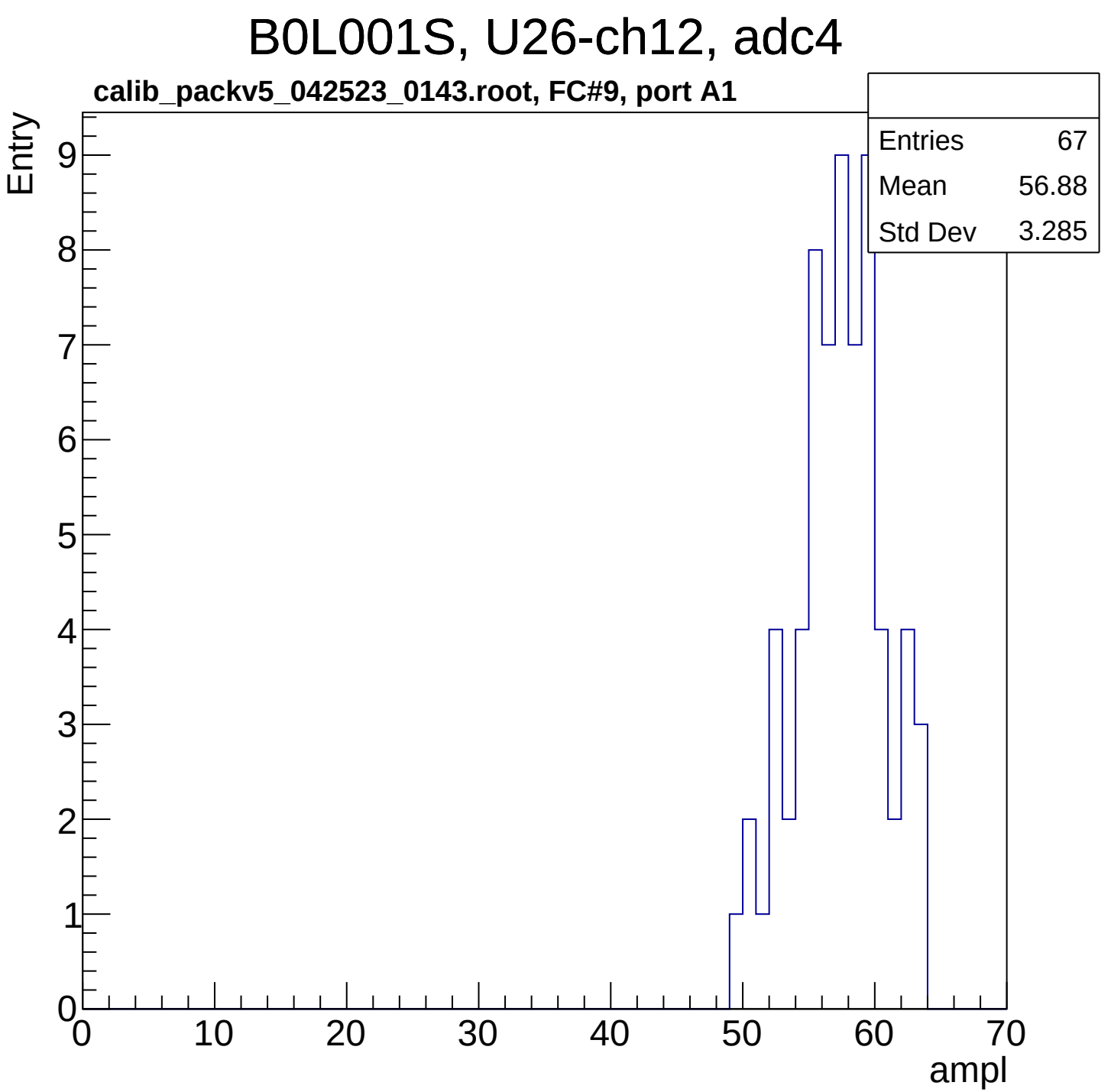

B0L001S, U26-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 50.53
Std Dev 2.812
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.88
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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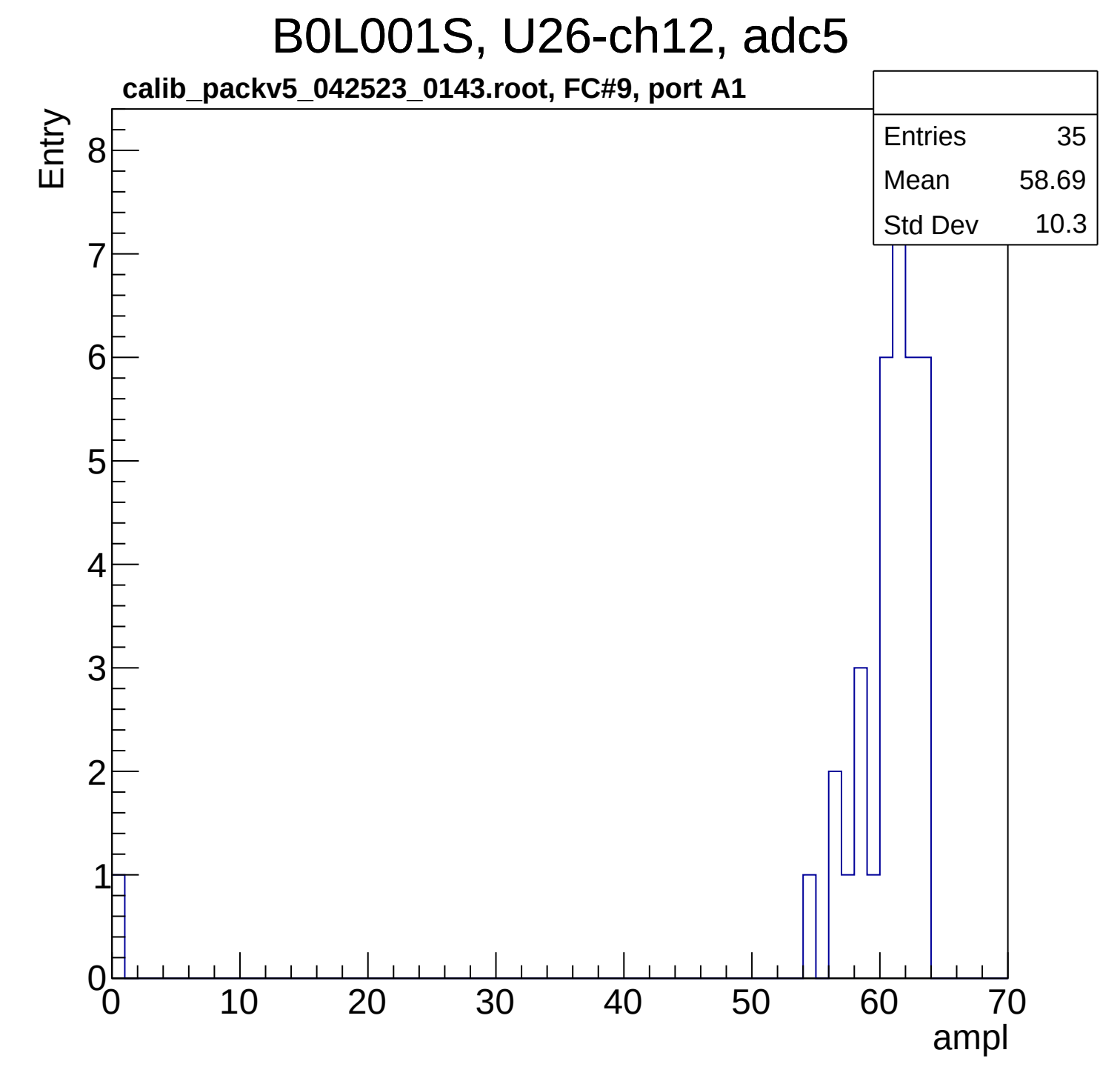
B0L001S, U26-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.69
Std Dev 10.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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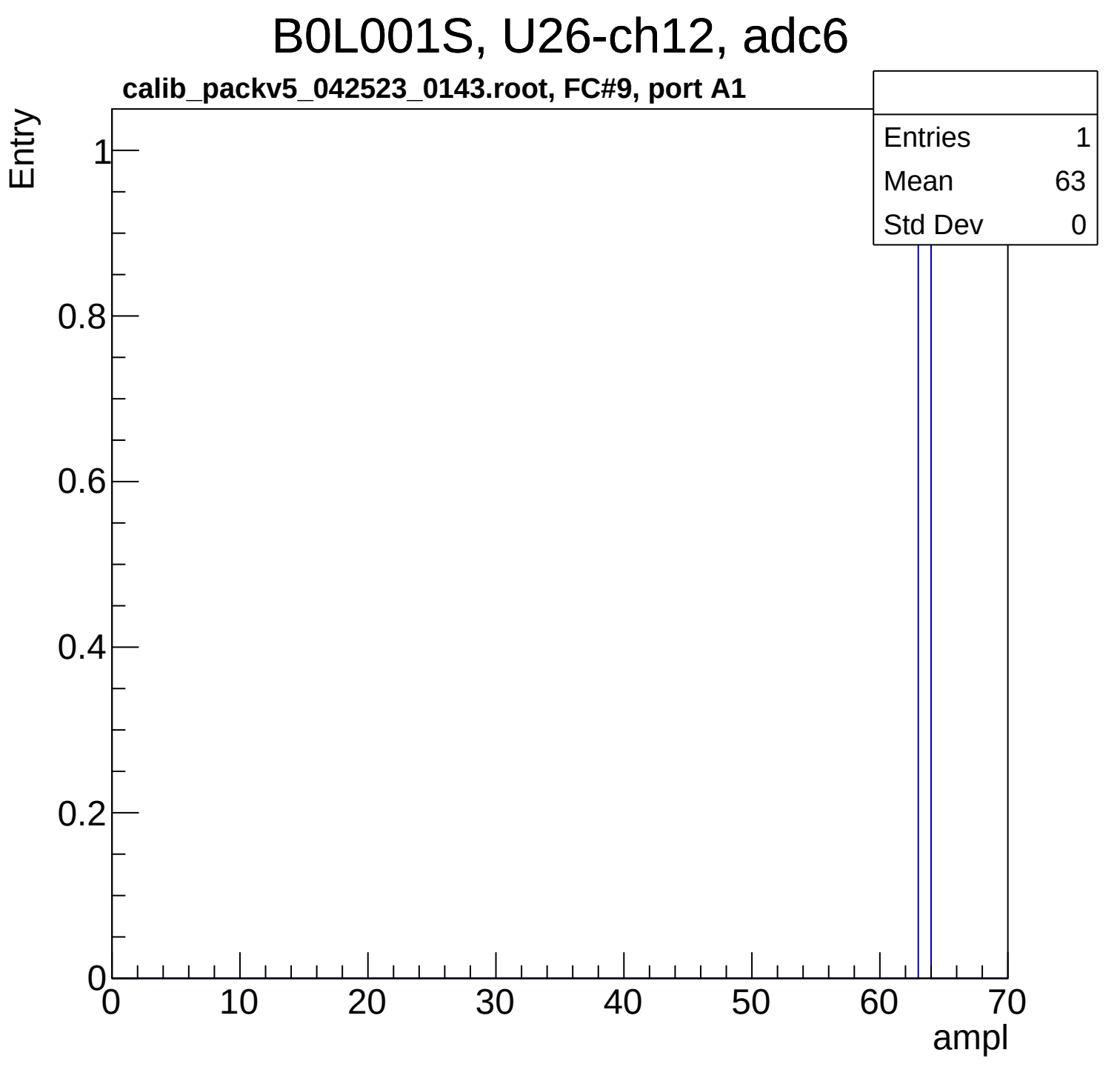
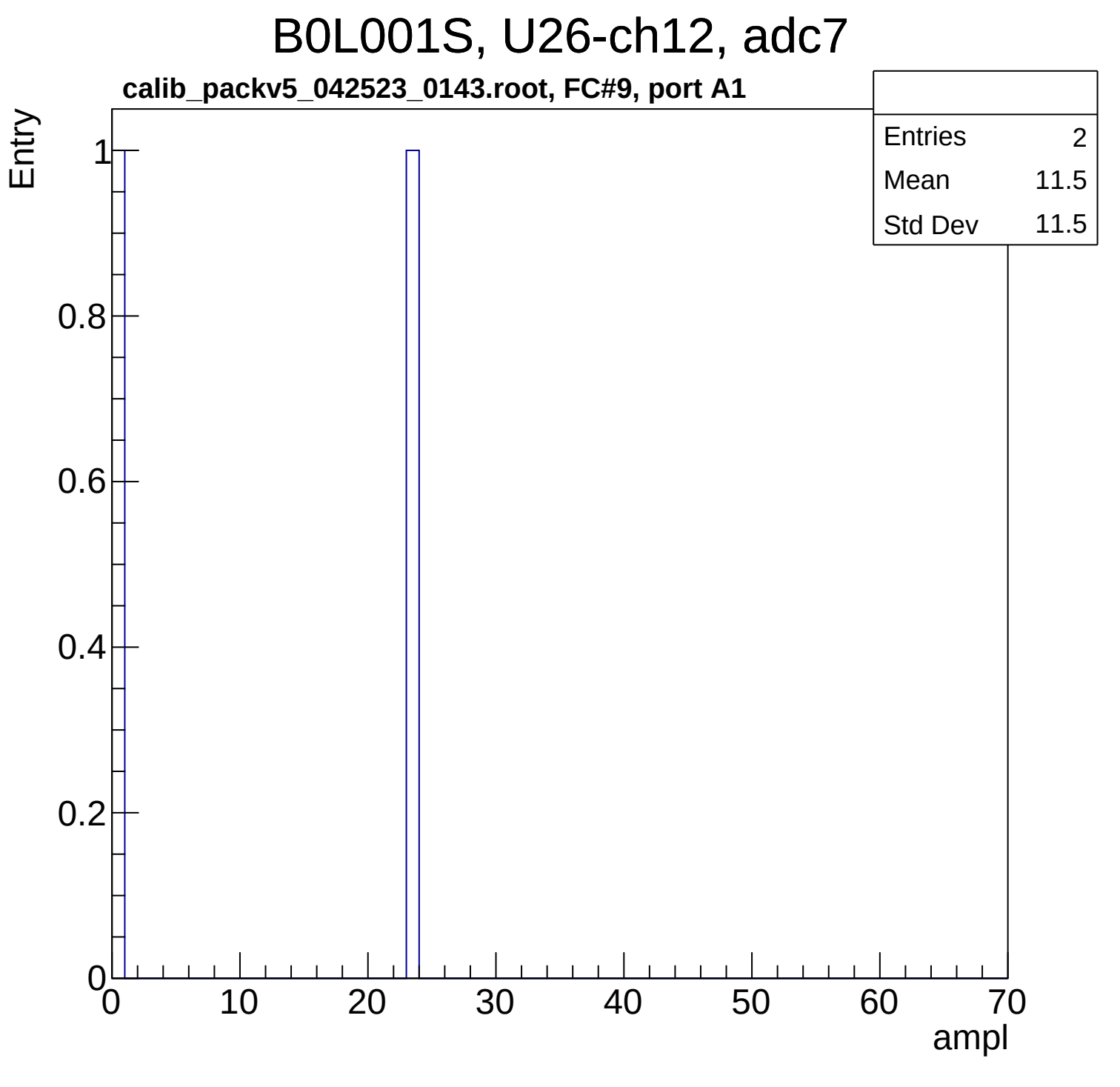
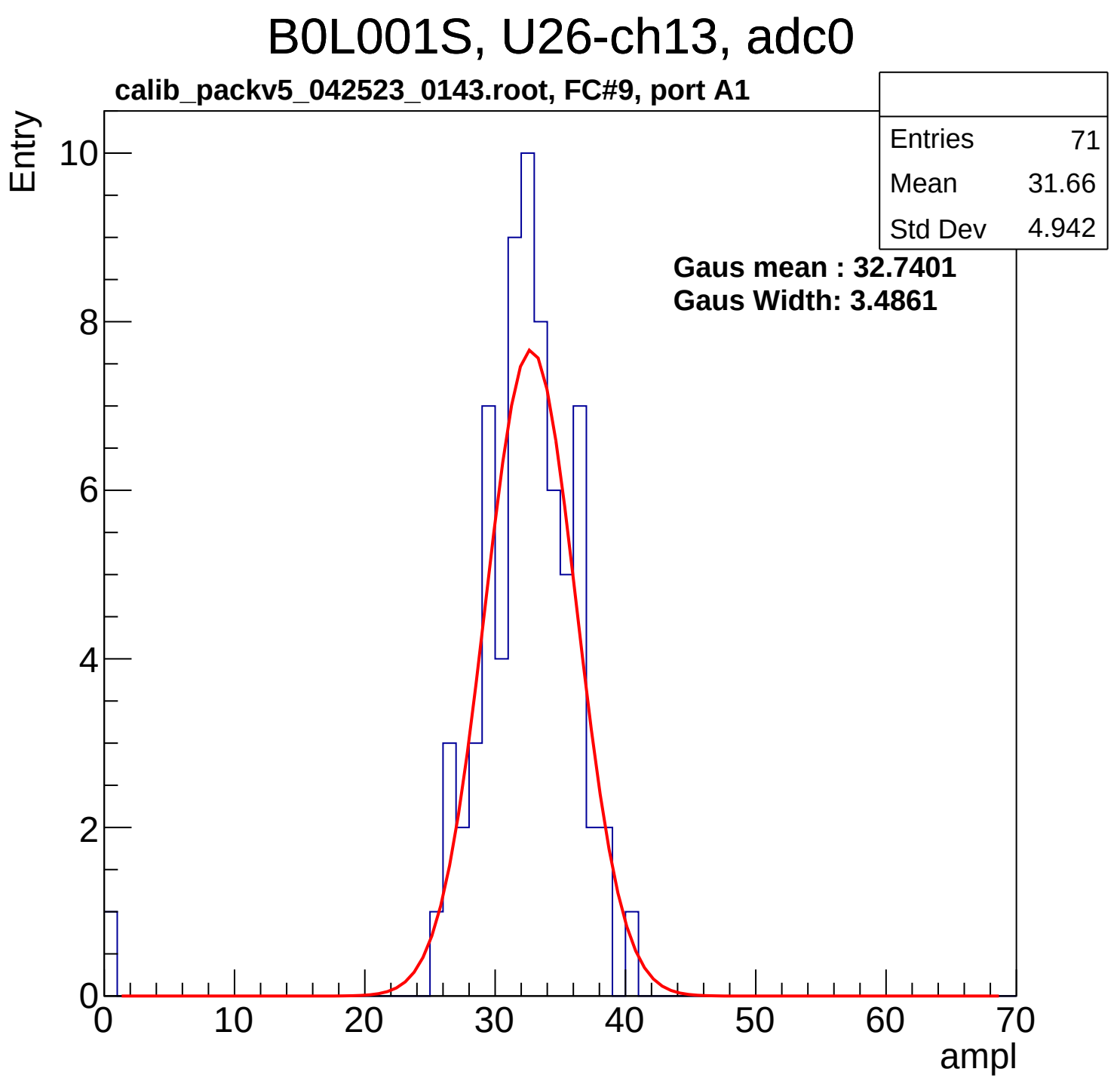

B0L001S, U26-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.66
Std Dev 4.942
Gaus mean : 32.7401
Gaus Width: 3.4861
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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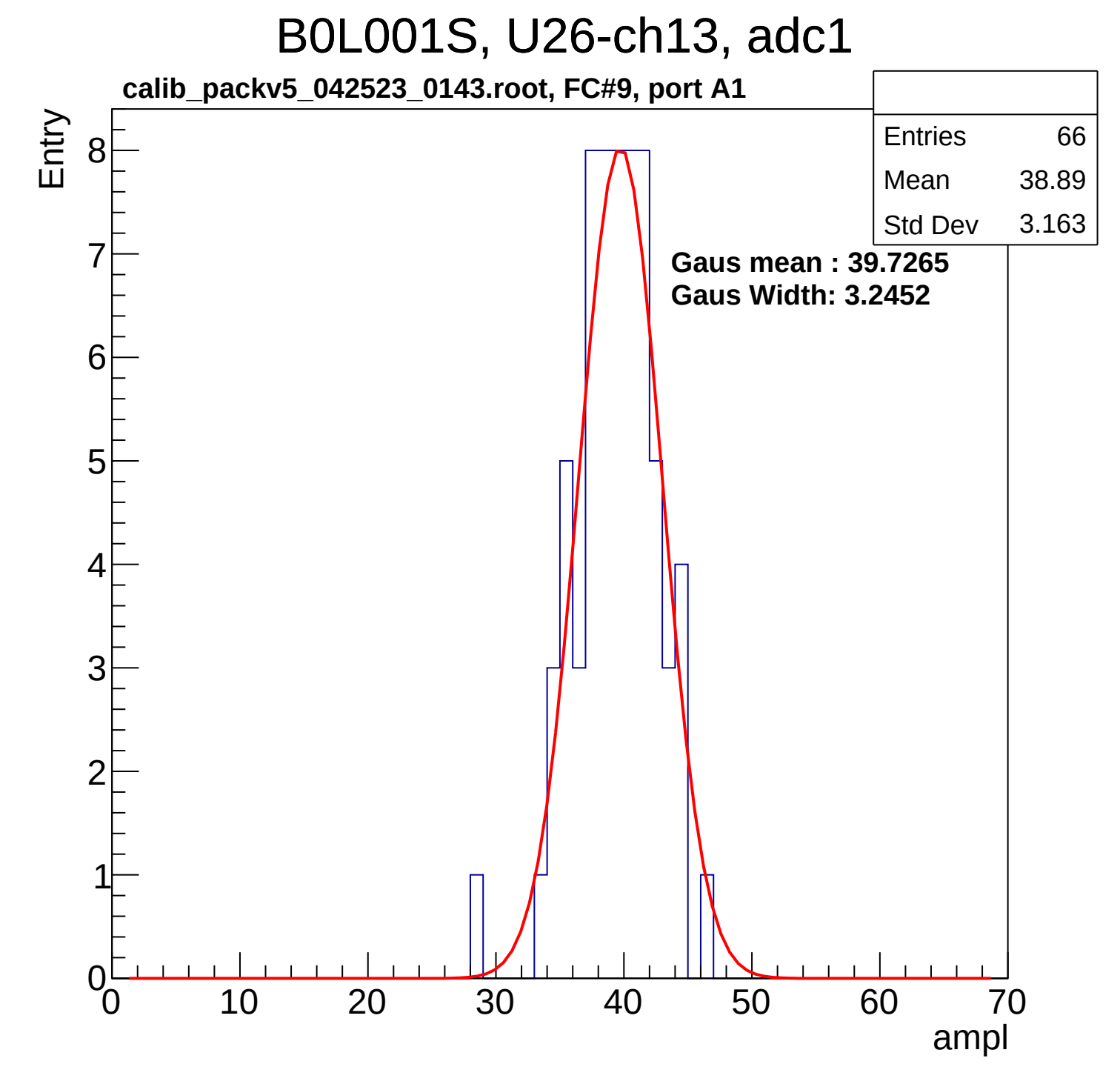
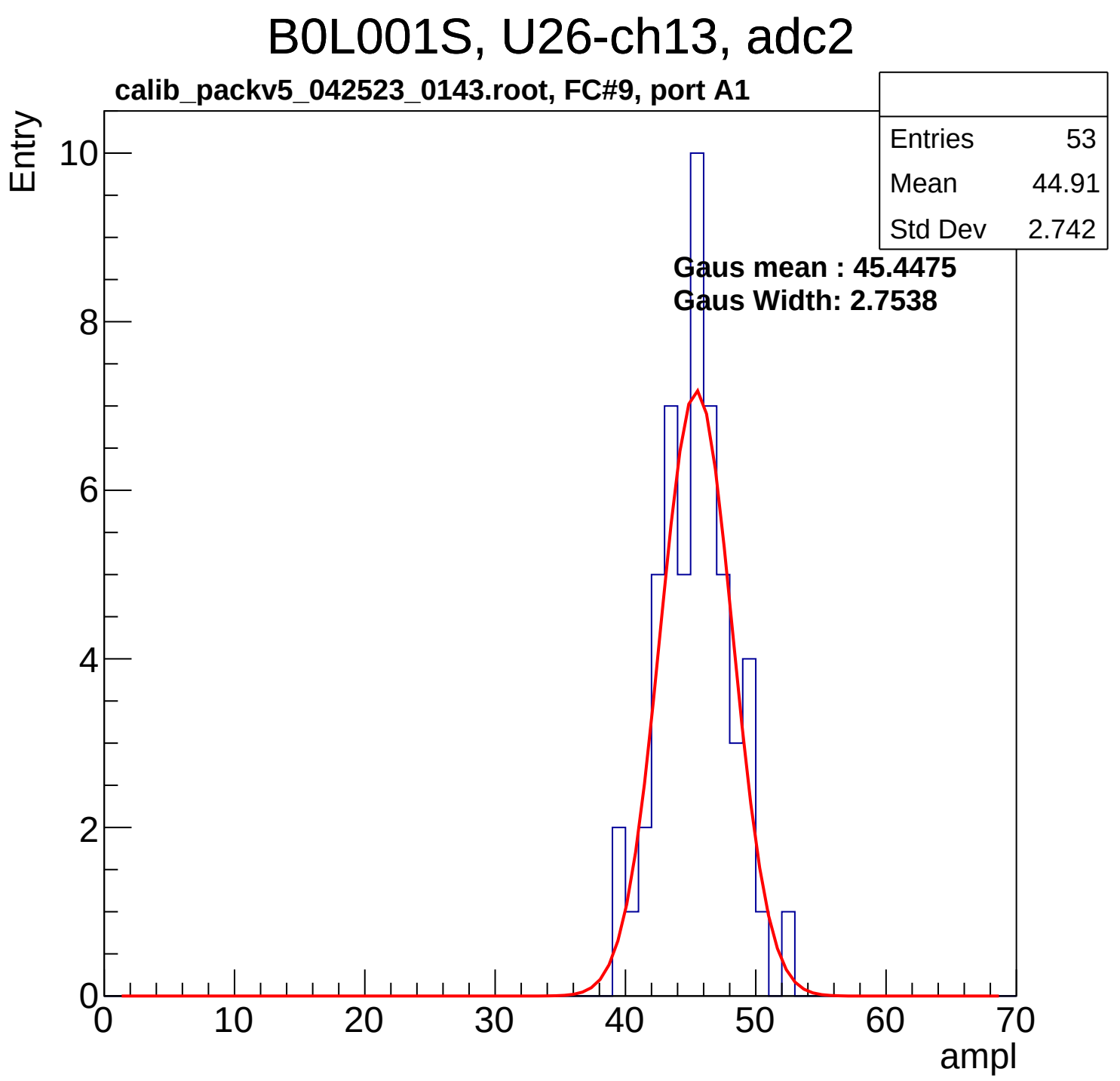
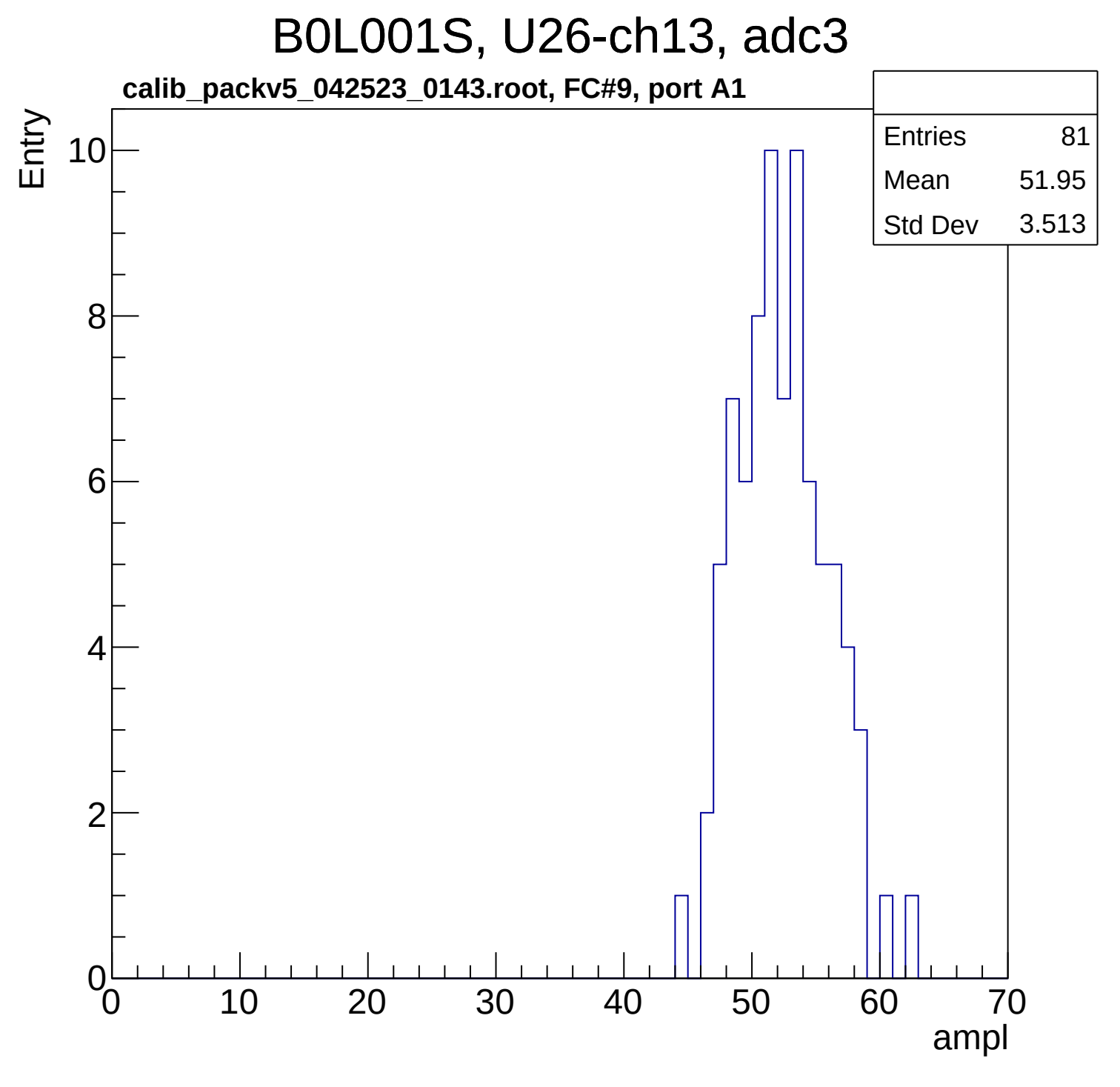
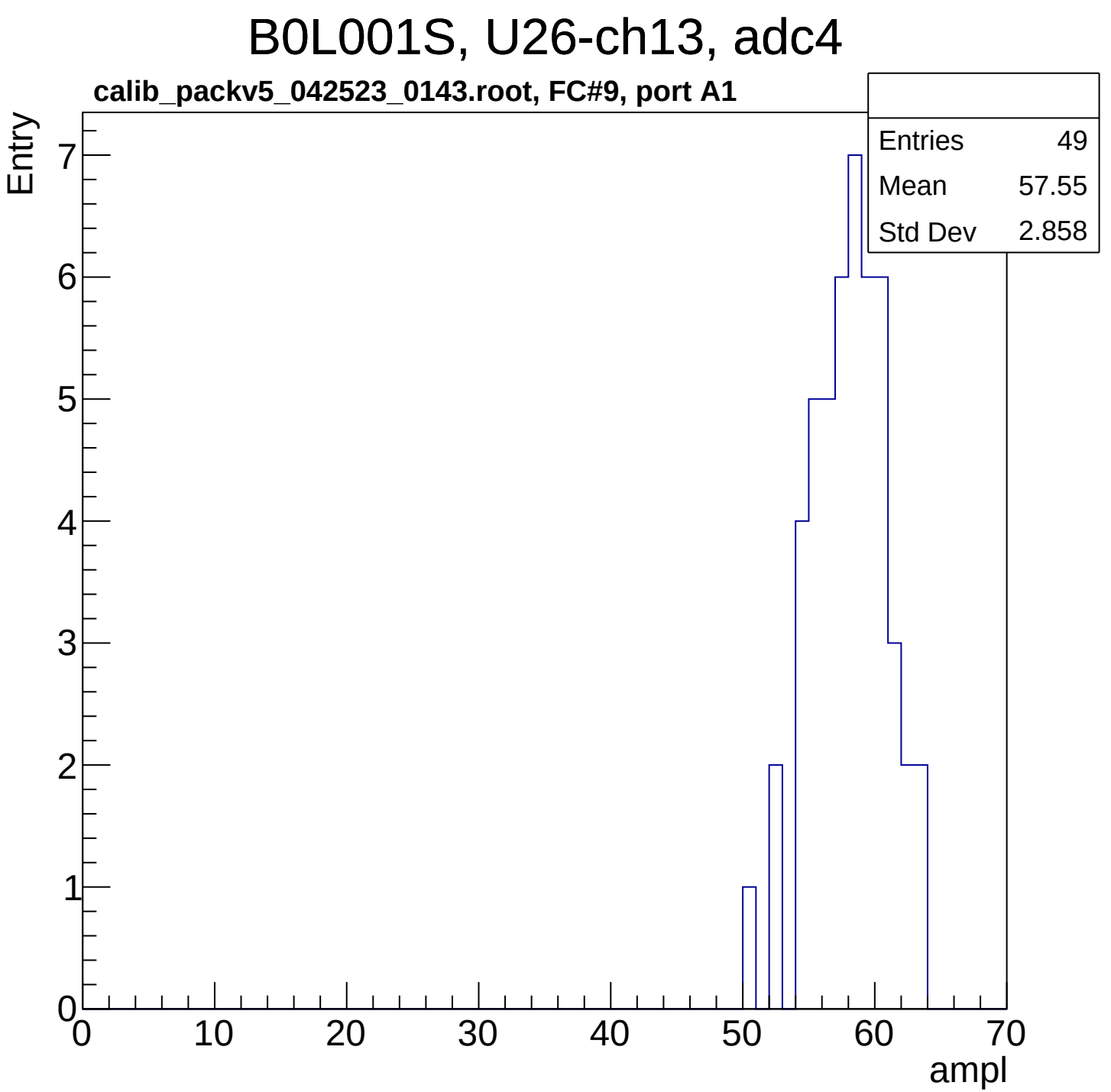

B0L001S, U26-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.89
Std Dev 3.163
Gaus mean : 39.7265
Gaus Width: 3.2452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.91
Std Dev 2.742
Gaus mean : 45.4475
Gaus Width: 2.7538
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 51.95
Std Dev 3.513
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.55
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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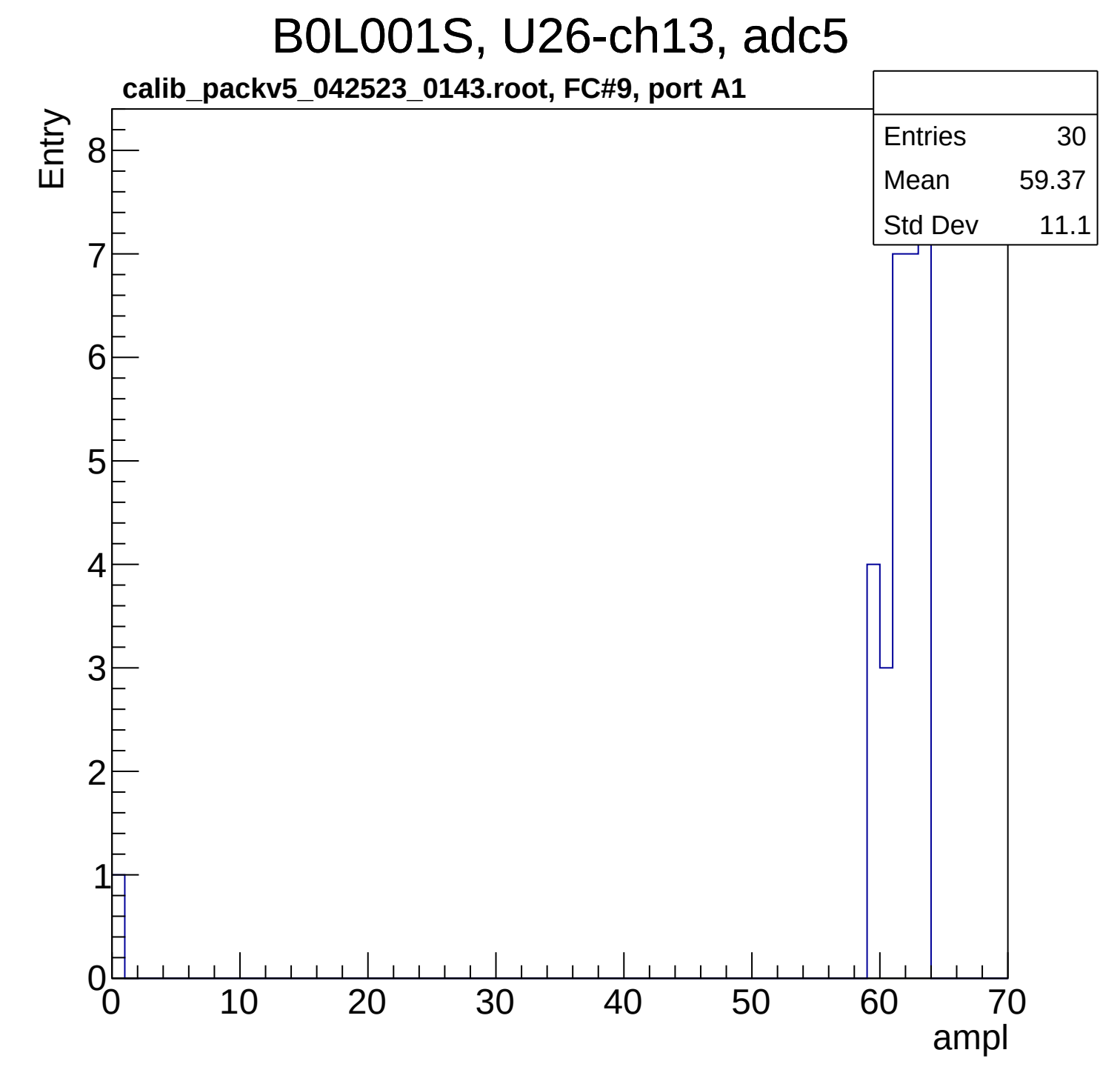
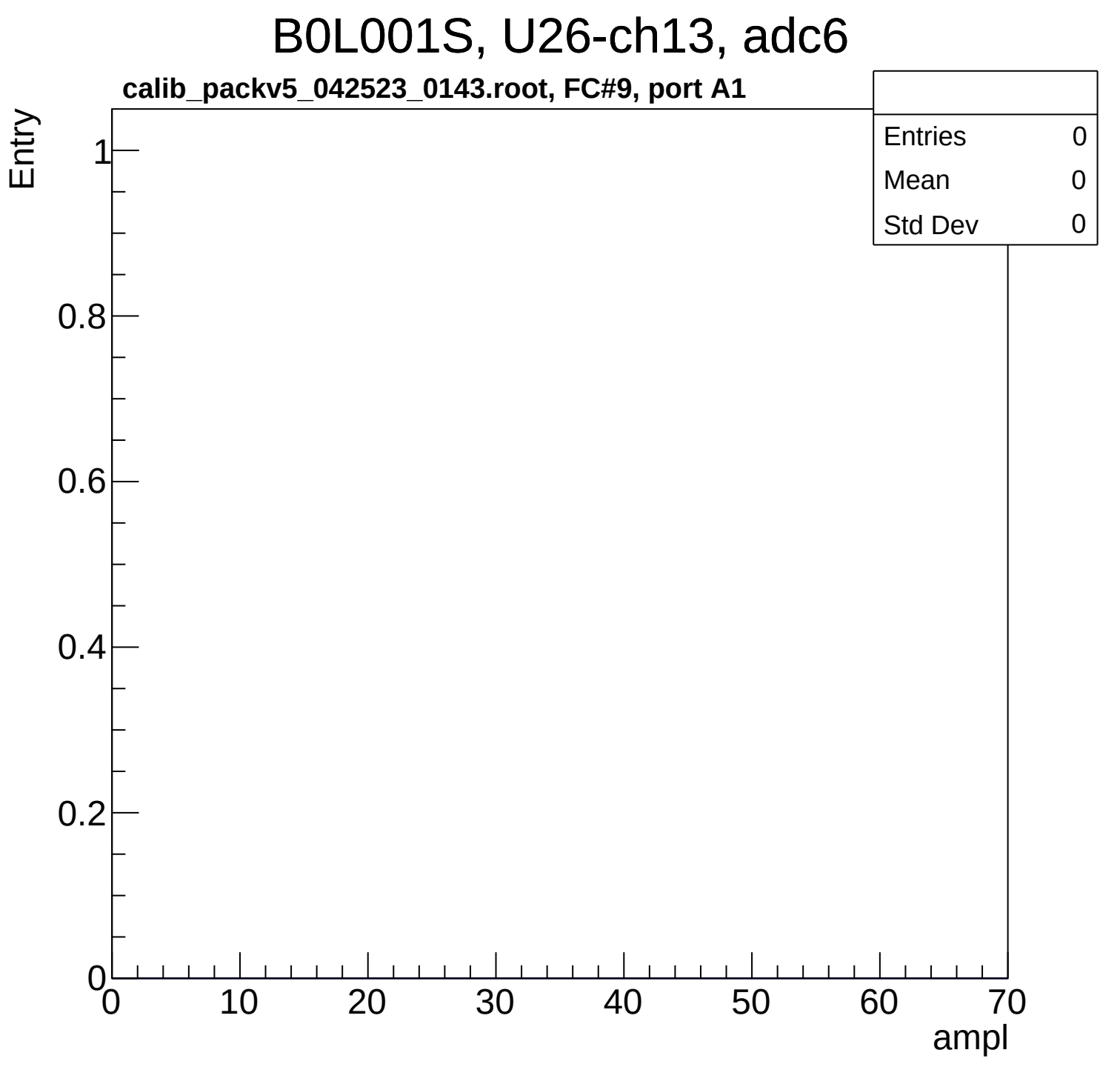
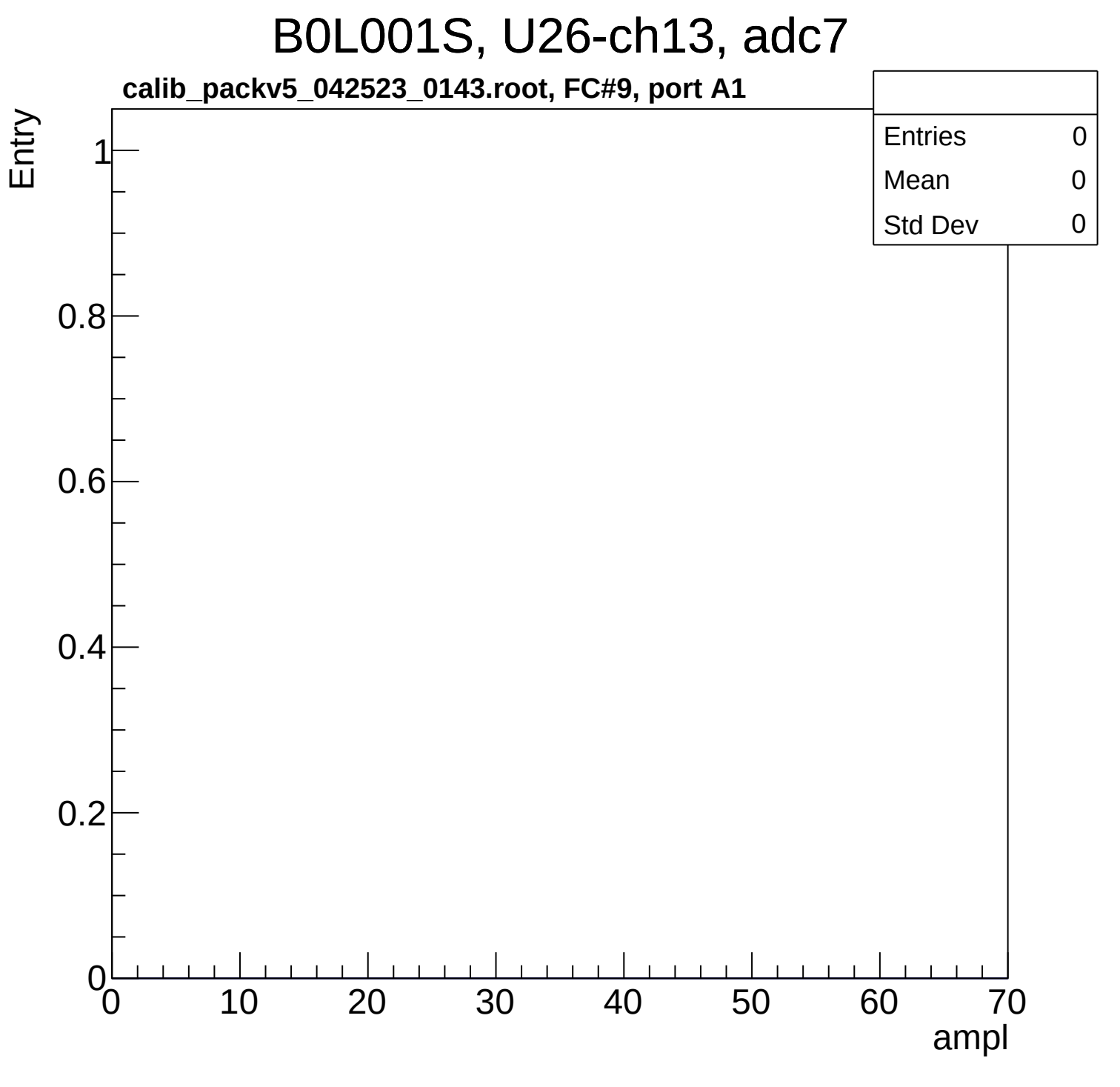

B0L001S, U26-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59.37
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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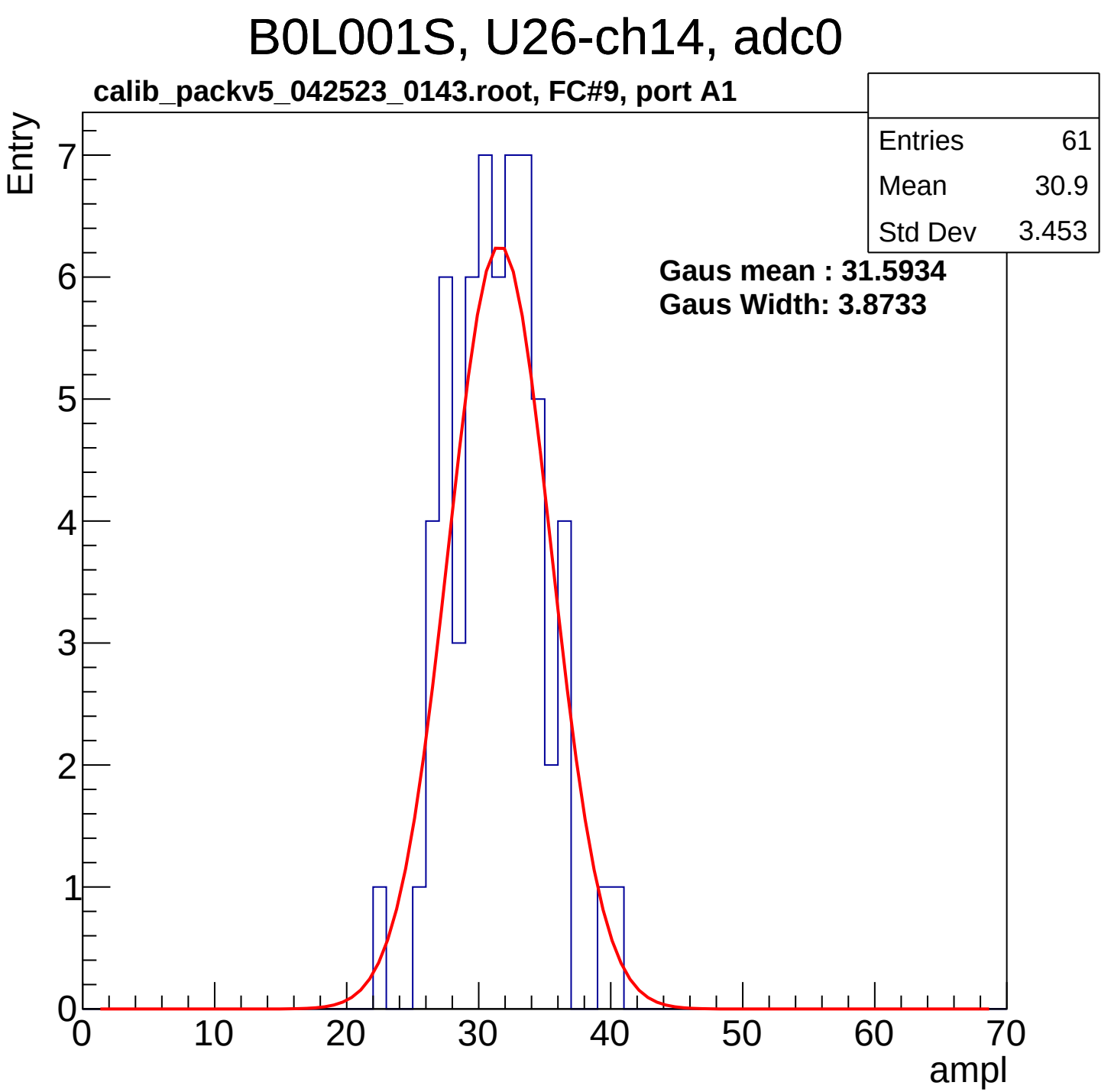
30
40
50
60
70
ampl


B0L001S, U26-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.9
Std Dev 3.453
Gaus mean : 31.5934
Gaus Width: 3.8733
Entry
ampl
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1
2
3
4
5
6
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0
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20
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60
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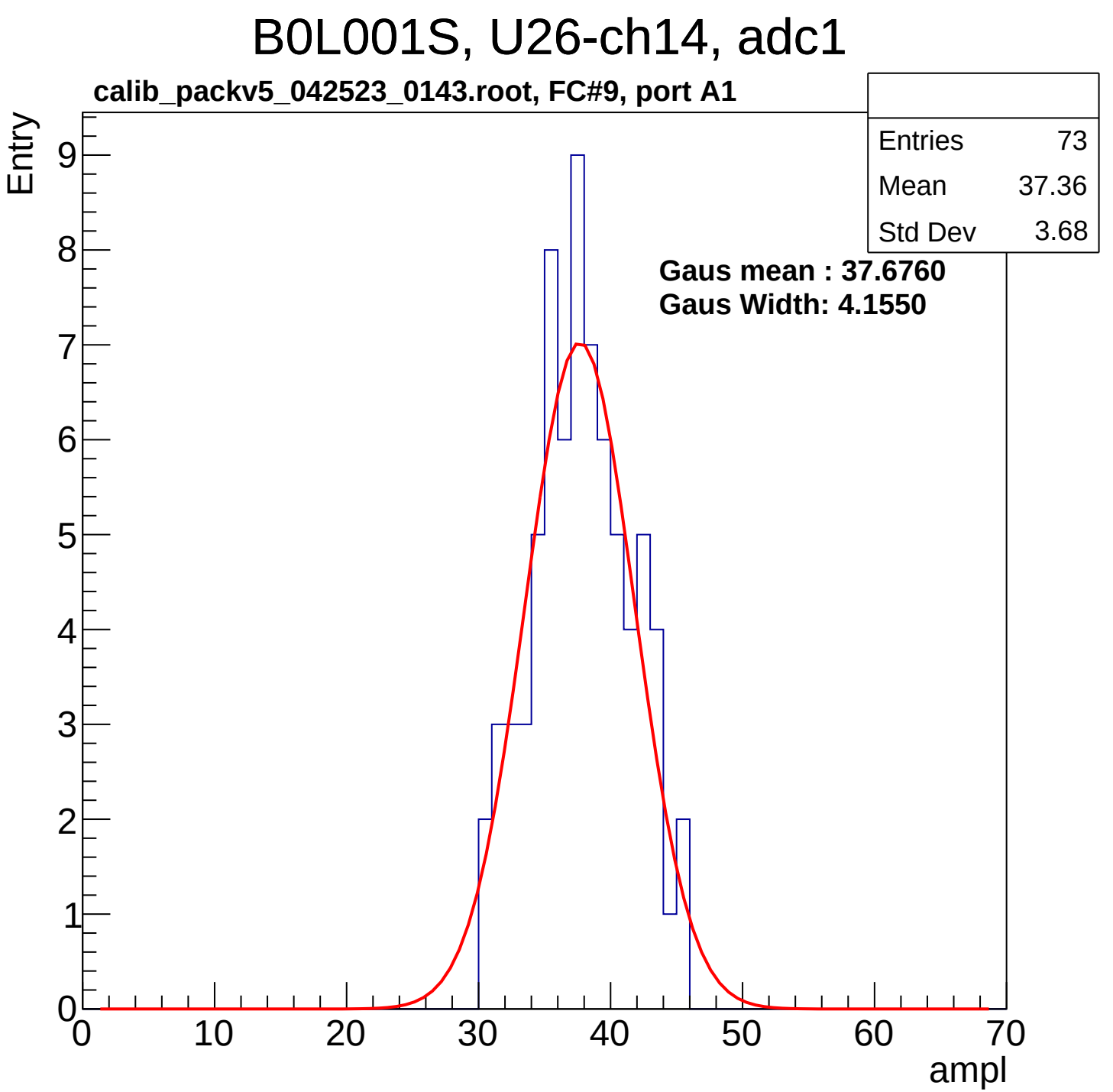
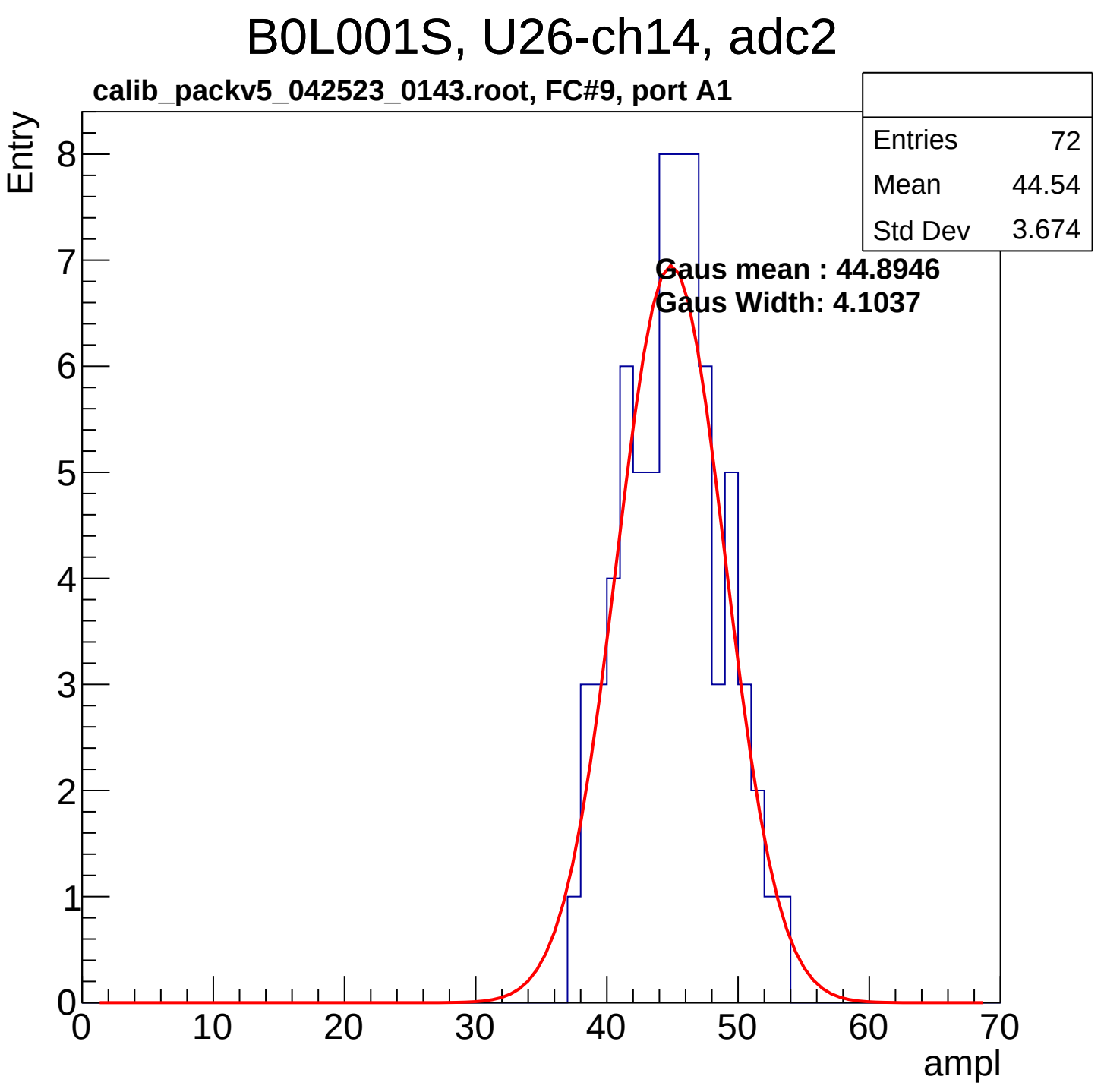

B0L001S, U26-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.36
Std Dev 3.68
Gaus mean : 37.6760
Gaus Width: 4.1550
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.54
Std Dev 3.674
Gaus mean : 44.8946
Gaus Width: 4.1037
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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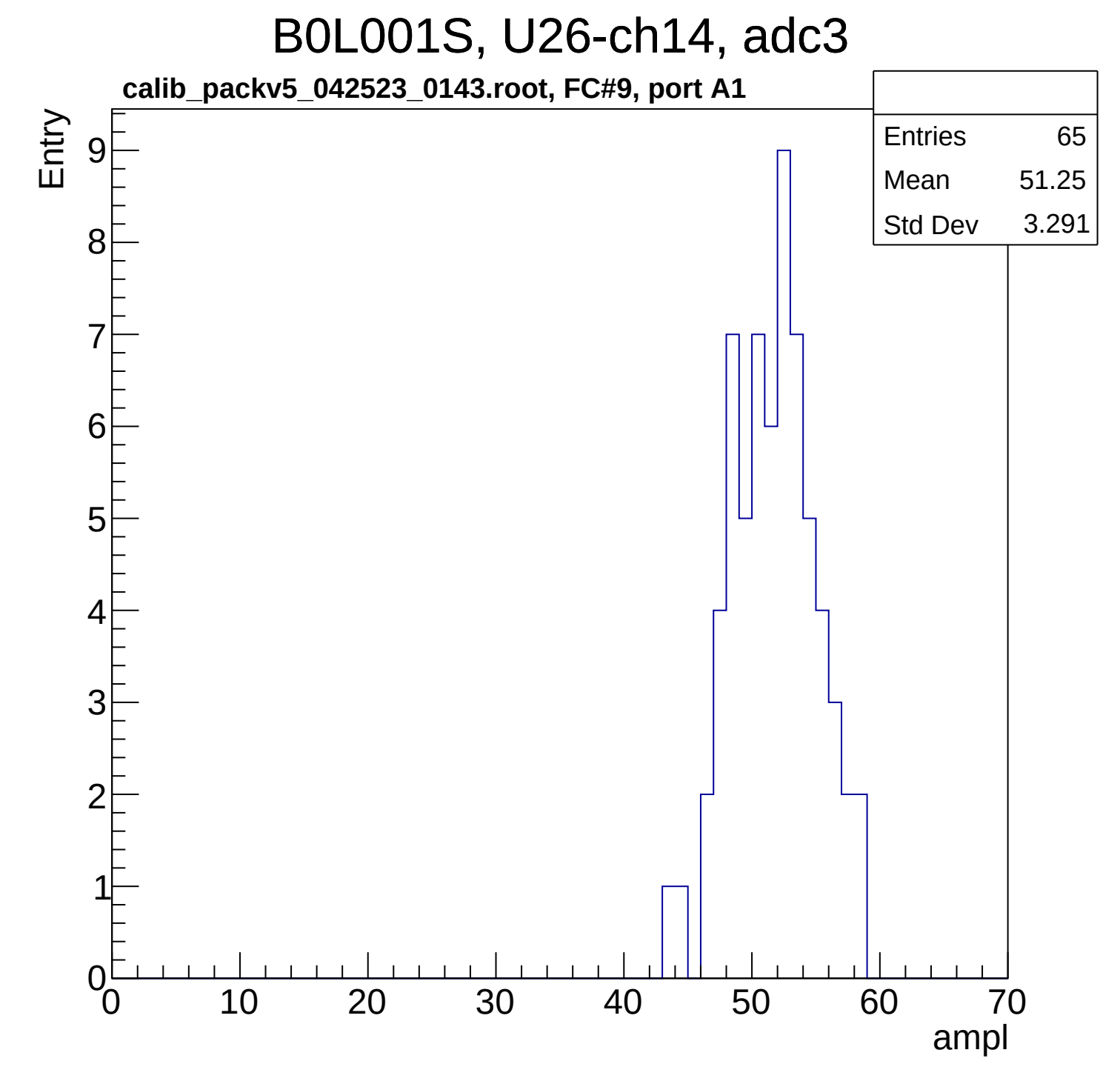
B0L001S, U26-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.25
Std Dev 3.291
Entry
ampl
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4
5
6
7
8
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0
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60
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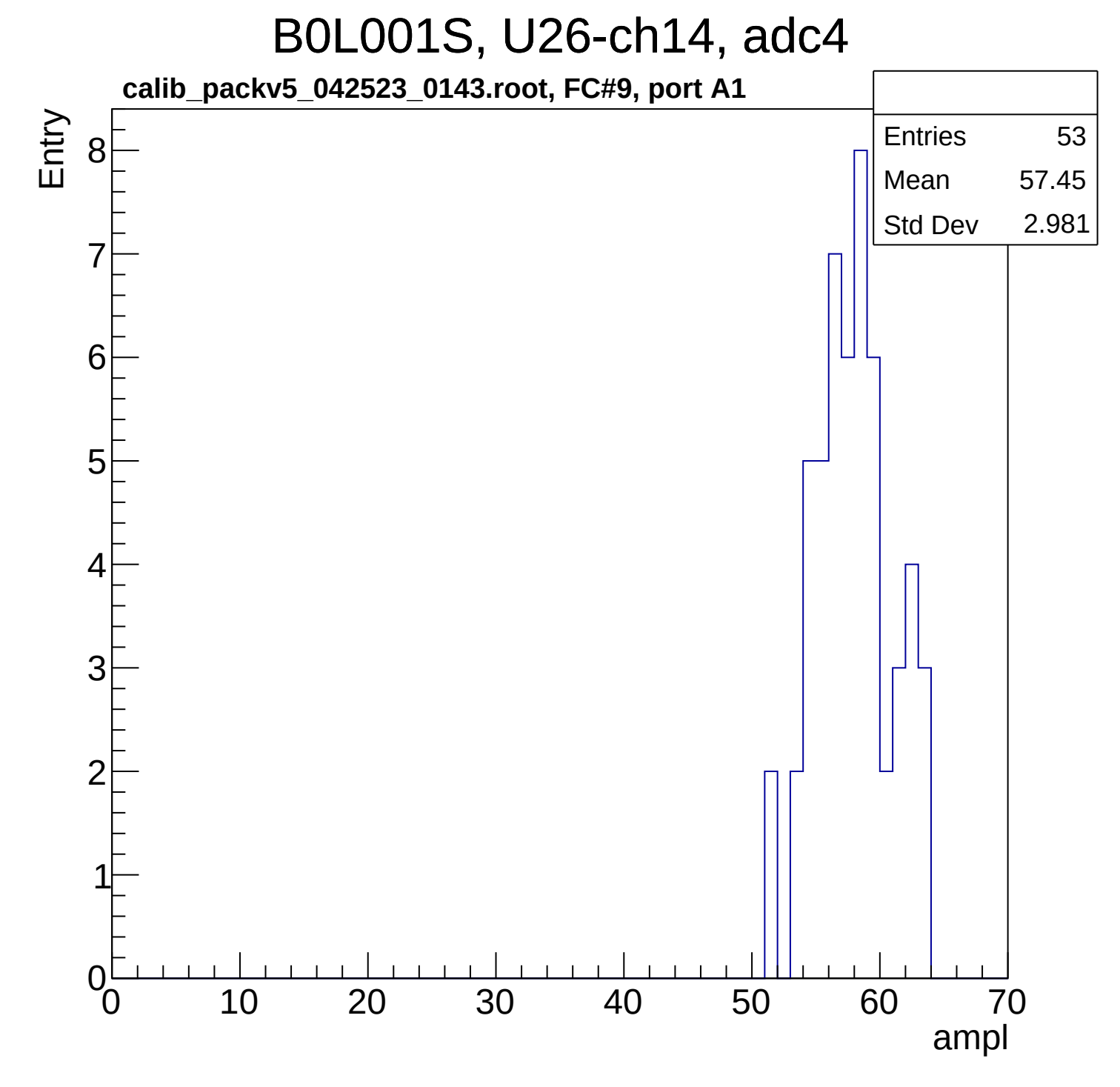
B0L001S, U26-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.45
Std Dev 2.981
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
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40
50
60
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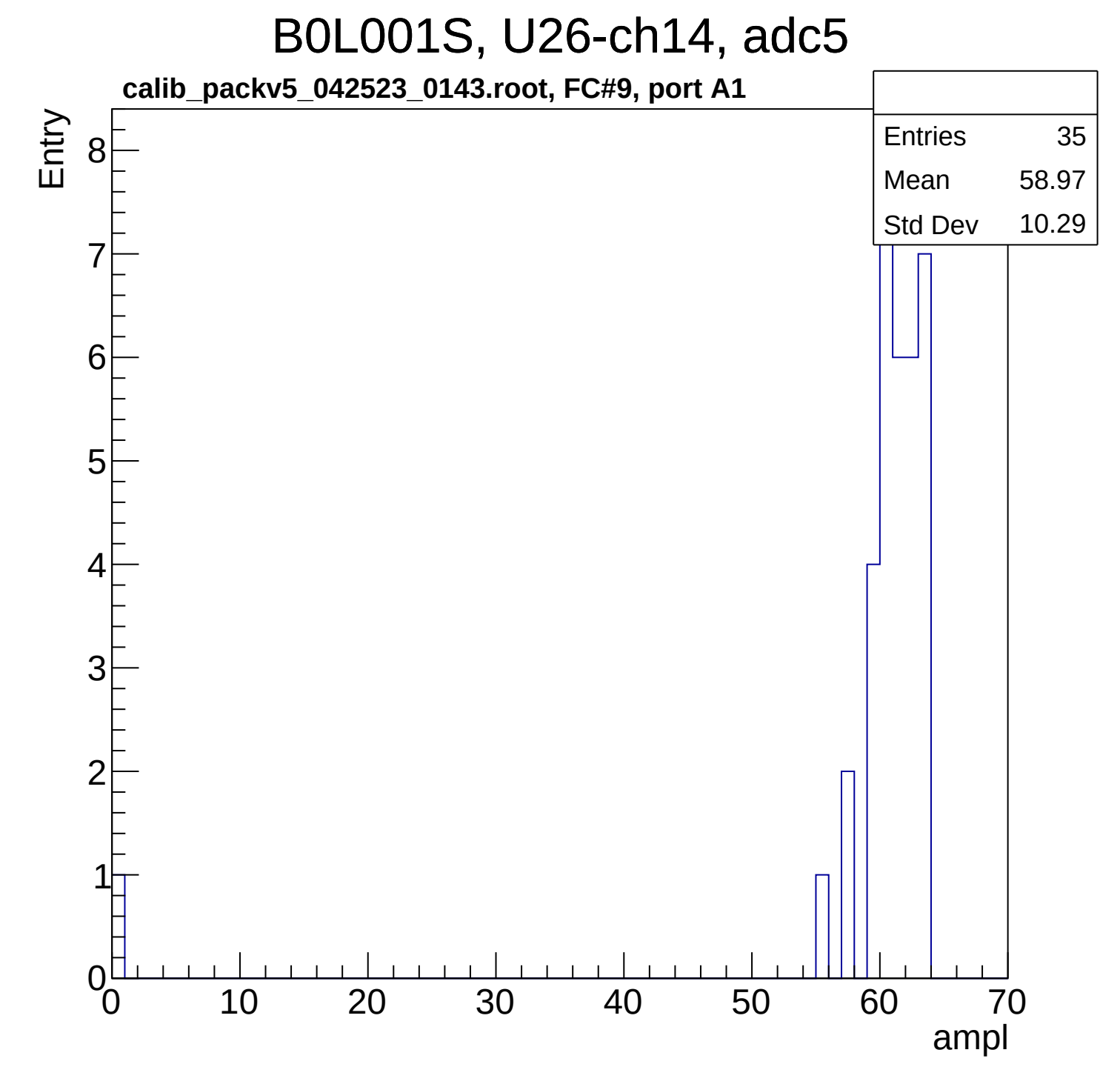
B0L001S, U26-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.97
Std Dev 10.29
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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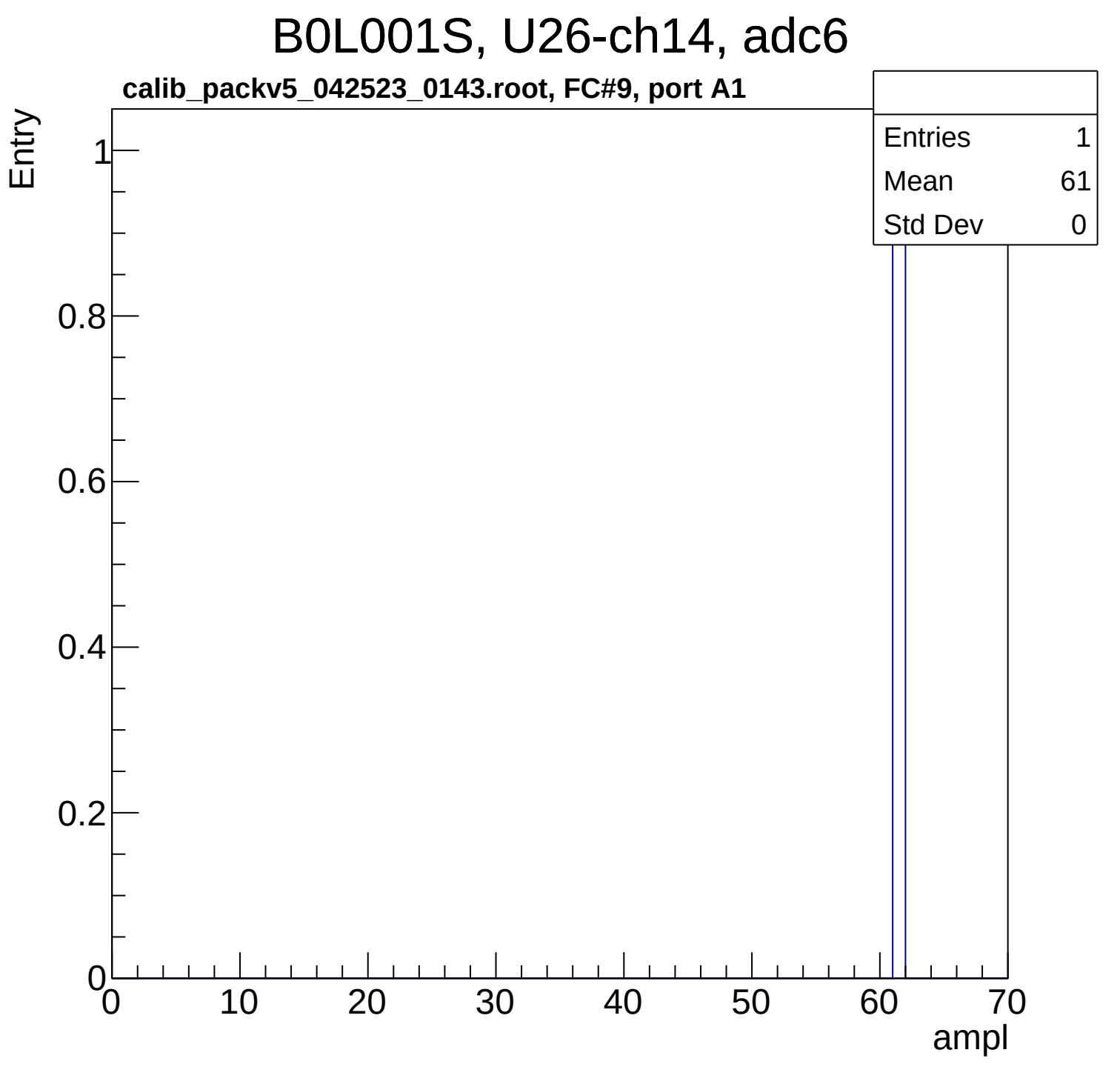
B0L001S, U26-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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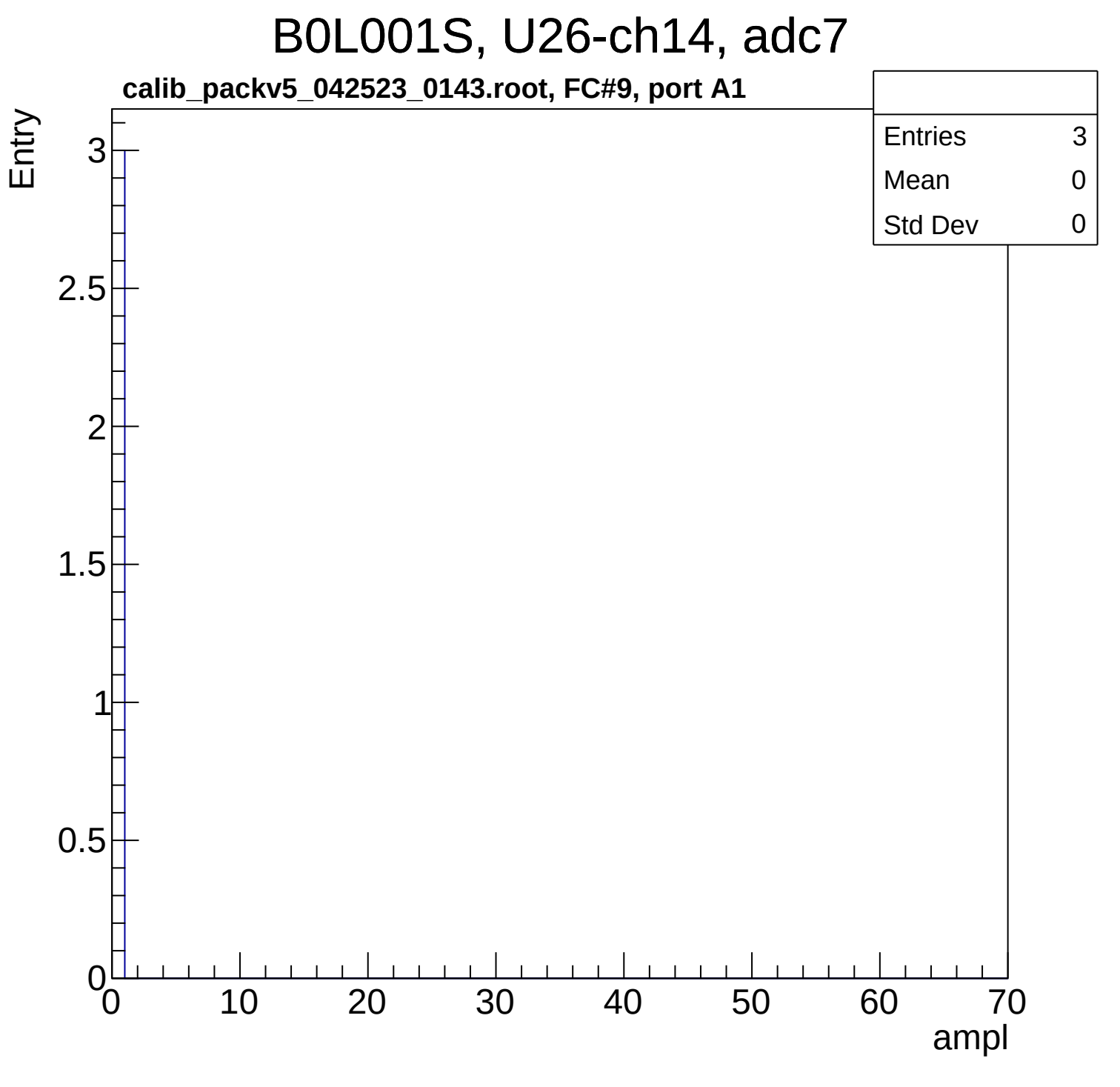
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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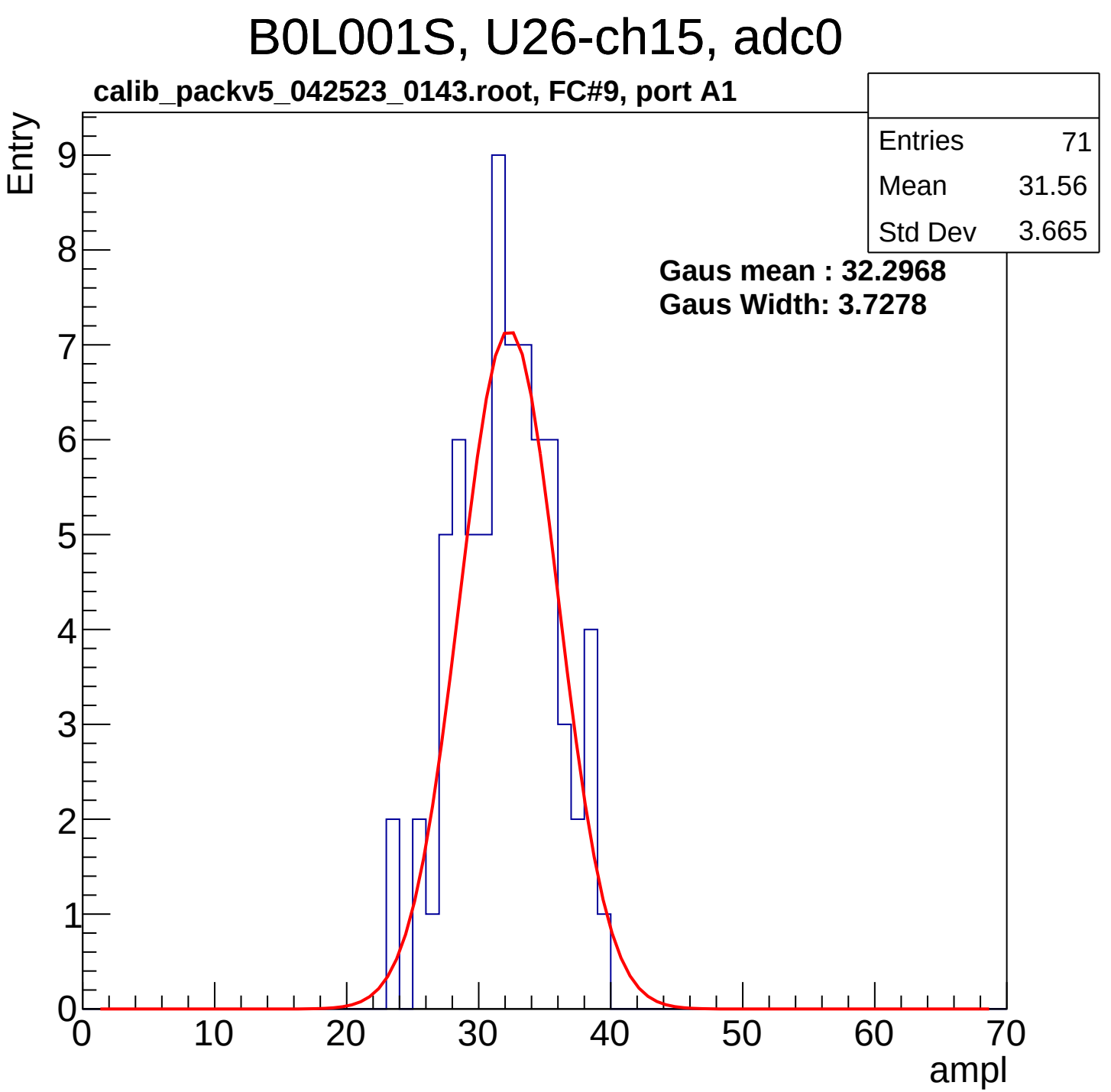
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.56
Std Dev 3.665
Gaus mean : 32.2968
Gaus Width: 3.7278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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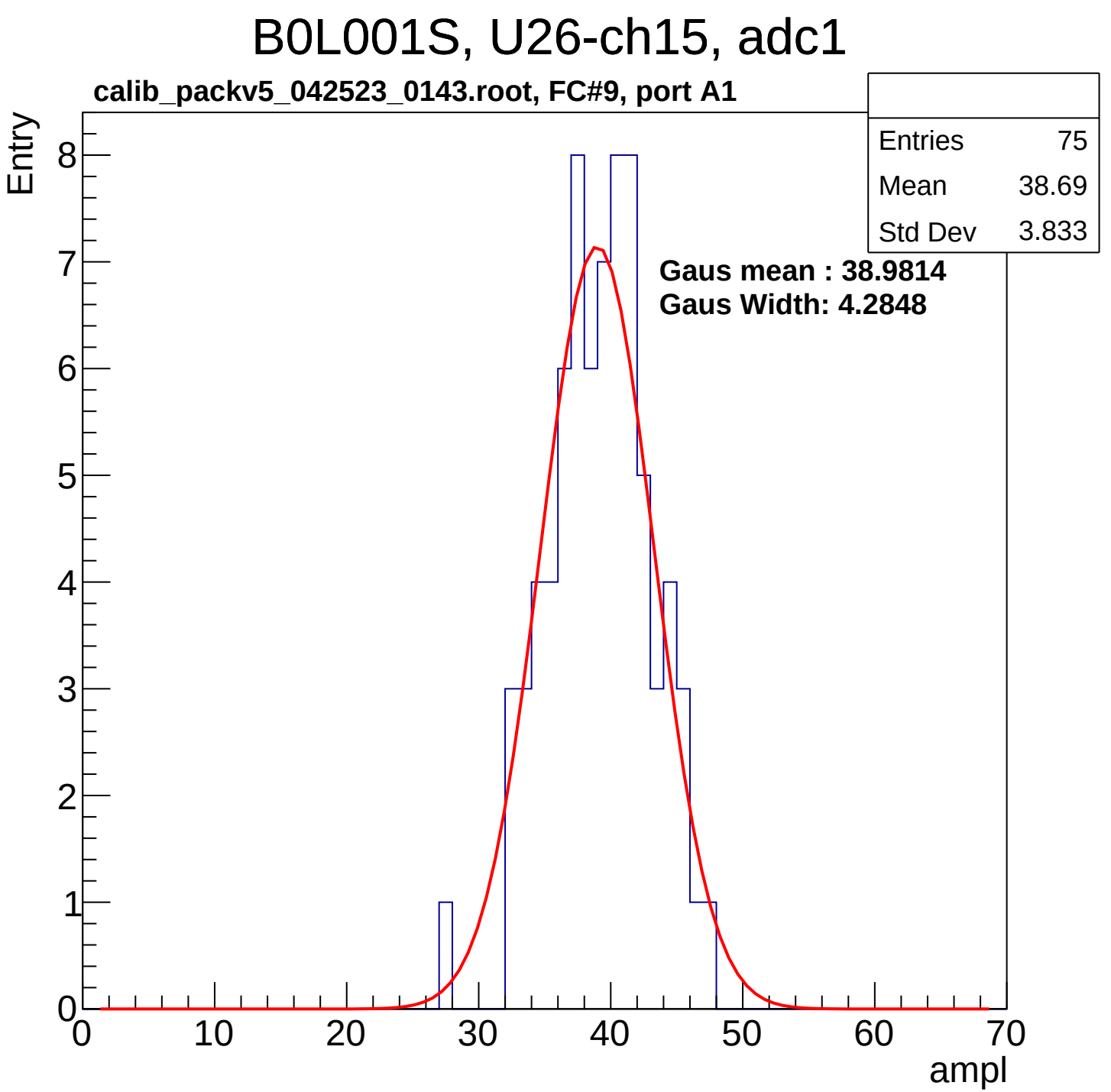
B0L001S, U26-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.69
Std Dev 3.833
Gaus mean : 38.9814
Gaus Width: 4.2848
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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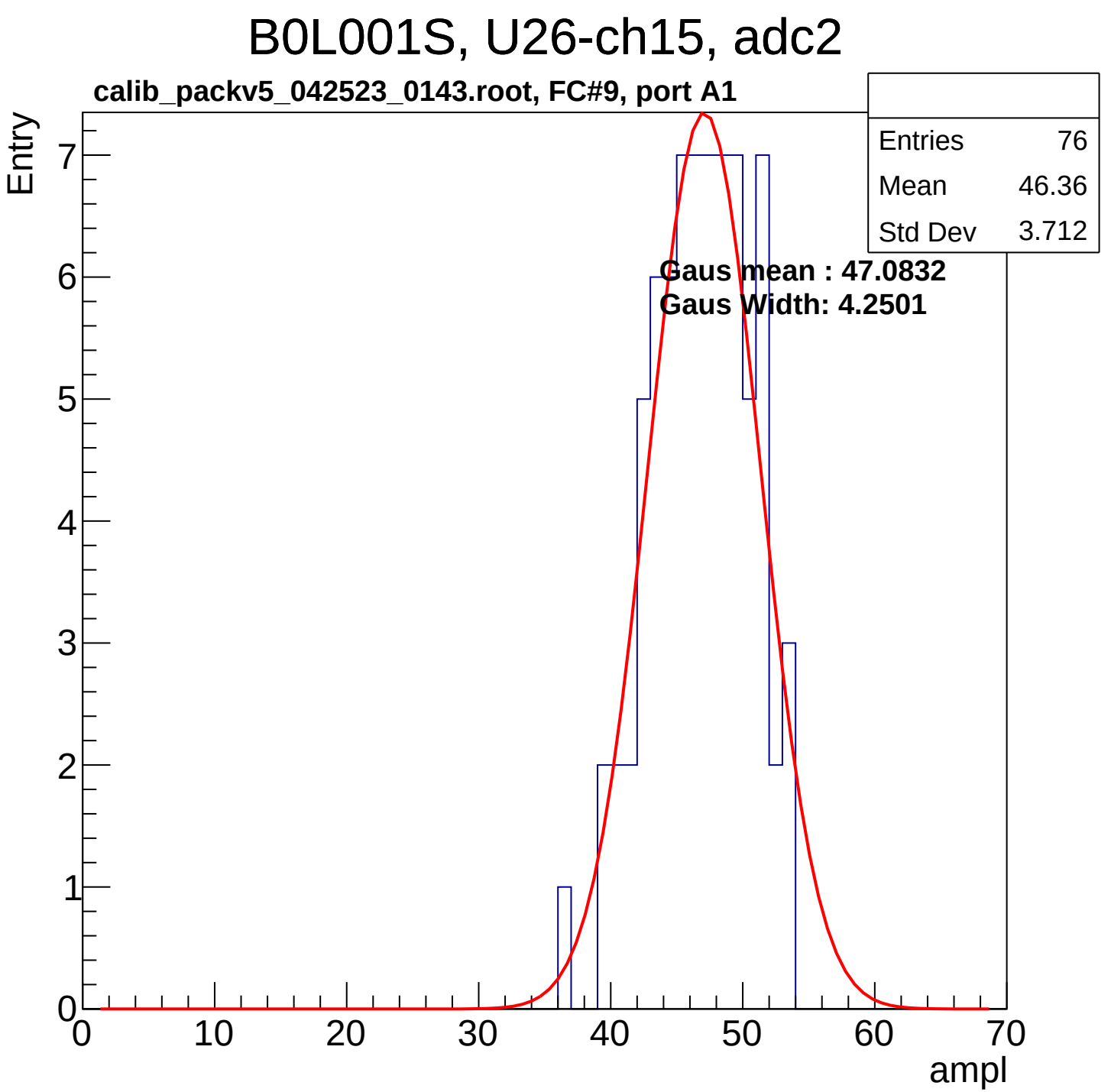
B0L001S, U26-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 46.36
Std Dev 3.712
Gaus mean : 47.0832
Gaus Width: 4.2501
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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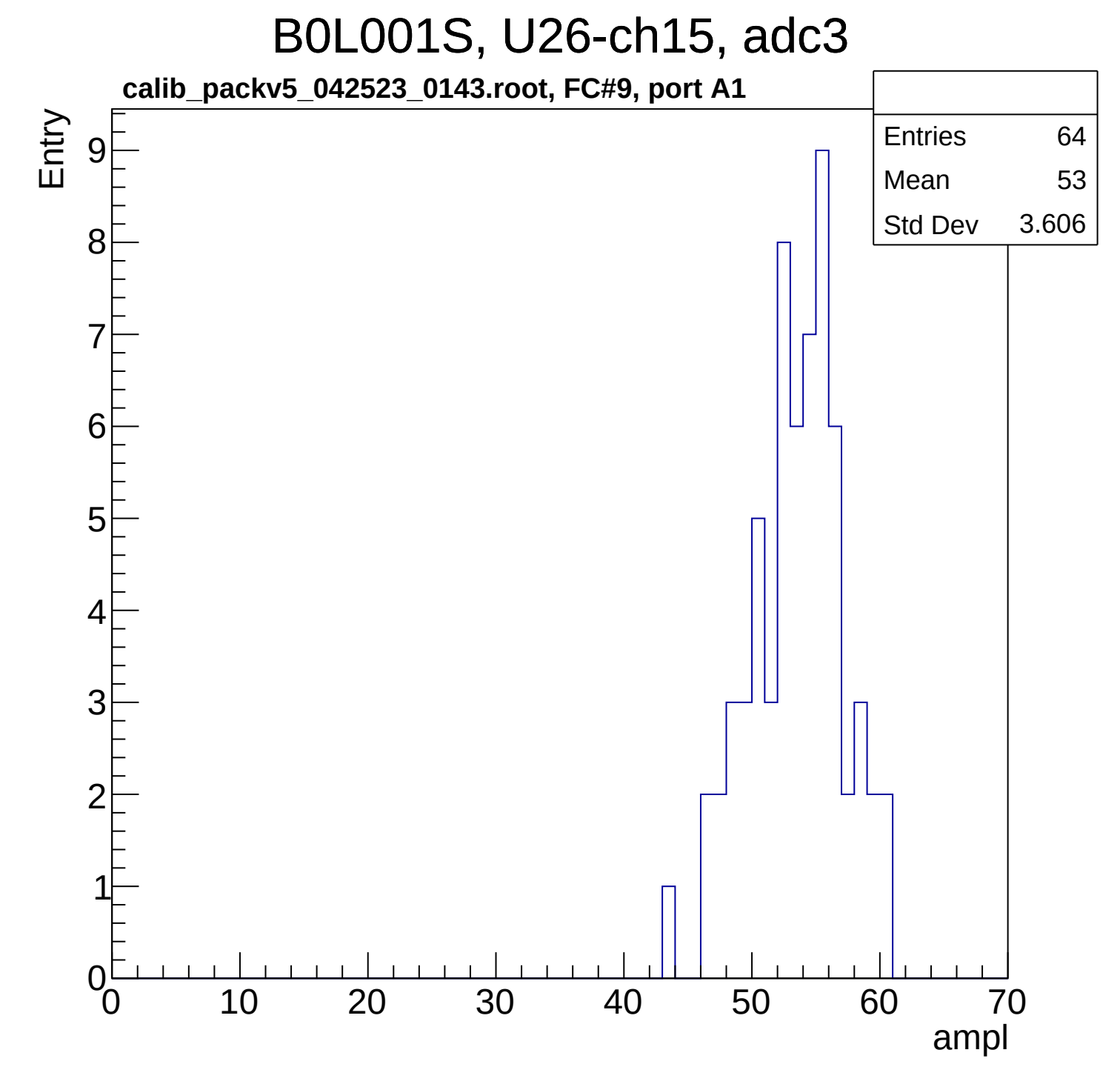
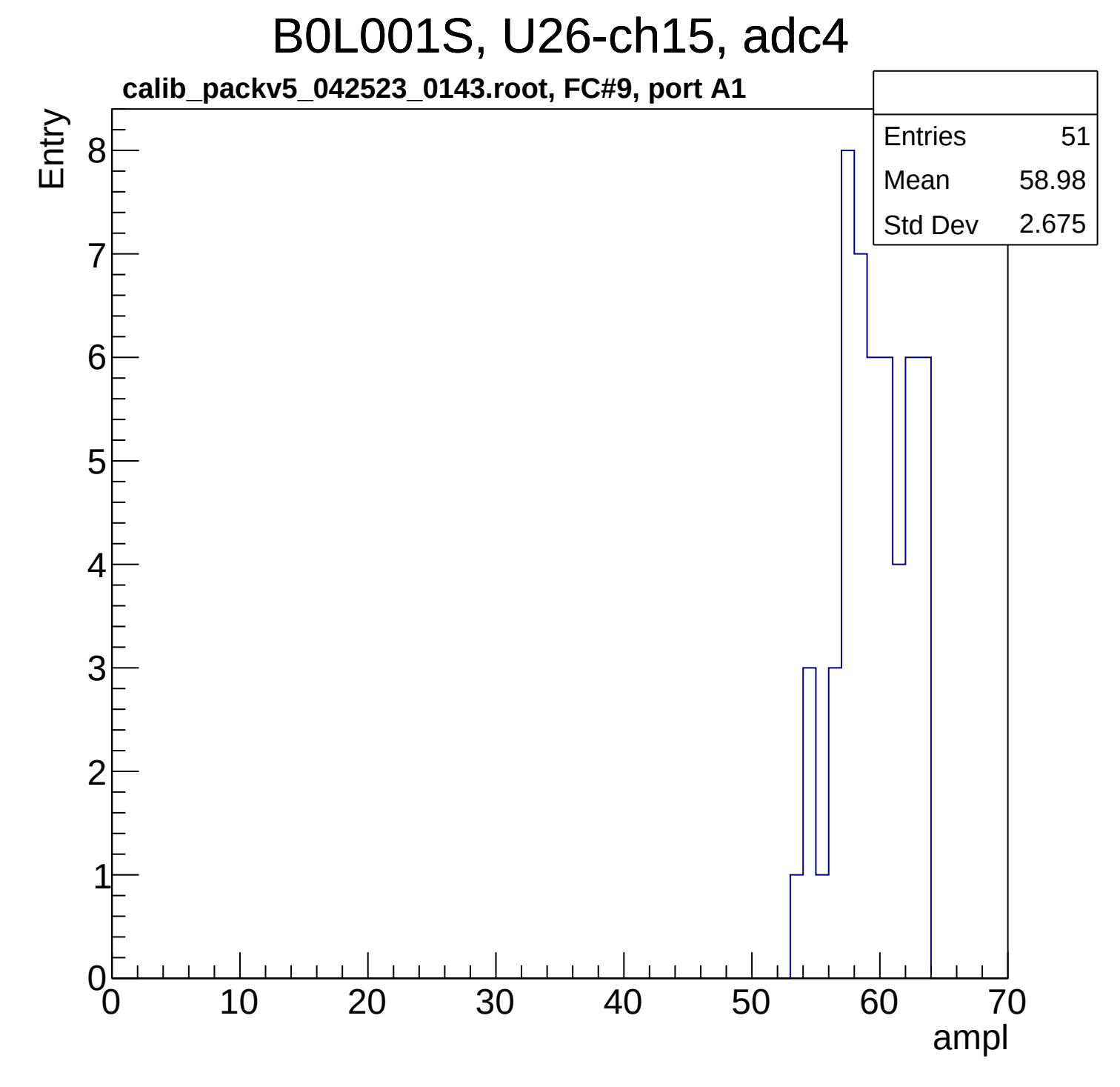

B0L001S, U26-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 53
Std Dev 3.606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.98
Std Dev 2.675
Entry
ampl
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1
2
3
4
5
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60
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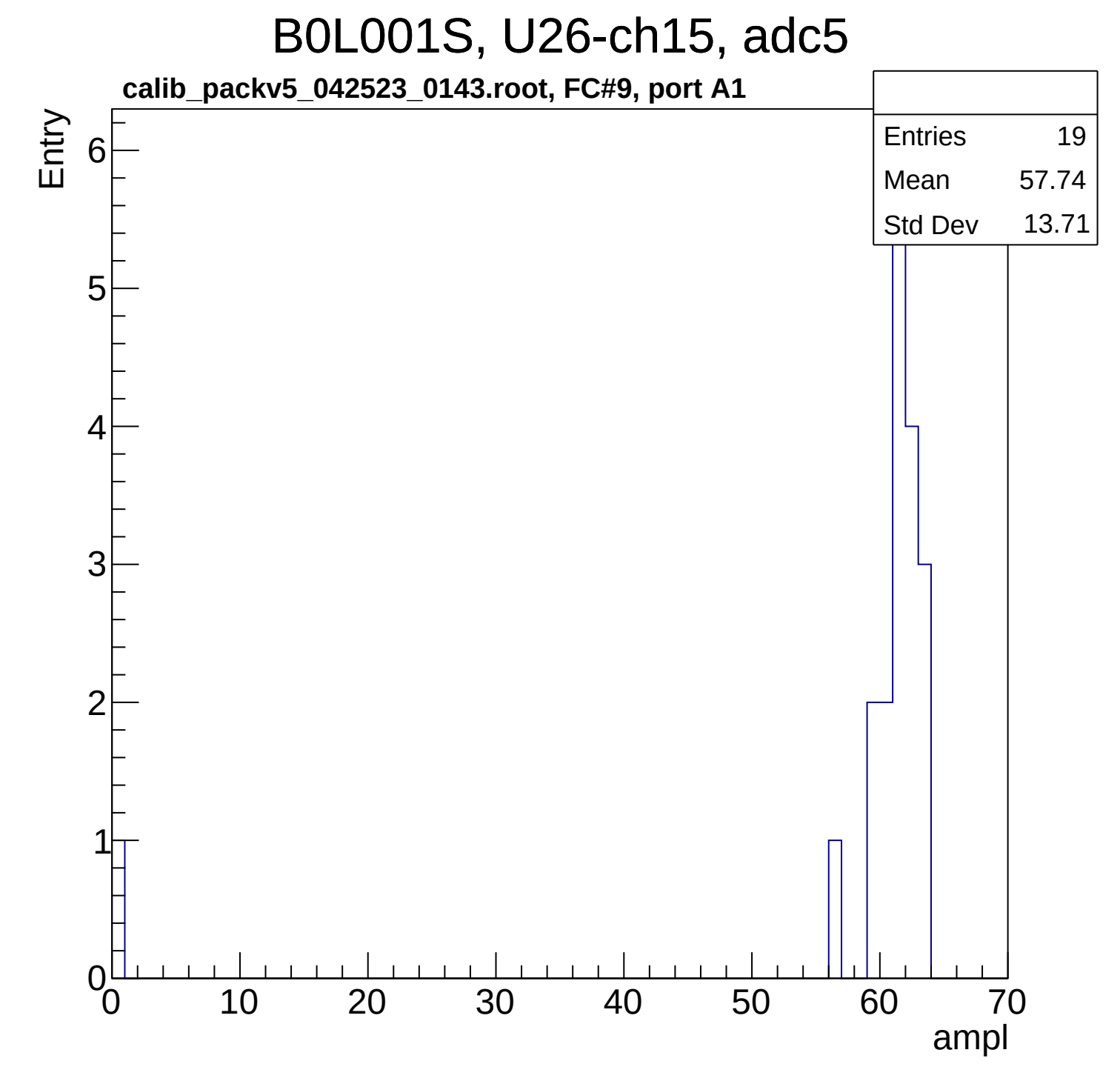
B0L001S, U26-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 57.74
Std Dev 13.71
Entry
0
1
2
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4
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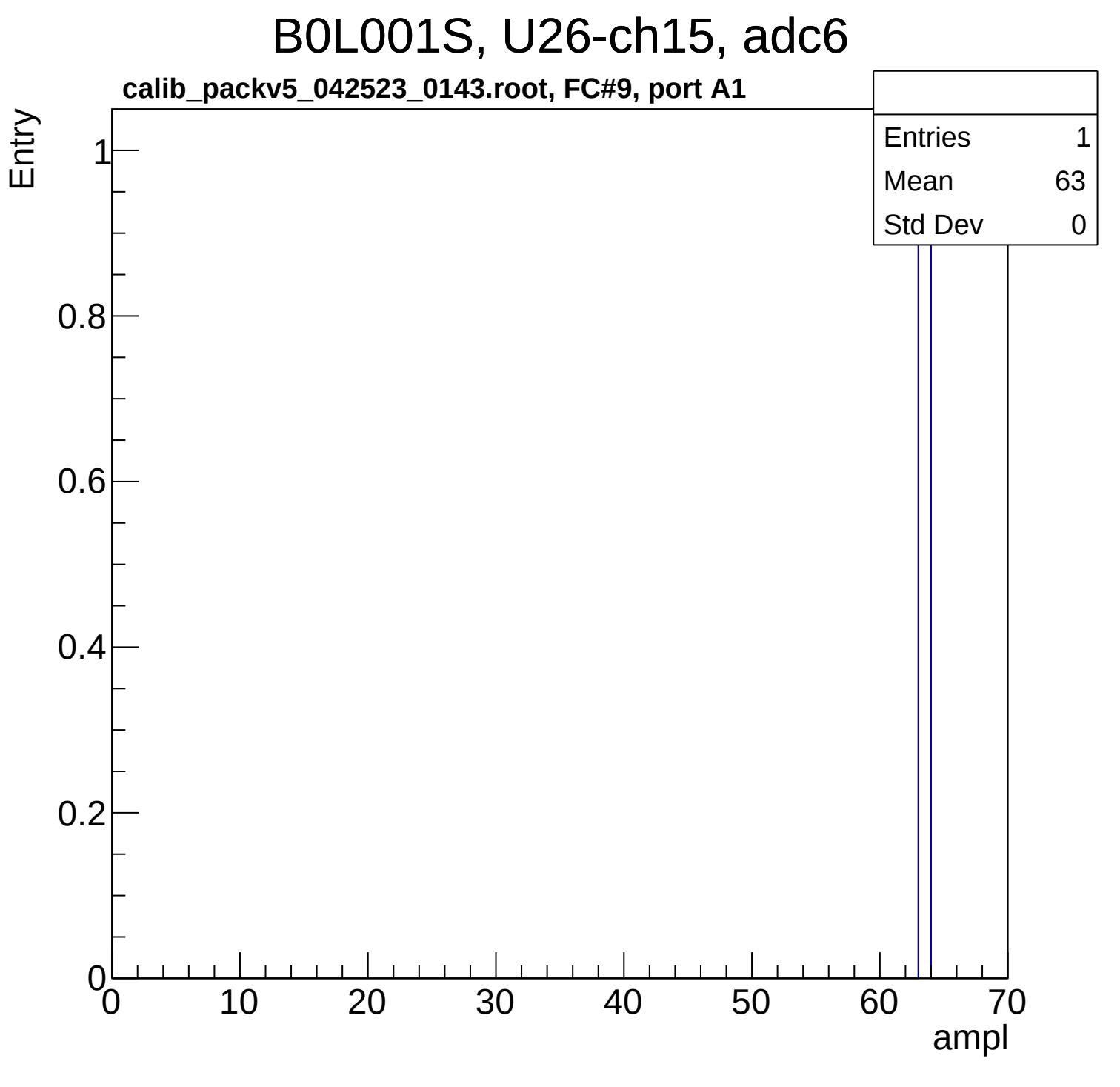
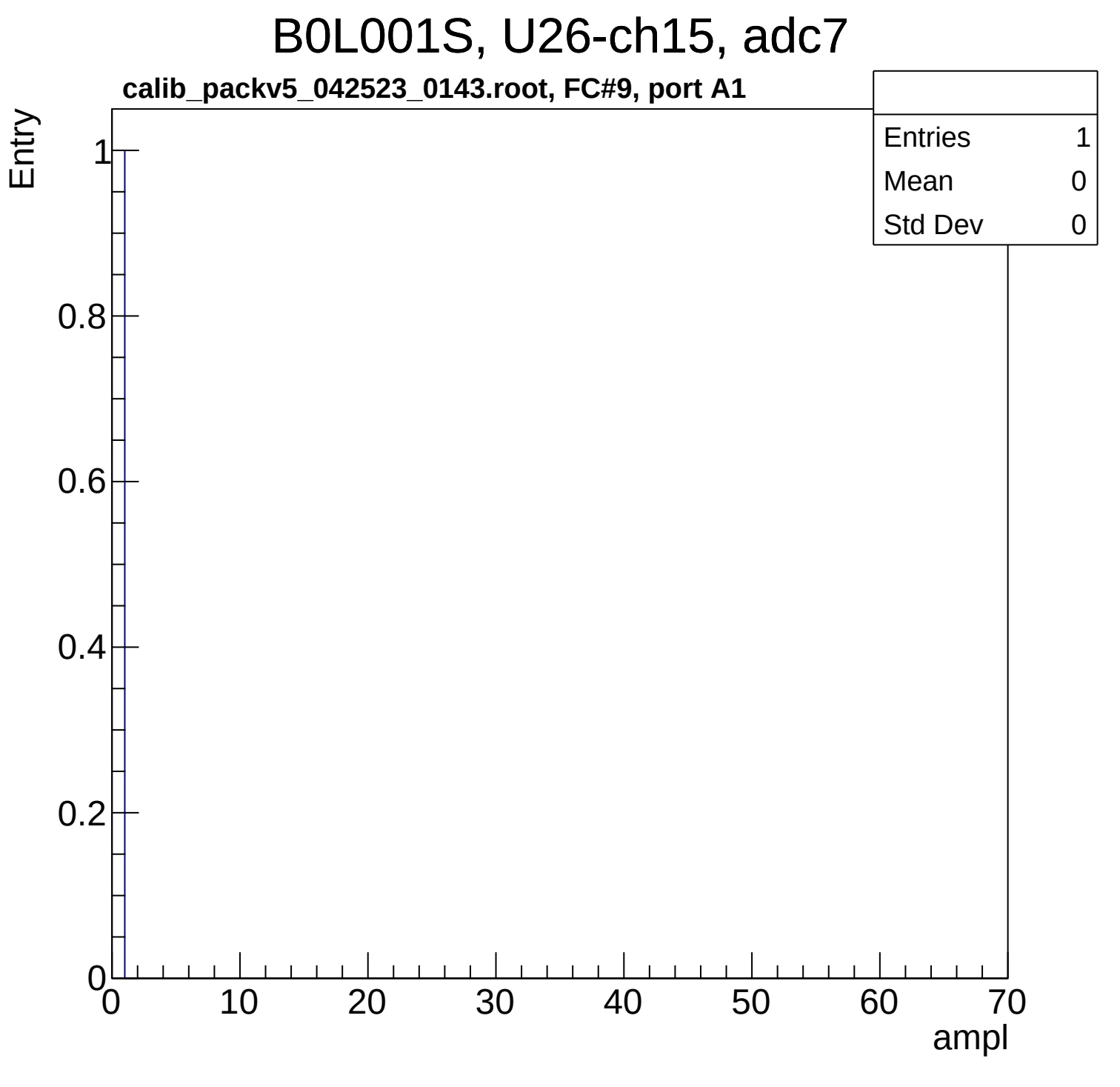
60
70
ampl


B0L001S, U26-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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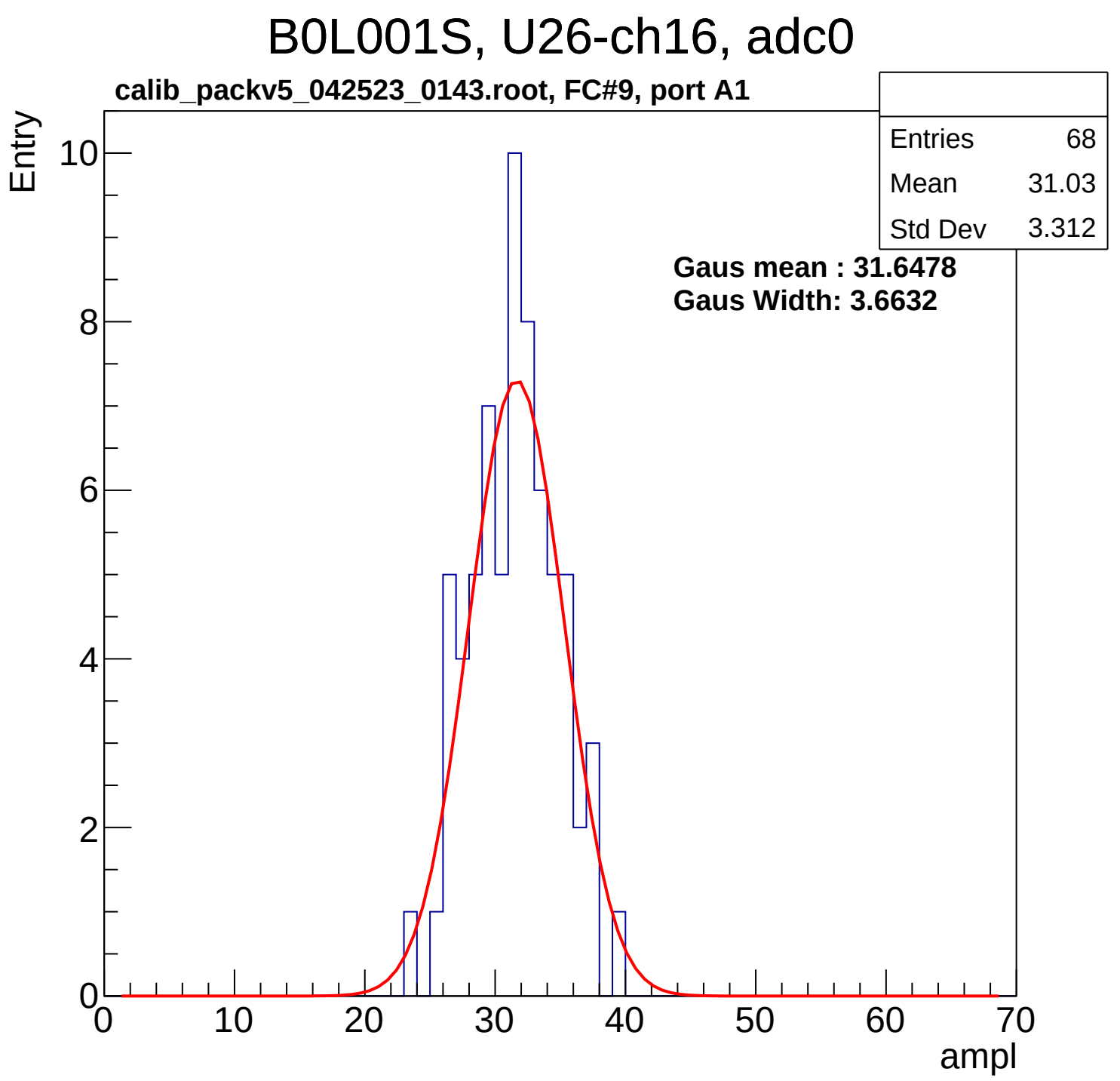
50
60
70
ampl


B0L001S, U26-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.03
Std Dev 3.312
Gaus mean : 31.6478
Gaus Width: 3.6632
Entry
ampl
0
2
4
6
8
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0
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40
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60
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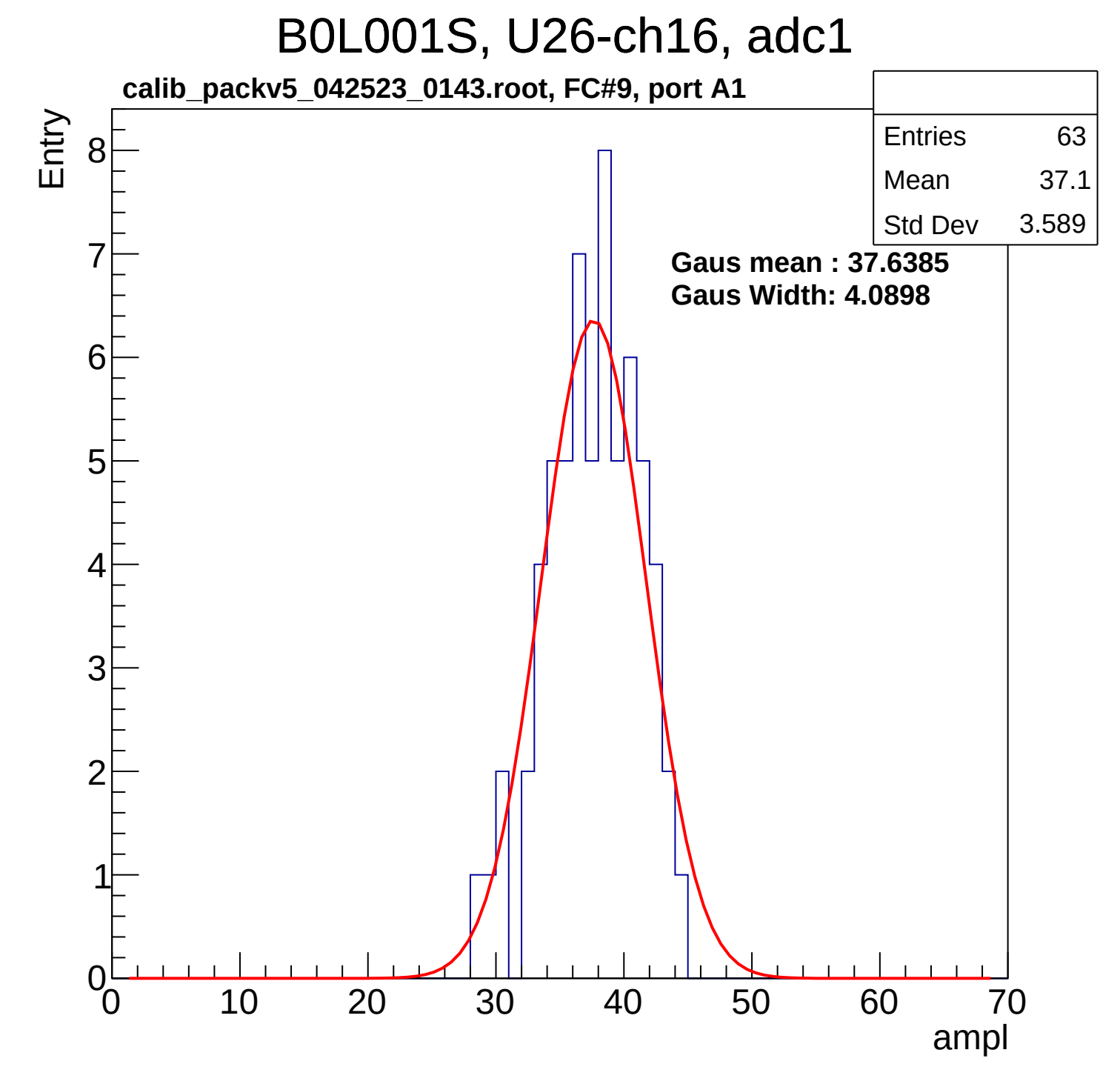
B0L001S, U26-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.1
Std Dev 3.589
Gaus mean : 37.6385
Gaus Width: 4.0898
Entry
ampl
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1
2
3
4
5
6
7
8
0
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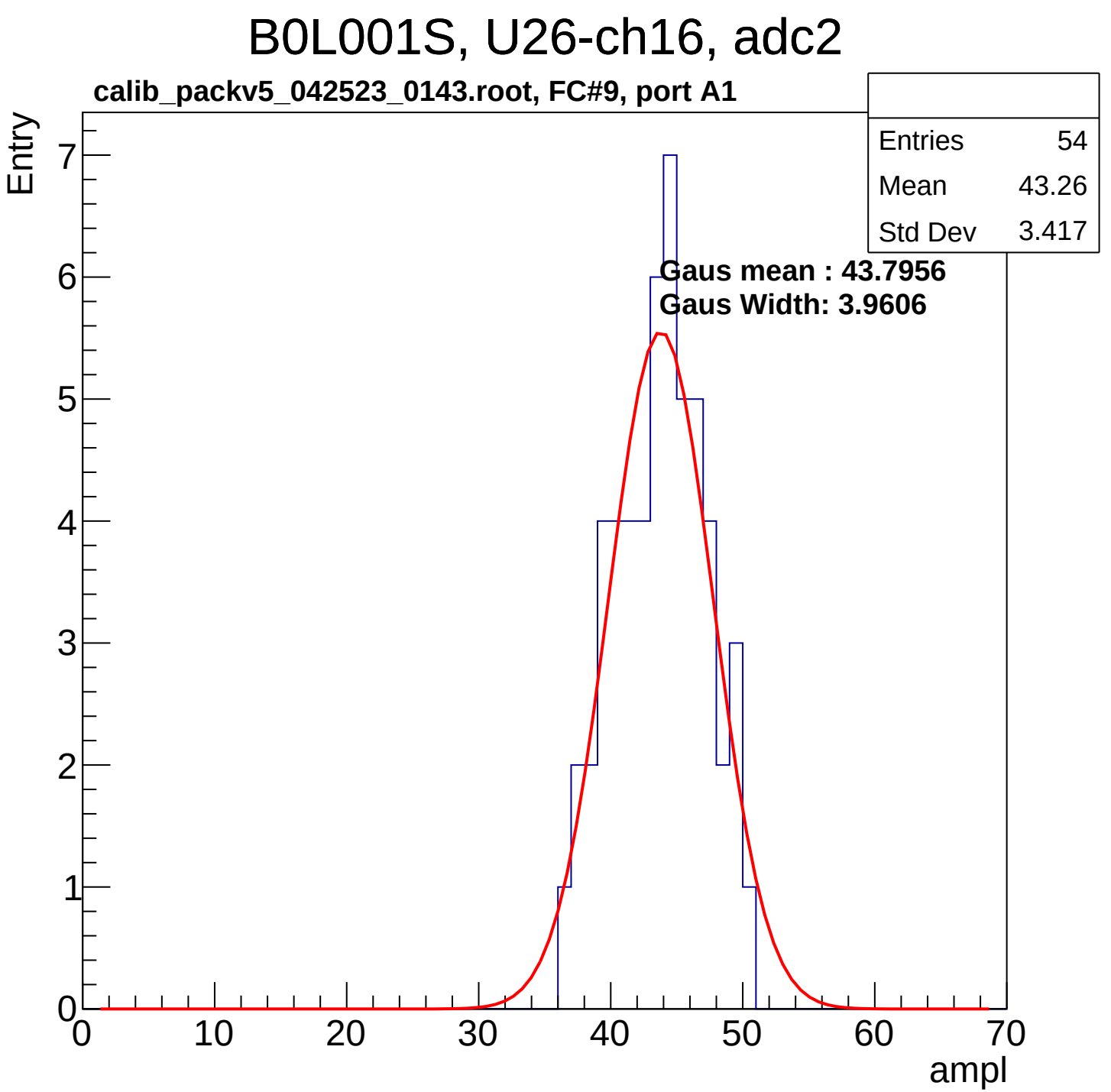
B0L001S, U26-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.26
Std Dev 3.417
Gaus mean : 43.7956
Gaus Width: 3.9606
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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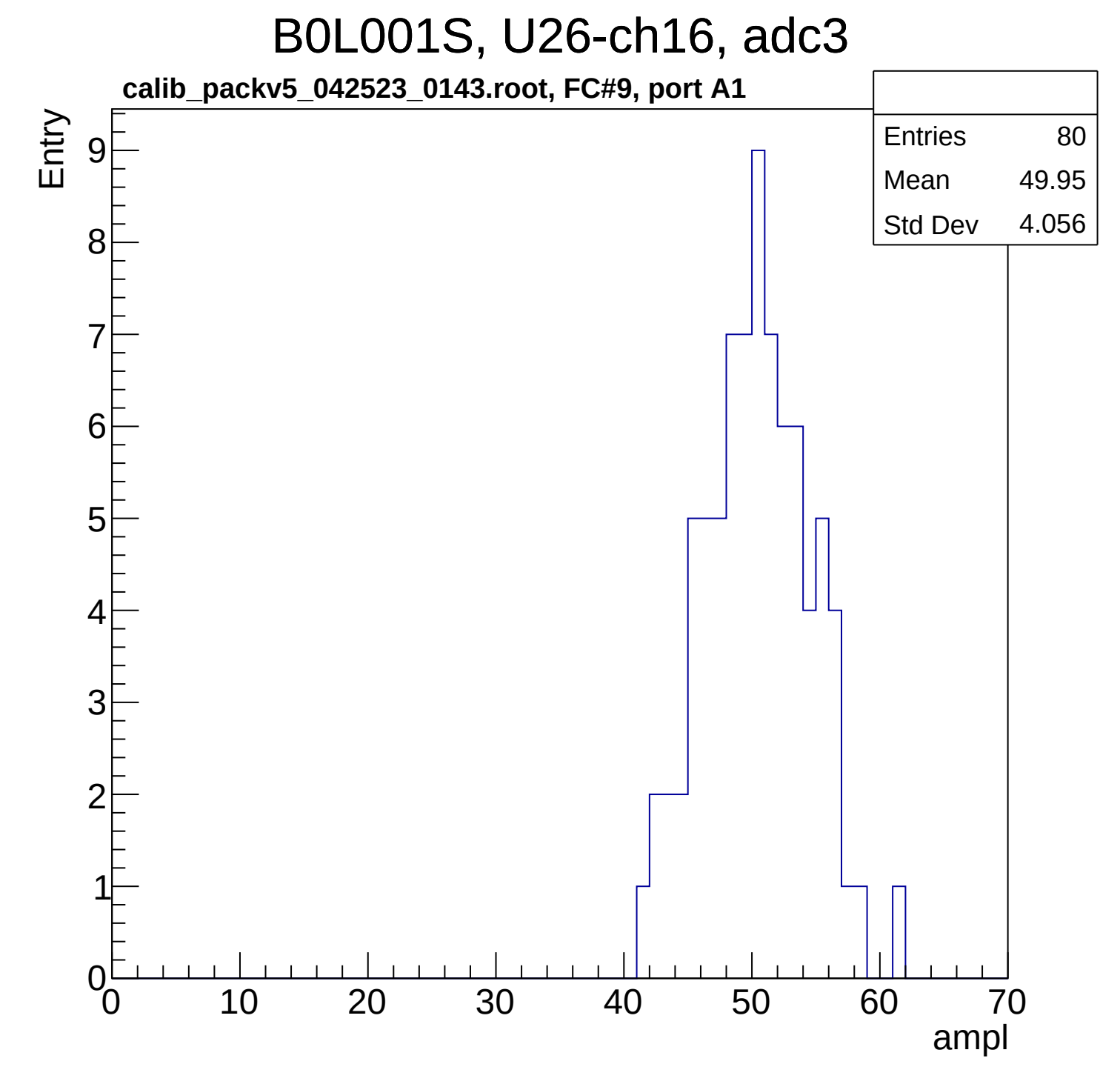
B0L001S, U26-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.95
Std Dev 4.056
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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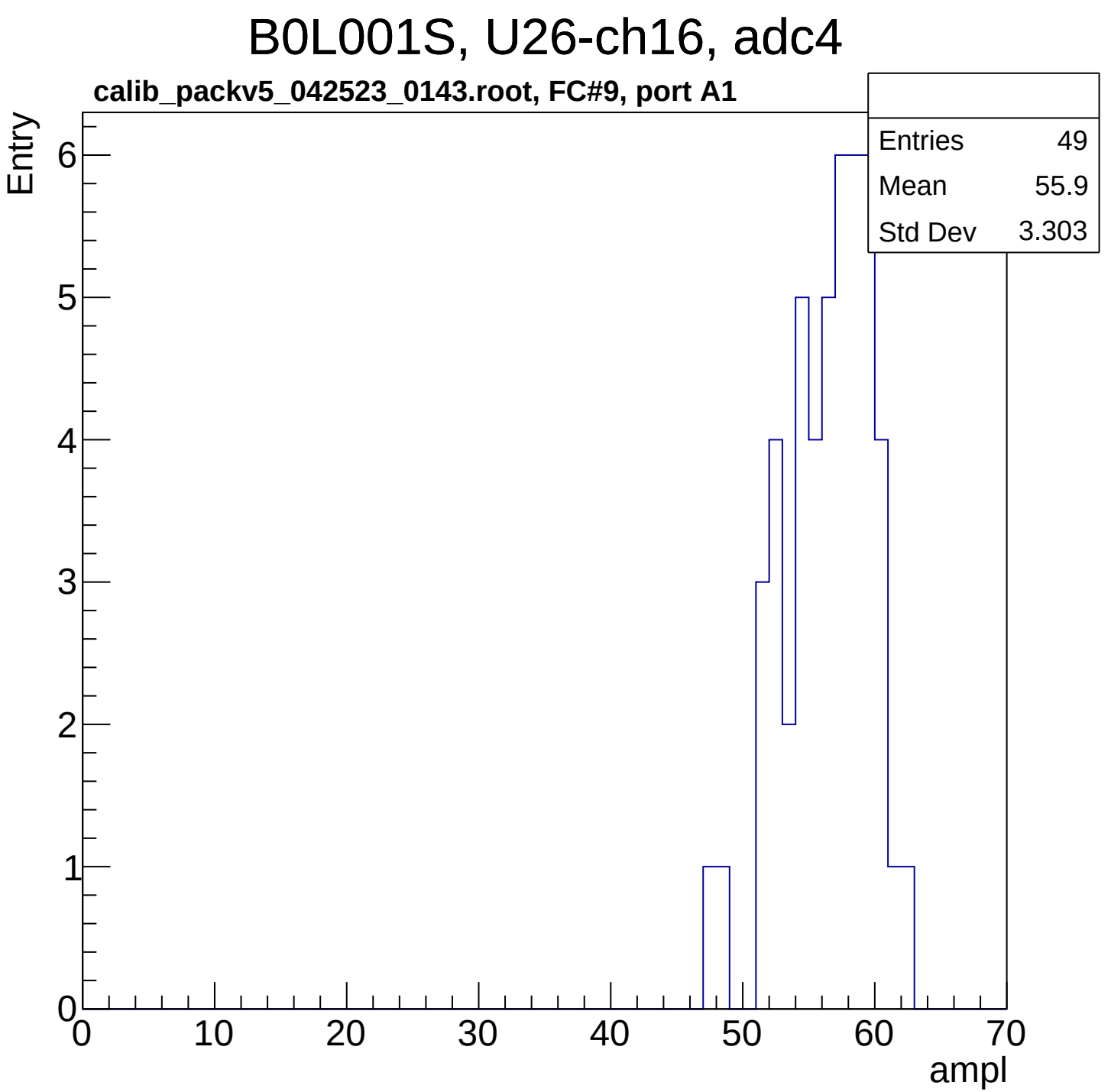
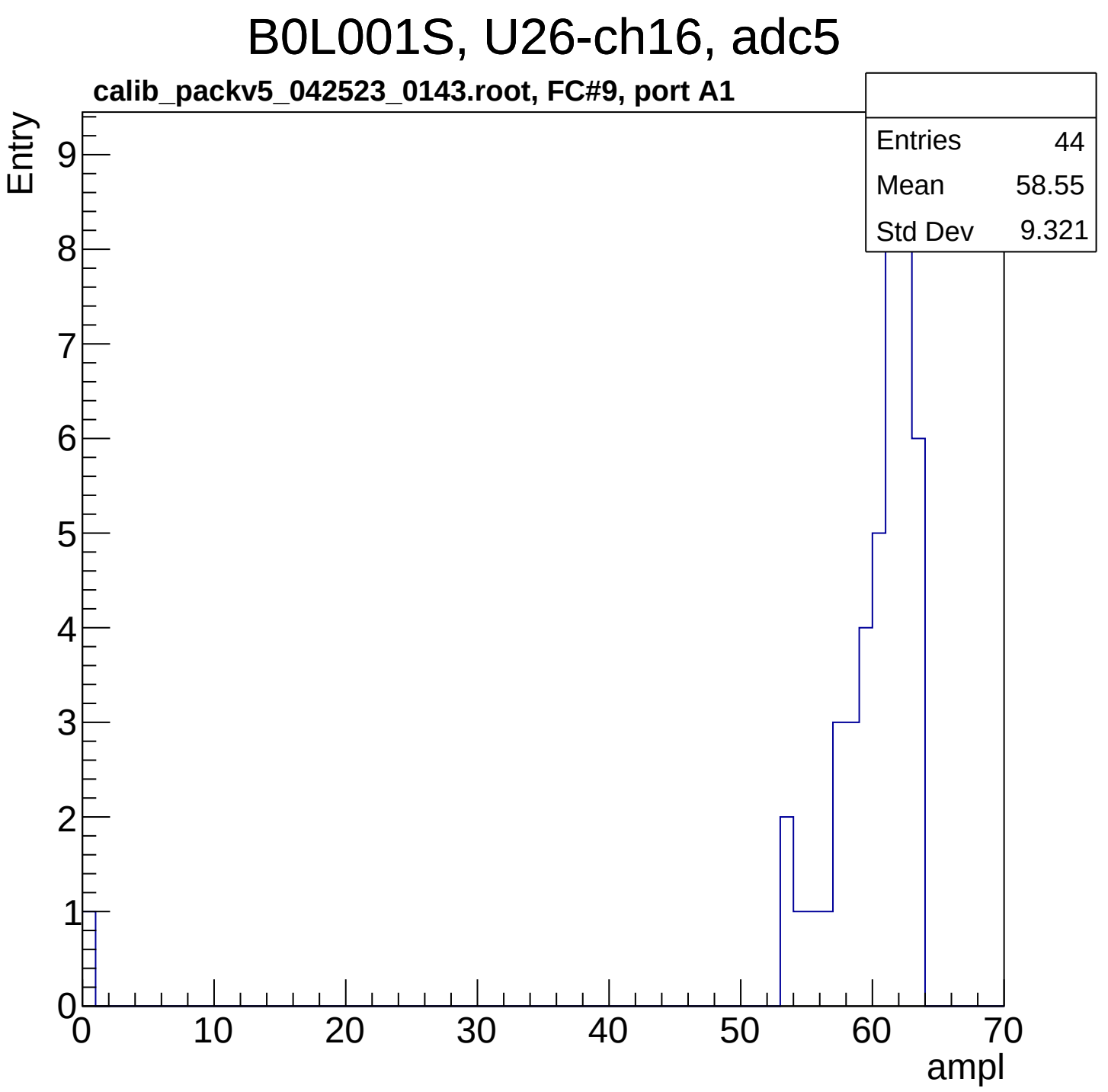

B0L001S, U26-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 55.9
Std Dev 3.303
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.55
Std Dev 9.321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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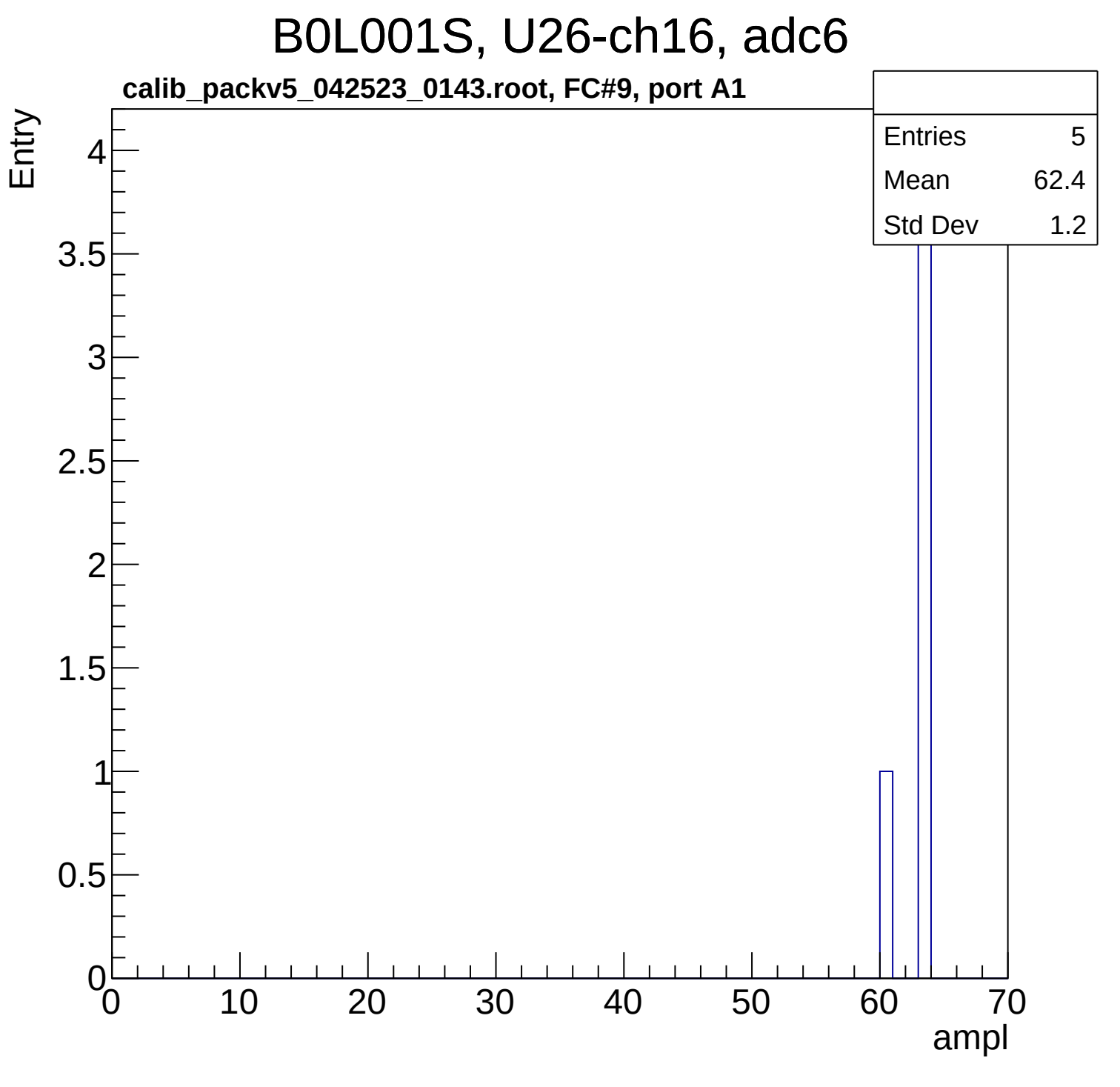
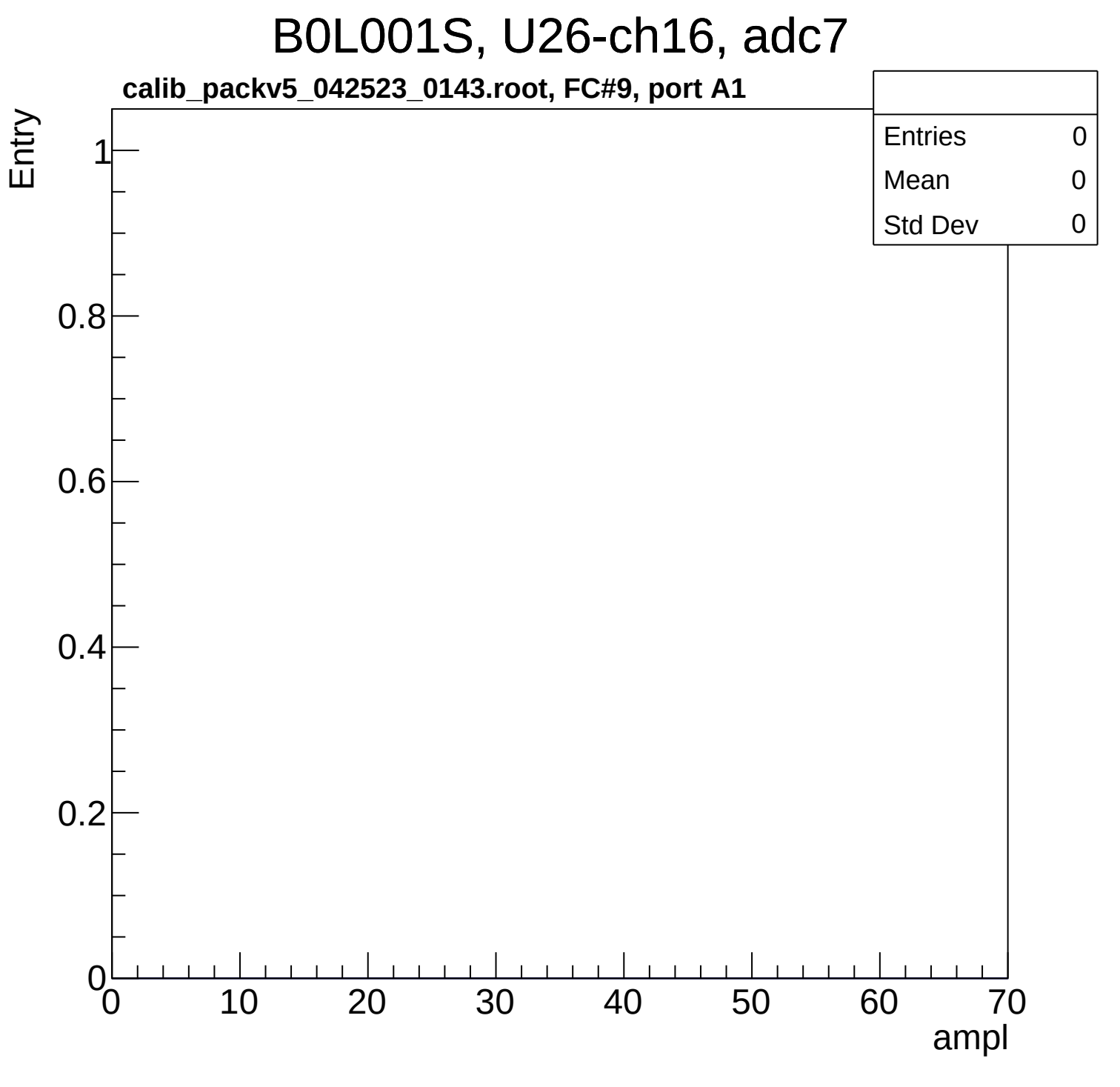
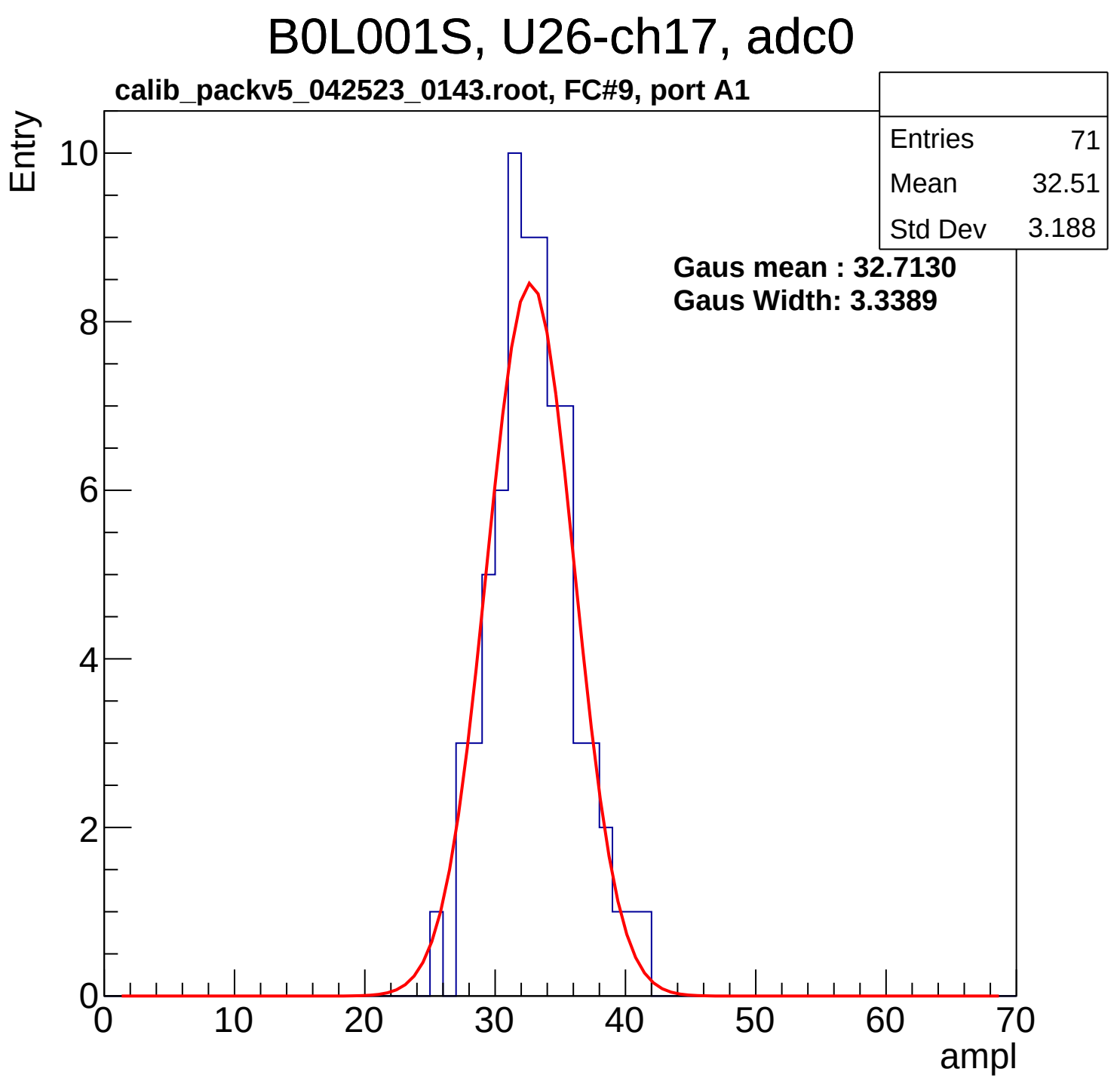
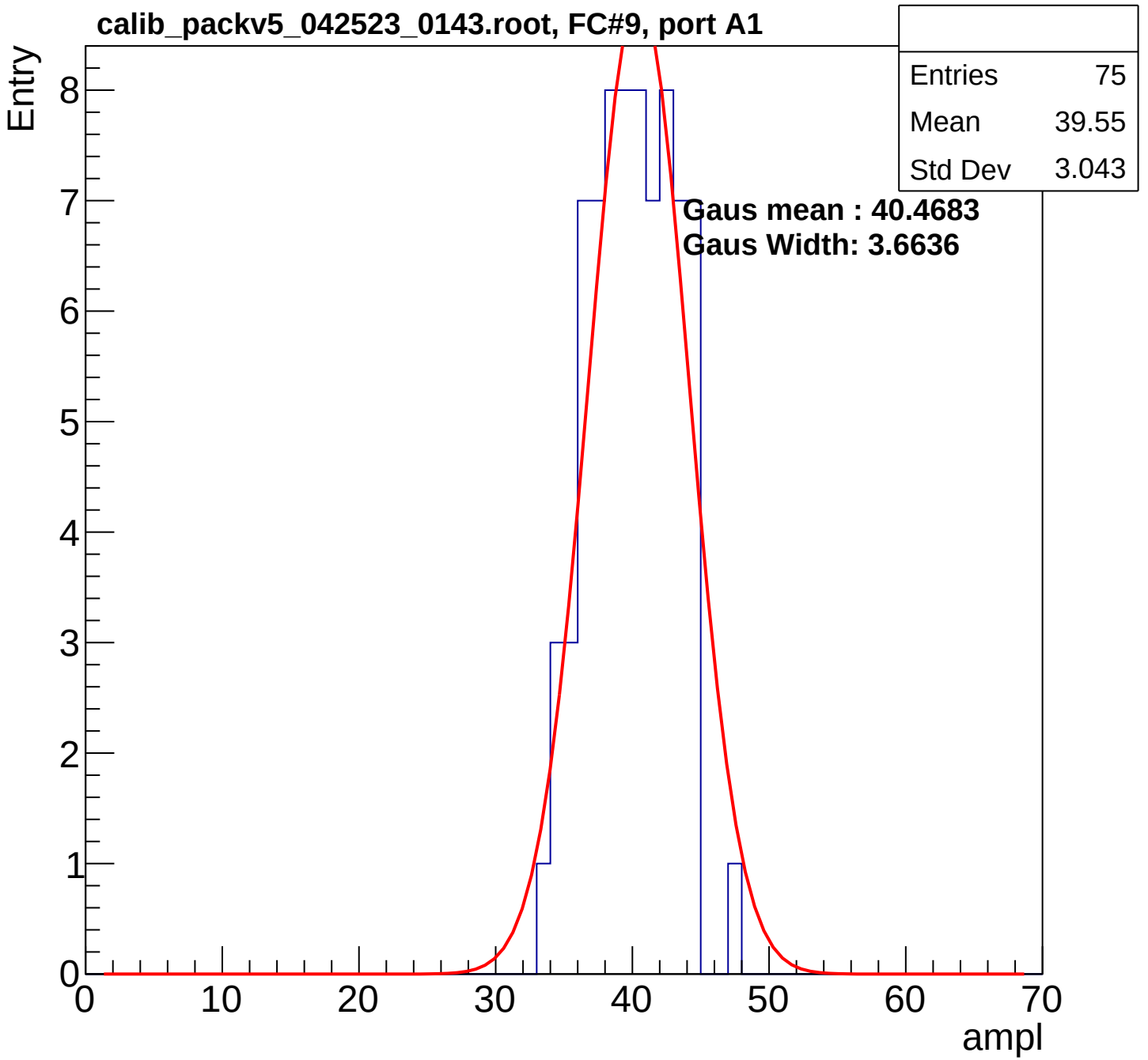
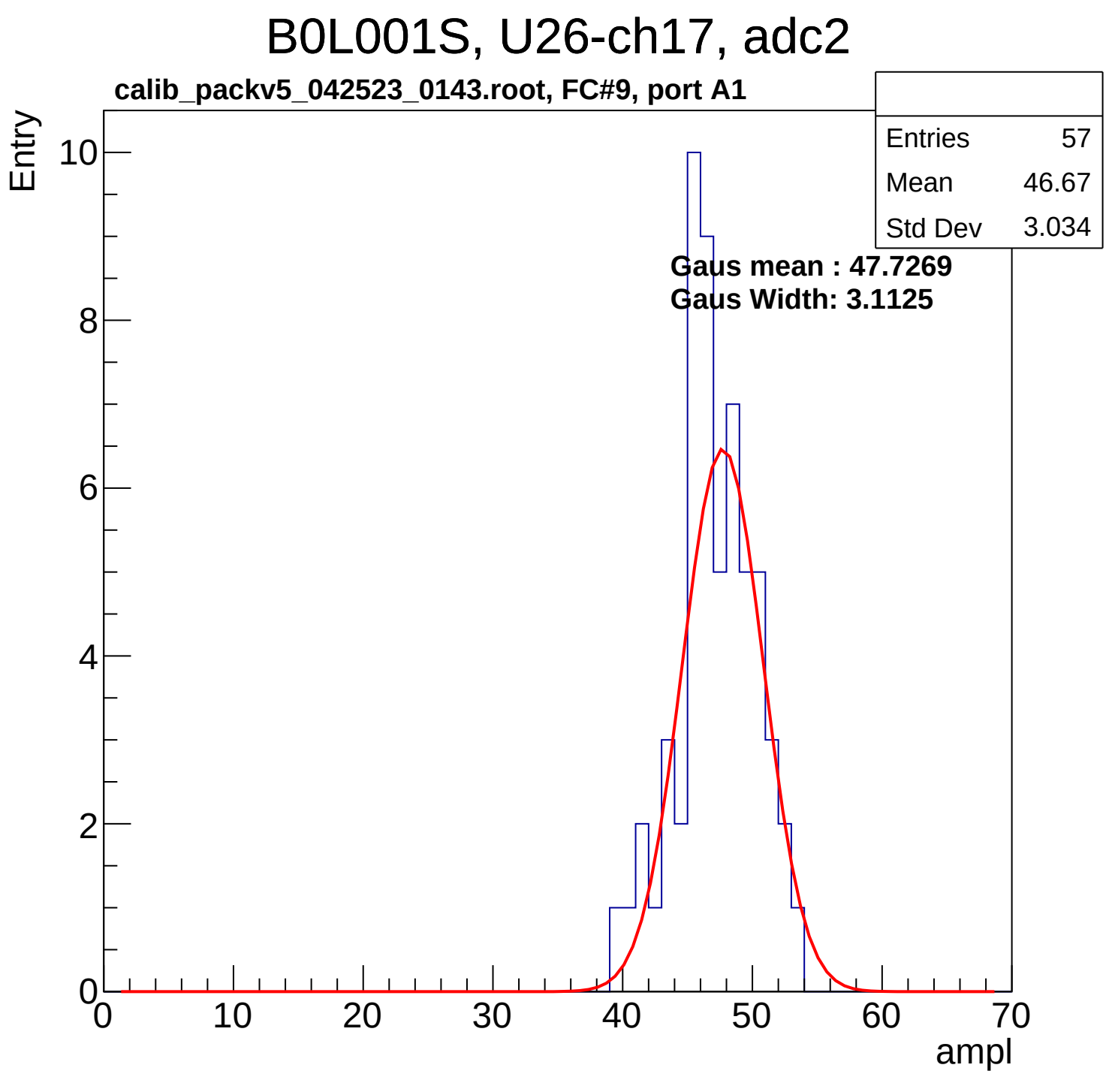
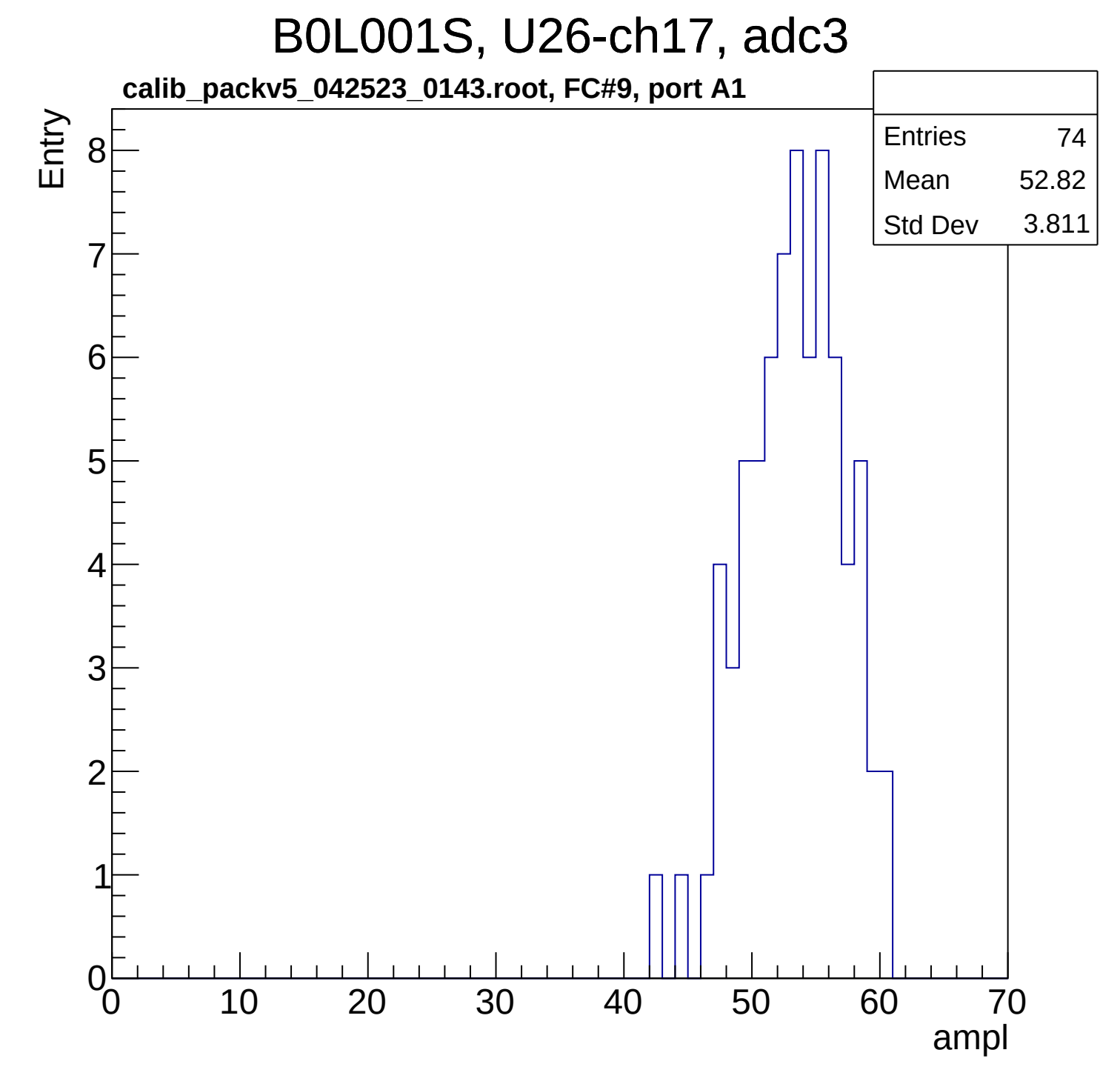

B0L001S, U26-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 1.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U26-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.51
Std Dev 3.188
Gaus mean : 32.7130
Gaus Width: 3.3389
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 39.55
Std Dev 3.043
Gaus mean : 40.4683
Gaus Width: 3.6636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 46.67
Std Dev 3.034
Gaus mean : 47.7269
Gaus Width: 3.1125
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 52.82
Std Dev 3.811
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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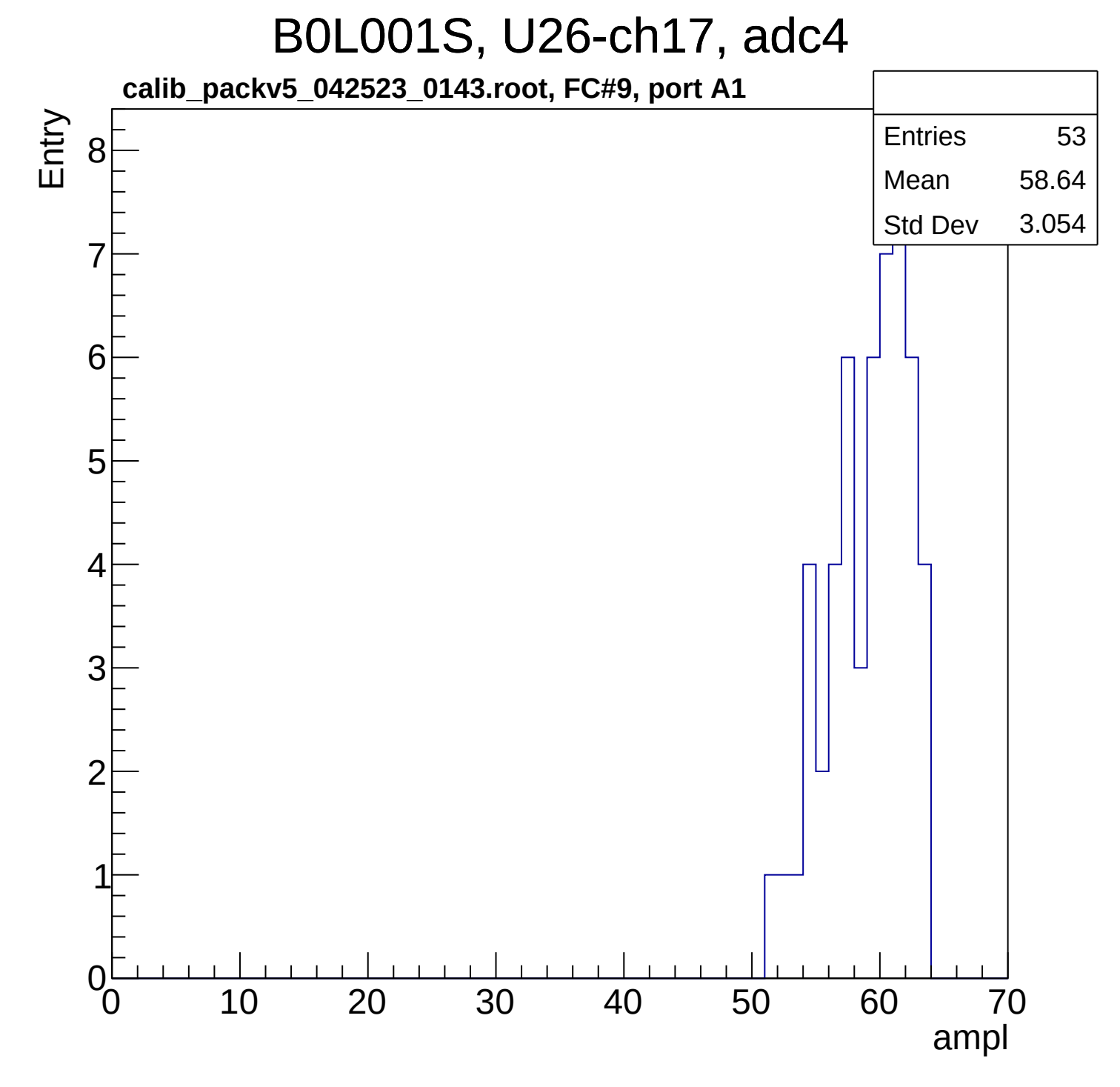
40
50
60
70


B0L001S, U26-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.64
Std Dev 3.054
Entry
ampl
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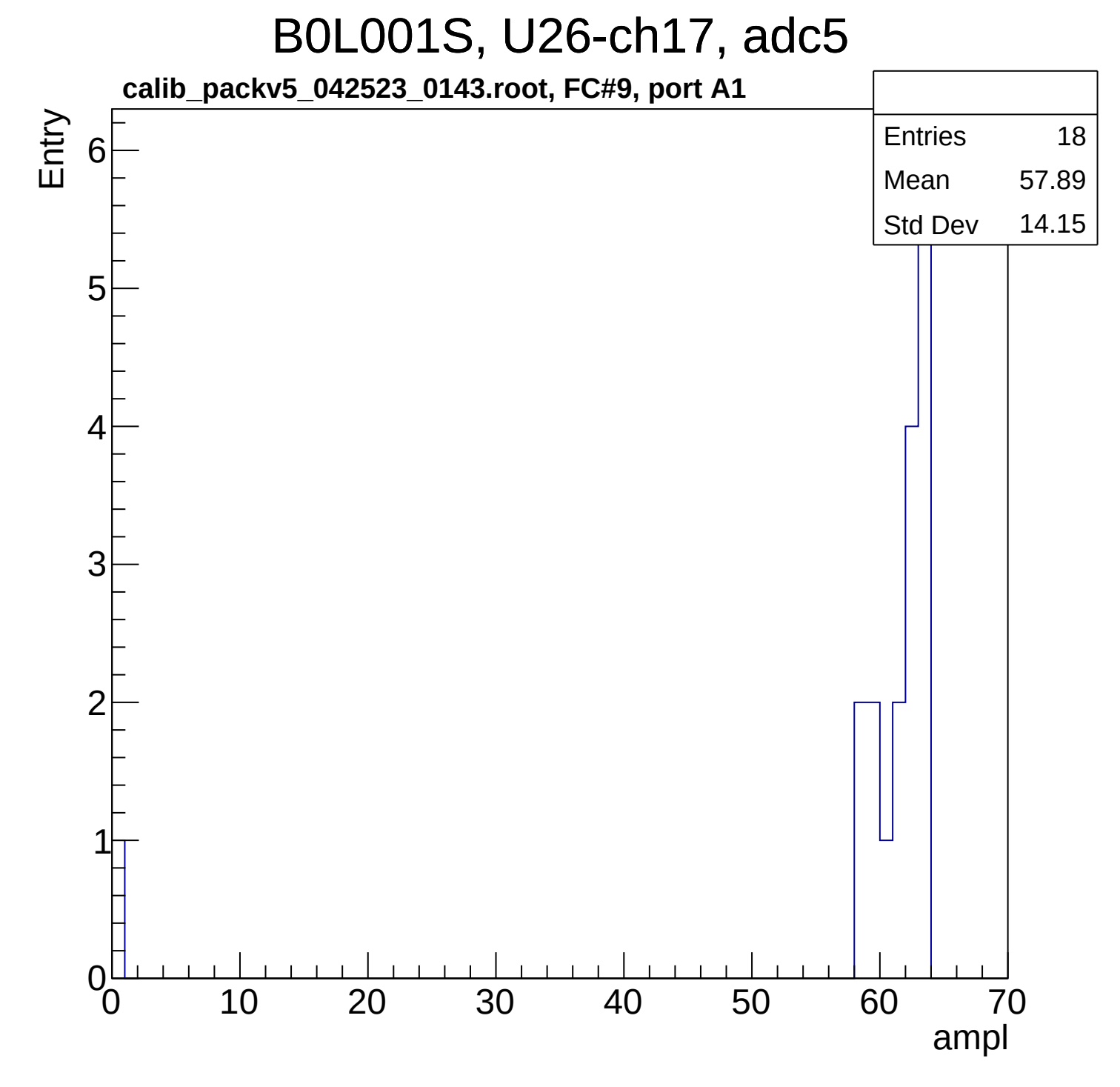
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 57.89
Std Dev 14.15
Entry
0
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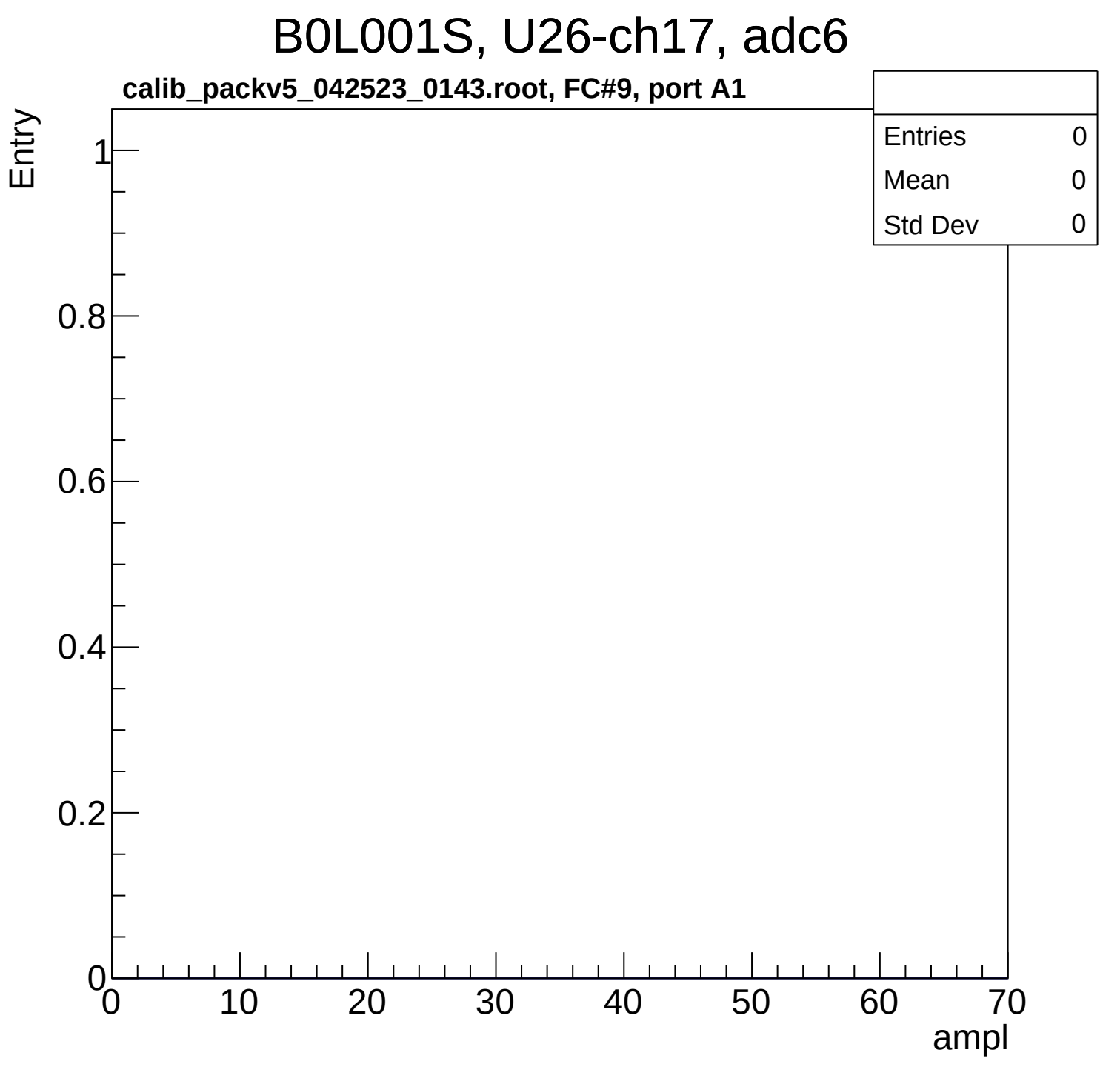
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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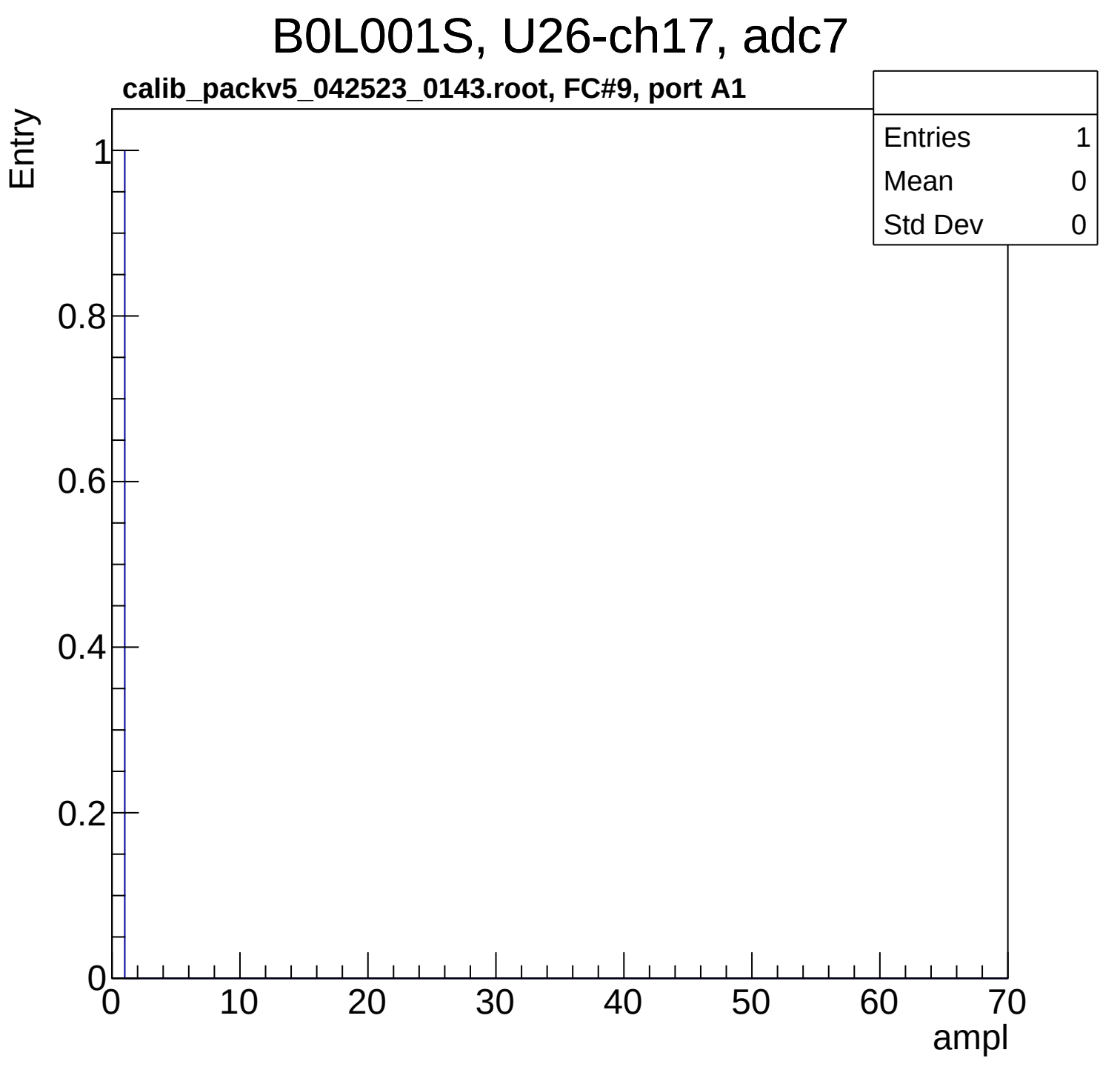
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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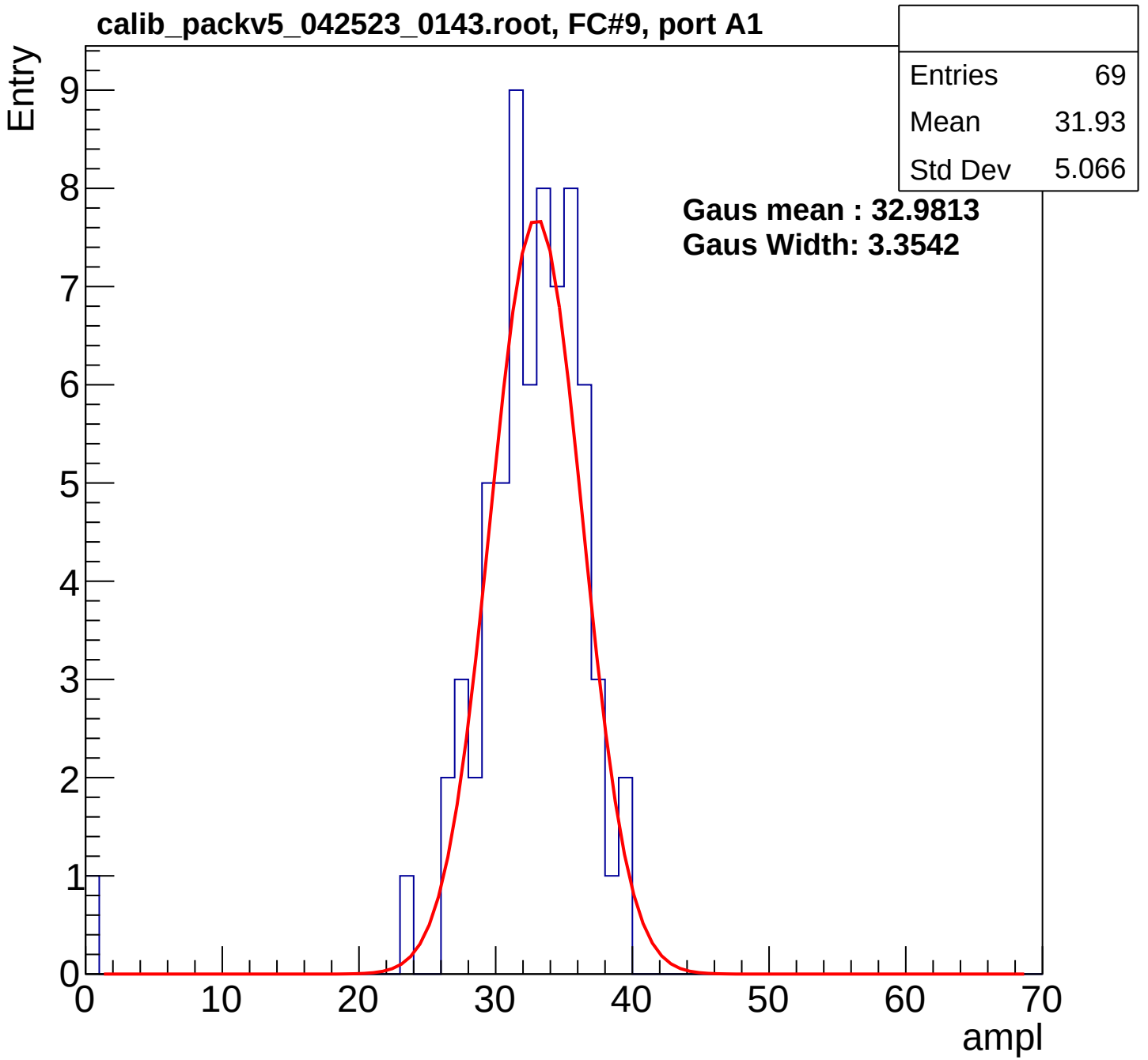
50
60
70
ampl


B0L001S, U26-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31.93
Std Dev 5.066
Gaus mean : 32.9813
Gaus Width: 3.3542
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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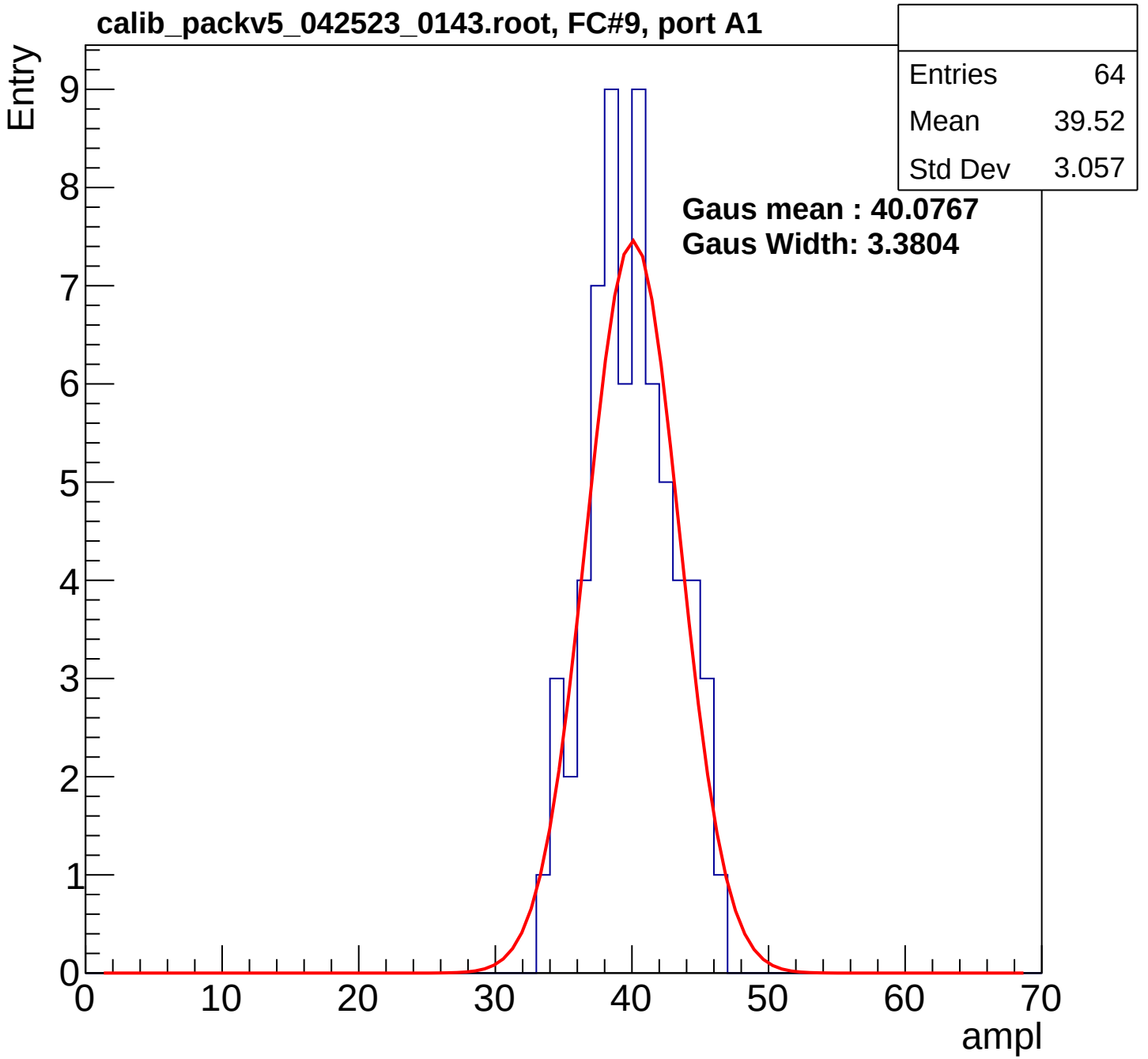
B0L001S, U26-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 39.52
Std Dev 3.057
Gaus mean : 40.0767
Gaus Width: 3.3804
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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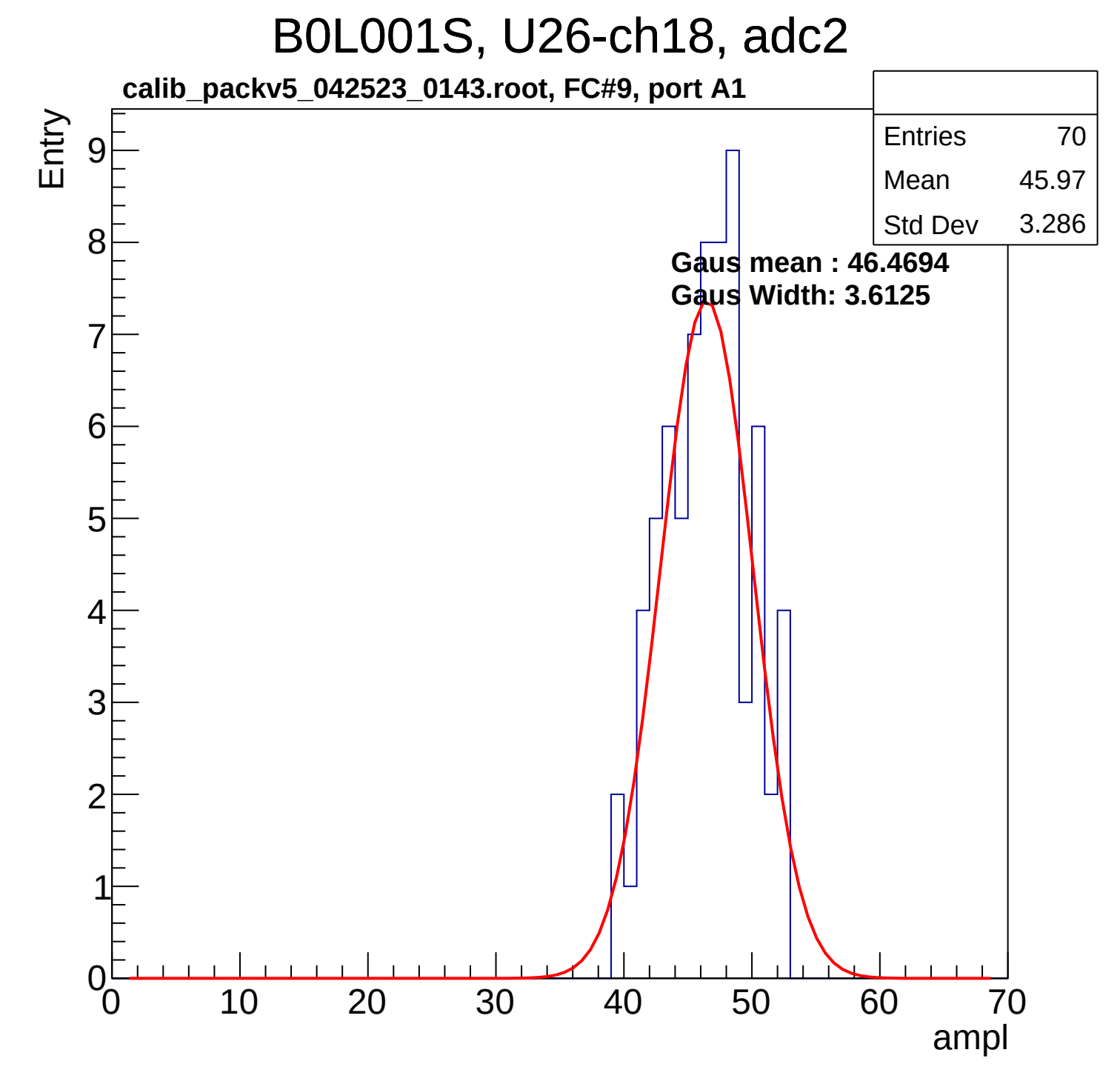
B0L001S, U26-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 45.97
Std Dev 3.286
Gaus mean : 46.4694
Gaus Width: 3.6125
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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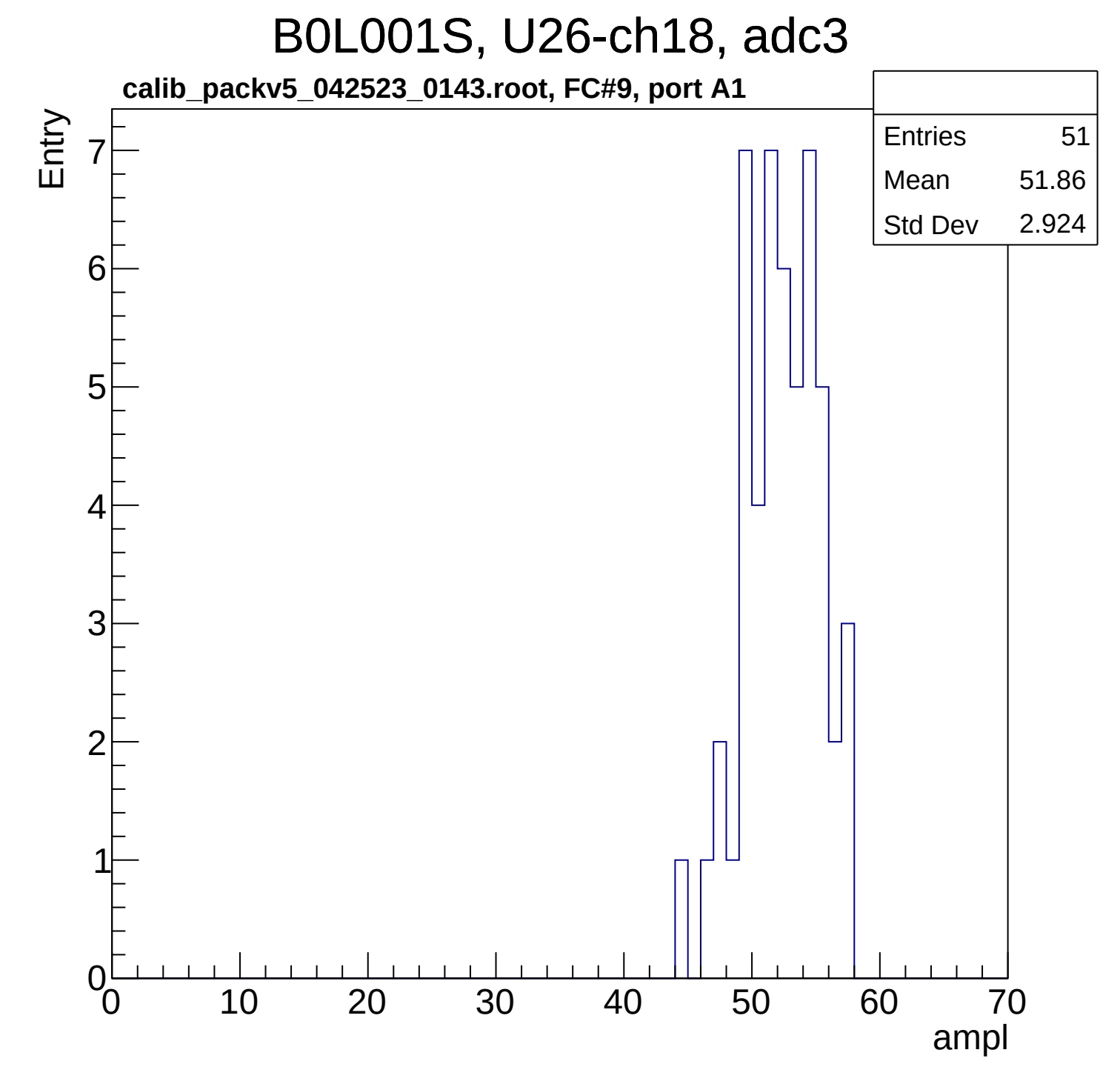
B0L001S, U26-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 51.86
Std Dev 2.924
Entry
ampl
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6
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0
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60
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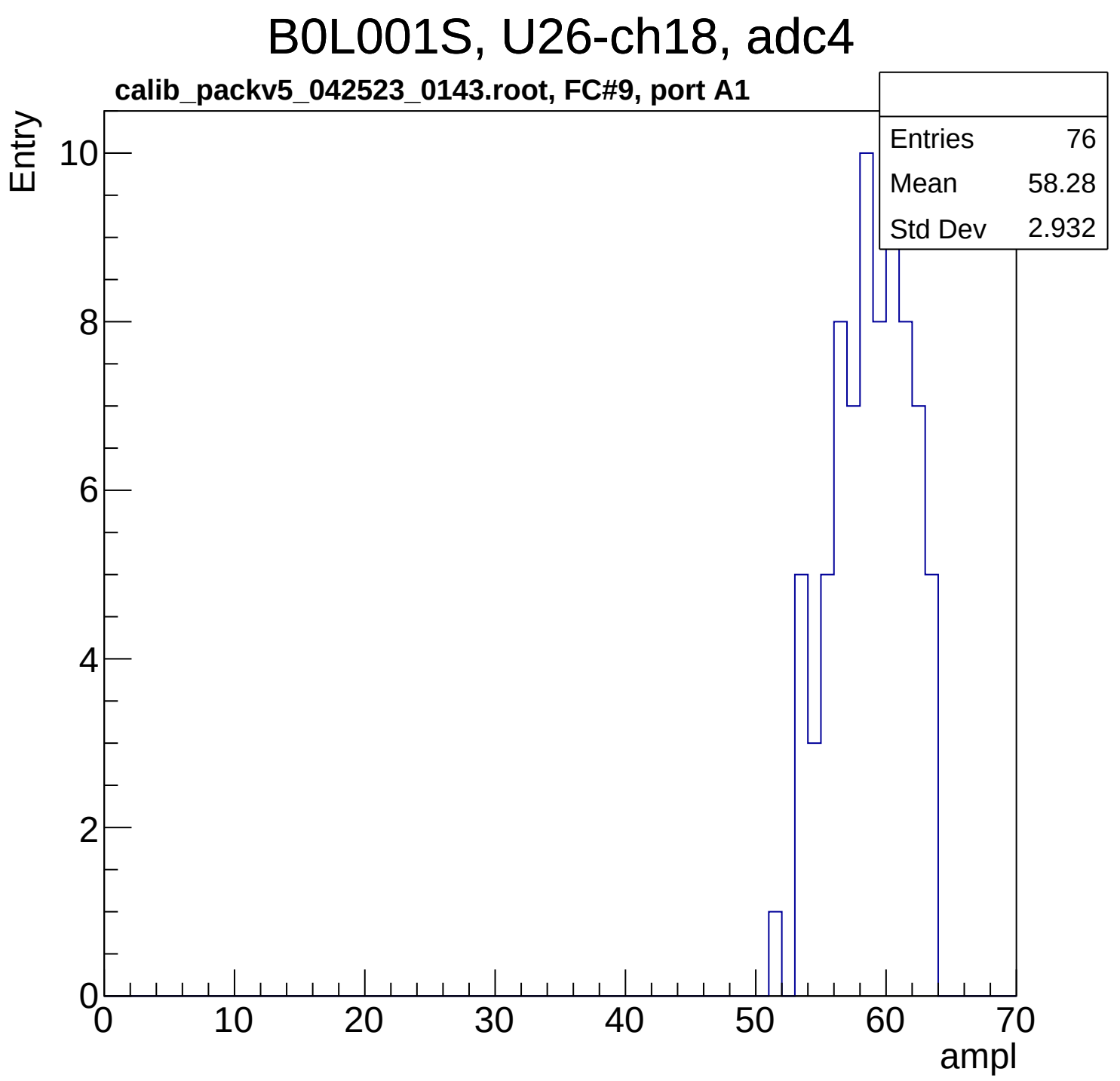
B0L001S, U26-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 58.28
Std Dev 2.932
Entry
ampl
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0
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60
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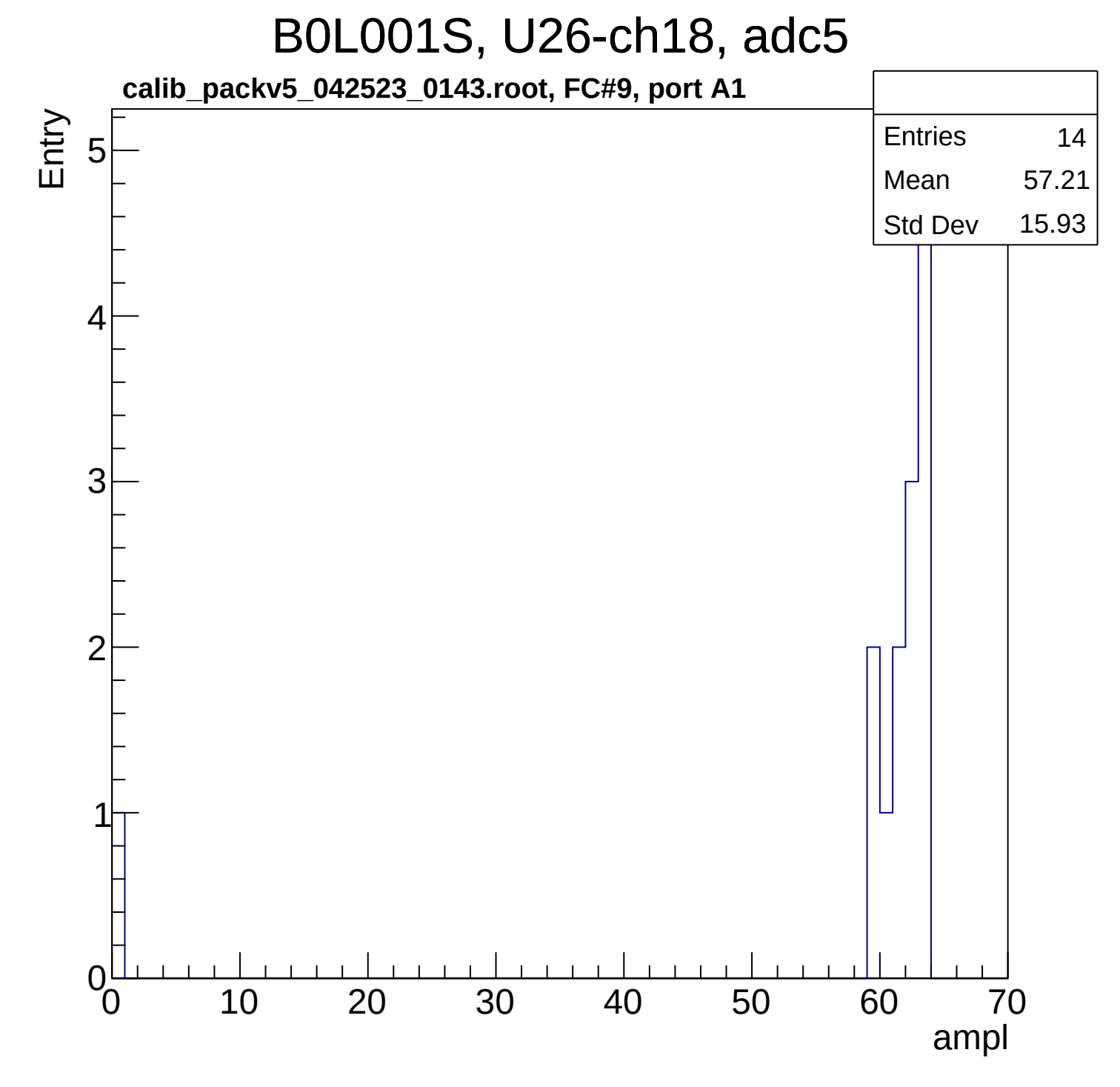
B0L001S, U26-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 57.21
Std Dev 15.93
Entry
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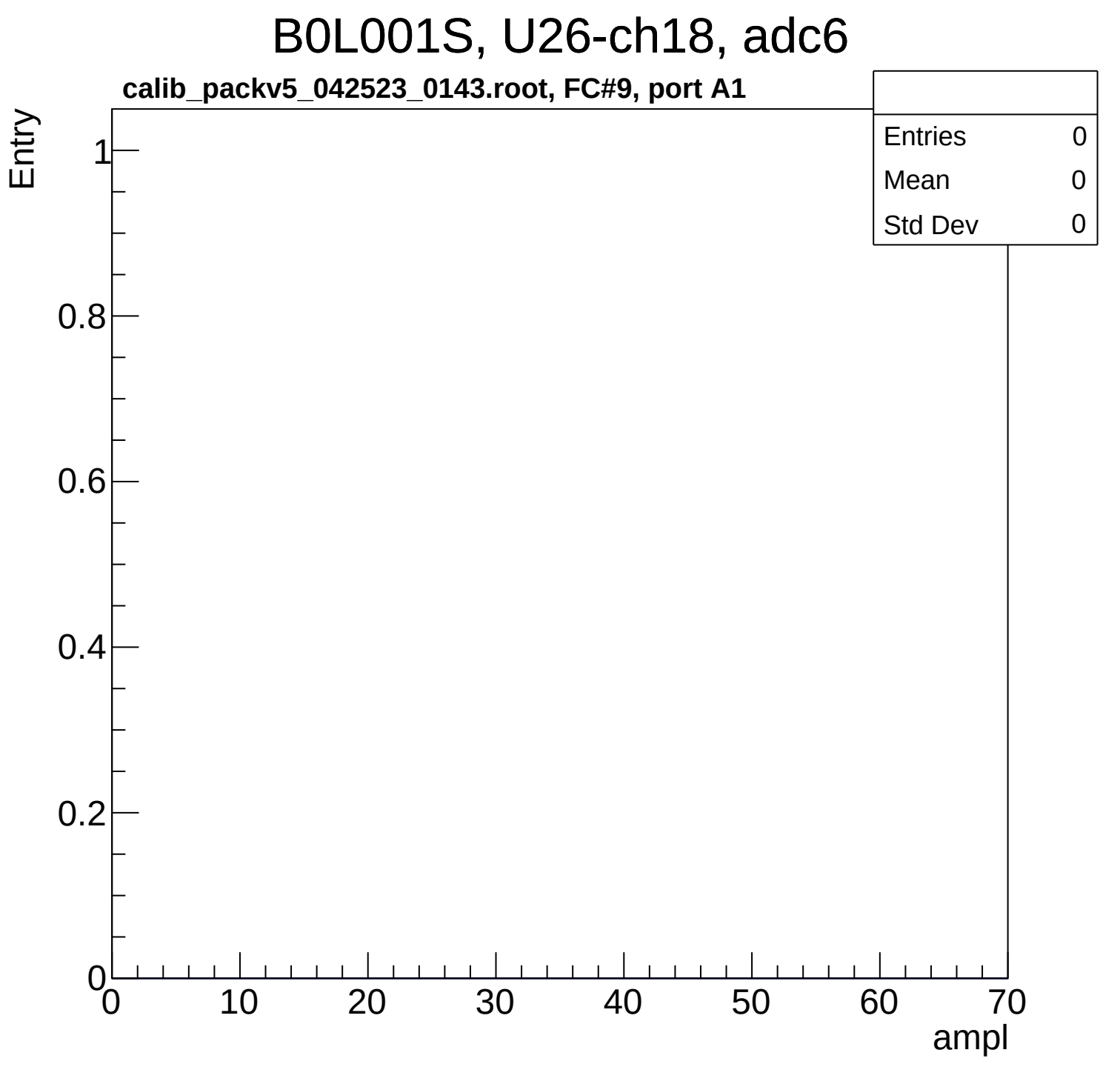
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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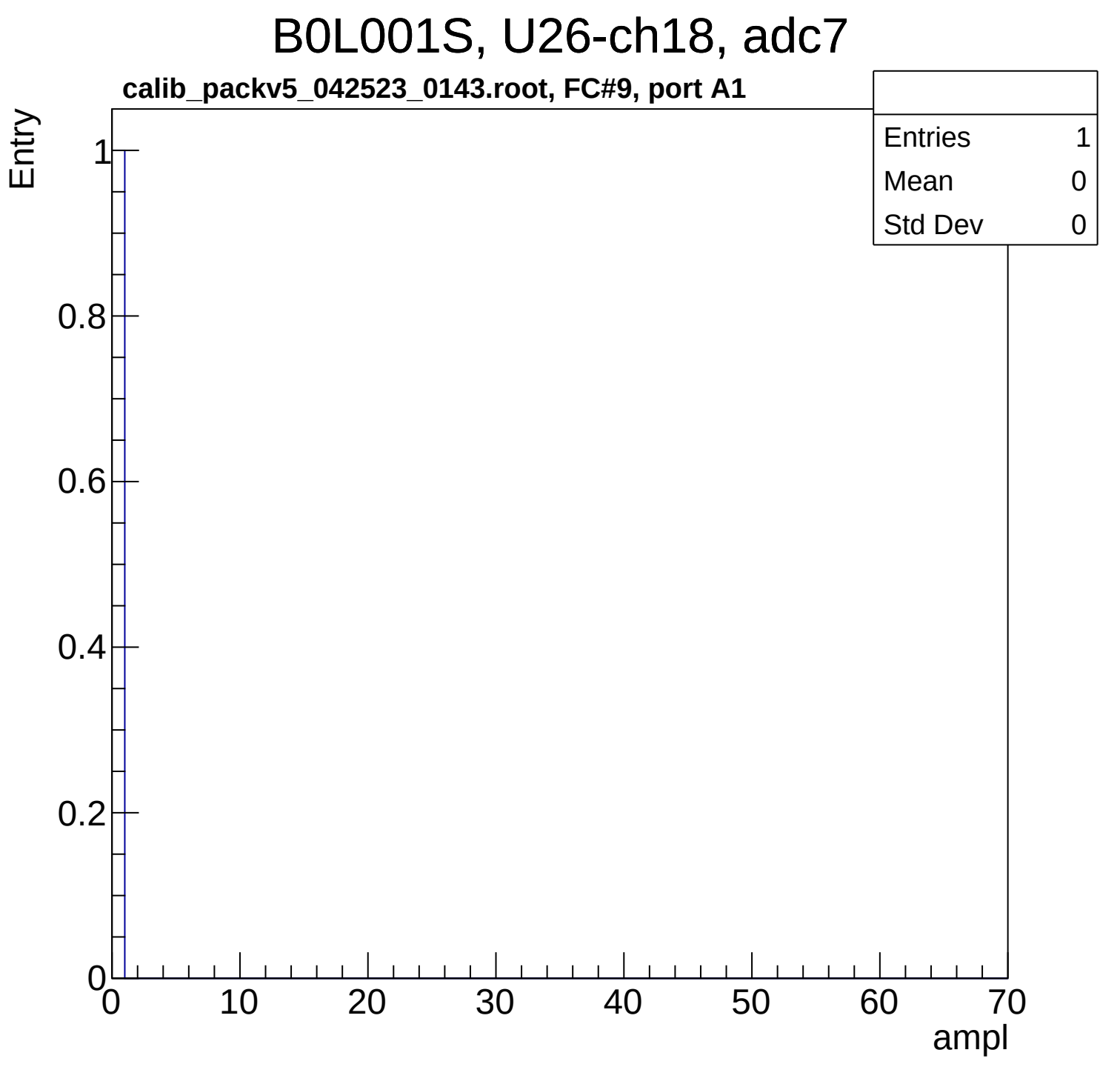
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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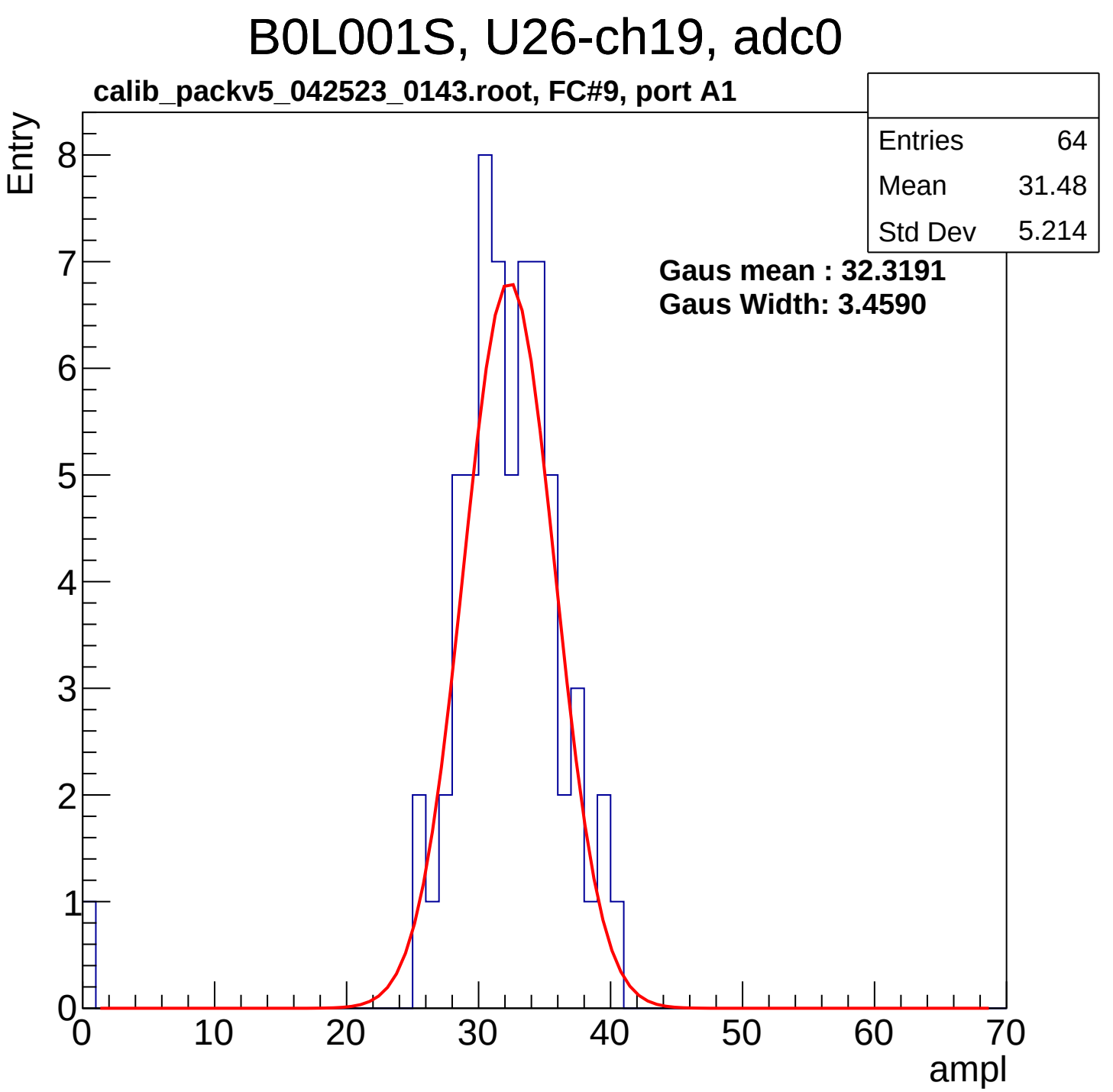
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 31.48
Std Dev 5.214
Gaus mean : 32.3191
Gaus Width: 3.4590
Entry
ampl
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2
3
4
5
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8
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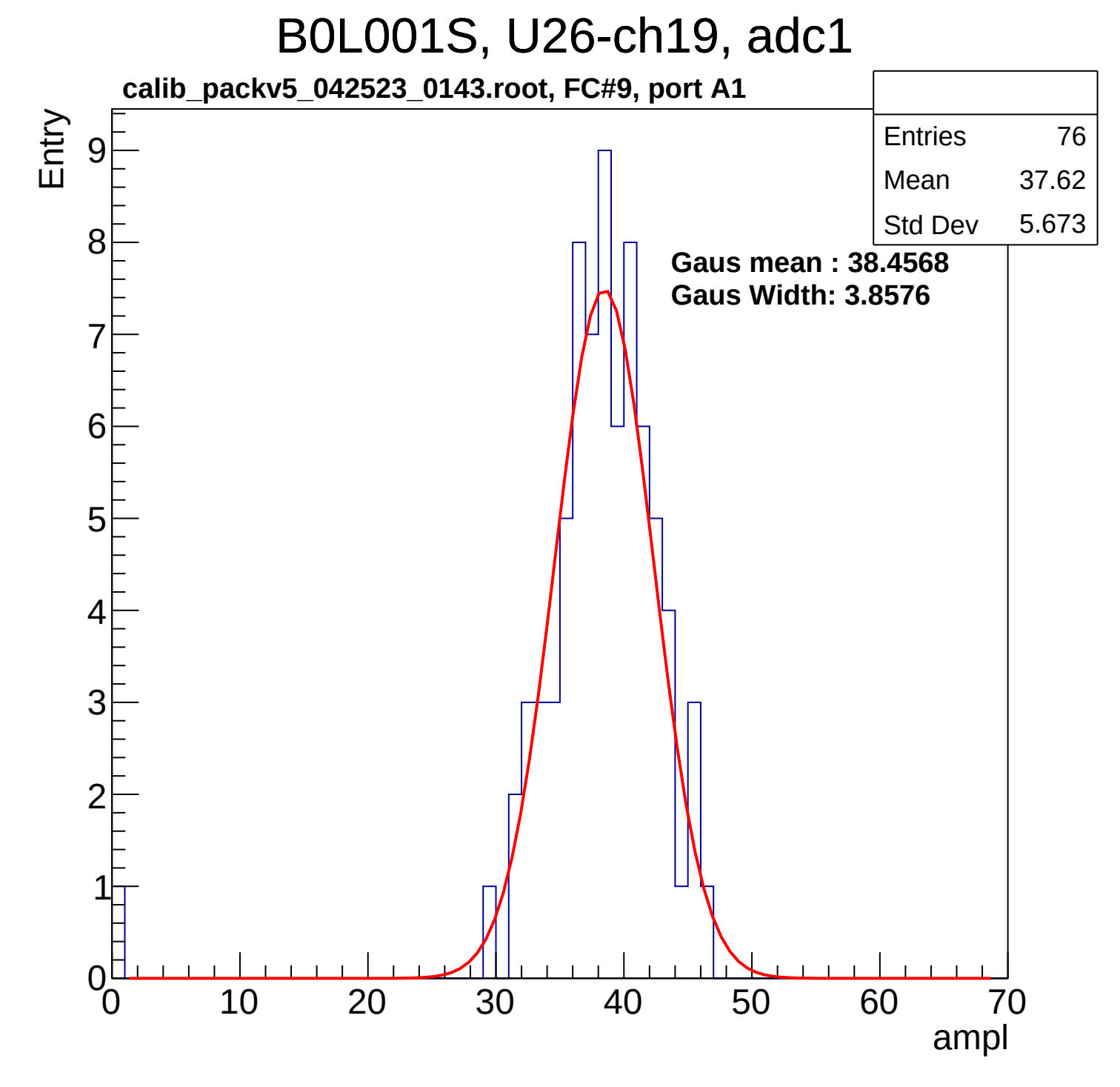
B0L001S, U26-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.62
Std Dev 5.673
Gaus mean : 38.4568
Gaus Width: 3.8576
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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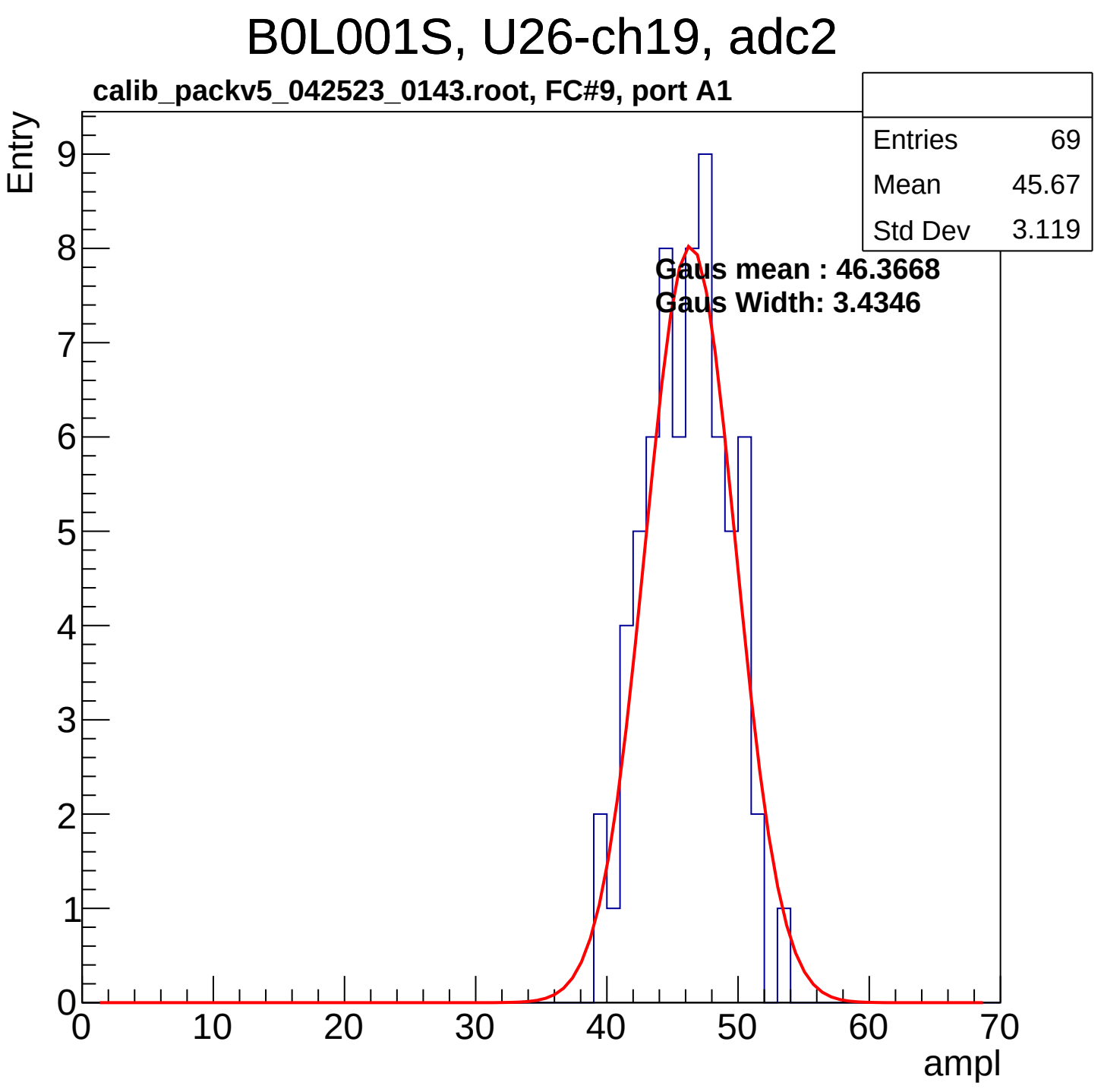
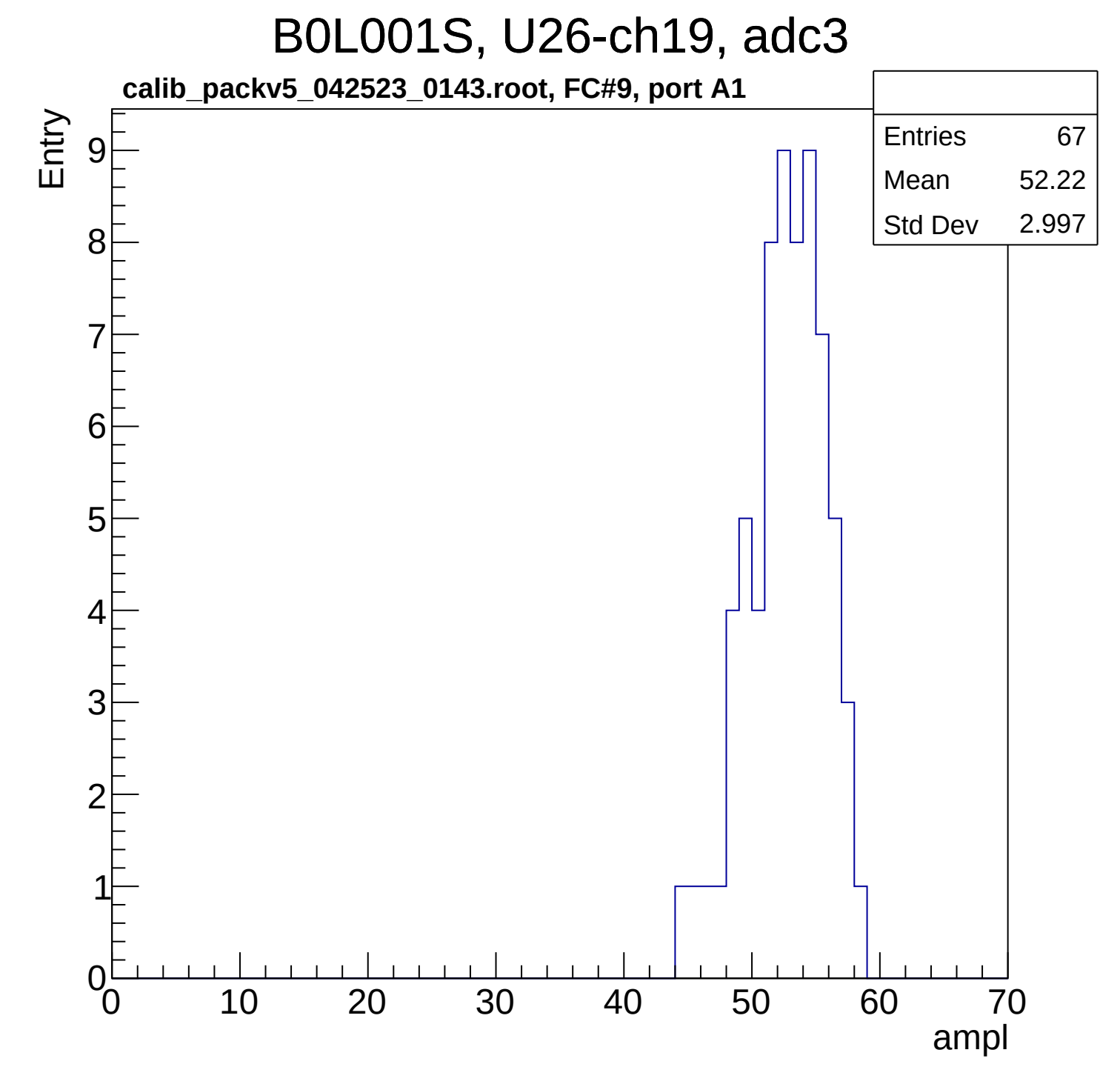
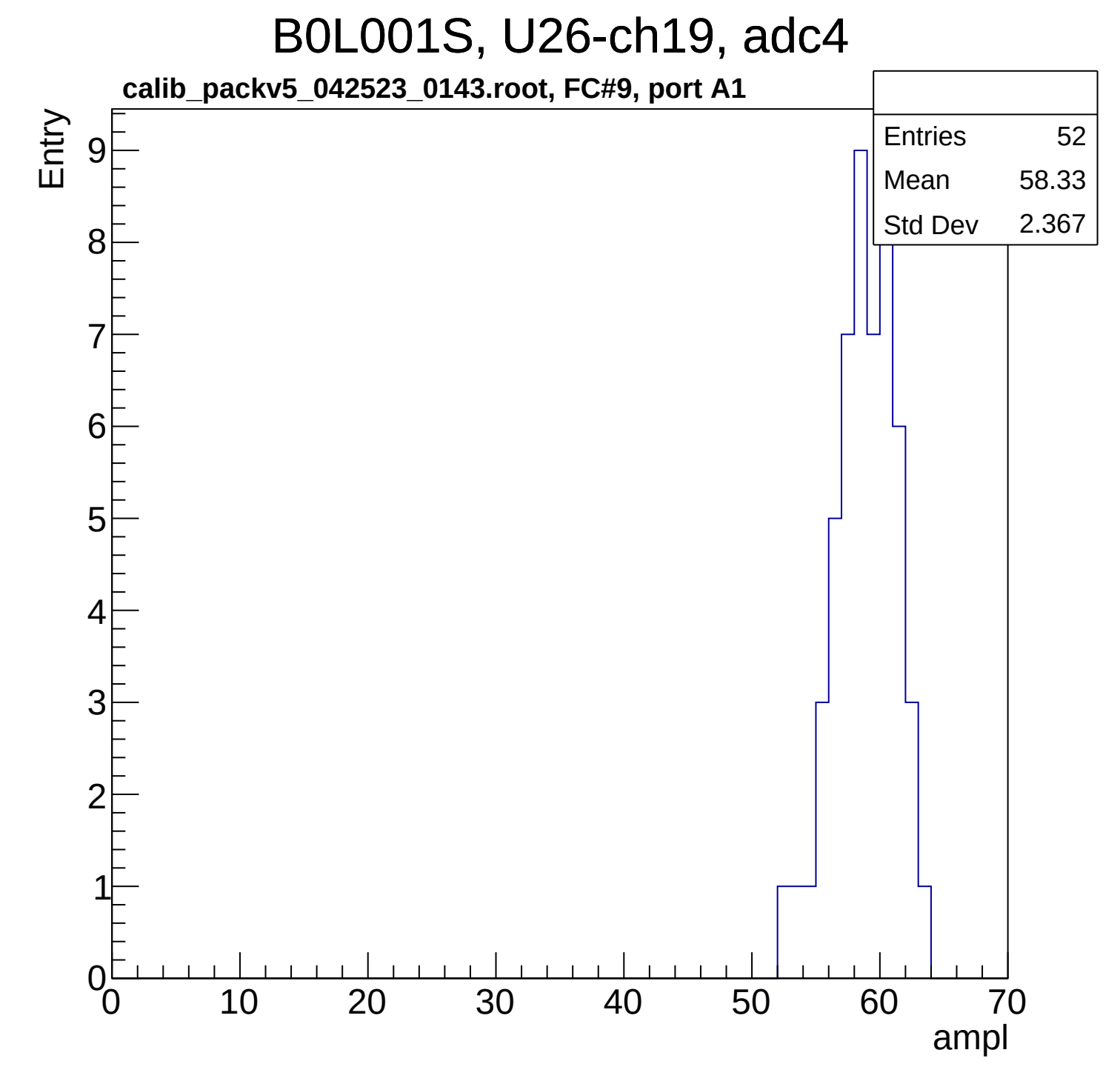
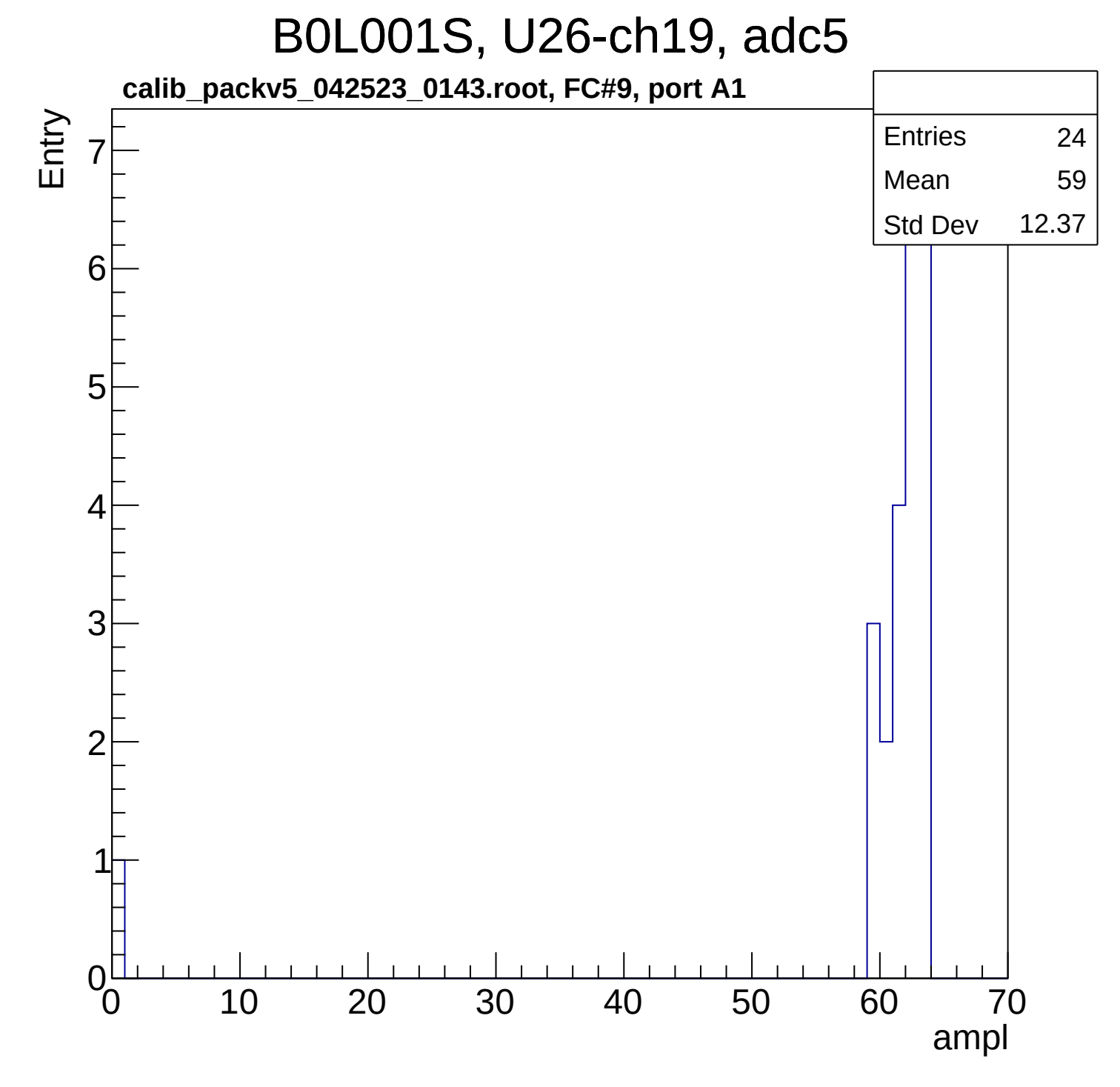
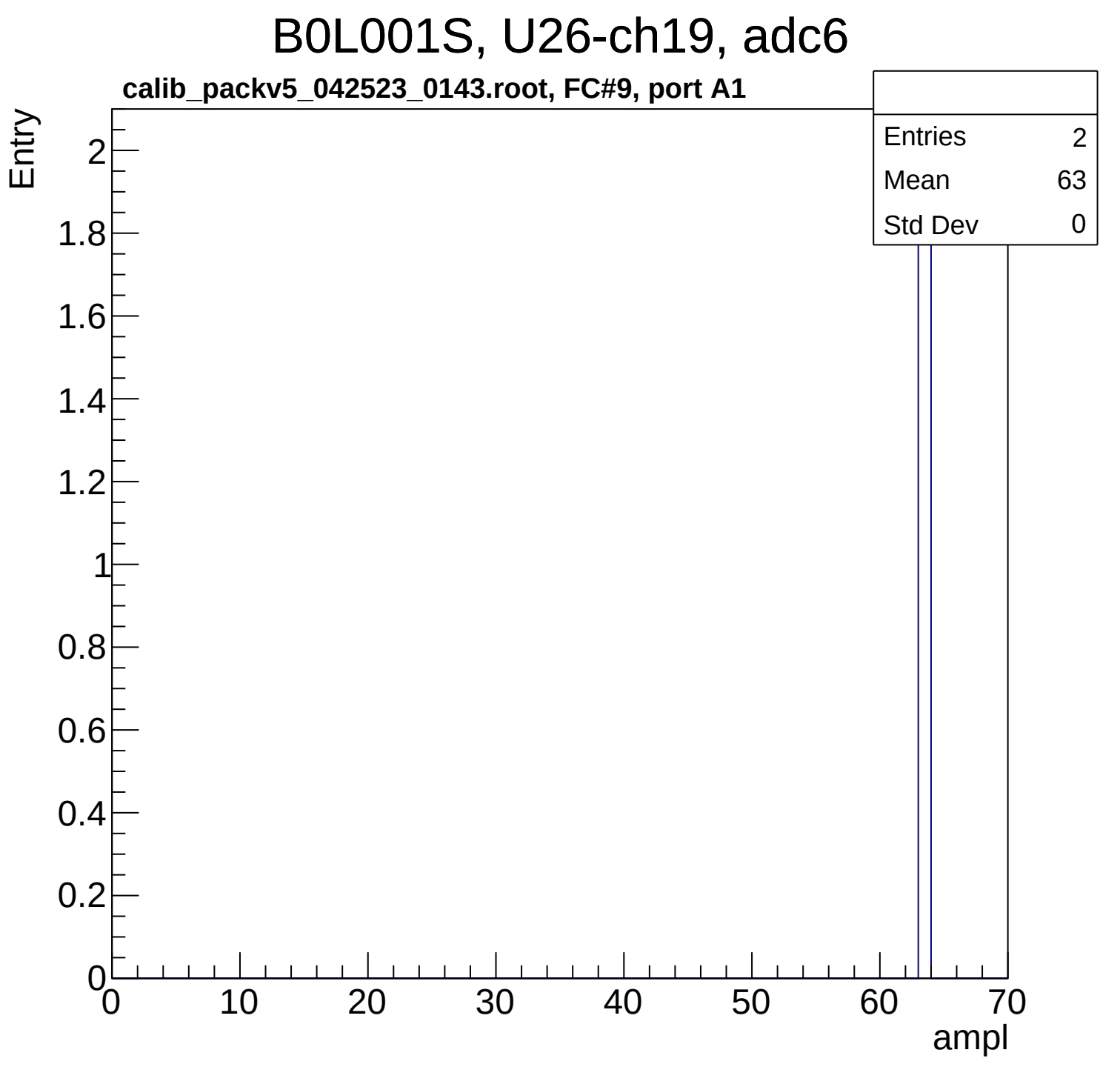
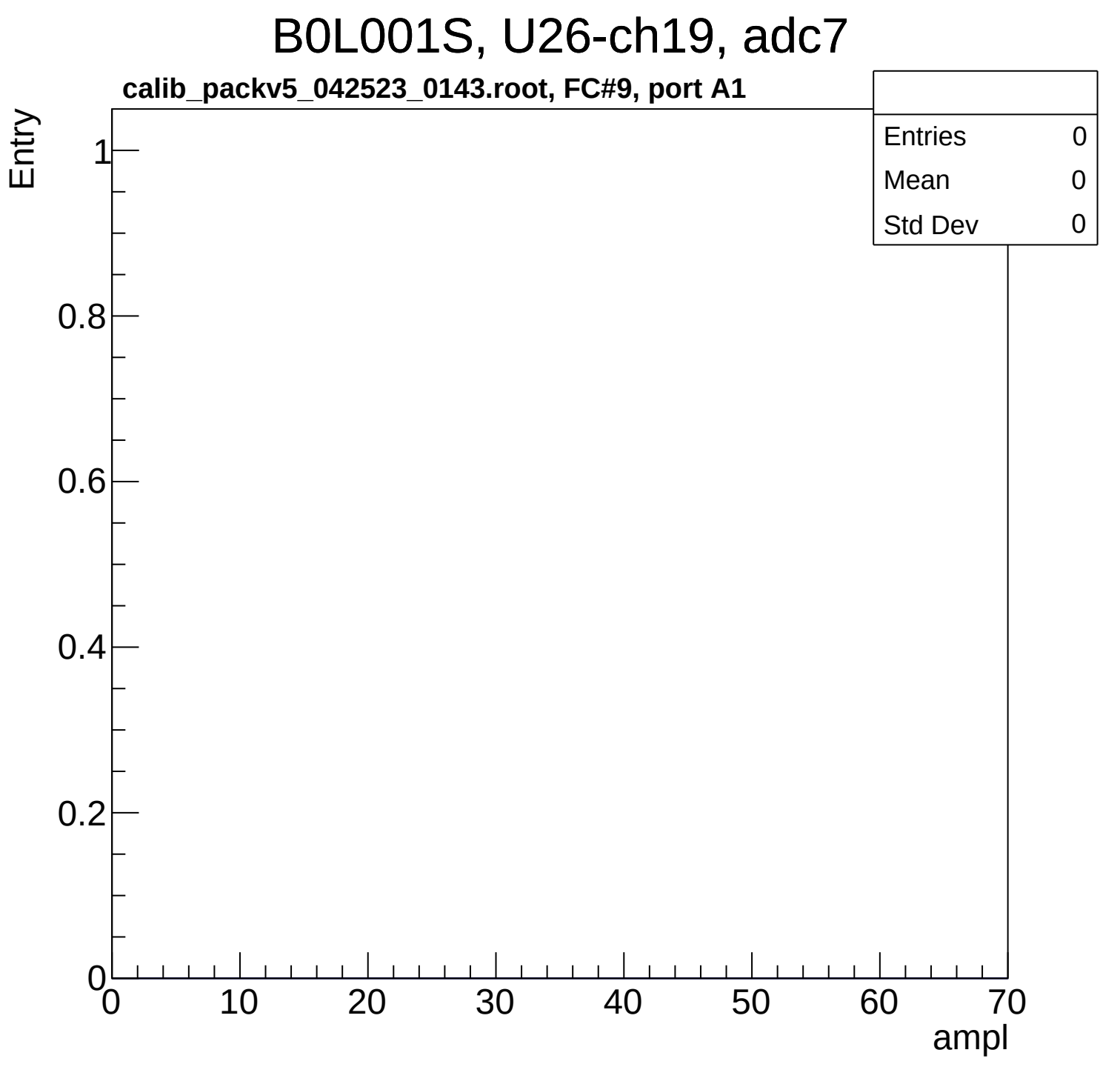

B0L001S, U26-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.67
Std Dev 3.119
Gaus mean : 46.3668
Gaus Width: 3.4346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

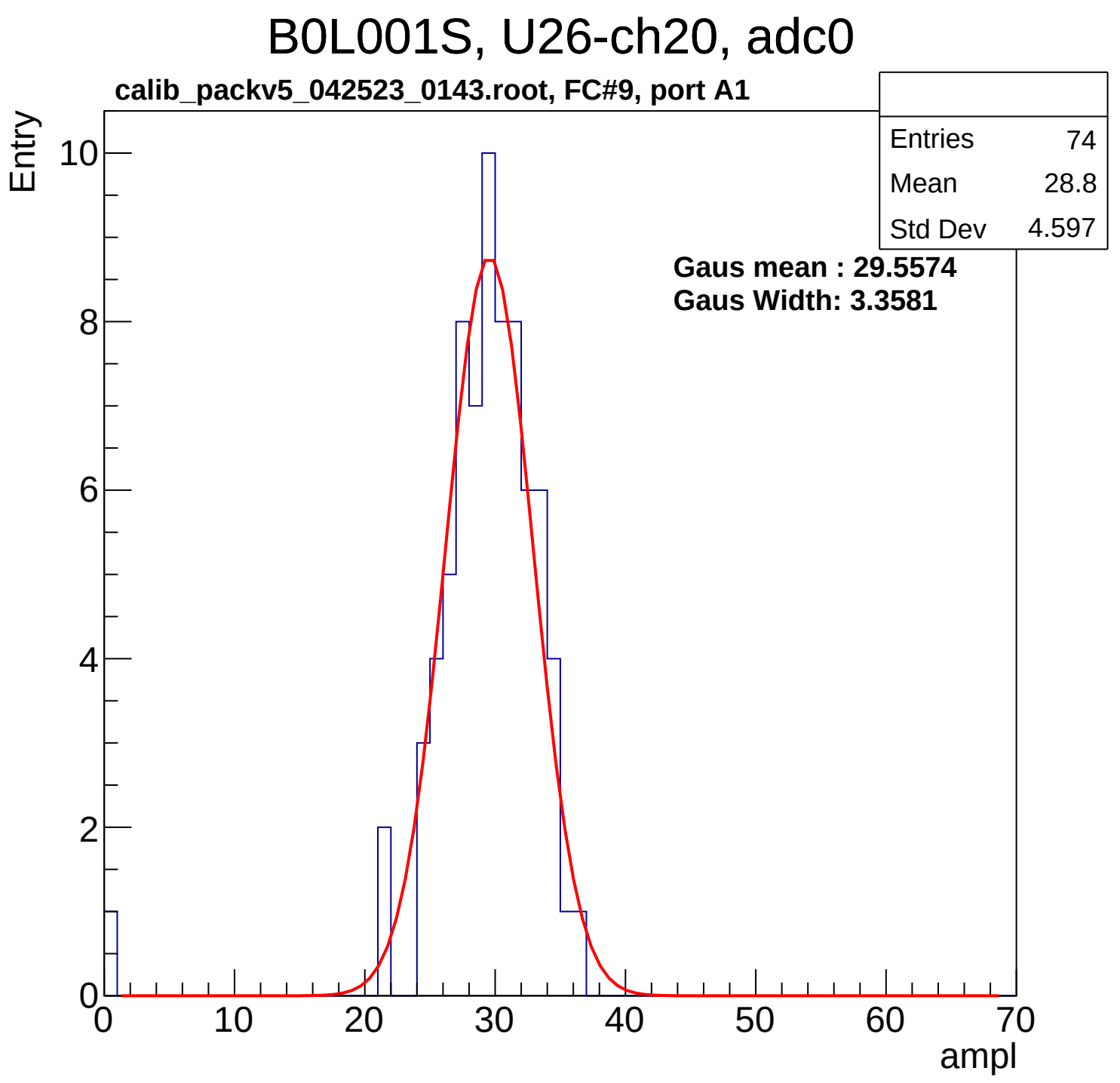

B0L001S, U26-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 52.22
Std Dev 2.997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

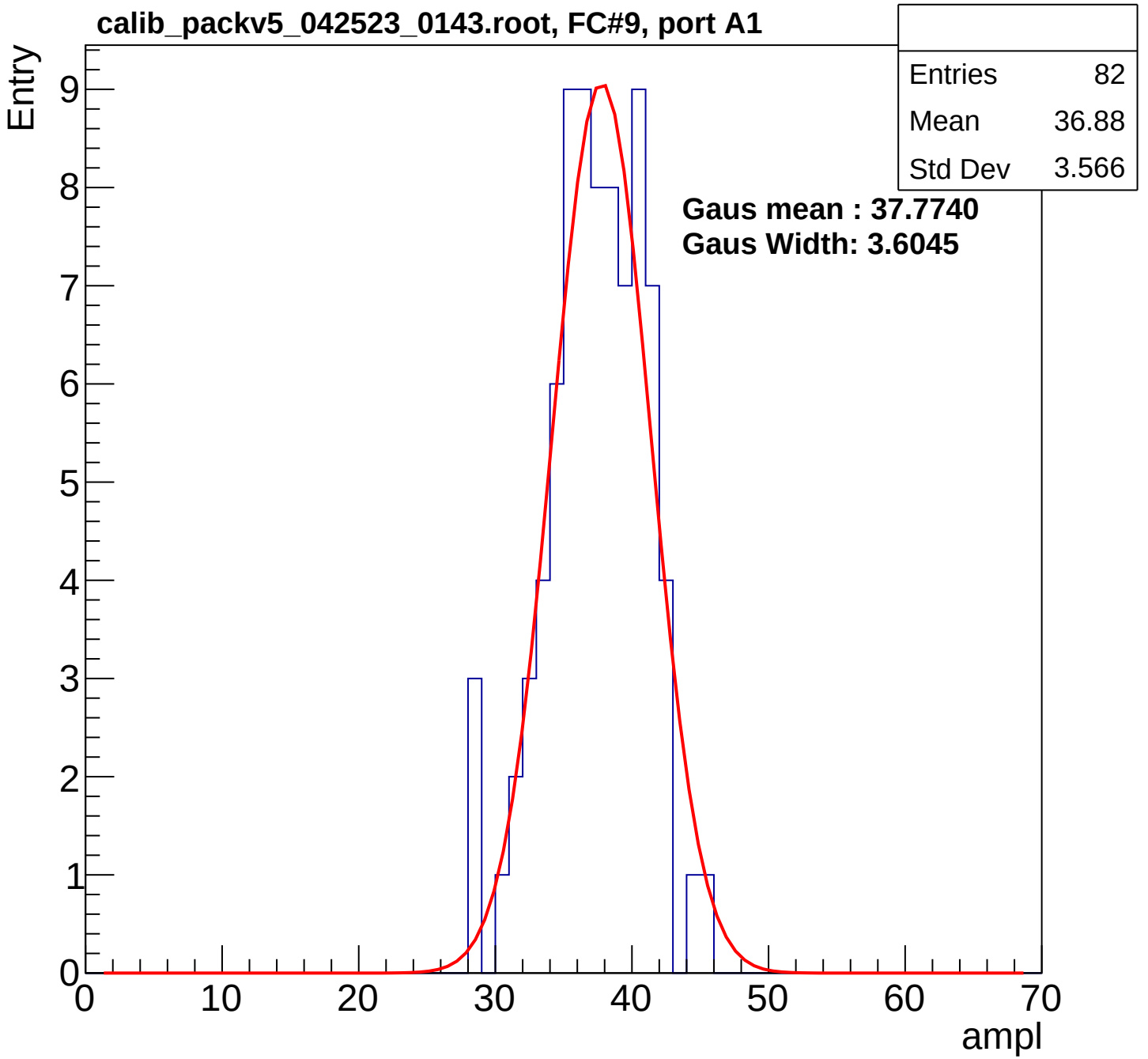

B0L001S, U26-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.33
Std Dev 2.367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

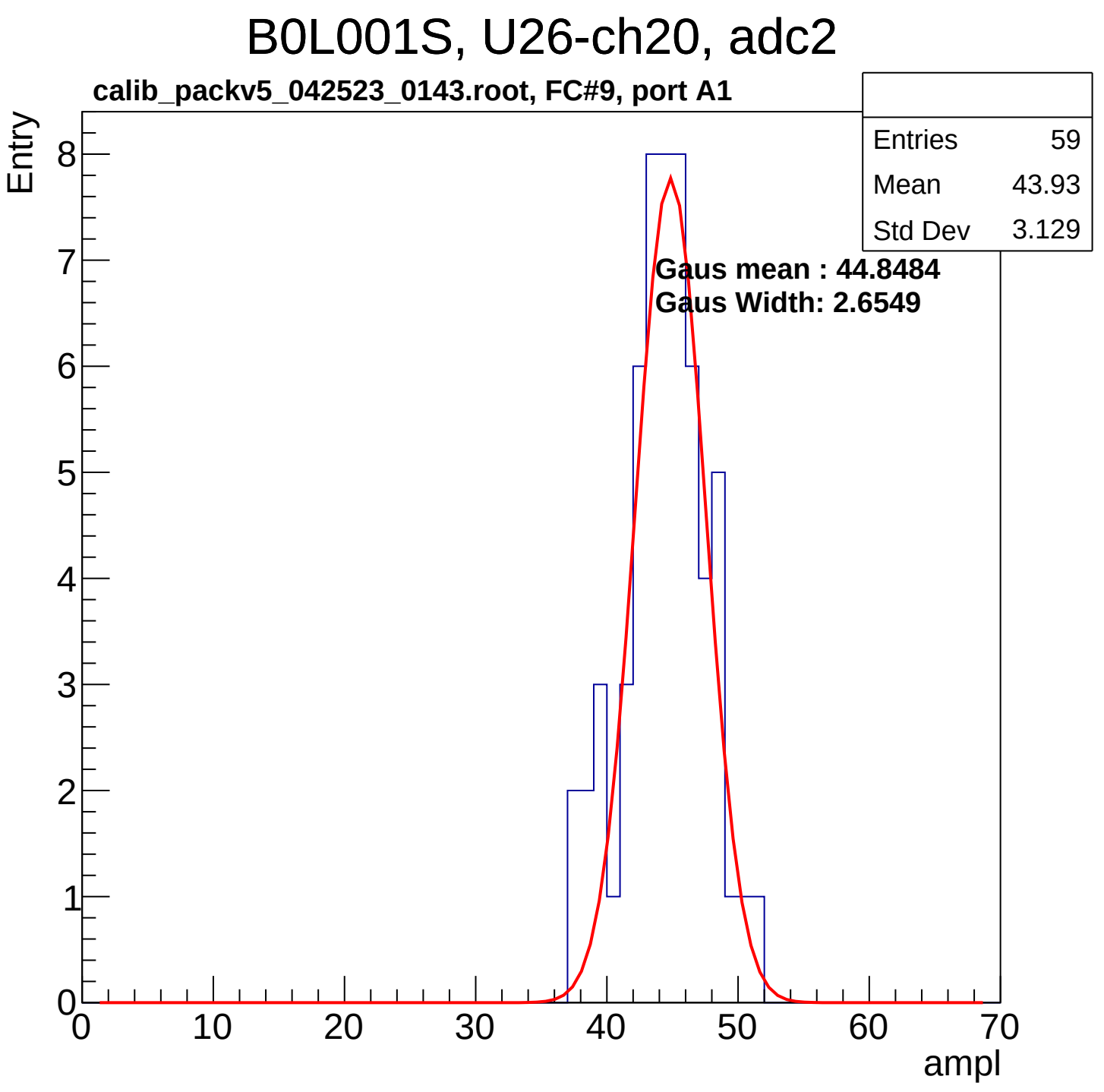

B0L001S, U26-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 59
Std Dev 12.37
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

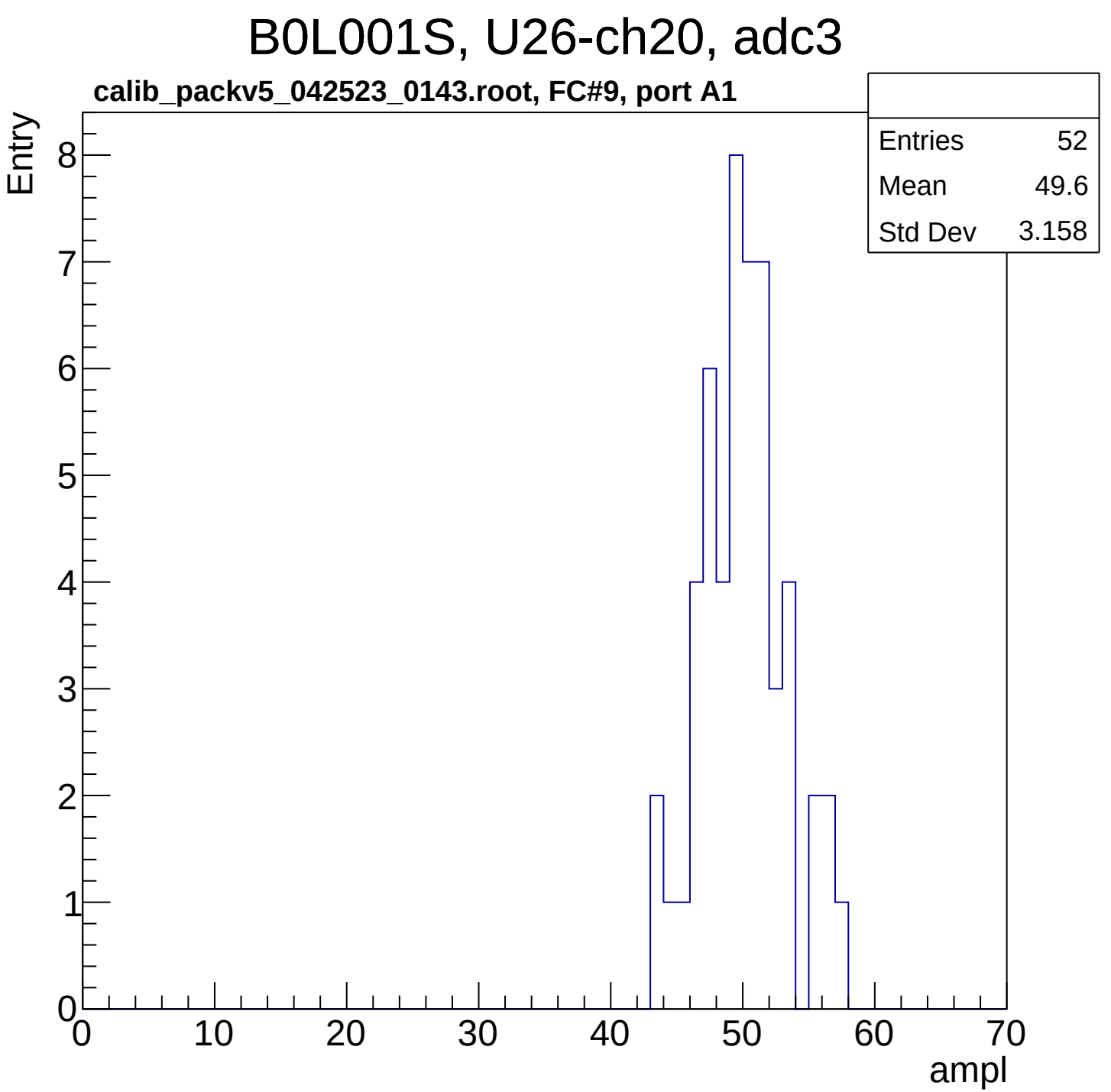

B0L001S, U26-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

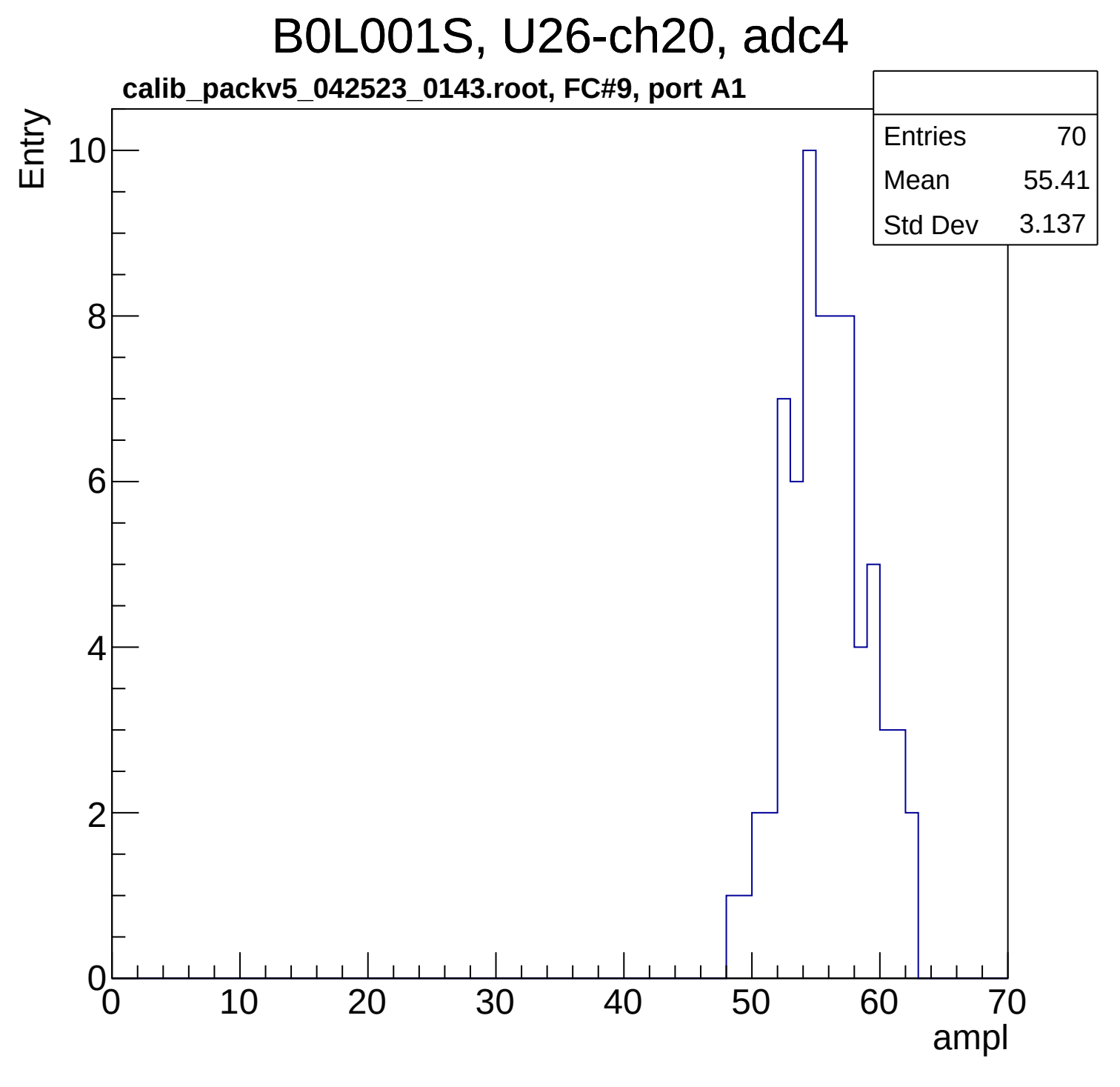

B0L001S, U26-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

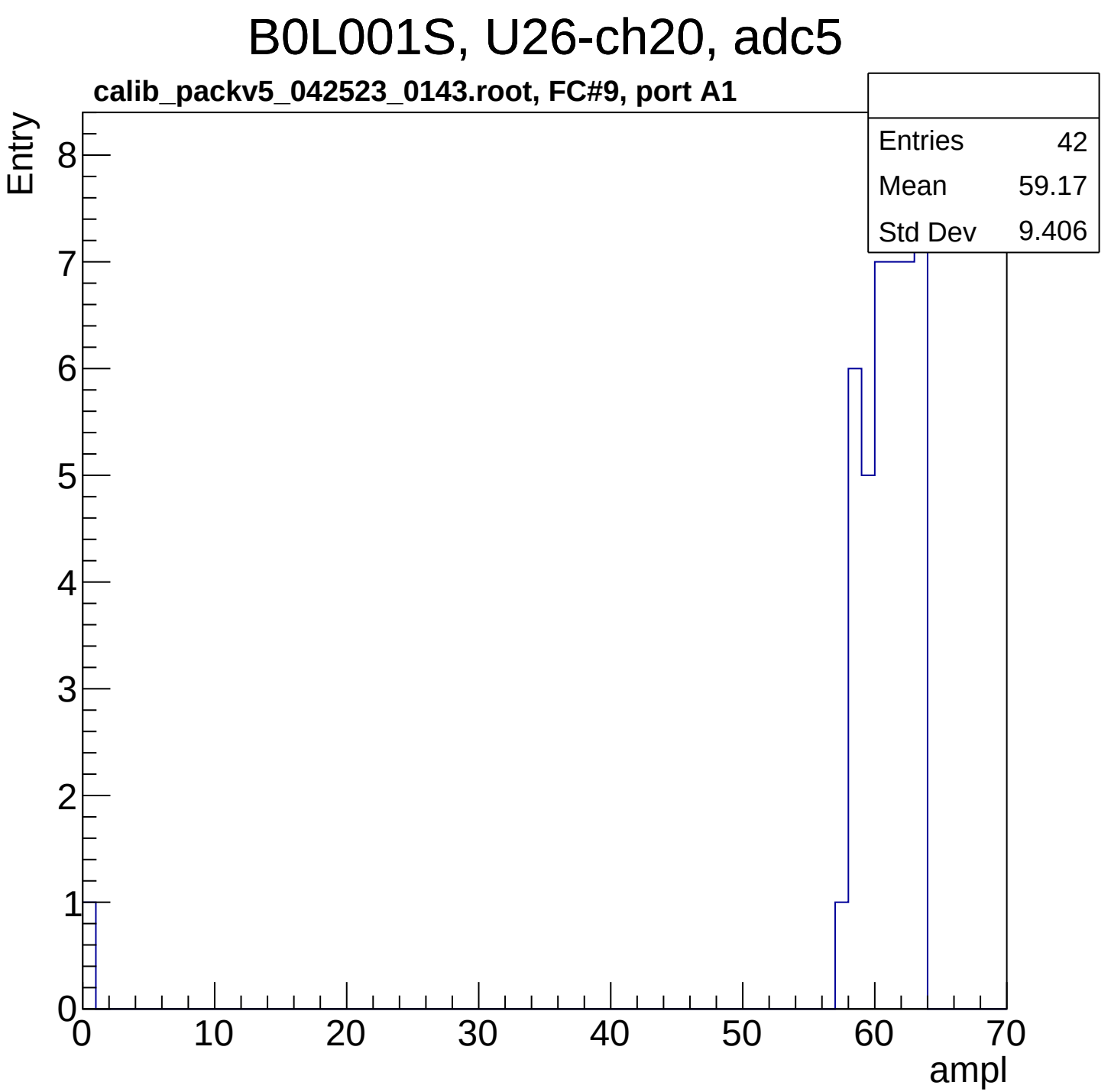

B0L001S, U26-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 28.8
Std Dev 4.597
Gaus mean : 29.5574
Gaus Width: 3.3581
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

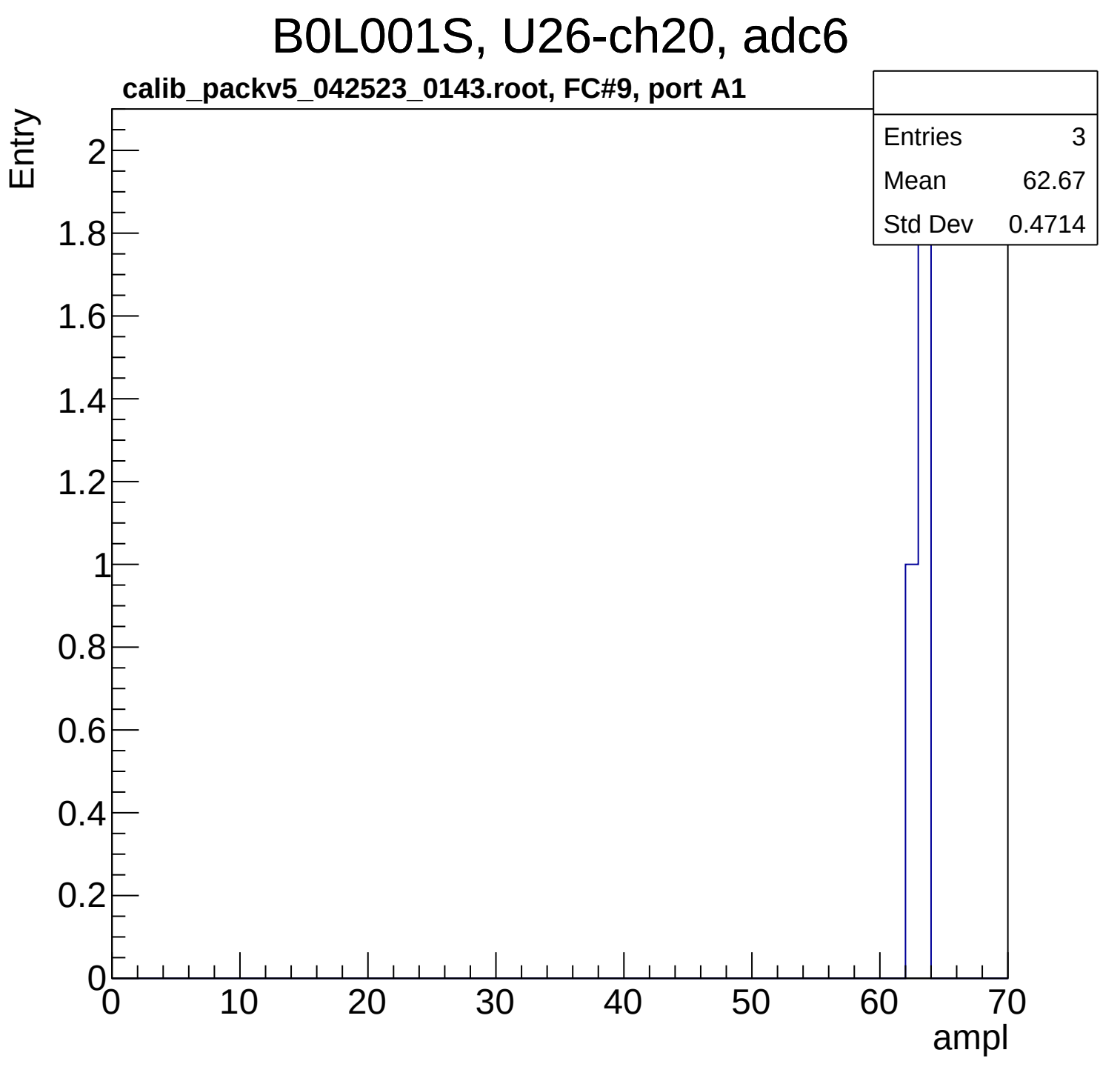

B0L001S, U26-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 36.88
Std Dev 3.566
Gaus mean : 37.7740
Gaus Width: 3.6045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

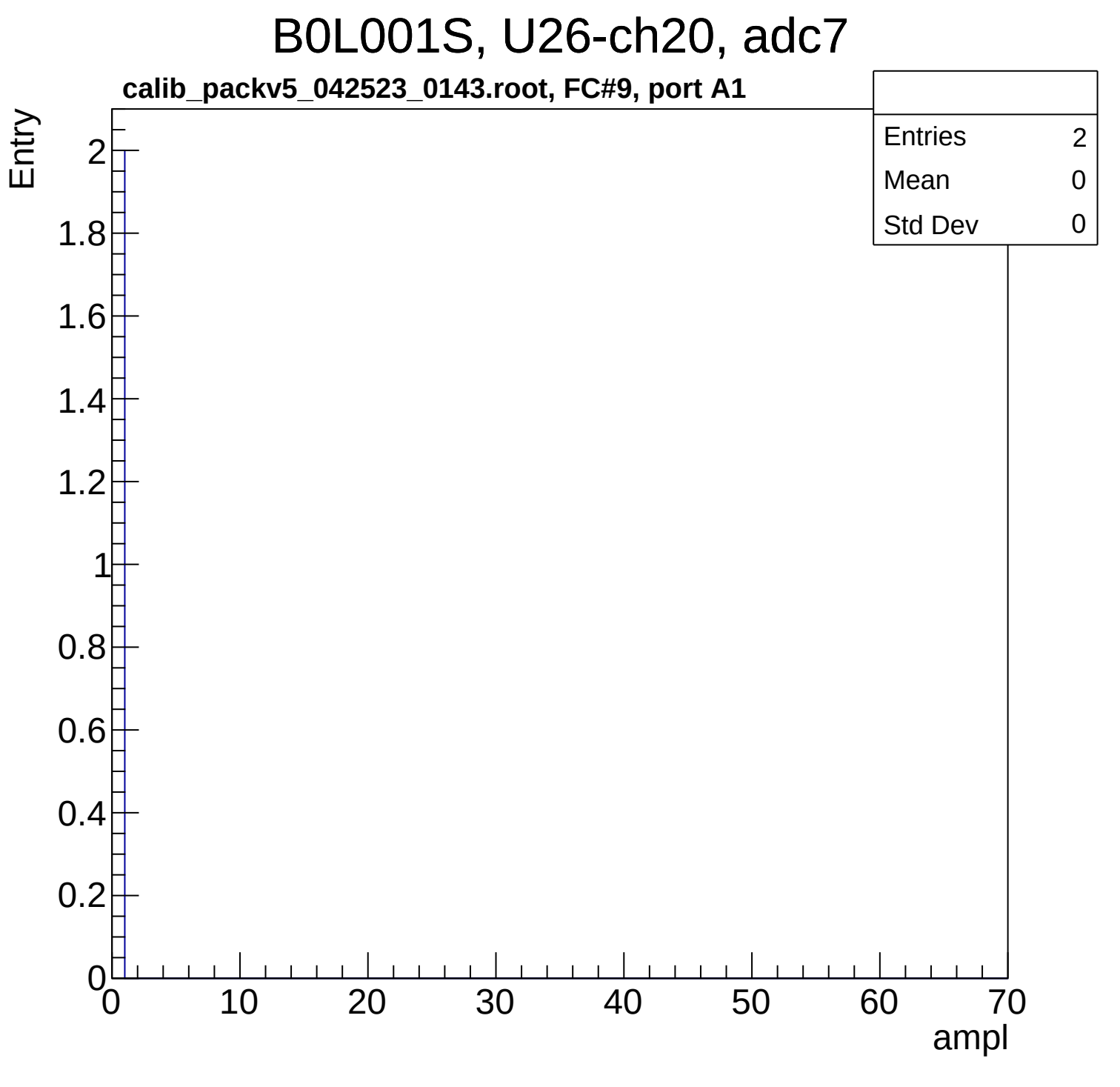

B0L001S, U26-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.93
Std Dev 3.129
Gaus mean : 44.8484
Gaus Width: 2.6549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

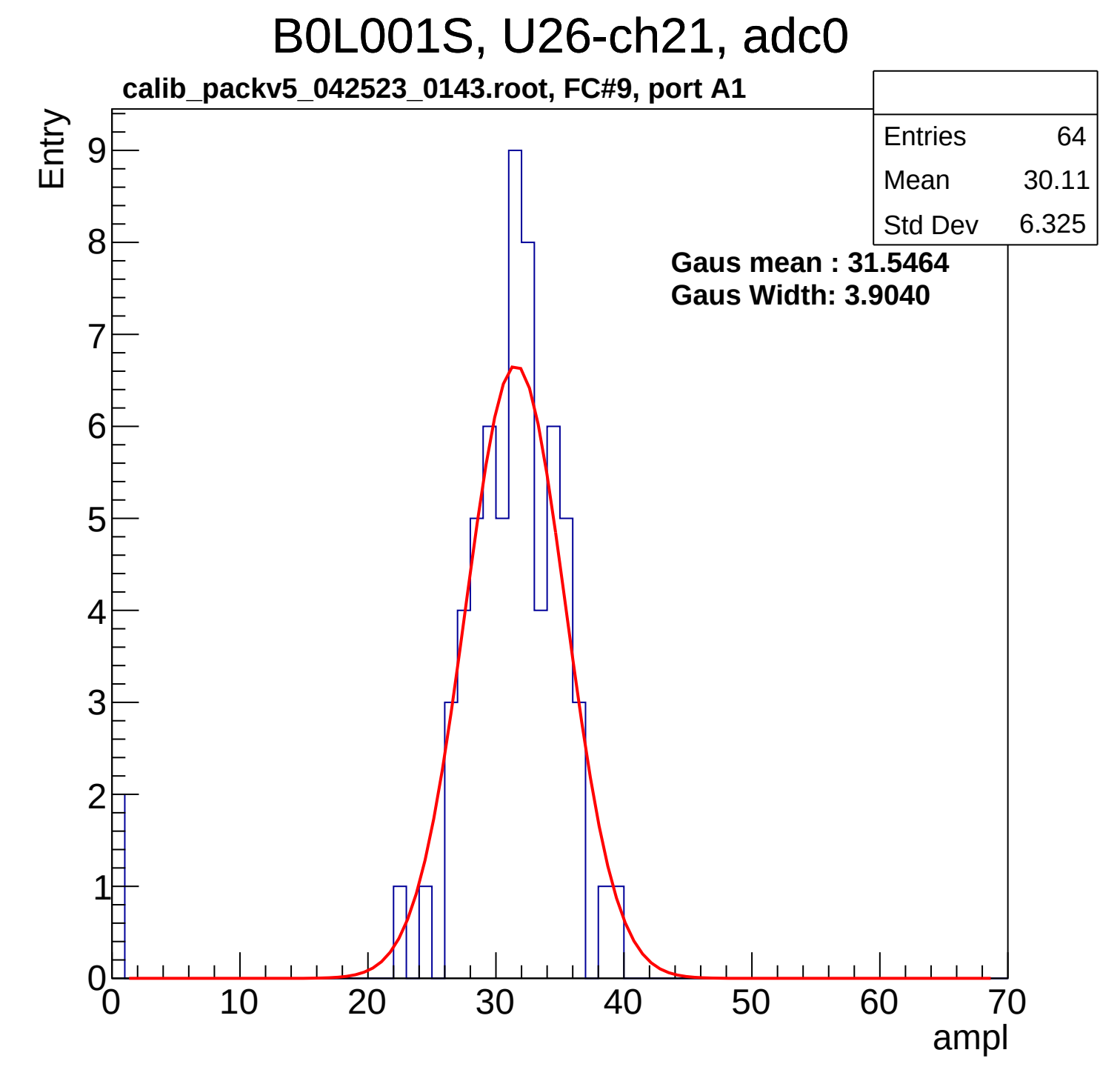

B0L001S, U26-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 49.6
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

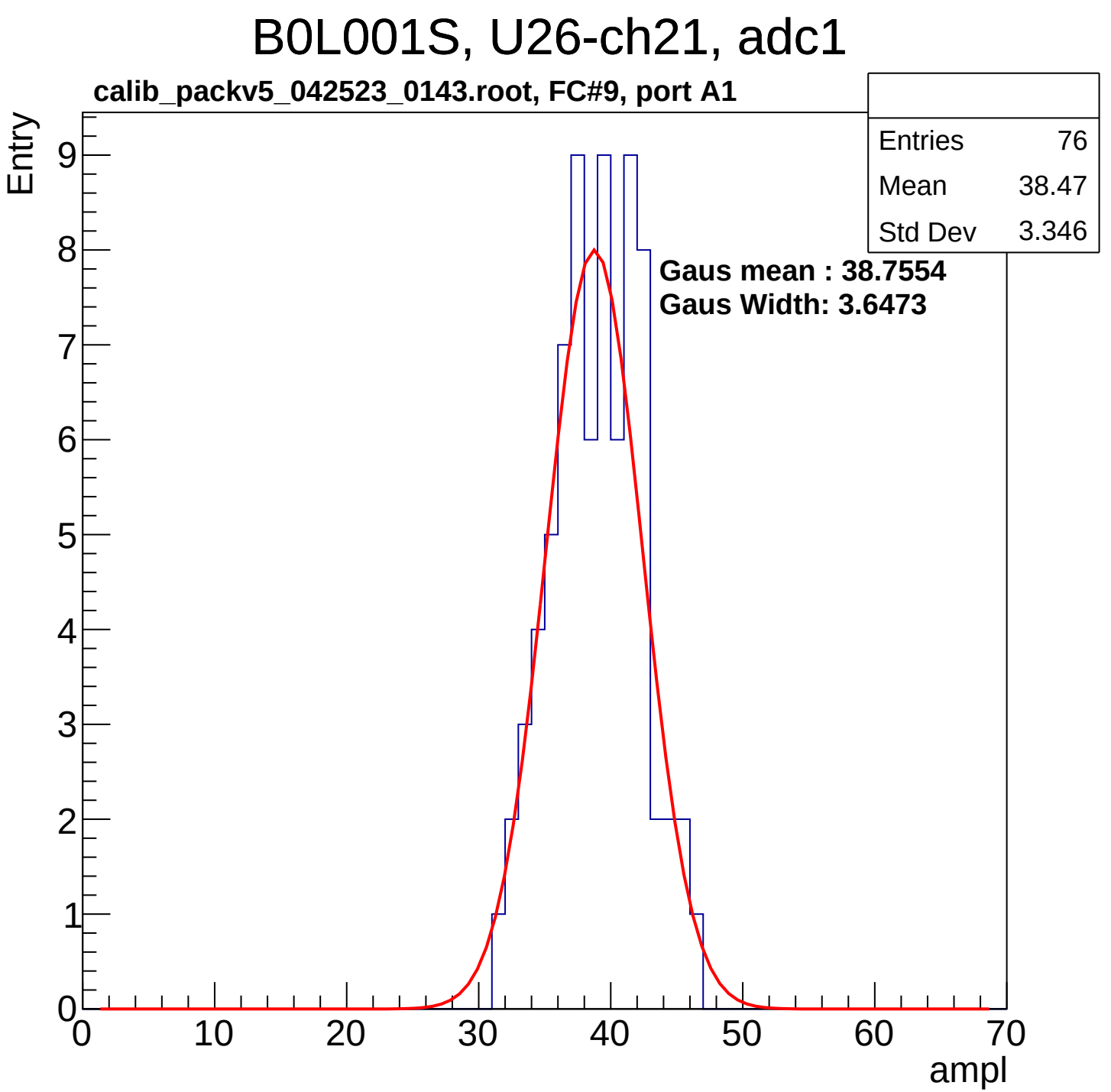

B0L001S, U26-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 55.41
Std Dev 3.137
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

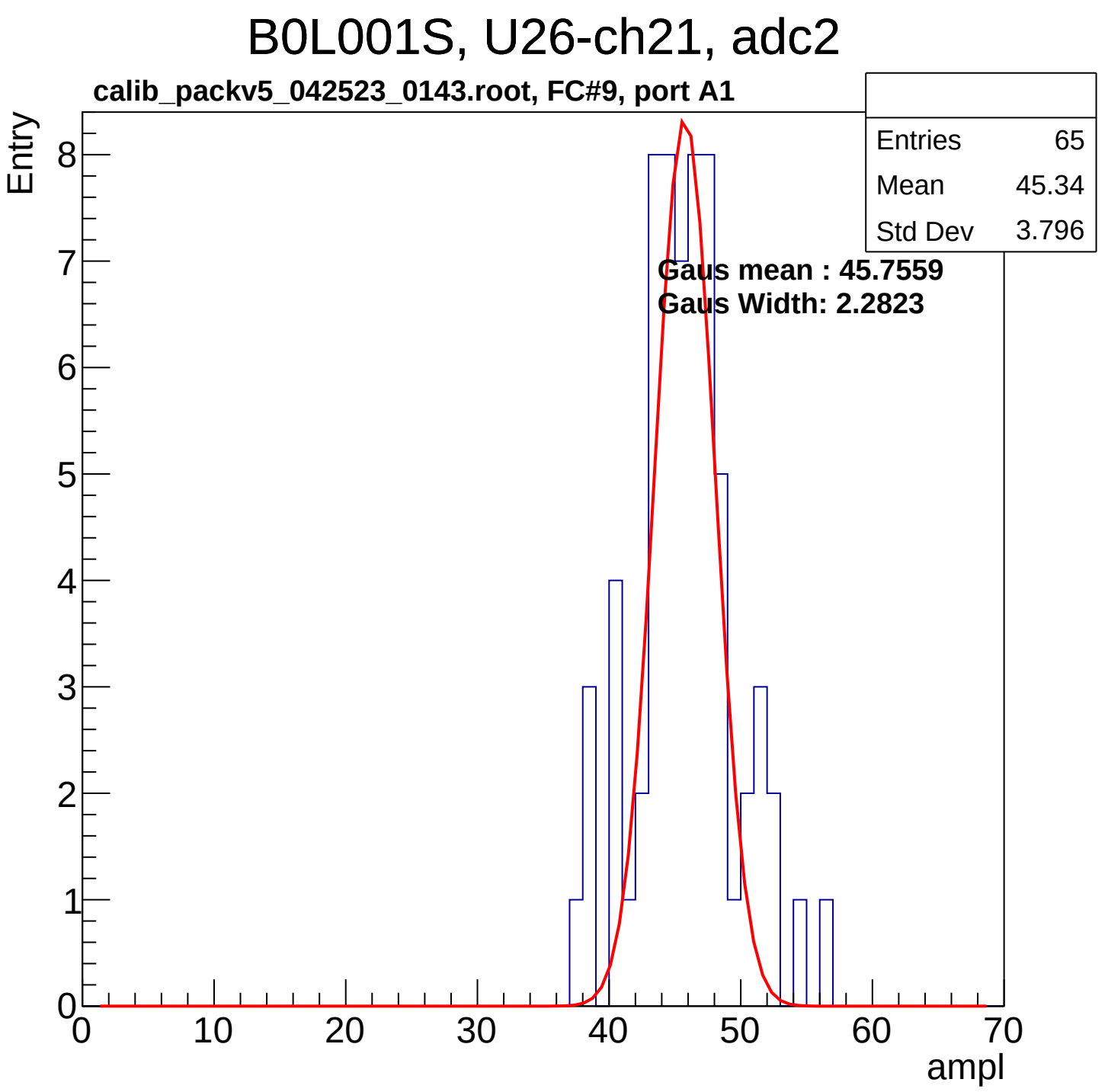

B0L001S, U26-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.17
Std Dev 9.406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

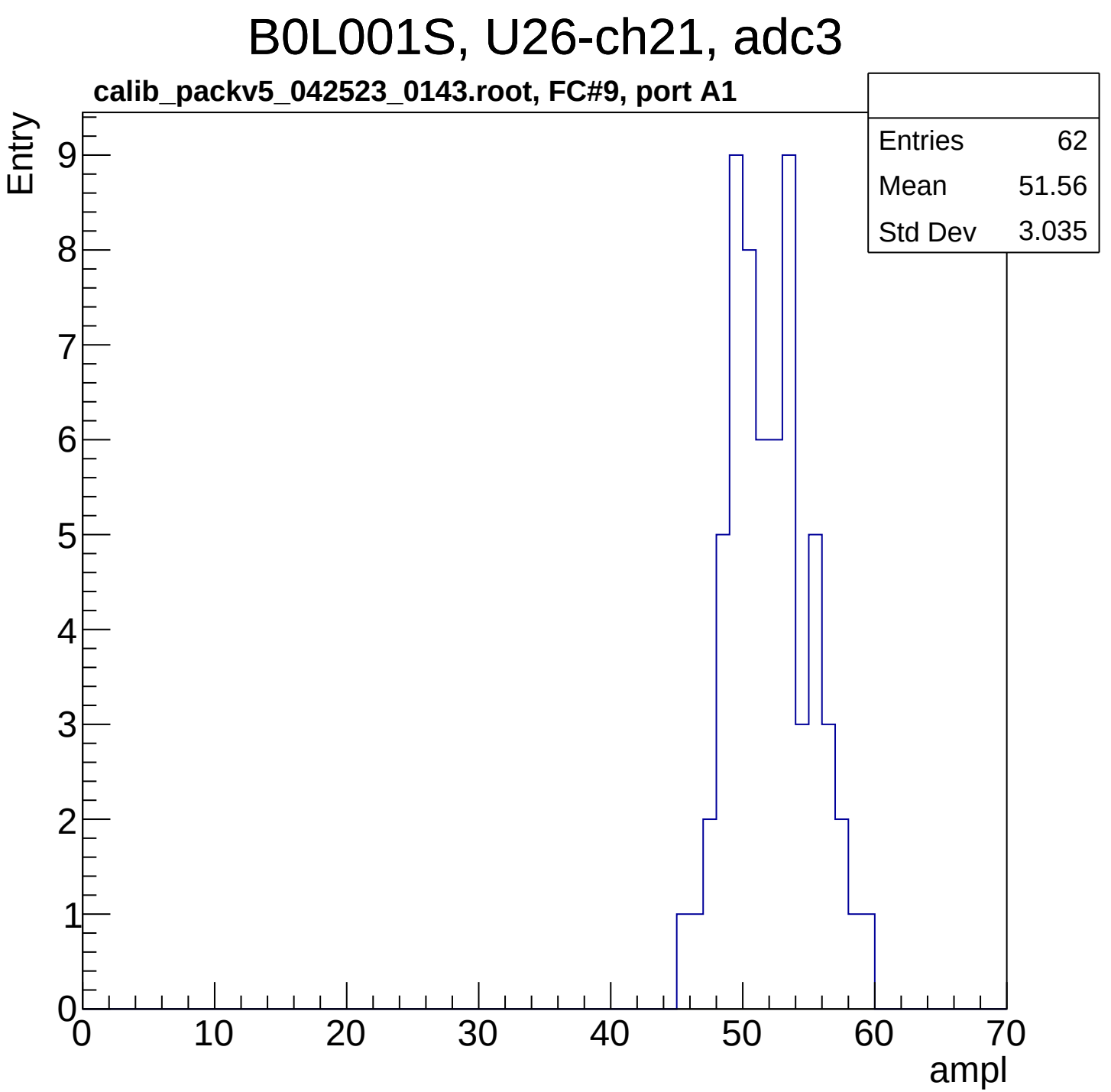

B0L001S, U26-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

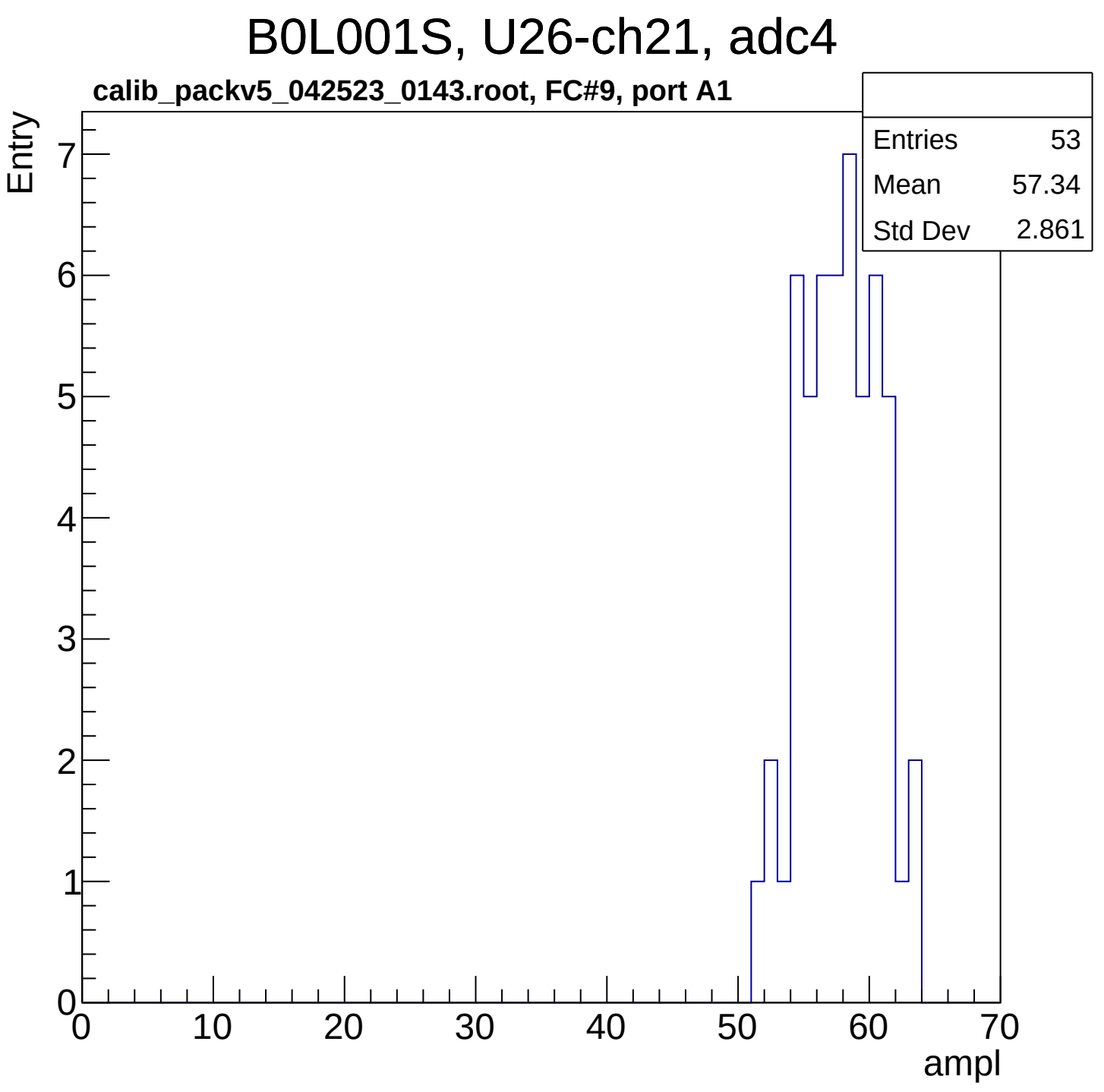

B0L001S, U26-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

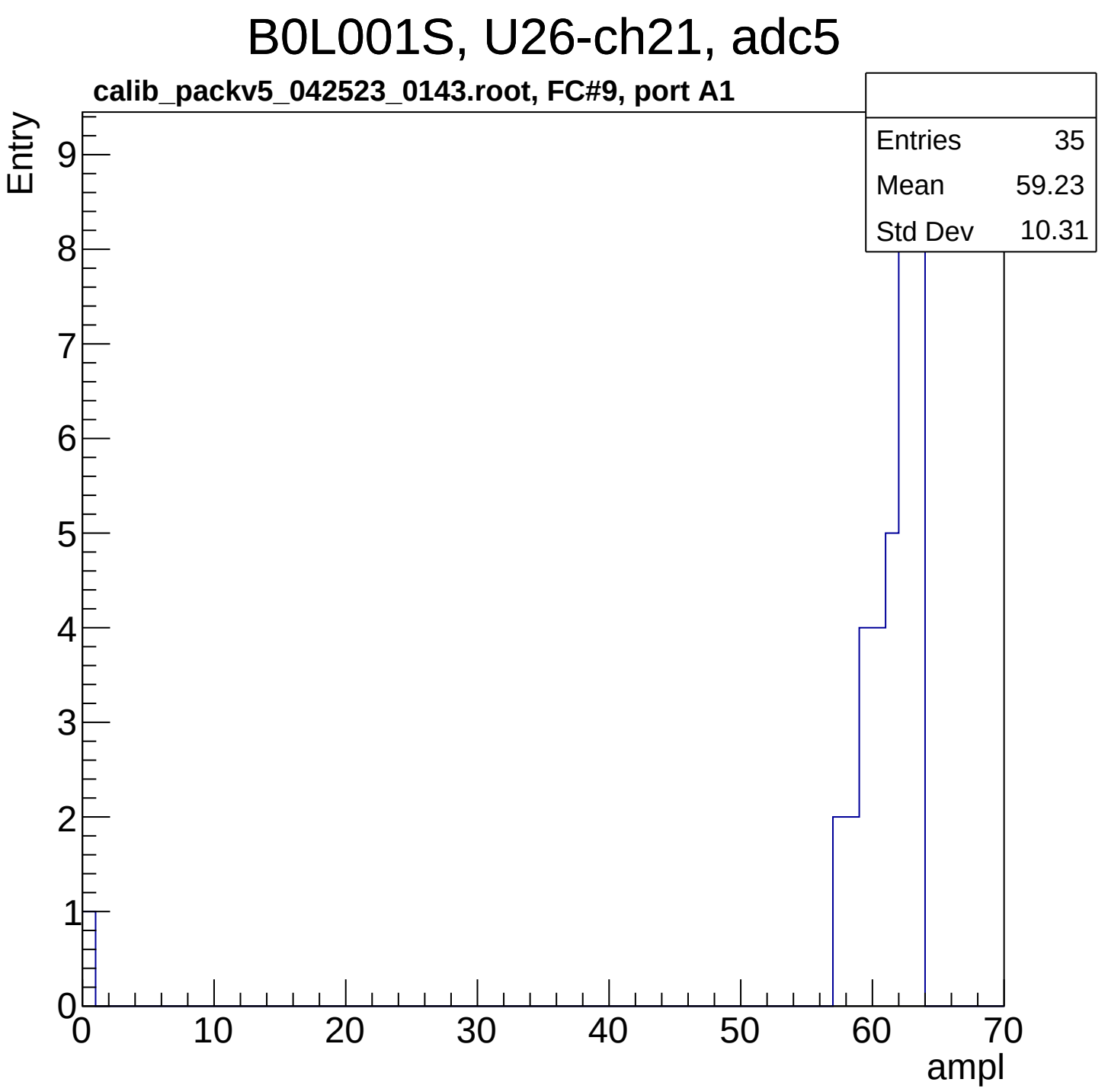

B0L001S, U26-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.11
Std Dev 6.325
Gaus mean : 31.5464
Gaus Width: 3.9040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

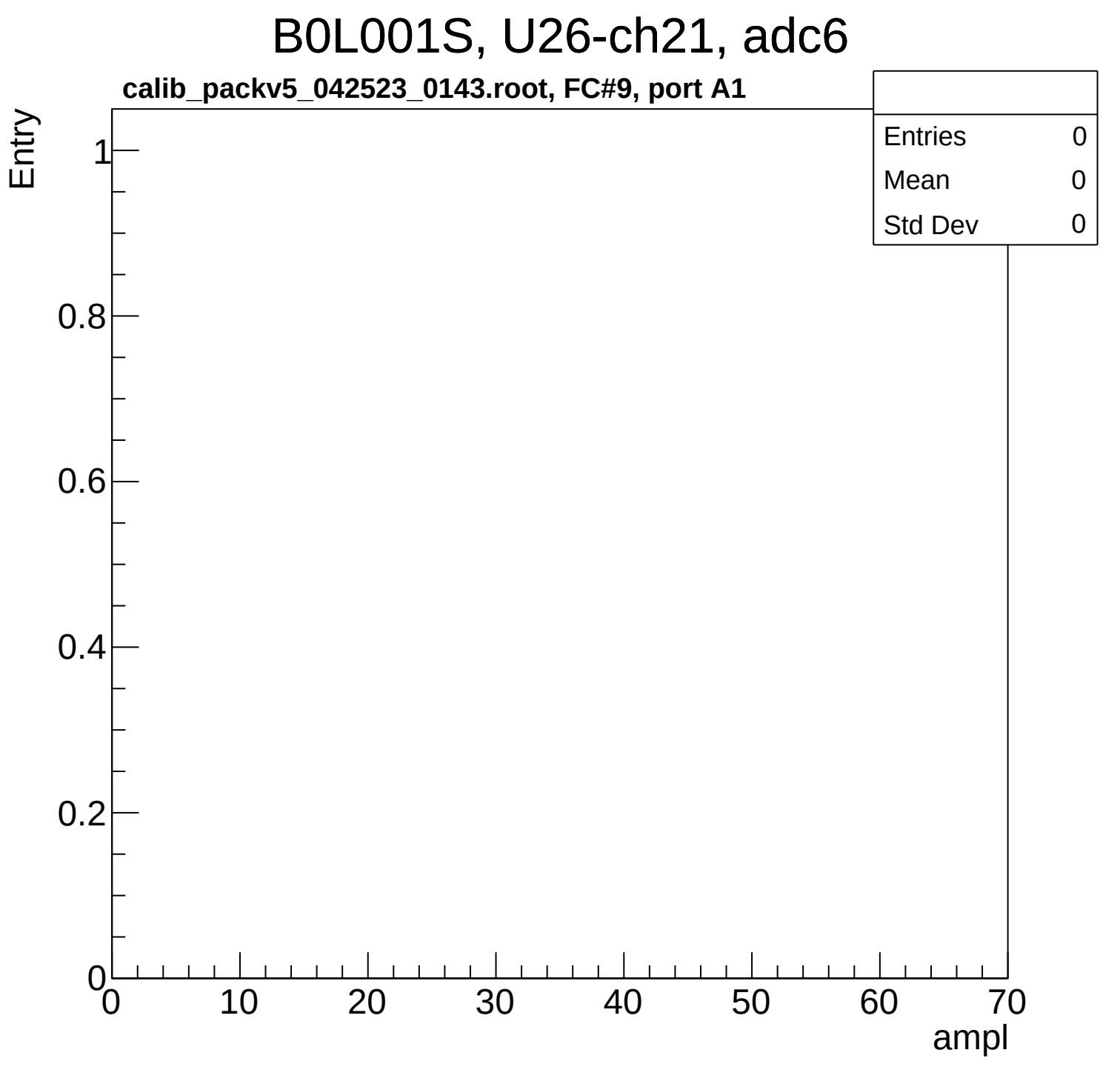

B0L001S, U26-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.47
Std Dev 3.346
Gaus mean : 38.7554
Gaus Width: 3.6473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

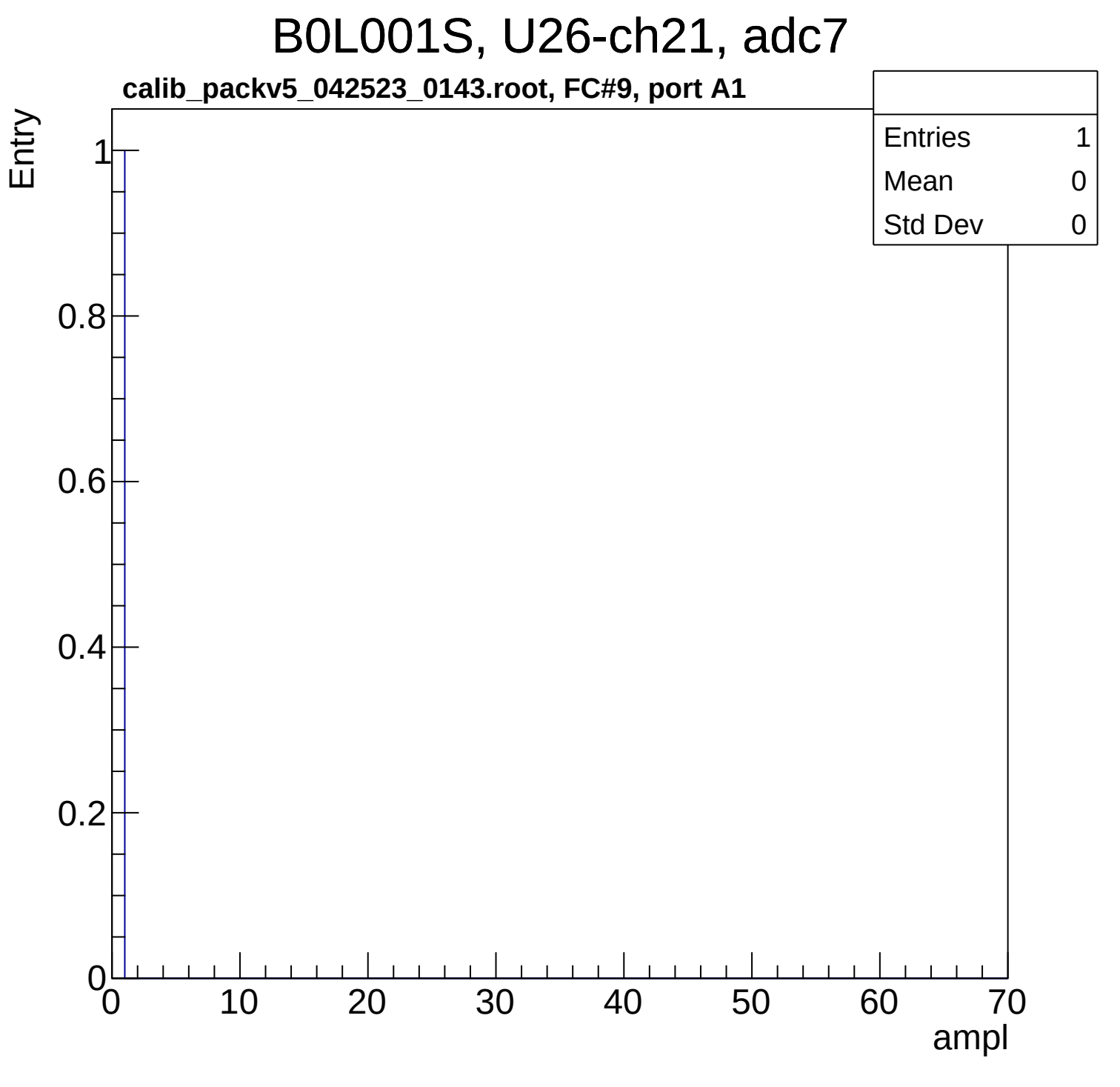

B0L001S, U26-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.34
Std Dev 3.796
Gaus mean : 45.7559
Gaus Width: 2.2823
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

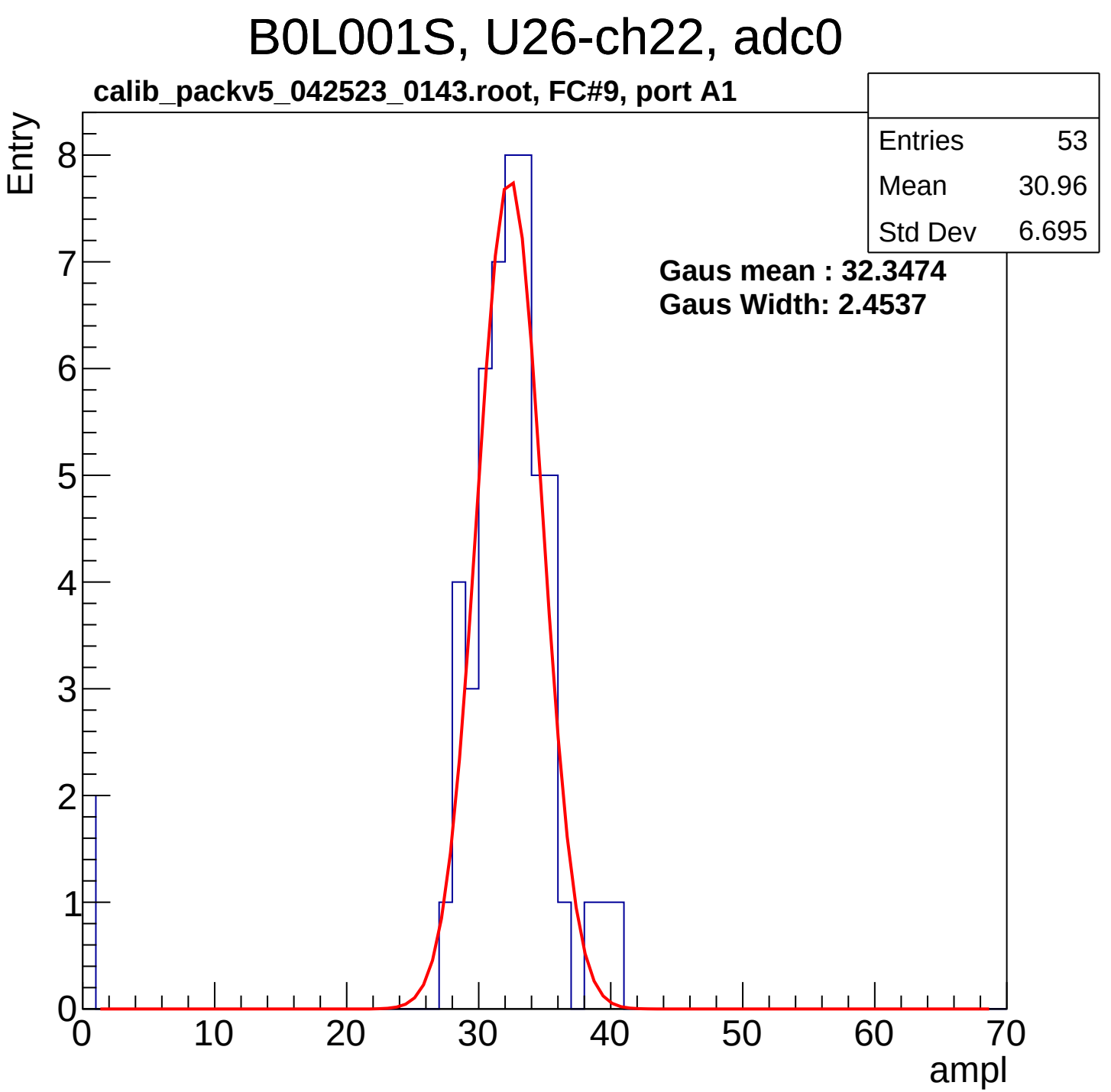

B0L001S, U26-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.56
Std Dev 3.035
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.34
Std Dev 2.861
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.23
Std Dev 10.31
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 30.96
Std Dev 6.695
Gaus mean : 32.3474
Gaus Width: 2.4537
Entry
ampl
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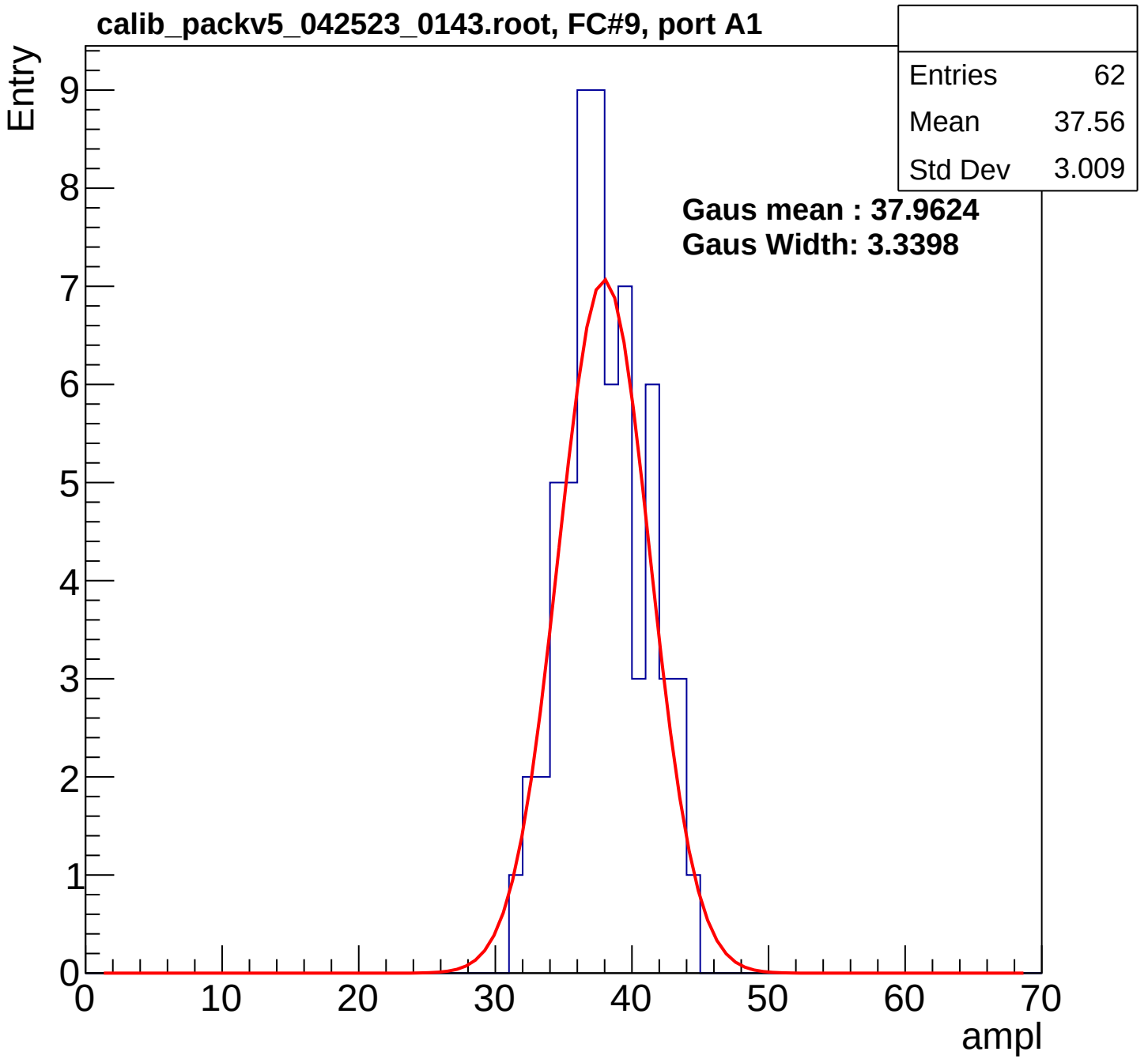
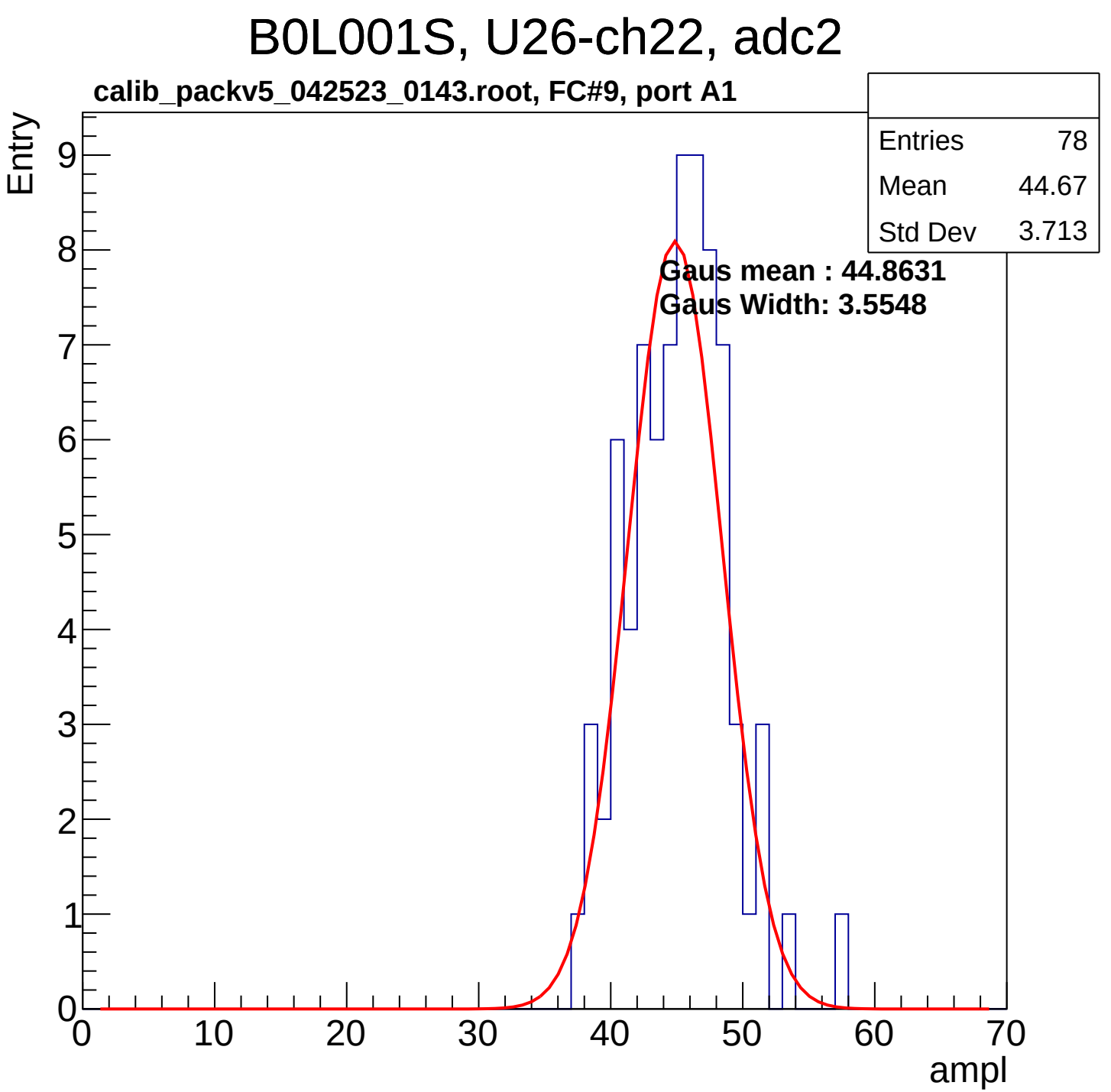
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.56
Std Dev 3.009
Gaus mean : 37.9624
Gaus Width: 3.3398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.67
Std Dev 3.713
Gaus mean : 44.8631
Gaus Width: 3.5548
Entry
ampl
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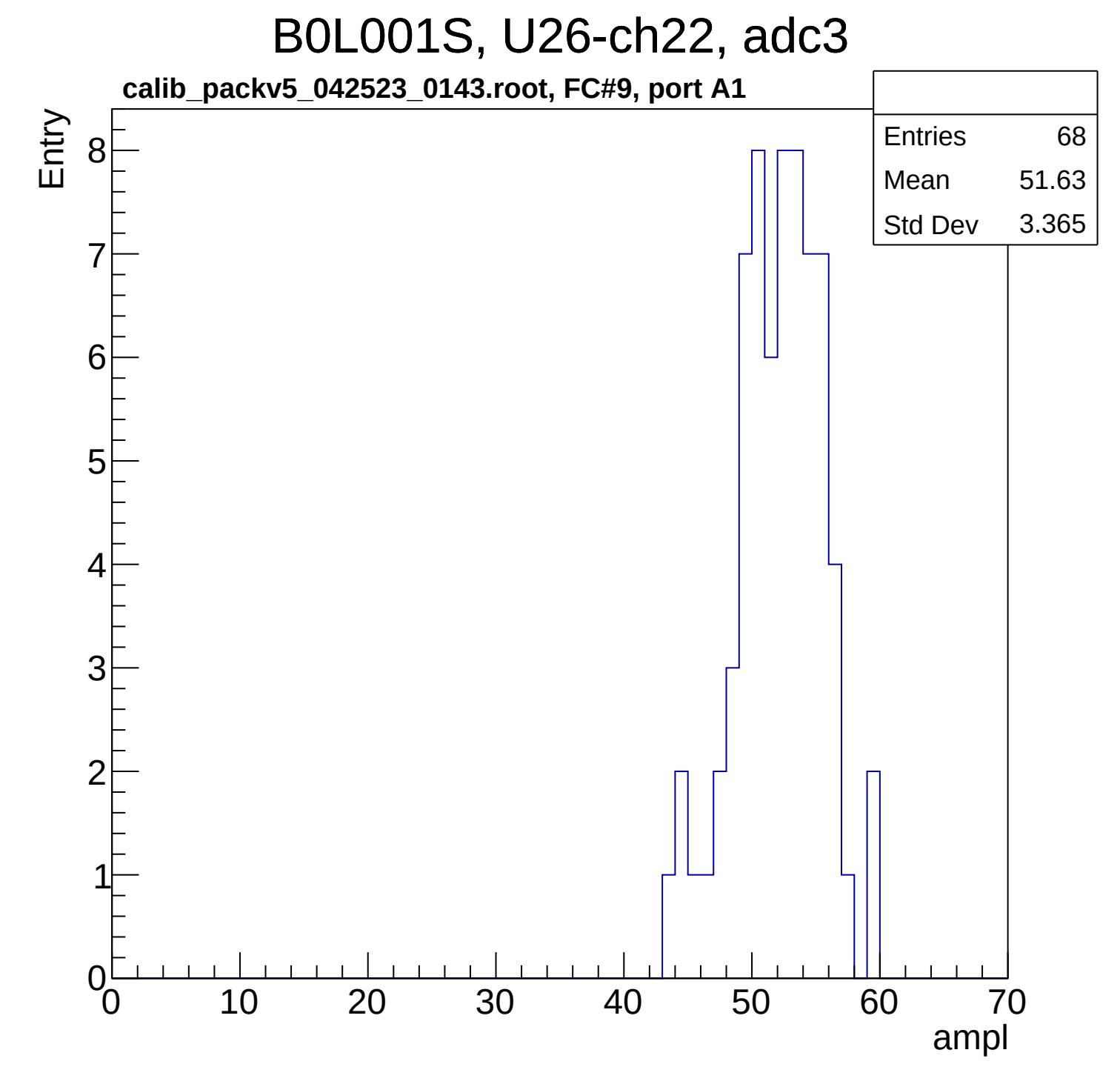
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.63
Std Dev 3.365
Entry
ampl
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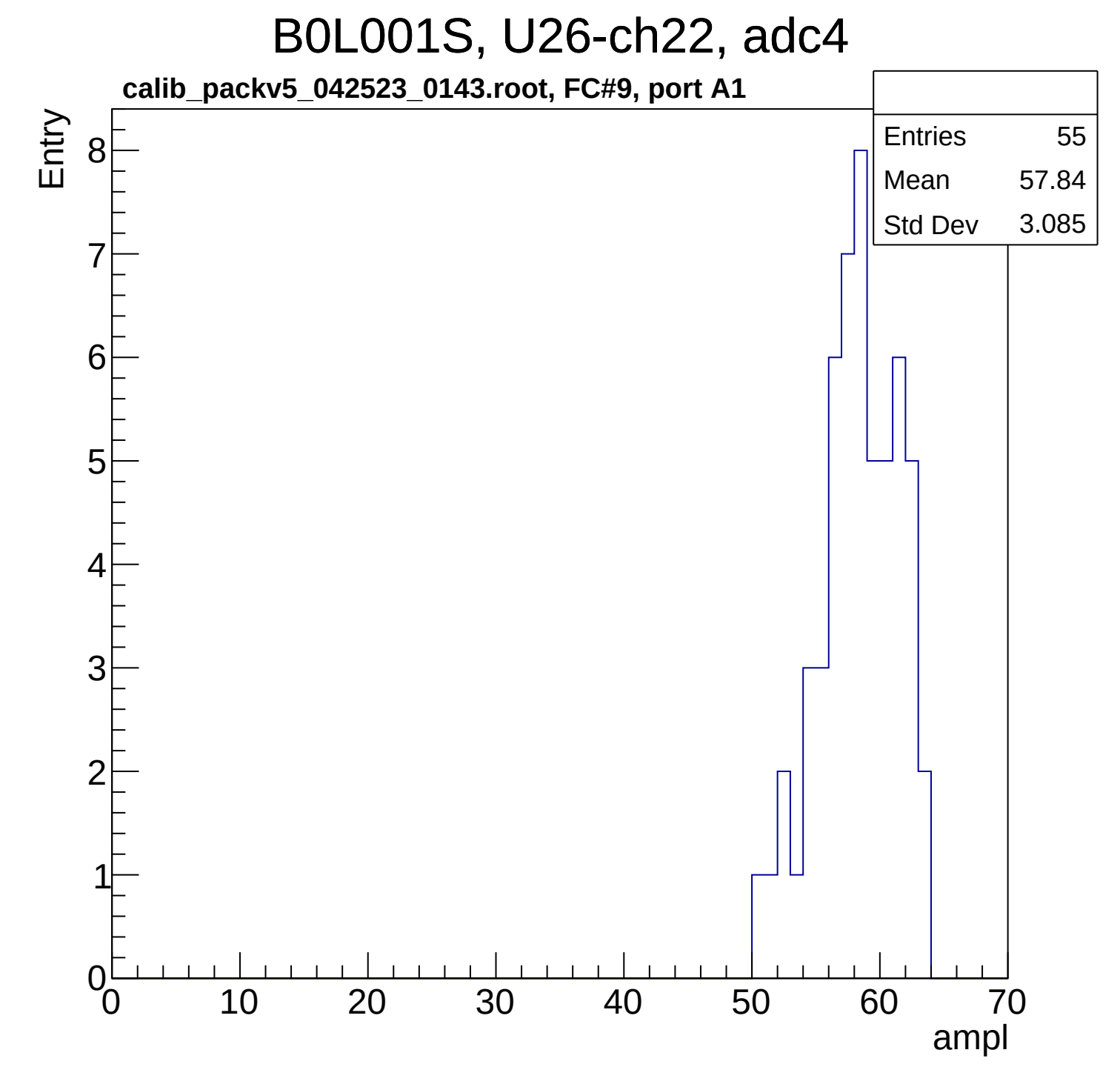
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.84
Std Dev 3.085
Entry
ampl
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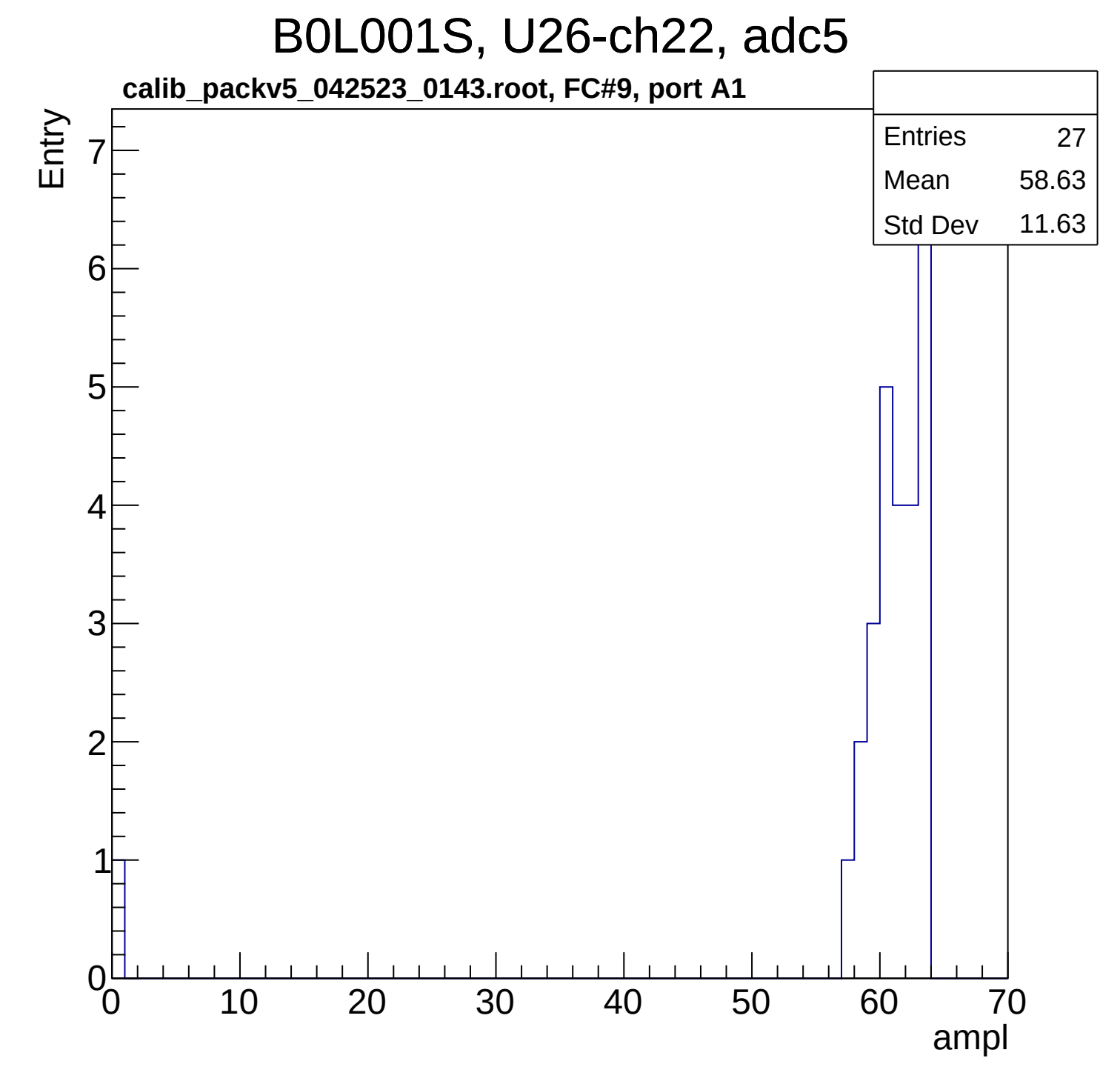
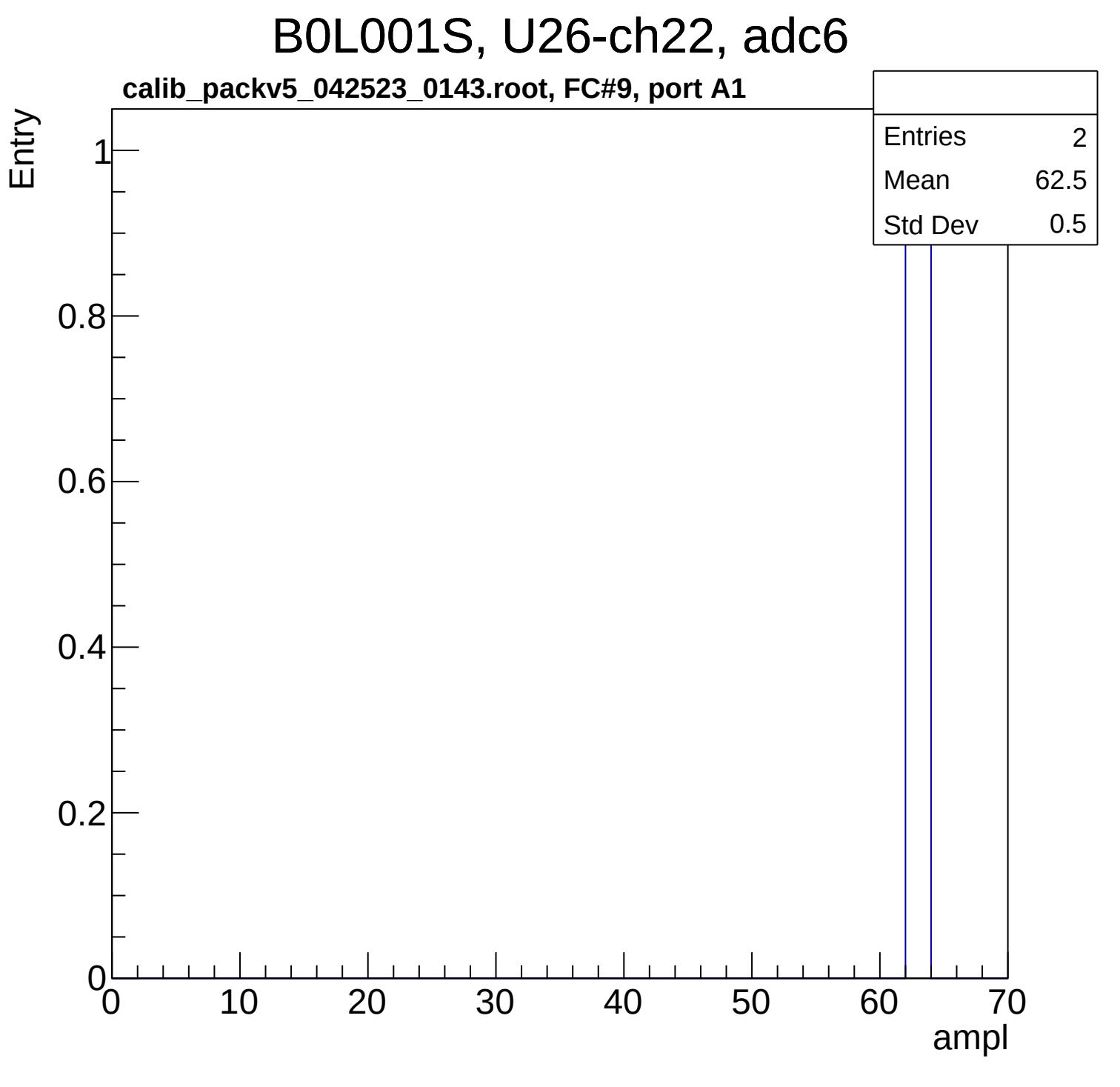
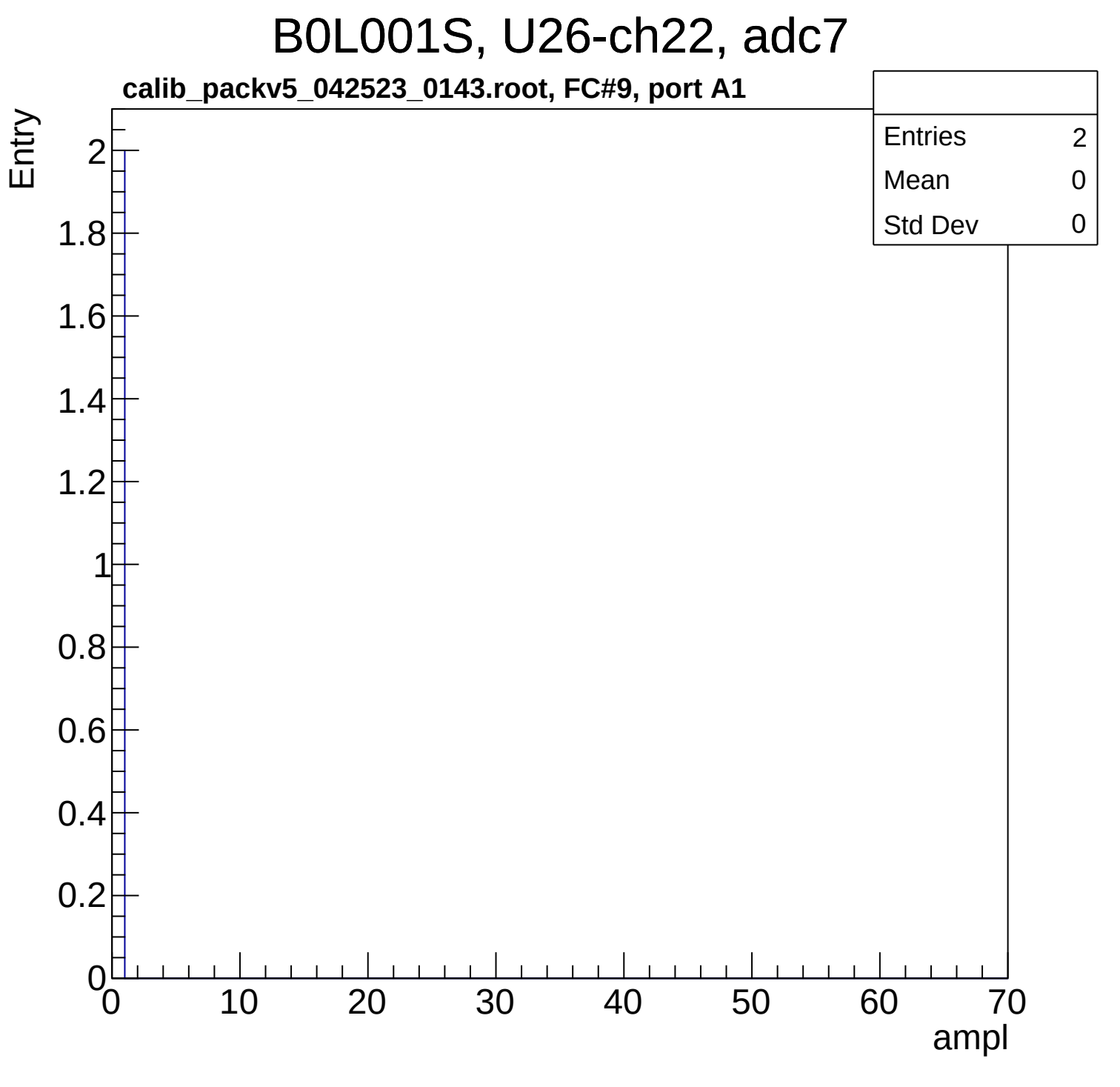
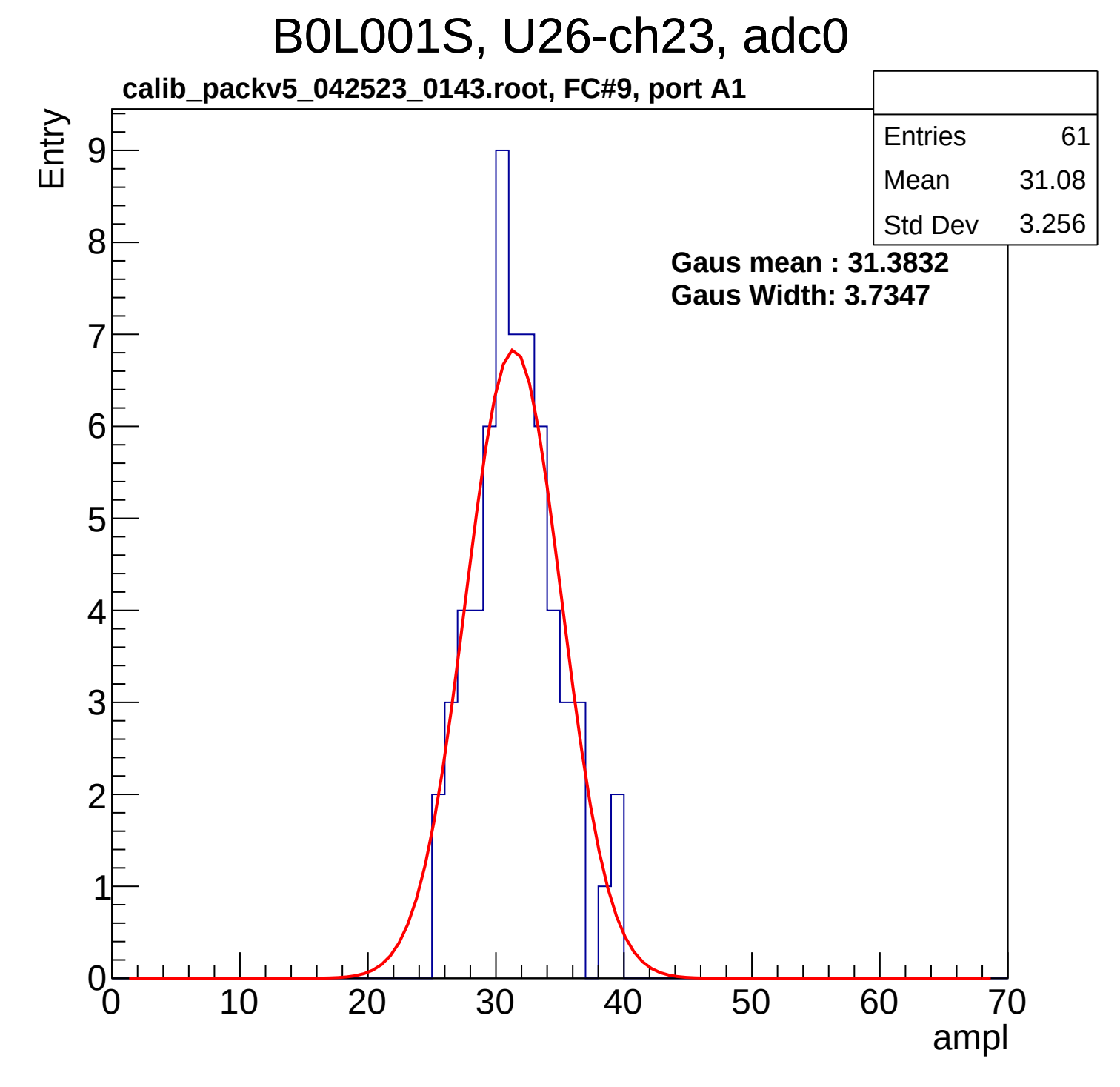
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.63
Std Dev 11.63
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.08
Std Dev 3.256
Gaus mean : 31.3832
Gaus Width: 3.7347
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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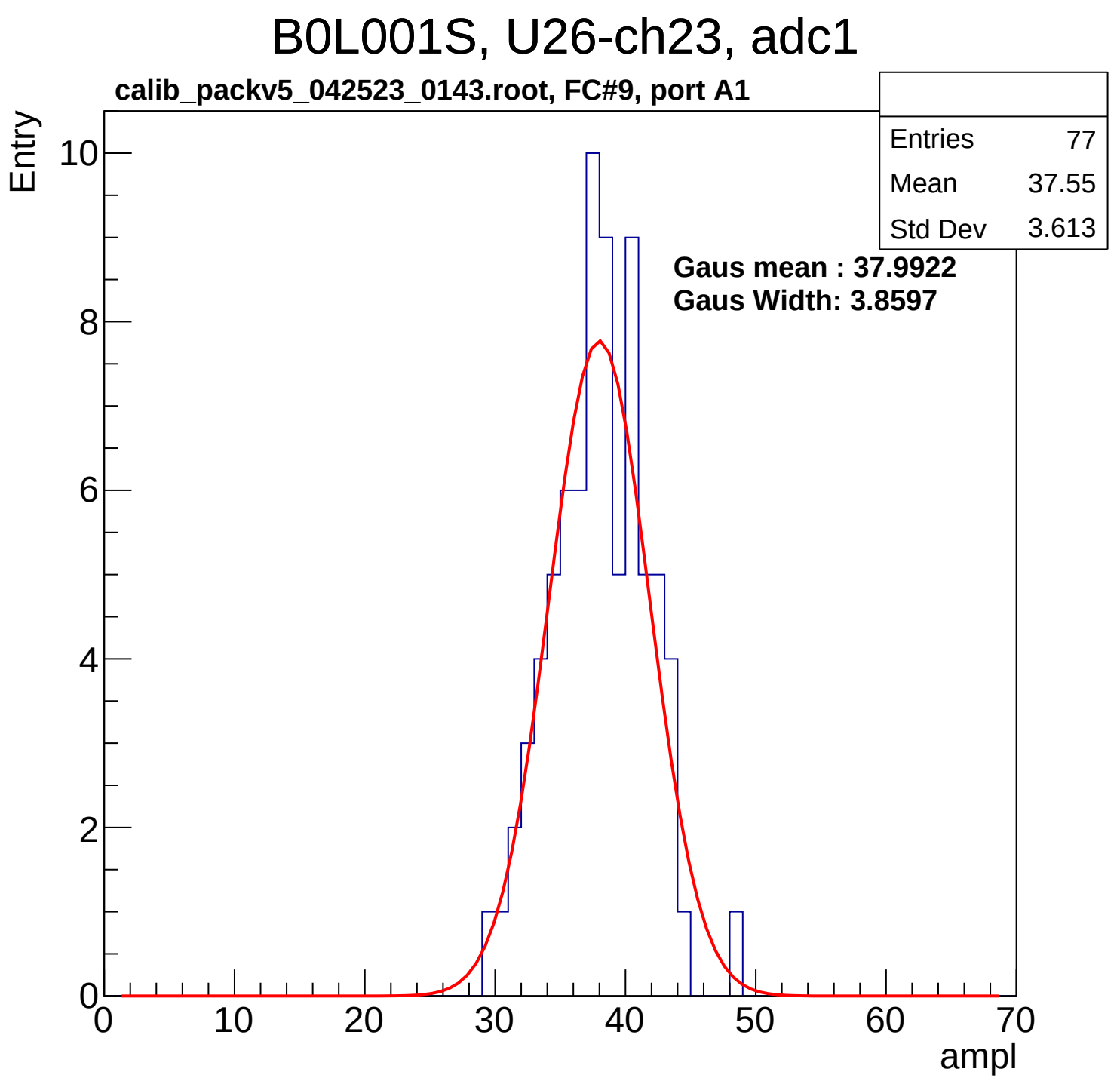
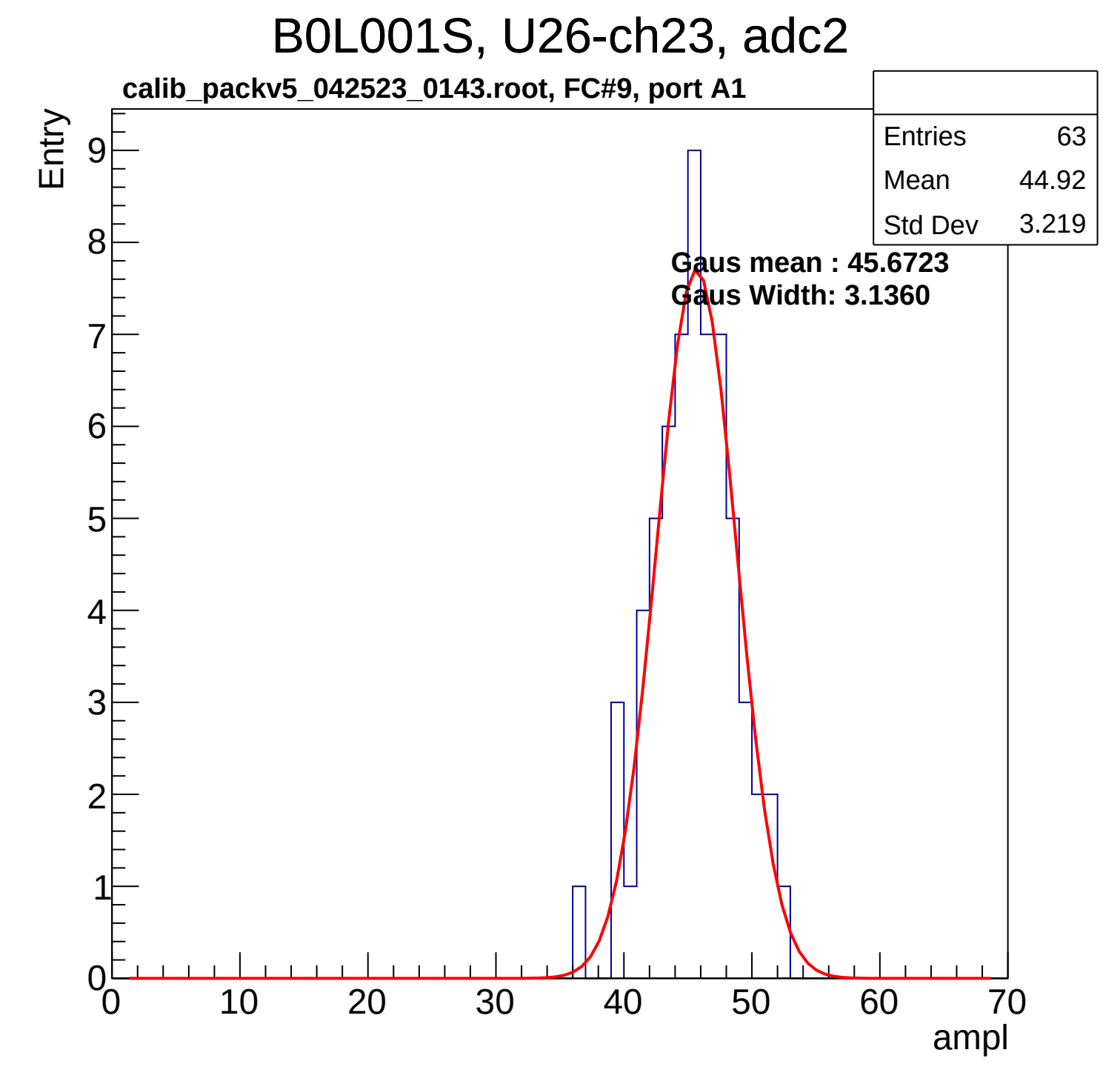
40
50
60
70


B0L001S, U26-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.55
Std Dev 3.613
Gaus mean : 37.9922
Gaus Width: 3.8597
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.92
Std Dev 3.219
Gaus mean : 45.6723
Gaus Width: 3.1360
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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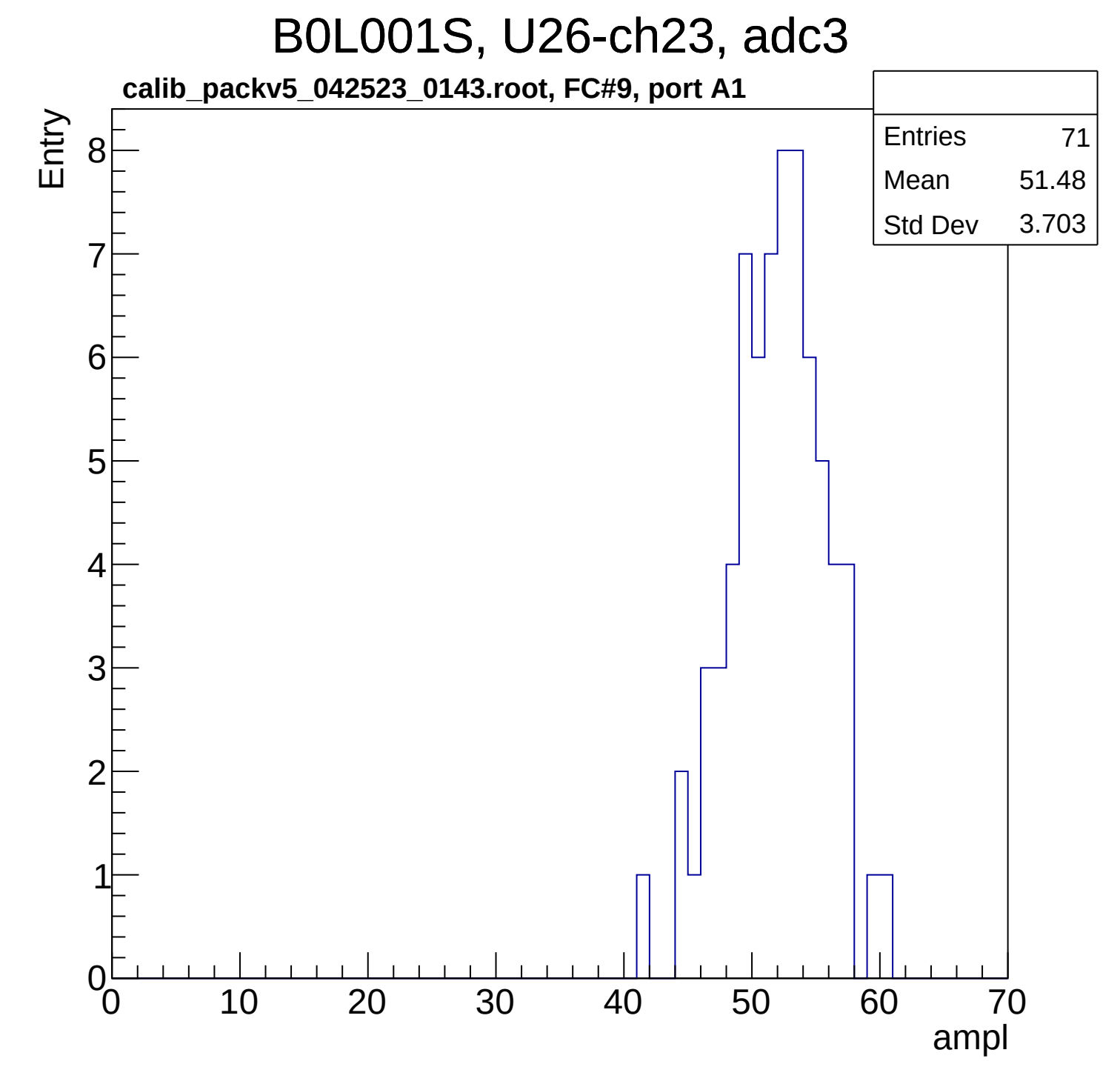
B0L001S, U26-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.48
Std Dev 3.703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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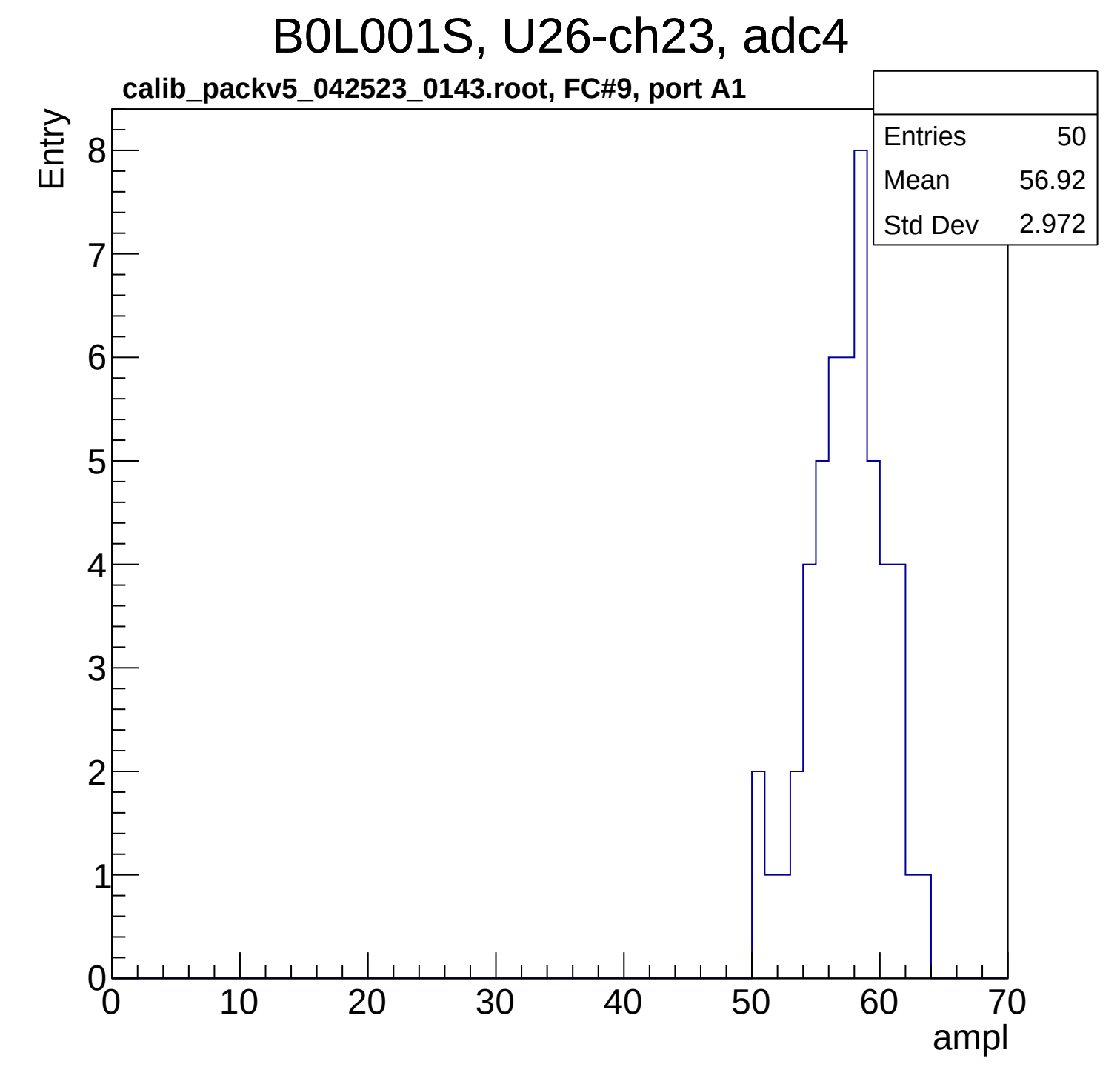
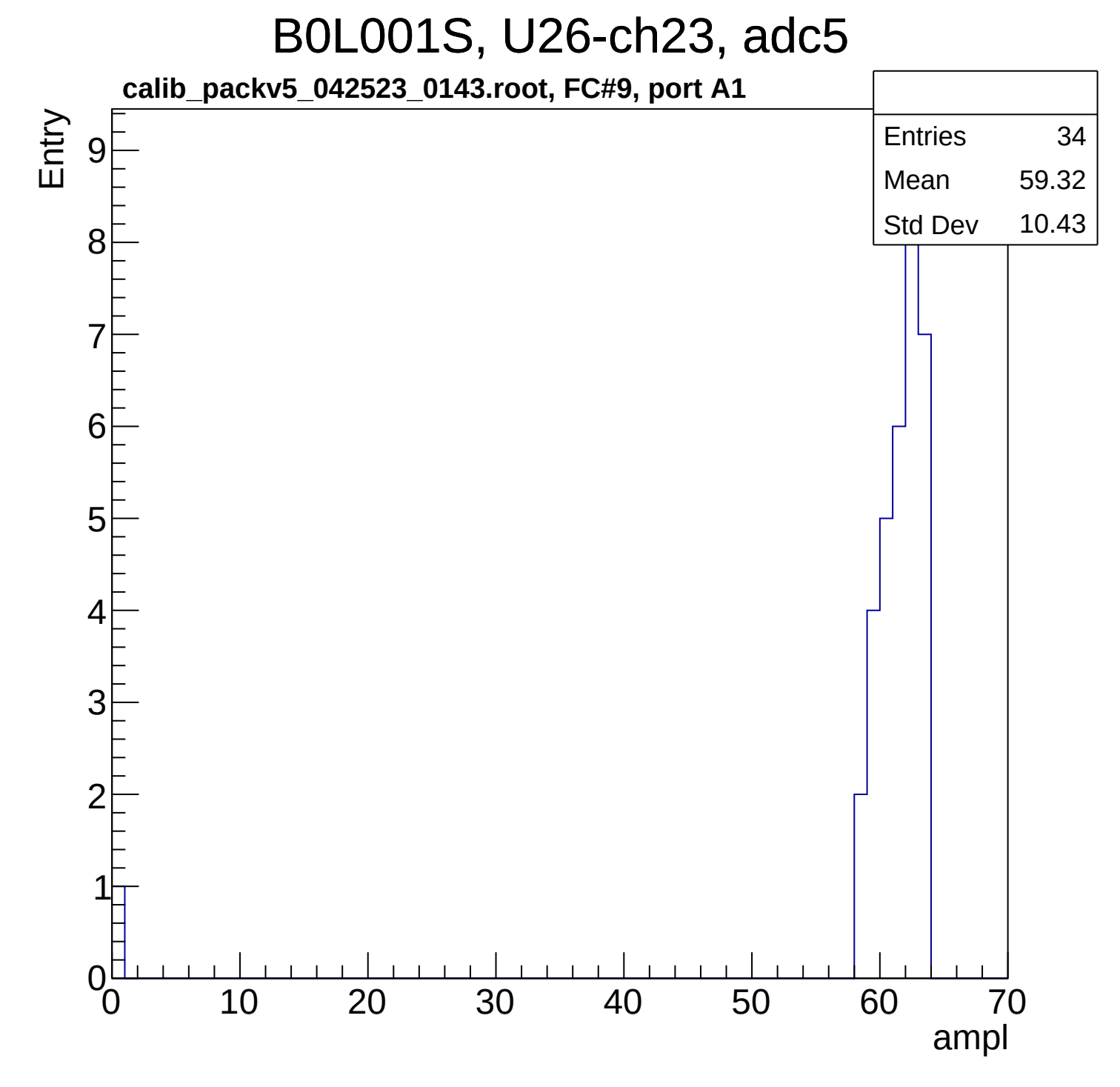

B0L001S, U26-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.92
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.32
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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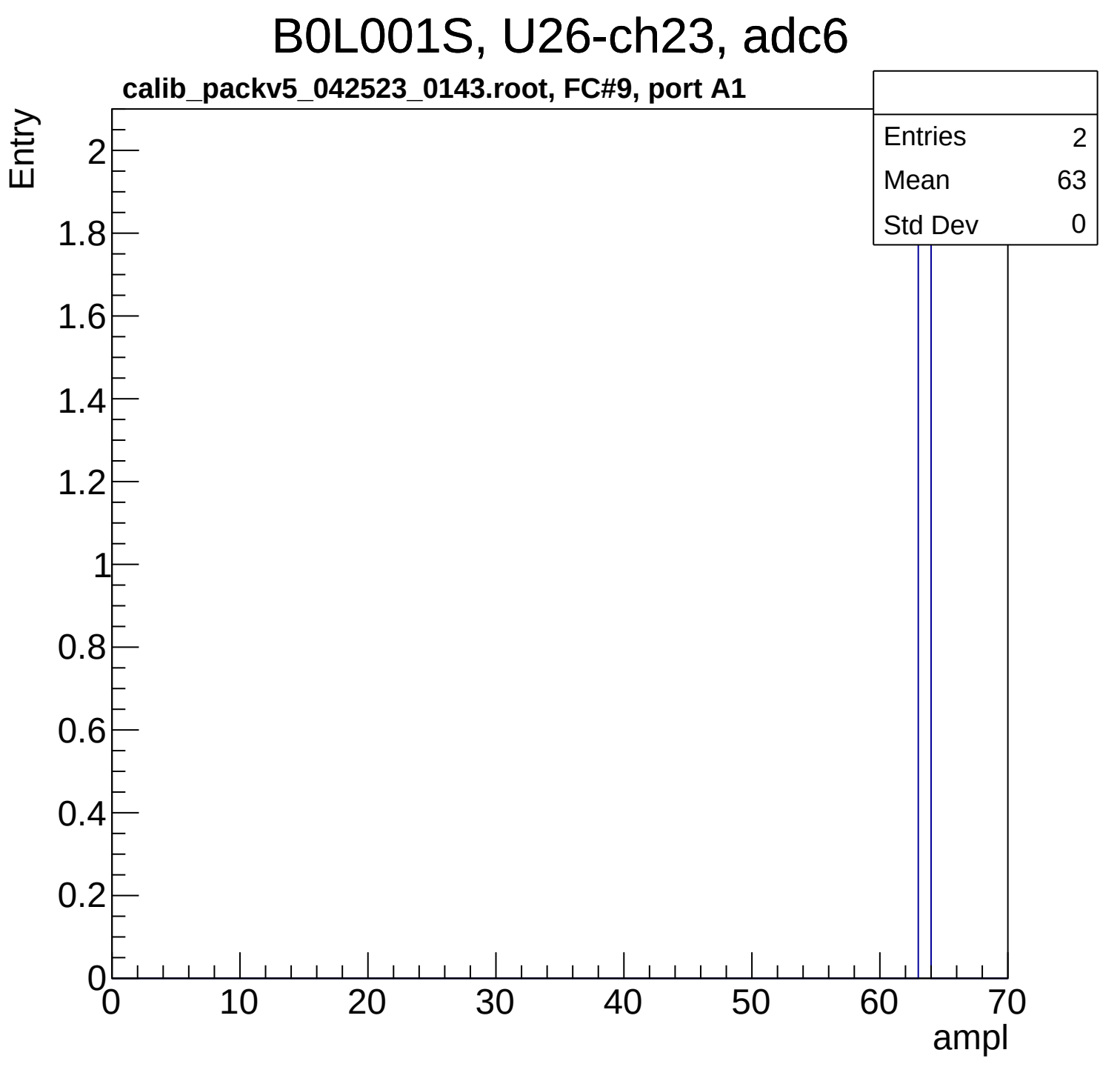
B0L001S, U26-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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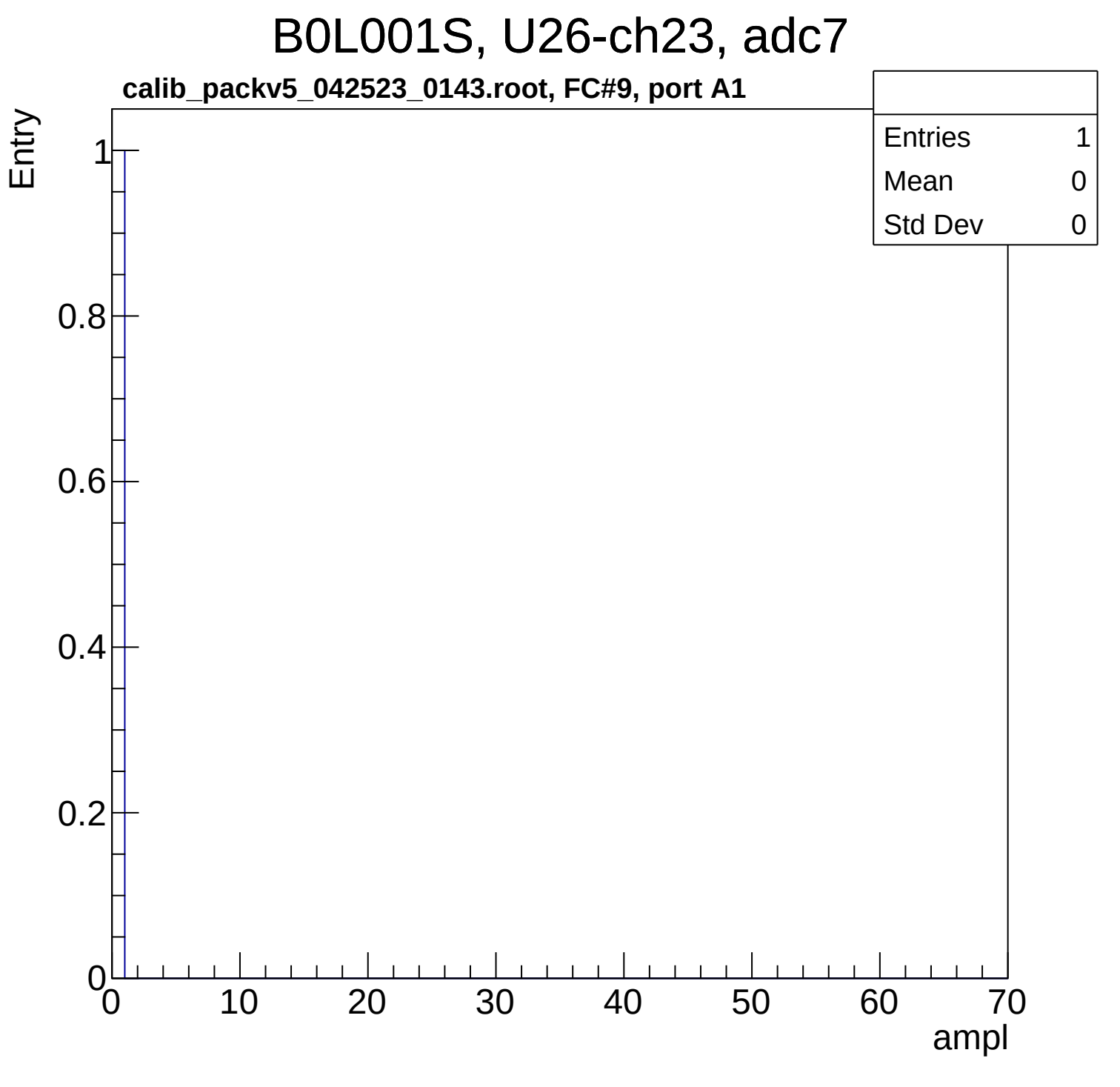
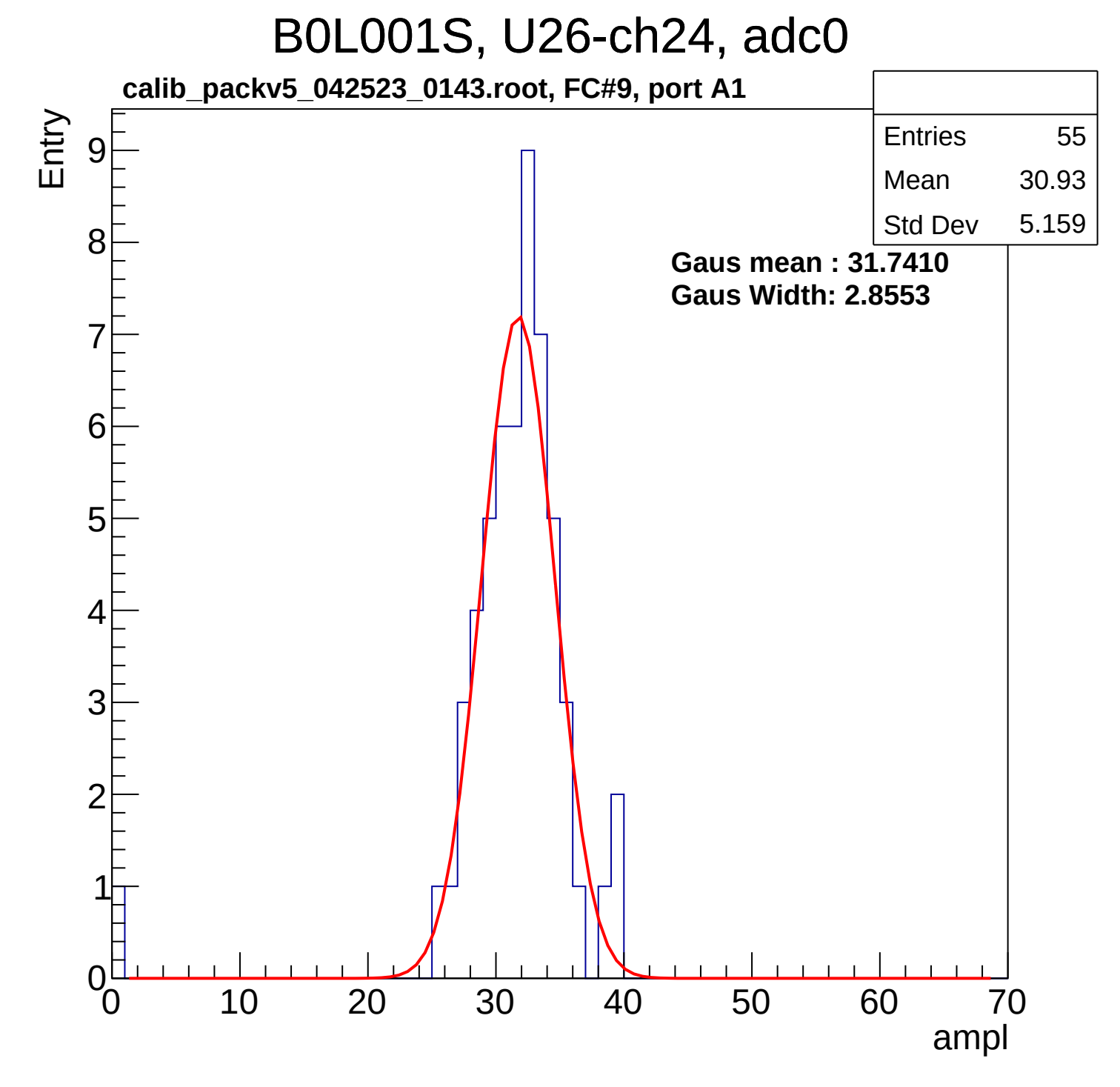
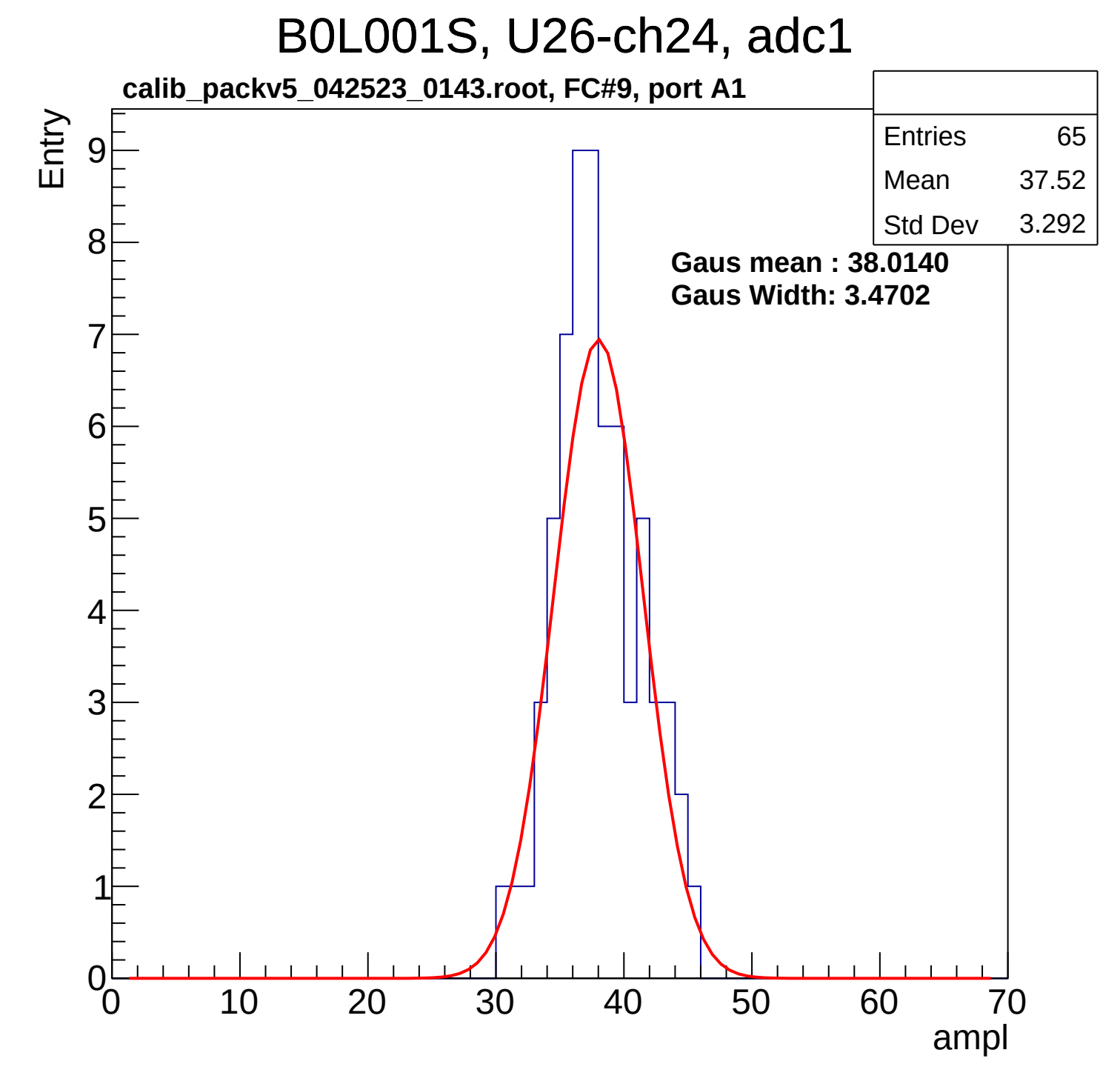
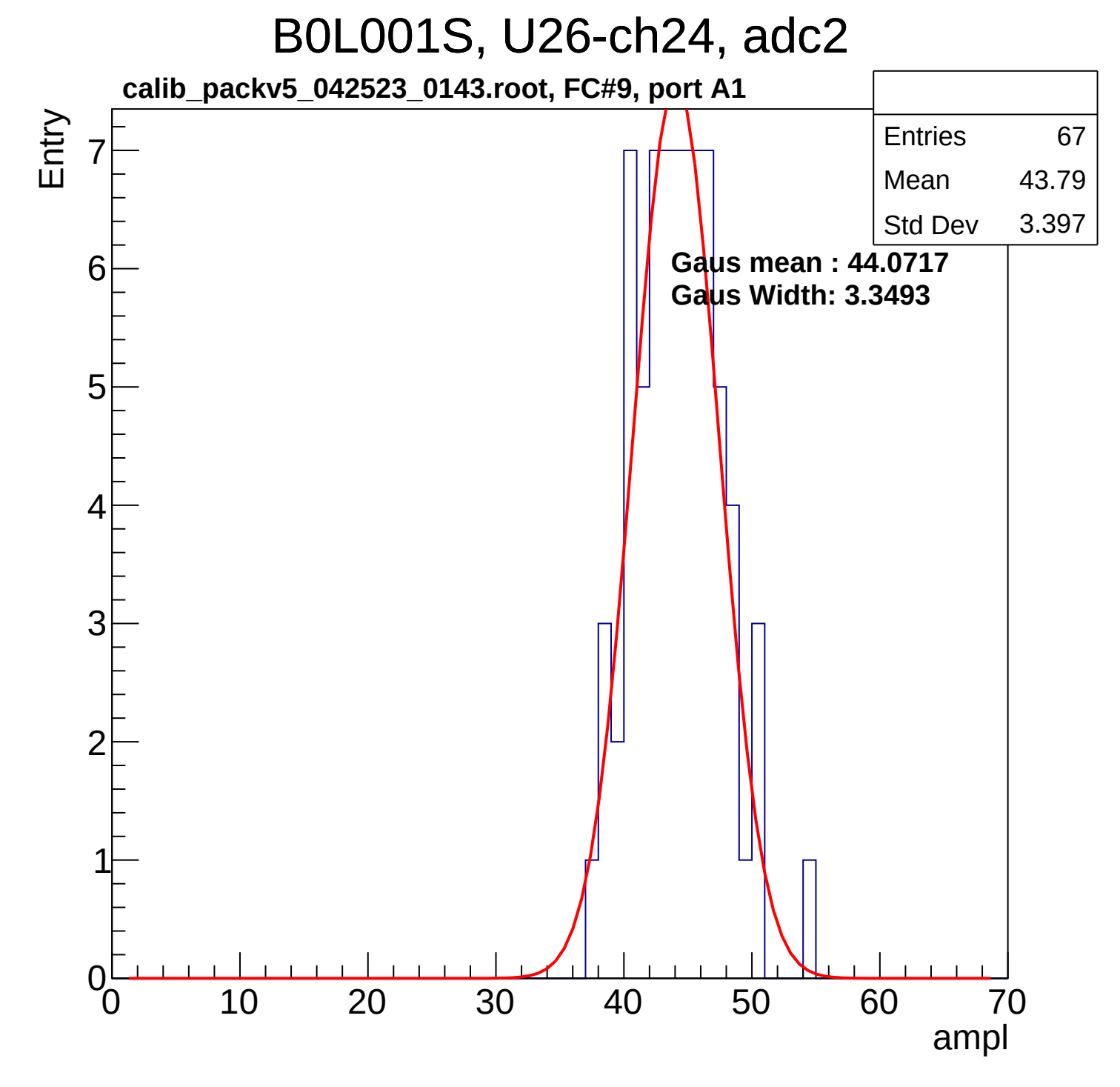
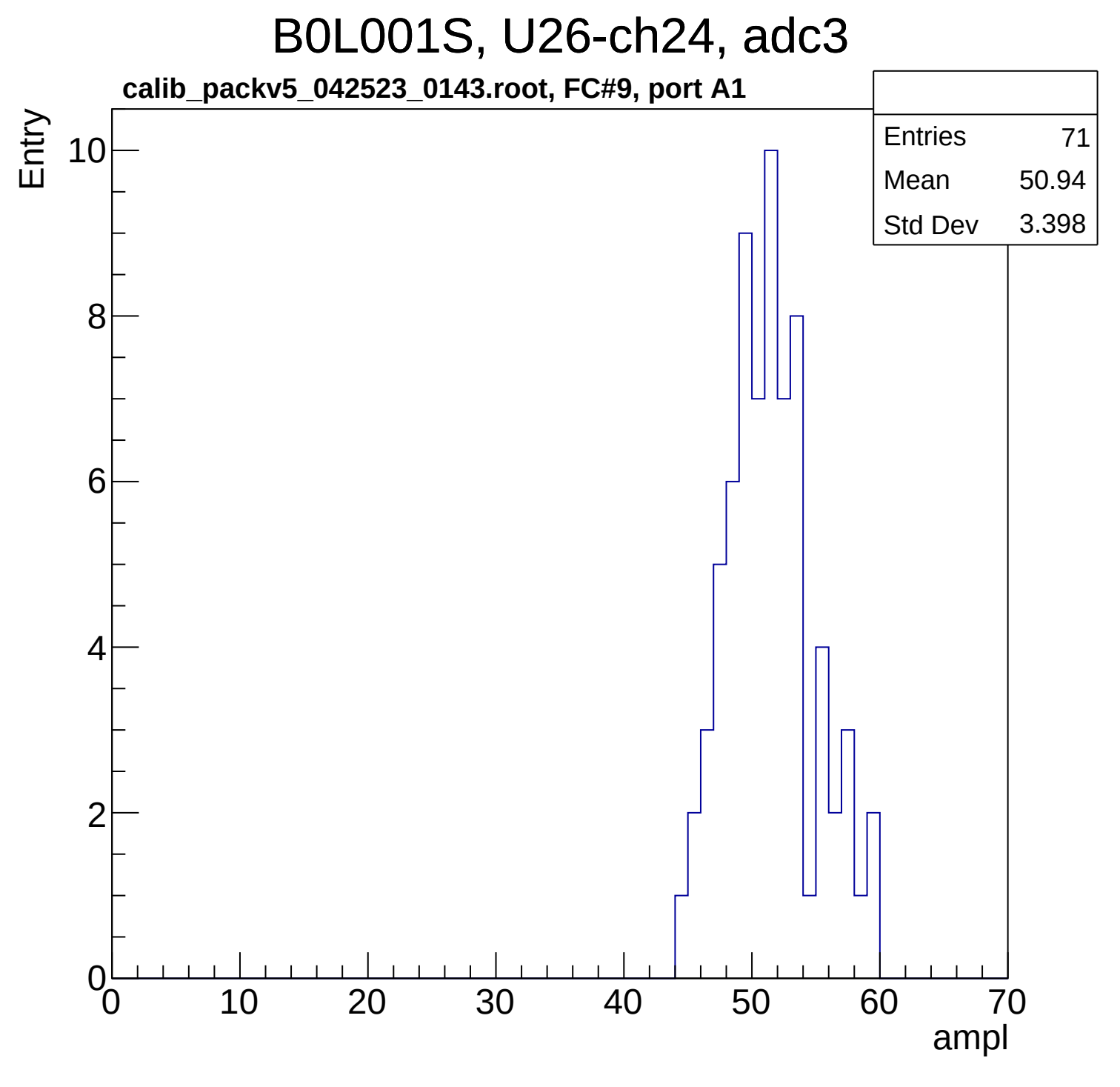
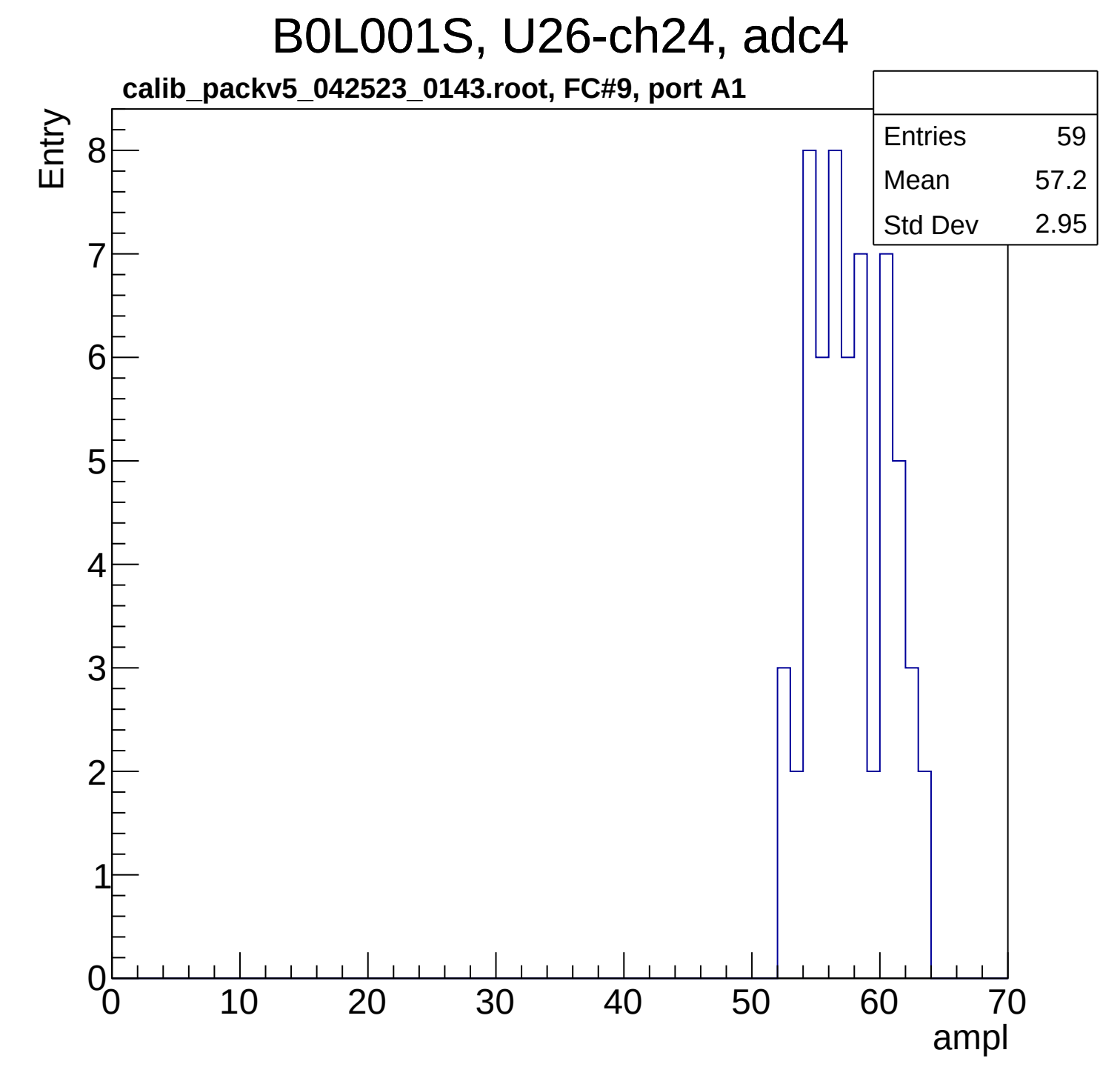

B0L001S, U26-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

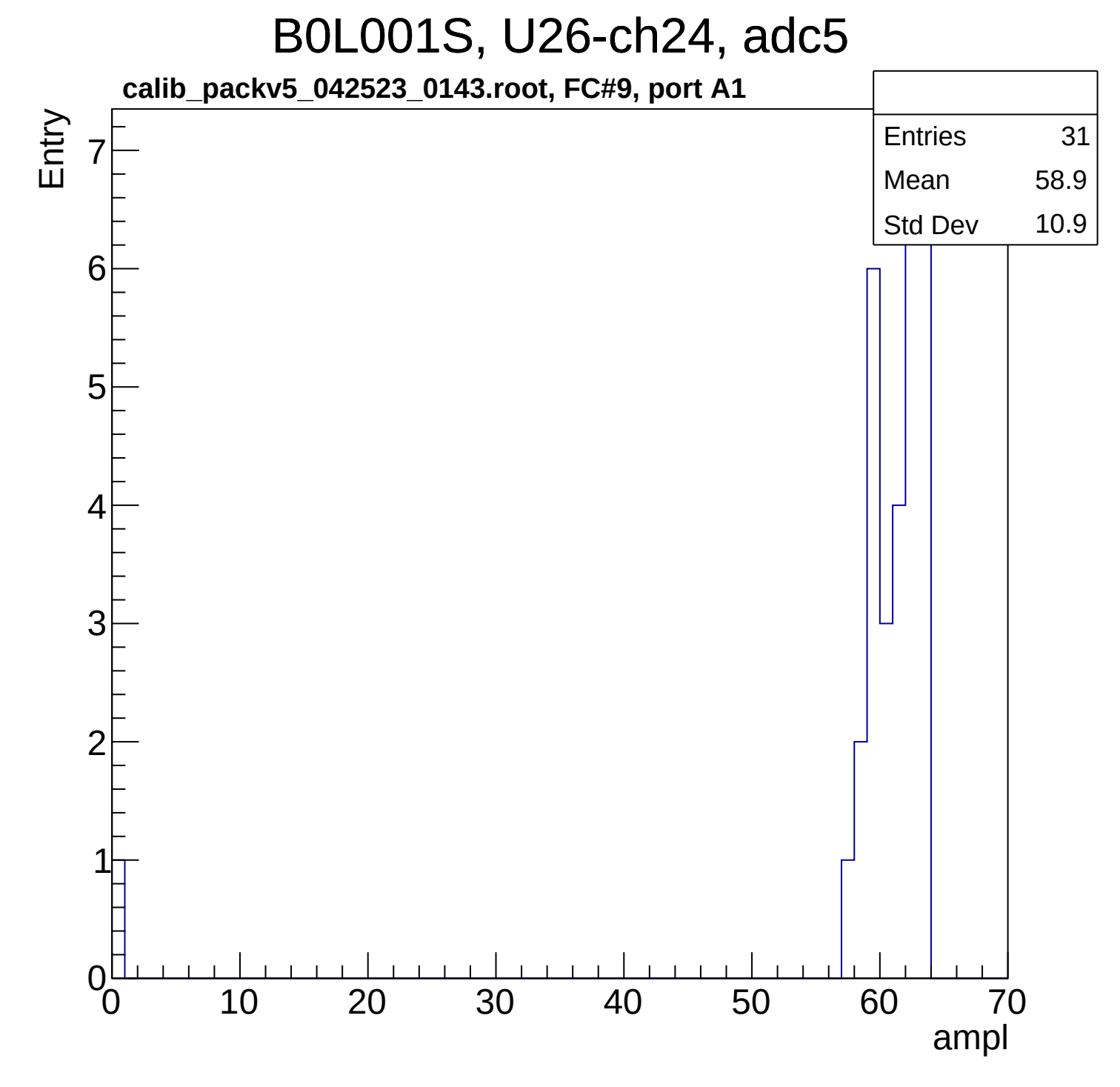

B0L001S, U26-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.93
Std Dev 5.159
Gaus mean : 31.7410
Gaus Width: 2.8553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

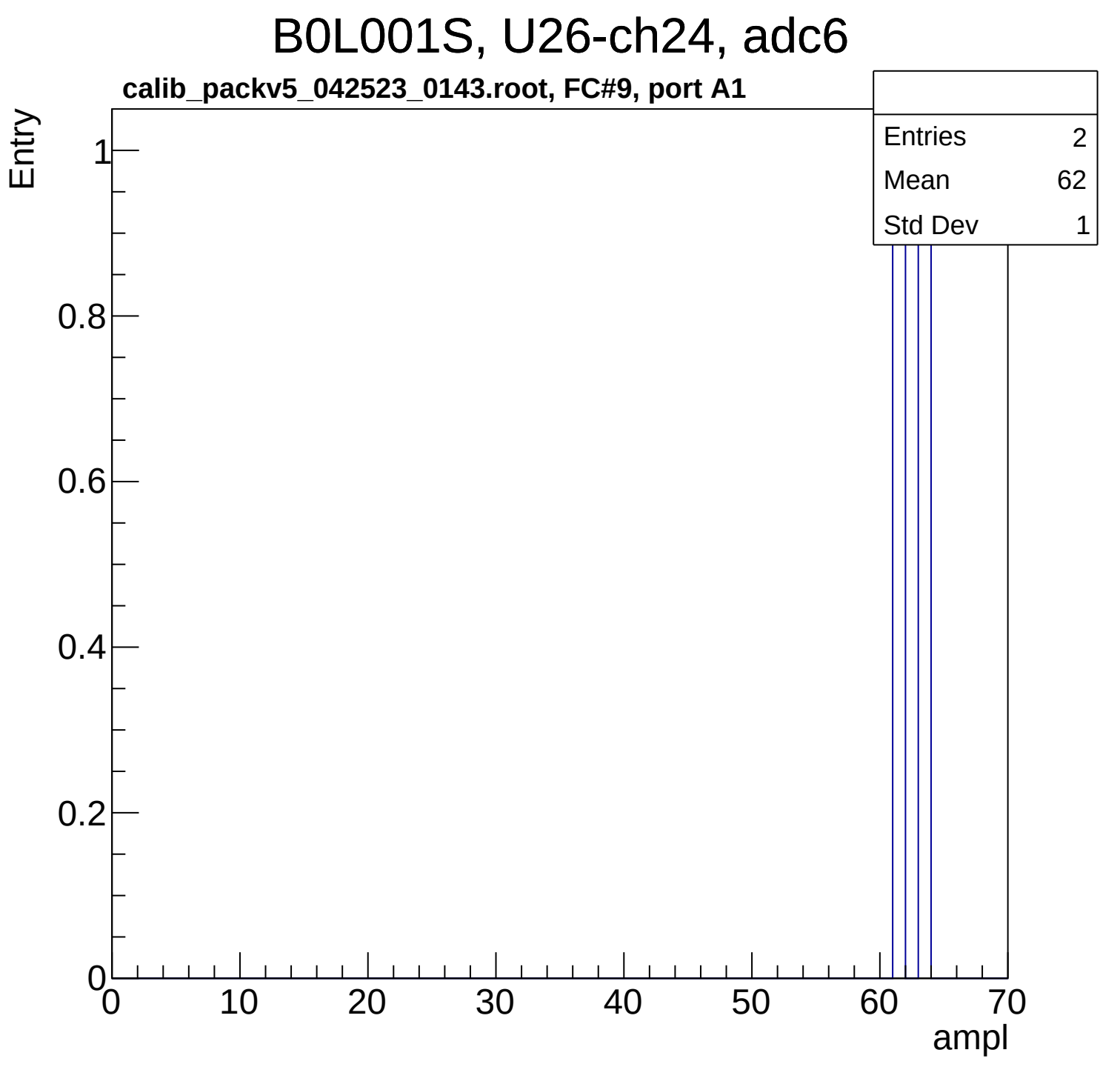

B0L001S, U26-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.52
Std Dev 3.292
Gaus mean : 38.0140
Gaus Width: 3.4702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

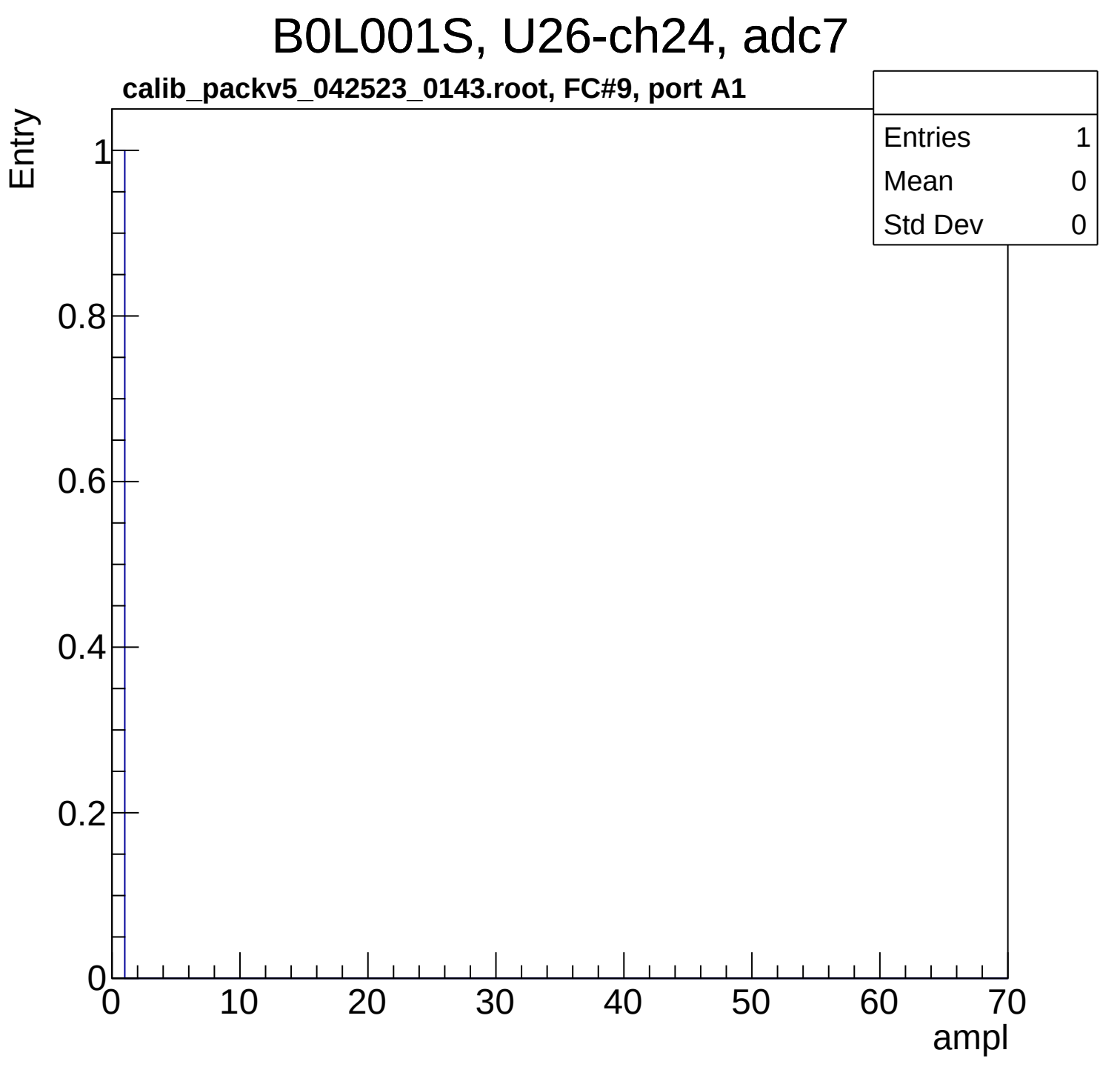

B0L001S, U26-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.79
Std Dev 3.397
Gaus mean : 44.0717
Gaus Width: 3.3493
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

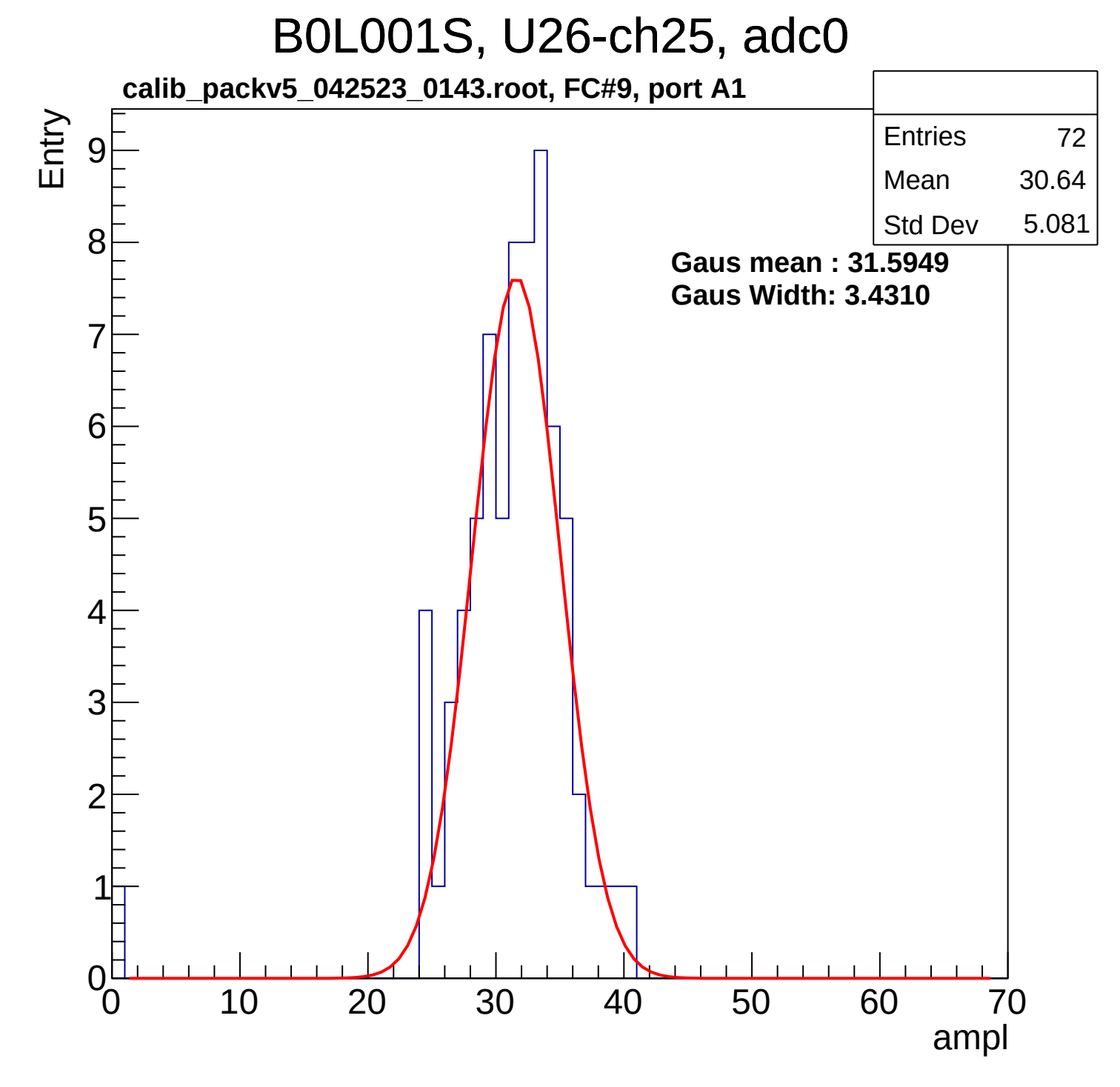

B0L001S, U26-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.94
Std Dev 3.398
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

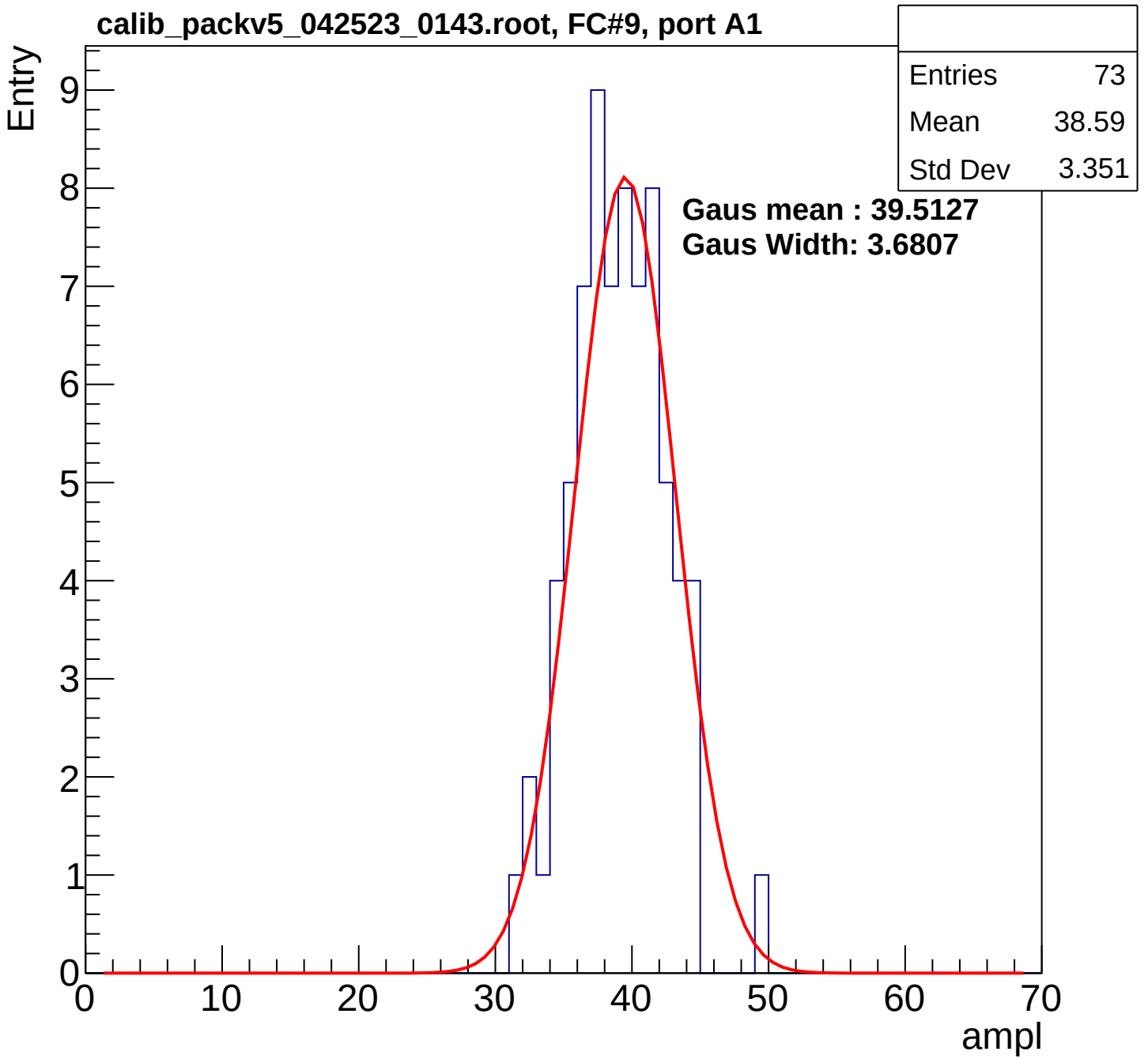

B0L001S, U26-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.2
Std Dev 2.95
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.9
Std Dev 10.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.64
Std Dev 5.081
Gaus mean : 31.5949
Gaus Width: 3.4310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.59
Std Dev 3.351
Gaus mean : 39.5127
Gaus Width: 3.6807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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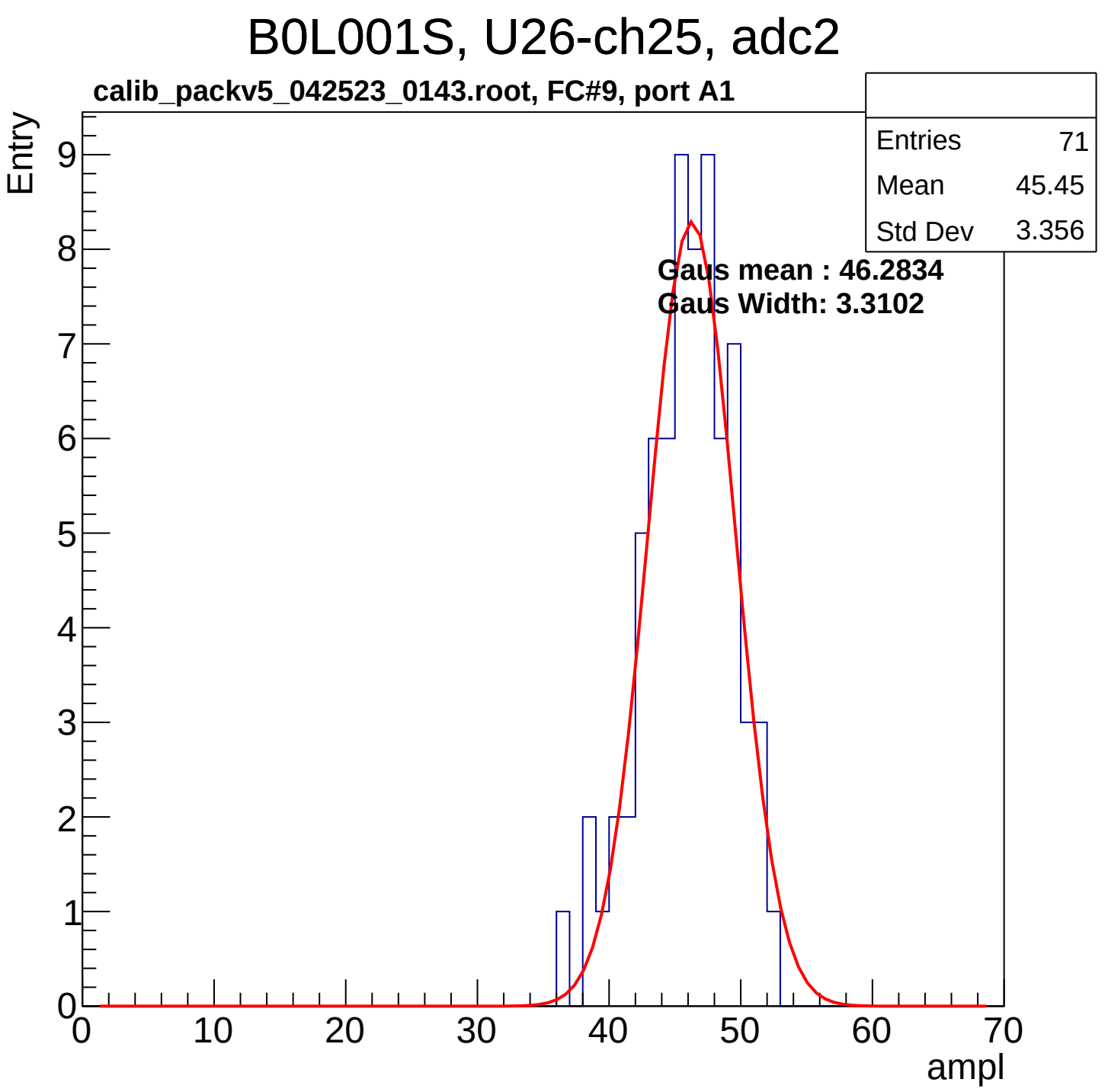
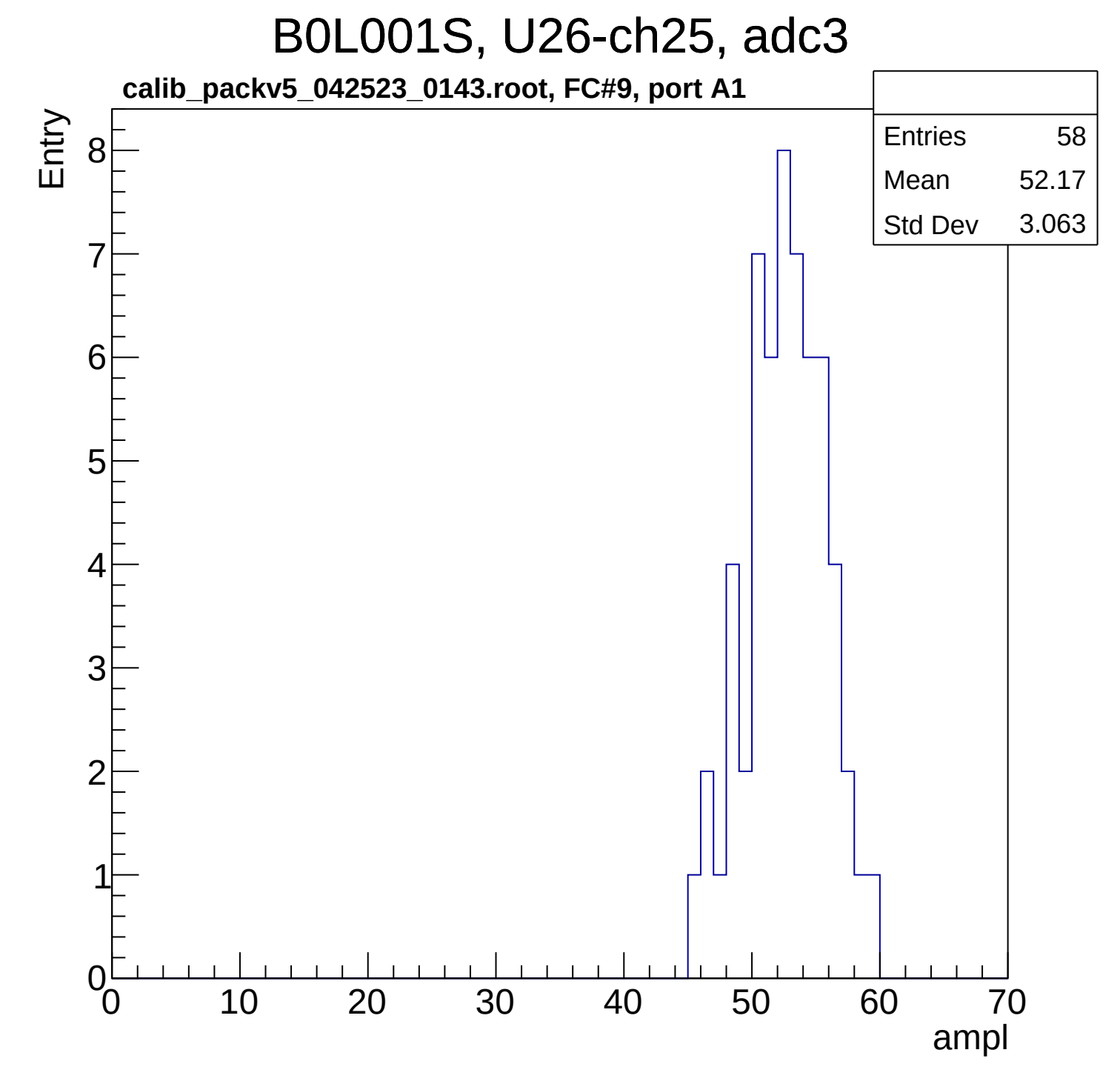
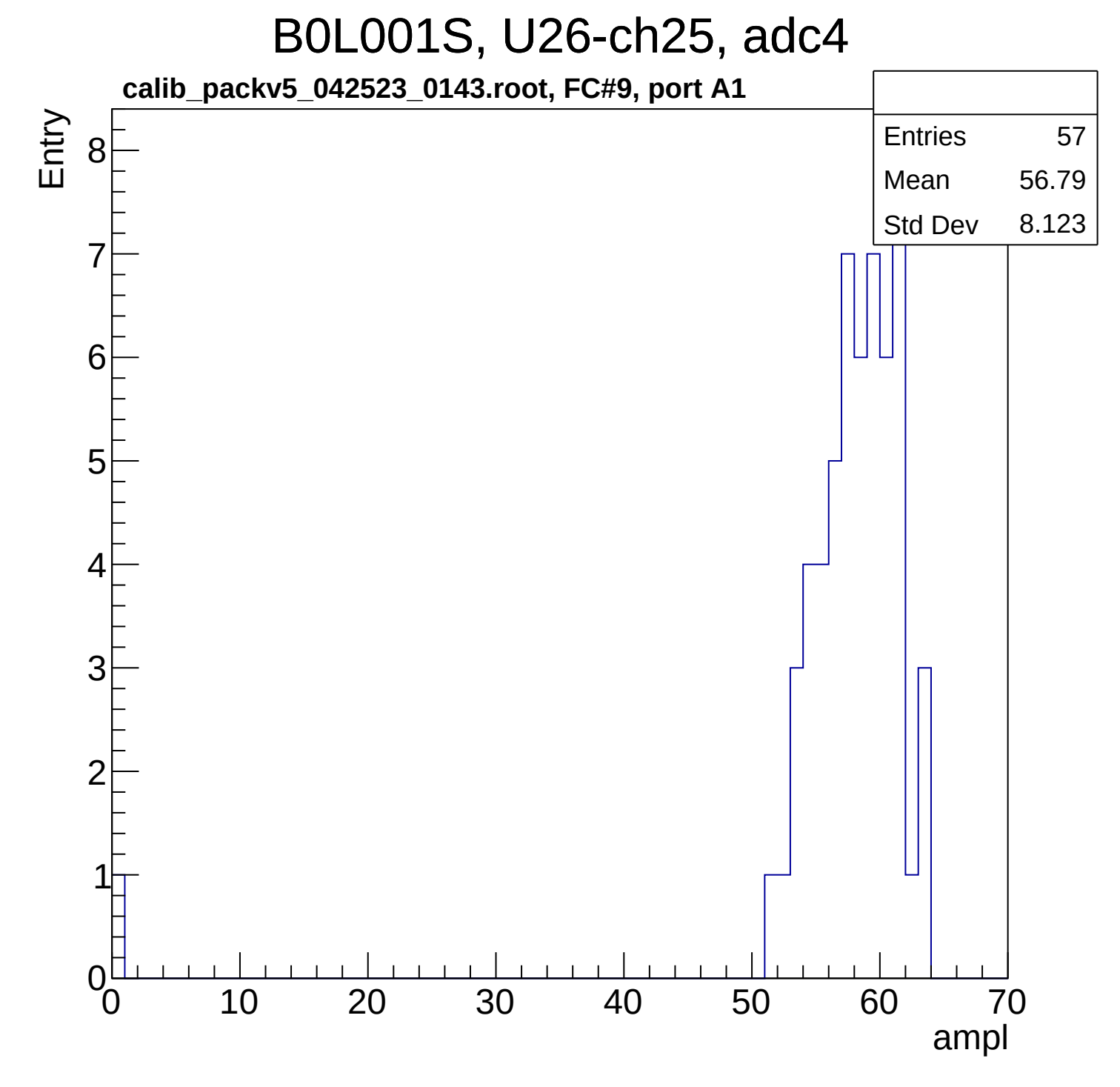
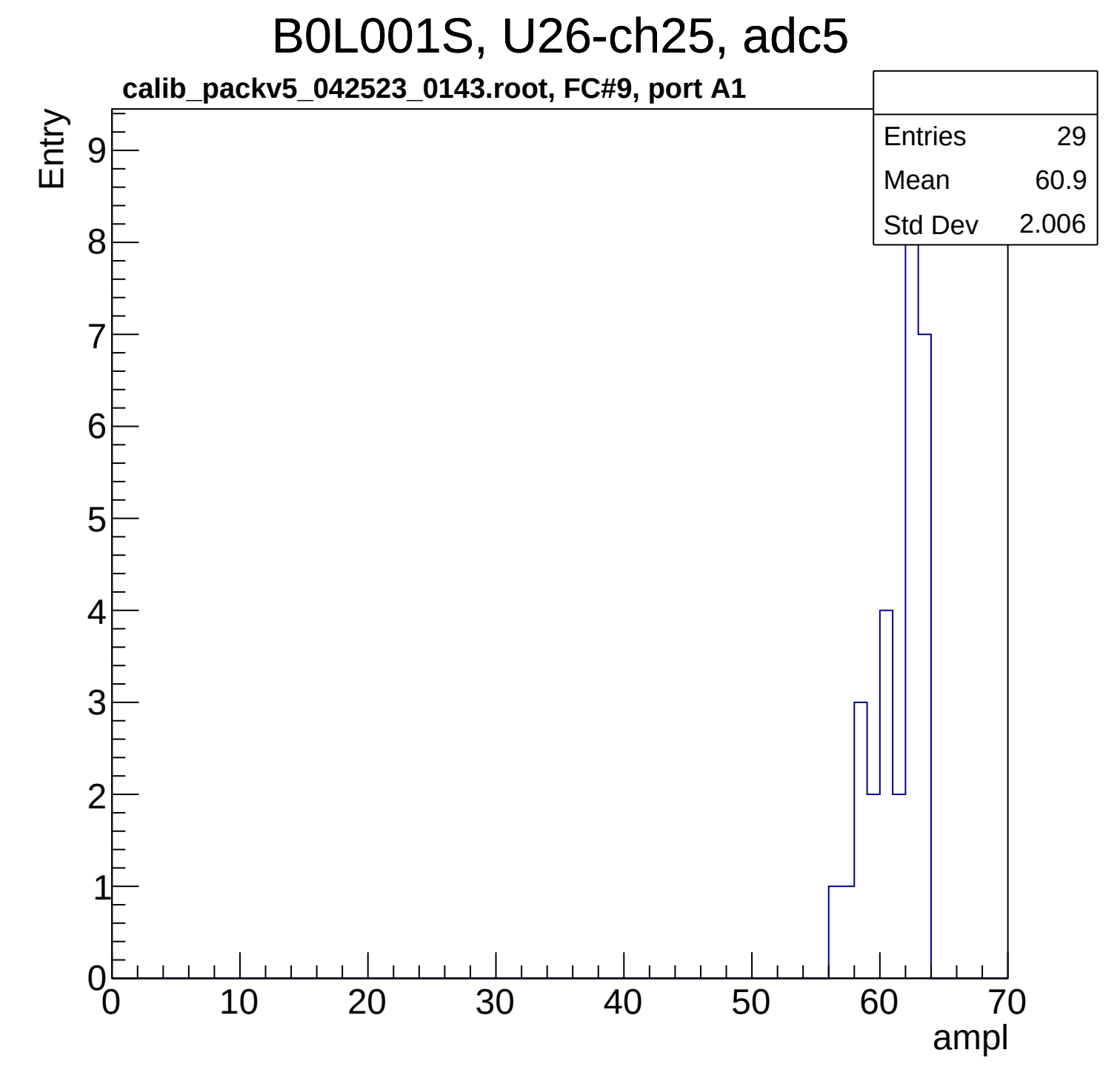
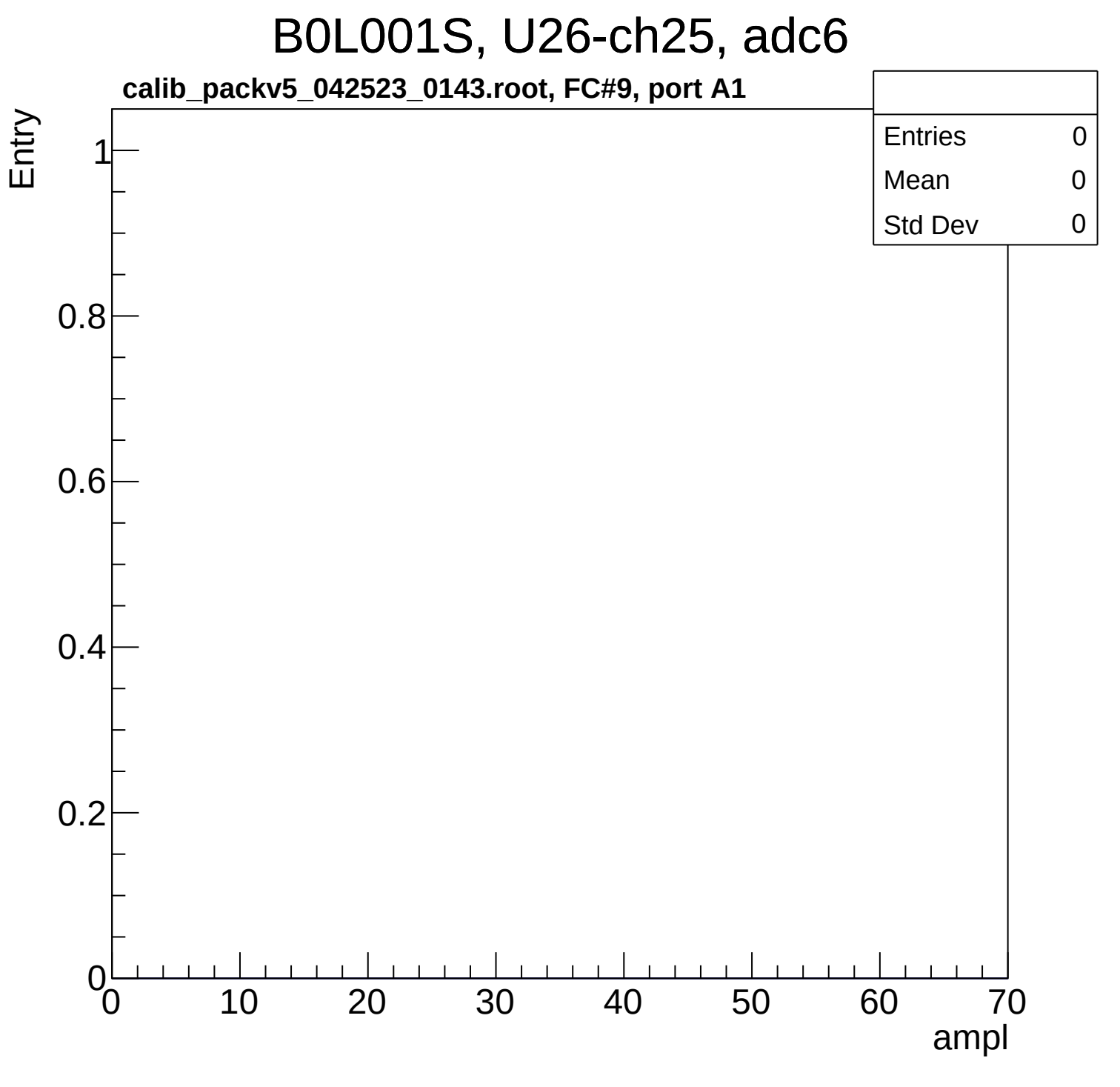
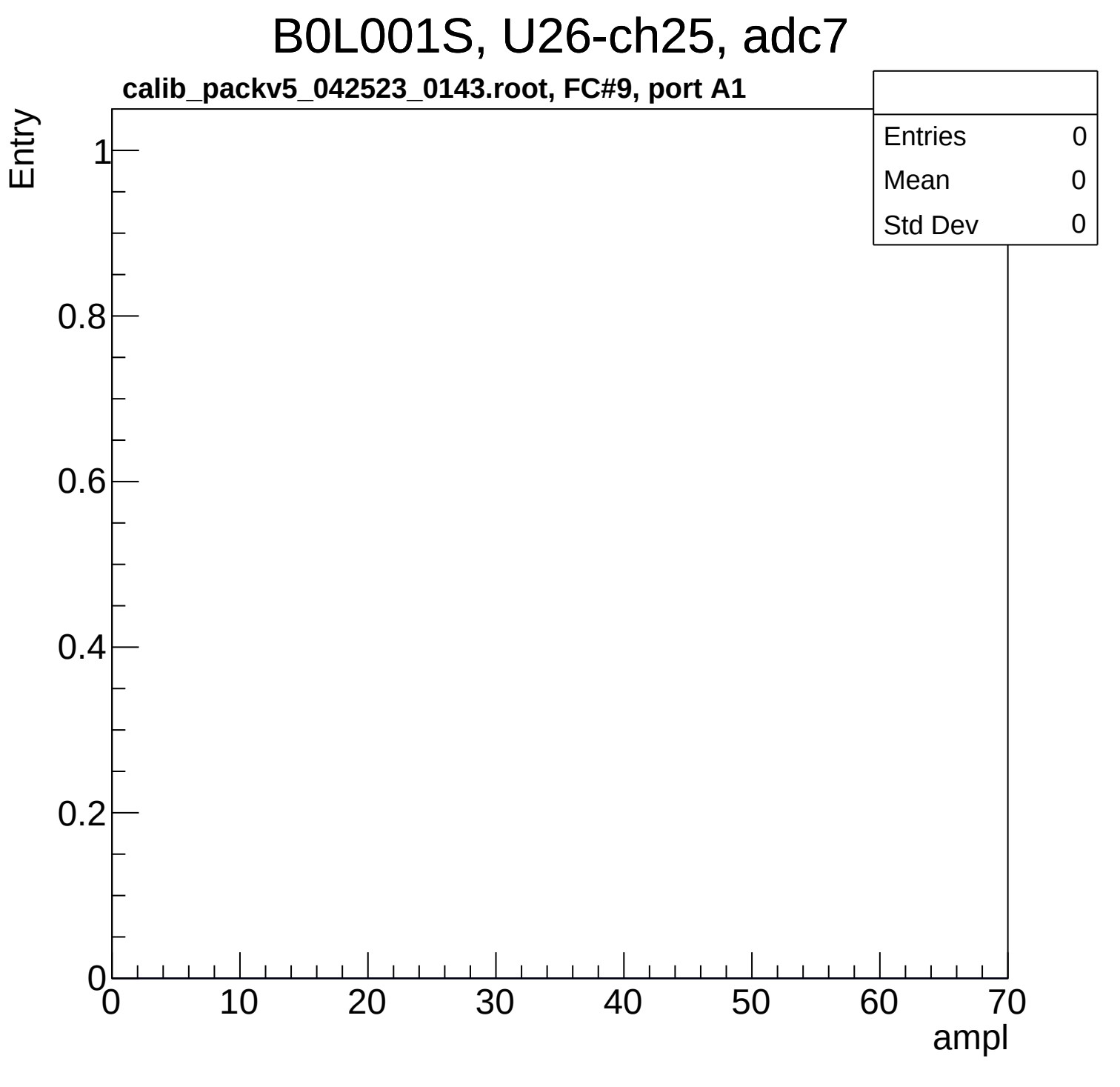

B0L001S, U26-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.45
Std Dev 3.356
Gaus mean : 46.2834
Gaus Width: 3.3102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

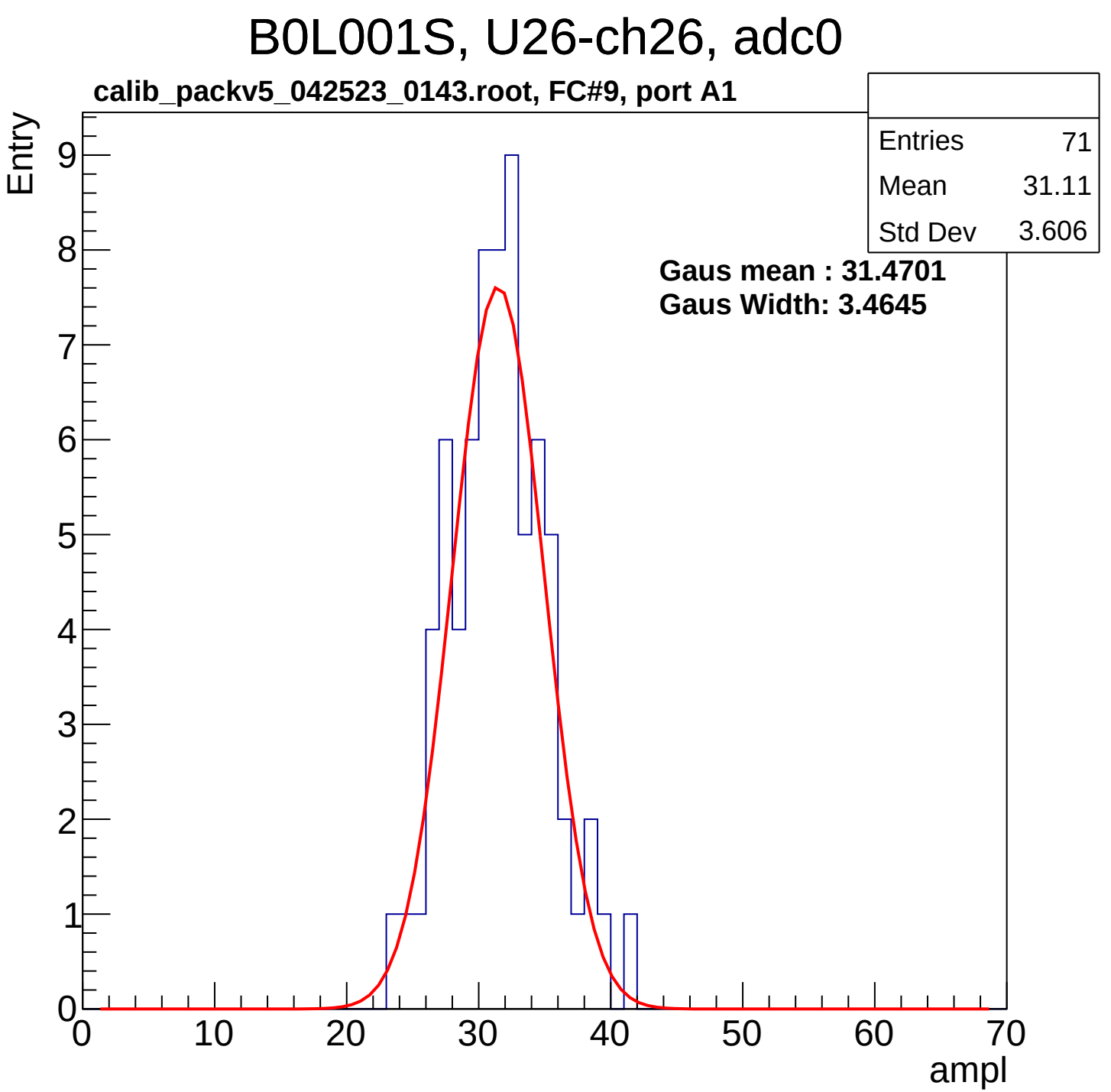

B0L001S, U26-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 52.17
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

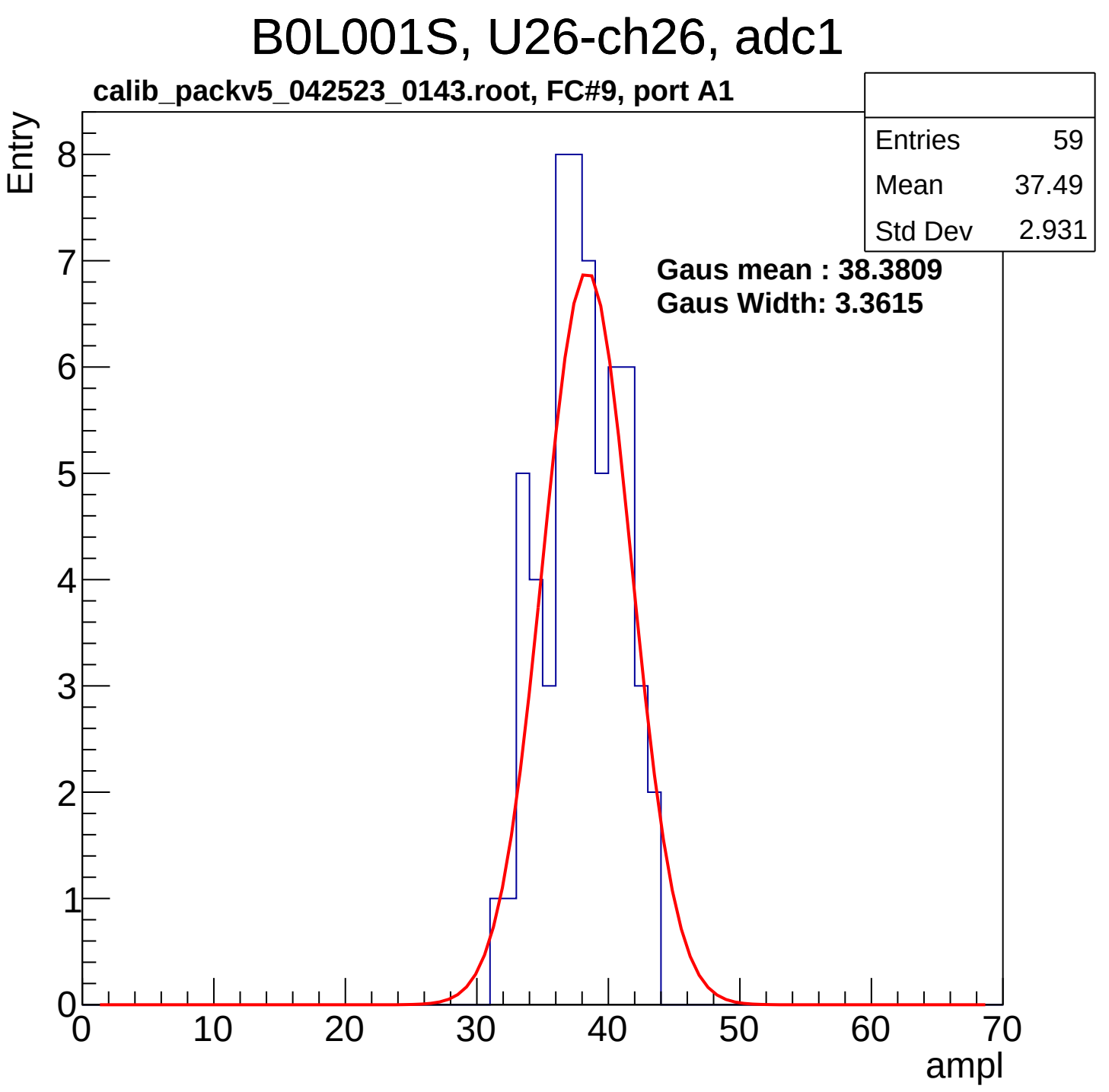

B0L001S, U26-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.79
Std Dev 8.123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

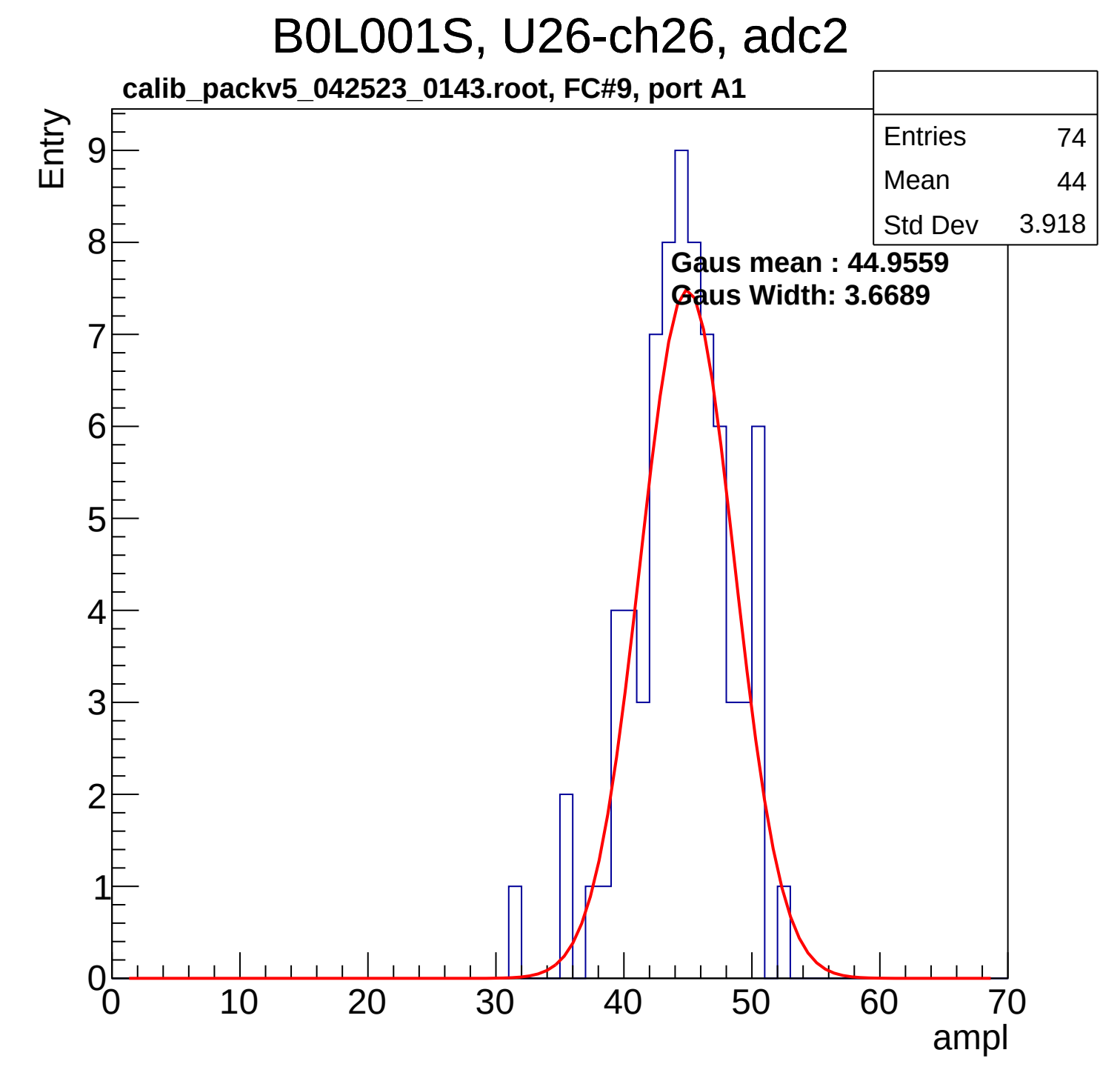

B0L001S, U26-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.9
Std Dev 2.006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

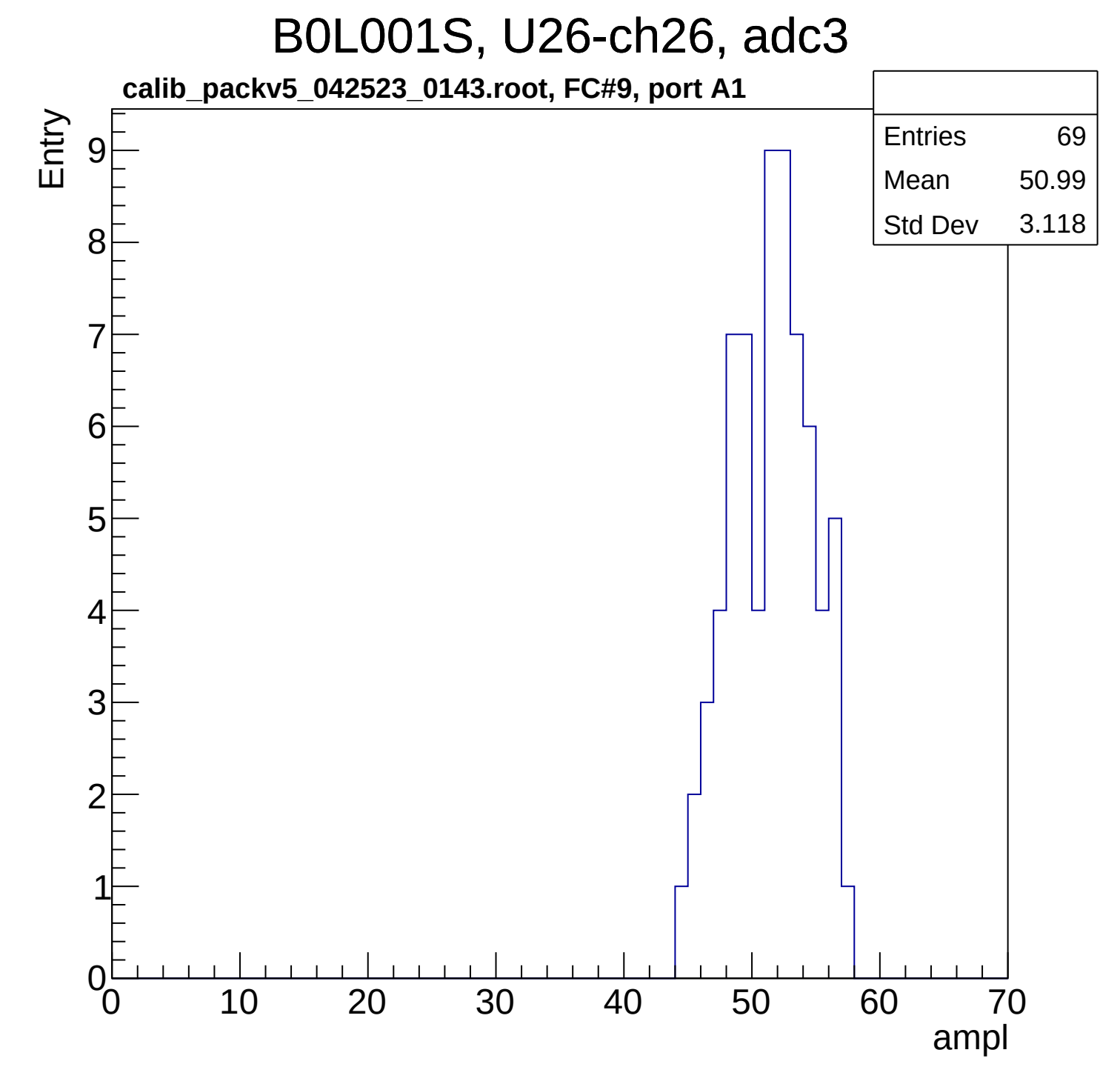

B0L001S, U26-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.11
Std Dev 3.606
Gaus mean : 31.4701
Gaus Width: 3.4645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.49
Std Dev 2.931
Gaus mean : 38.3809
Gaus Width: 3.3615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44
Std Dev 3.918
Gaus mean : 44.9559
Gaus Width: 3.6689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.99
Std Dev 3.118
Entry
ampl
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2
3
4
5
6
7
8
9
0
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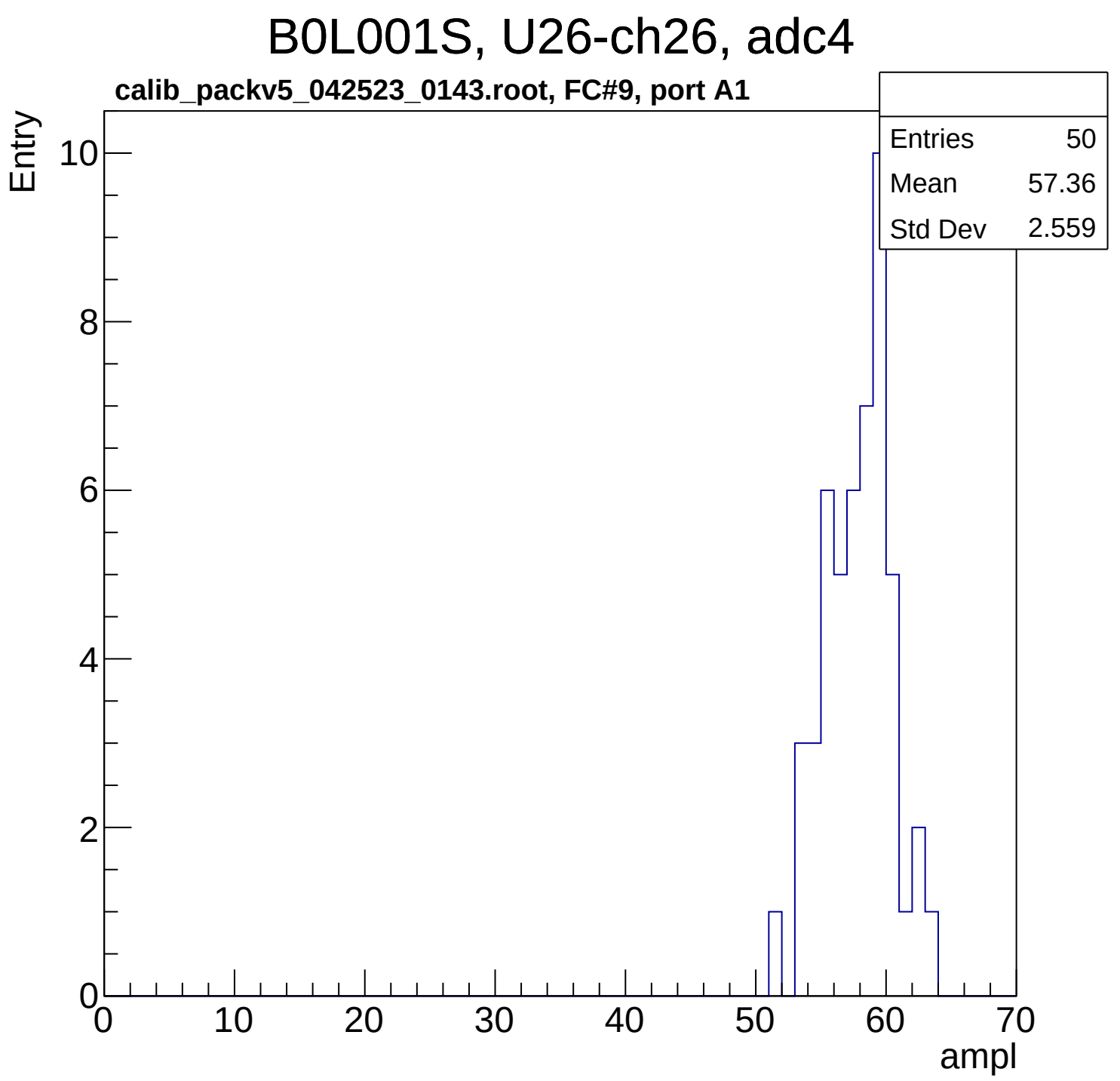
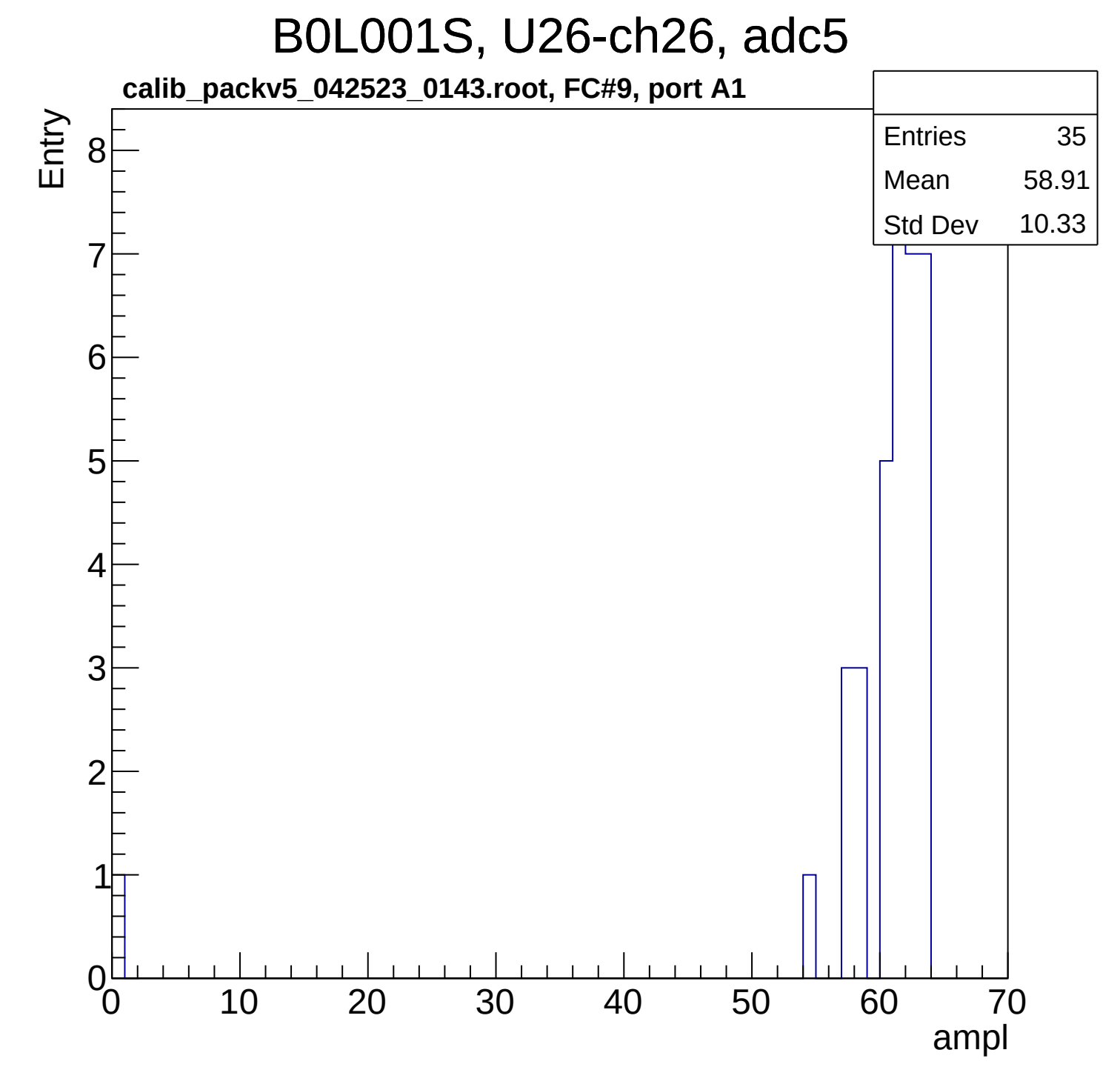
20
30
40
50
60
70


B0L001S, U26-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.36
Std Dev 2.559
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.91
Std Dev 10.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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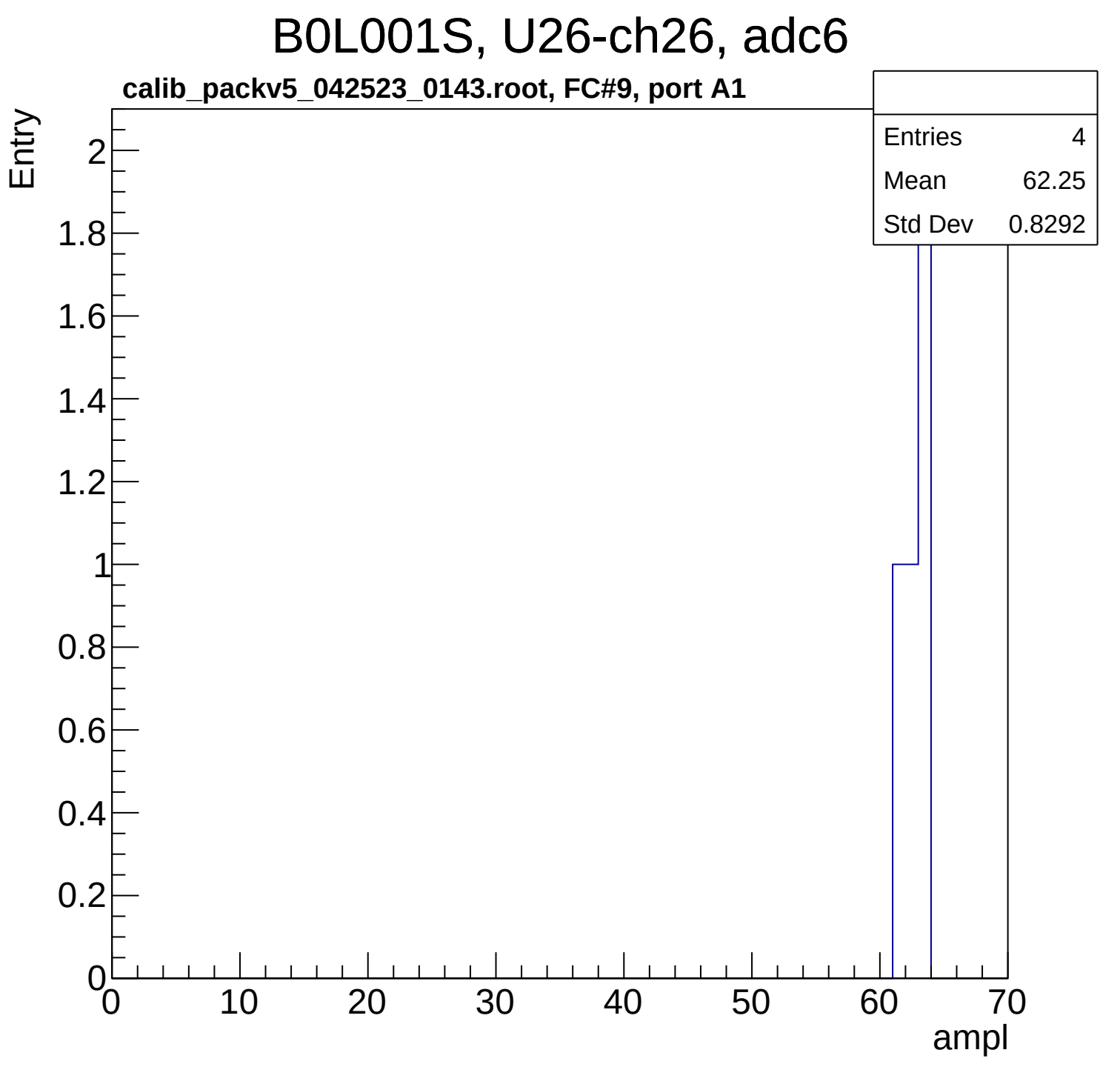
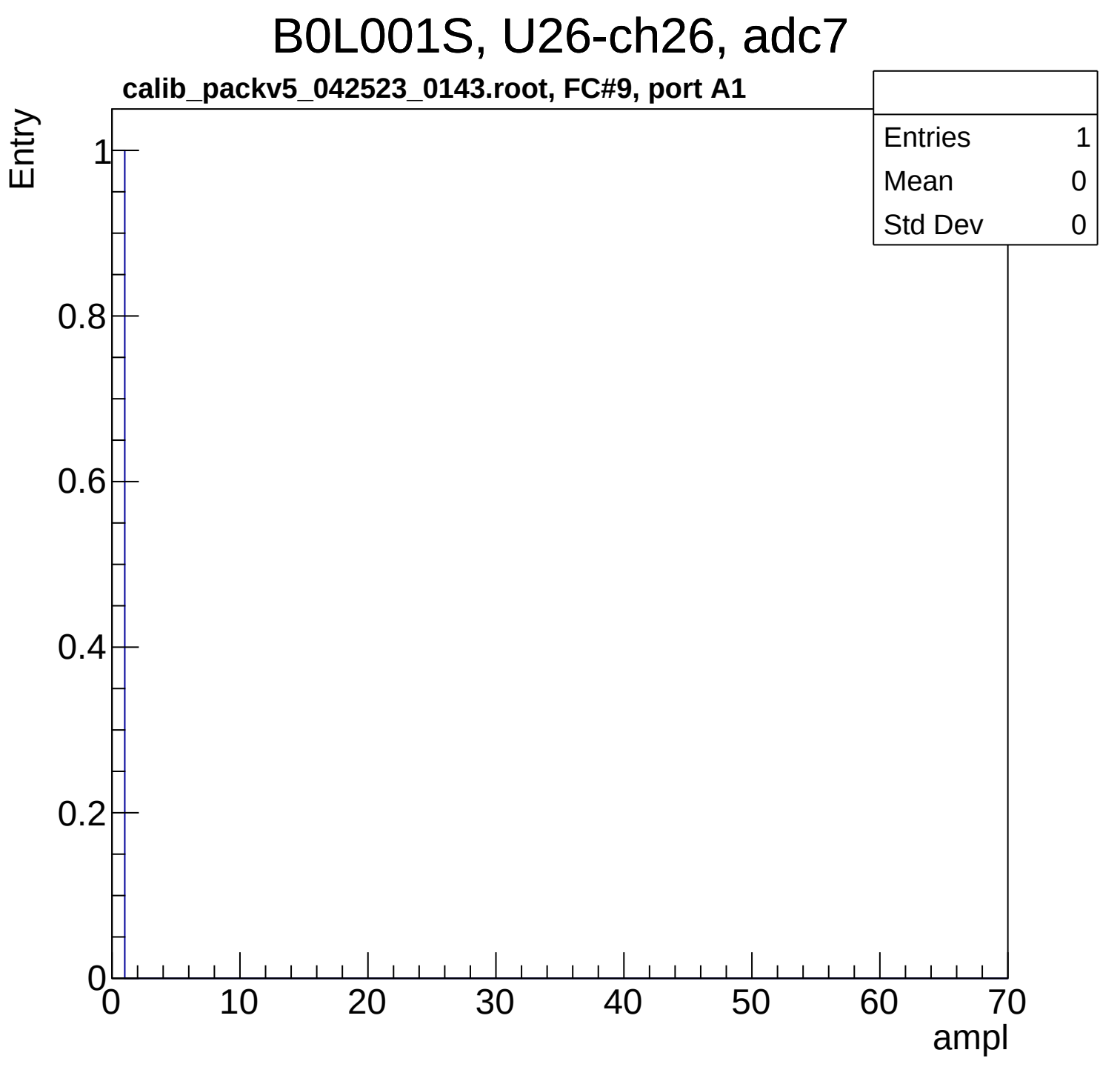
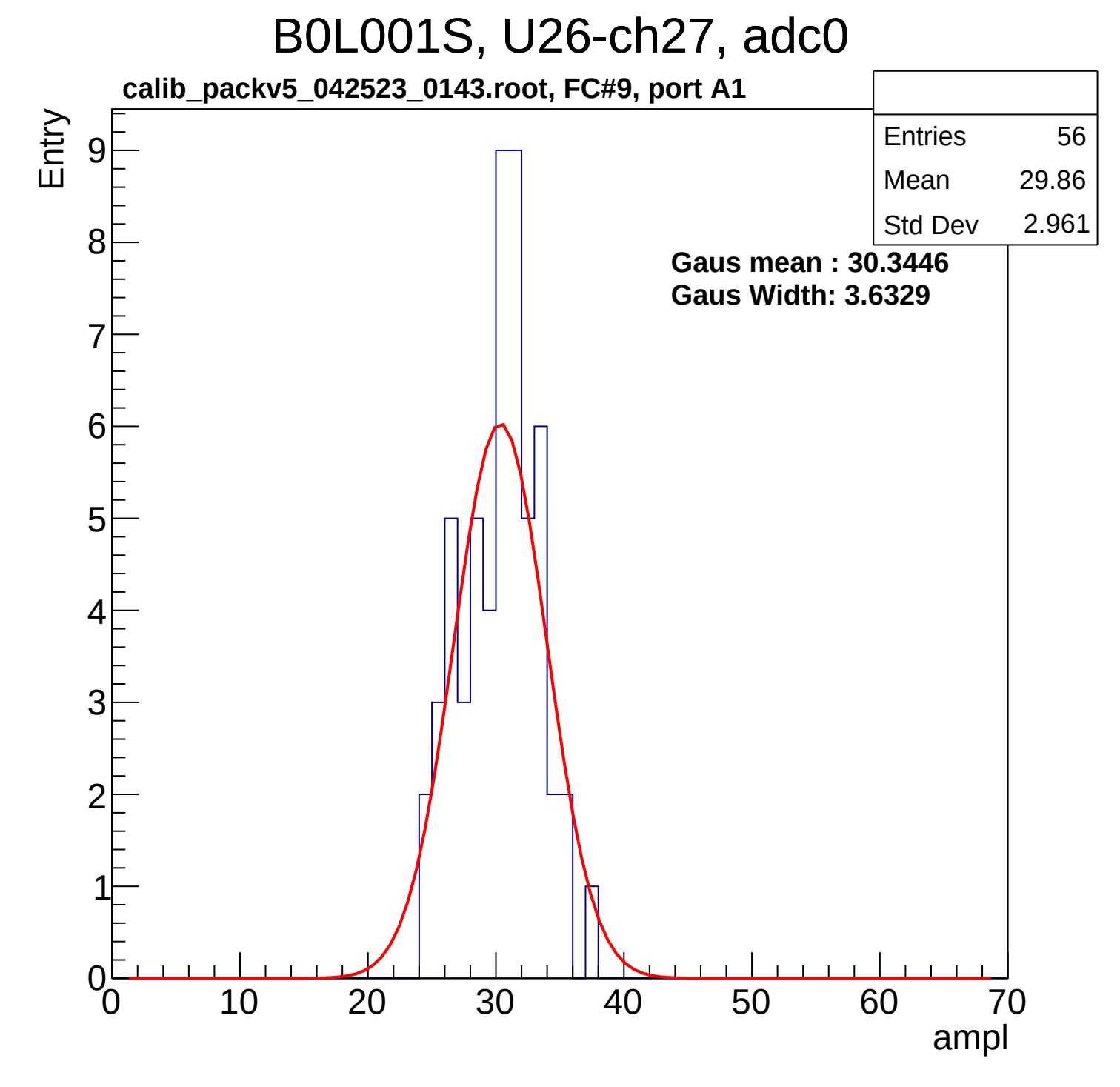

B0L001S, U26-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 29.86
Std Dev 2.961
Gaus mean : 30.3446
Gaus Width: 3.6329
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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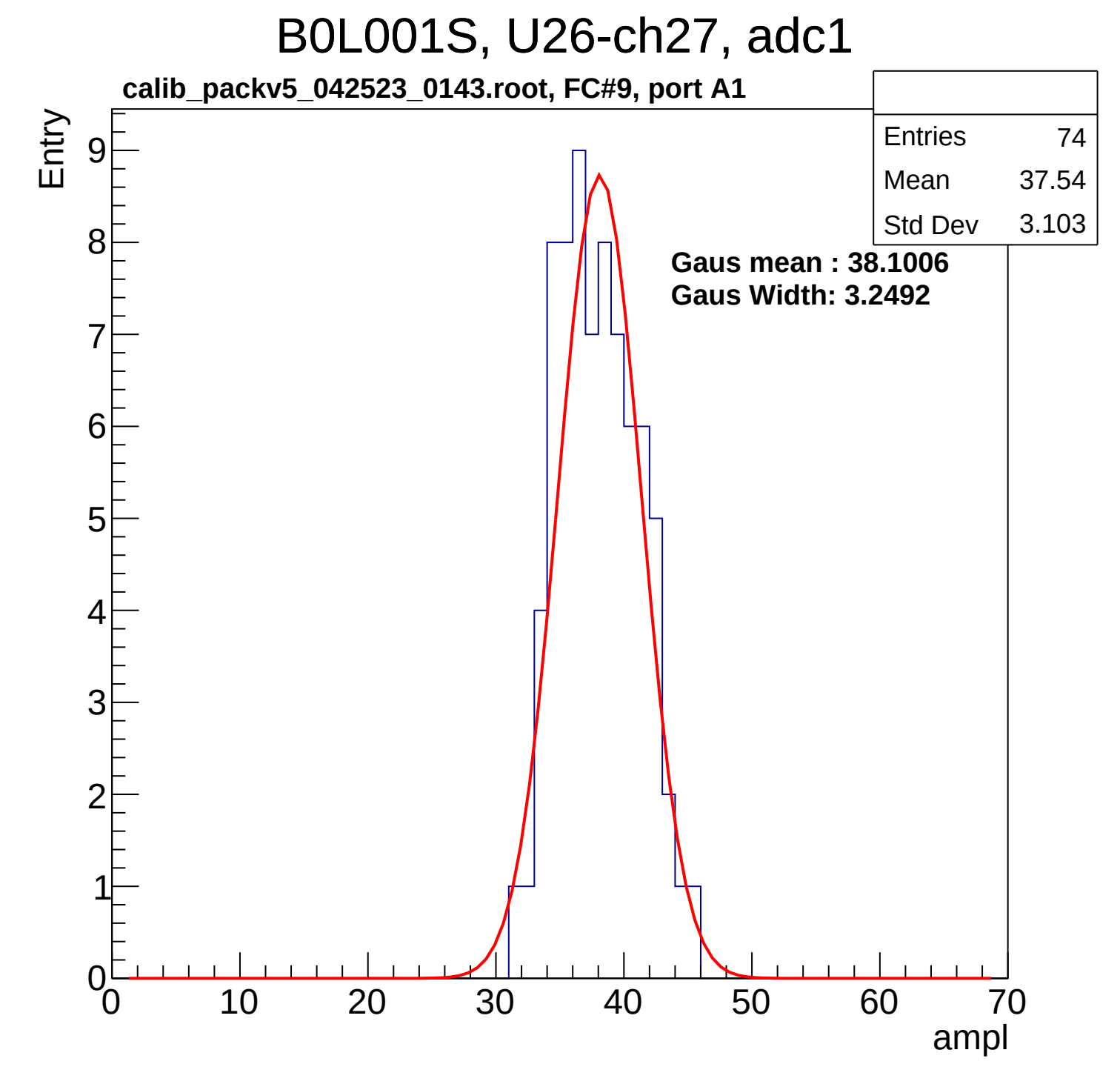
B0L001S, U26-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.54
Std Dev 3.103
Gaus mean : 38.1006
Gaus Width: 3.2492
Entry
ampl
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2
3
4
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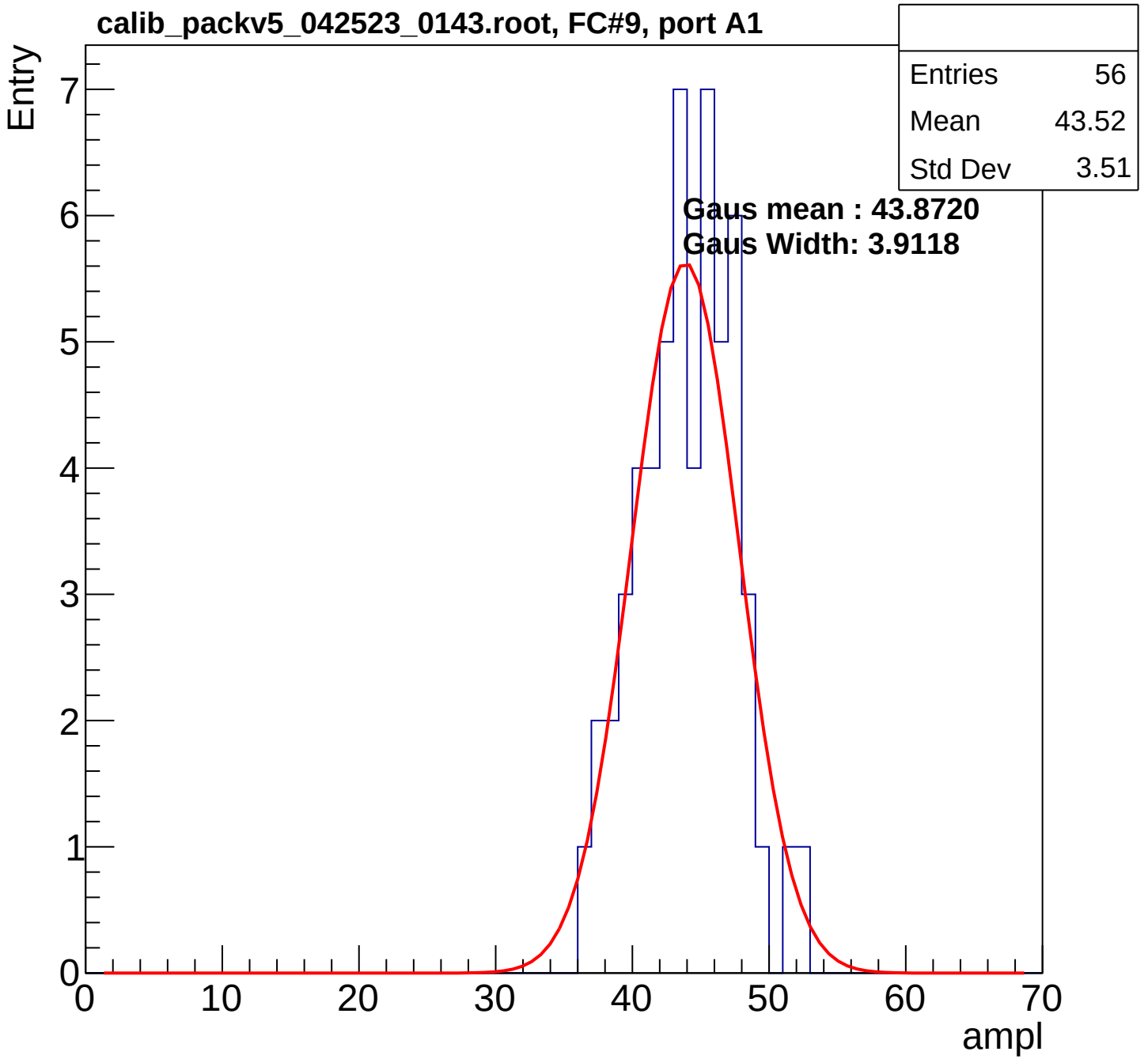
B0L001S, U26-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.52
Std Dev 3.51
Gaus mean : 43.8720
Gaus Width: 3.9118
Entry
ampl
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1
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0
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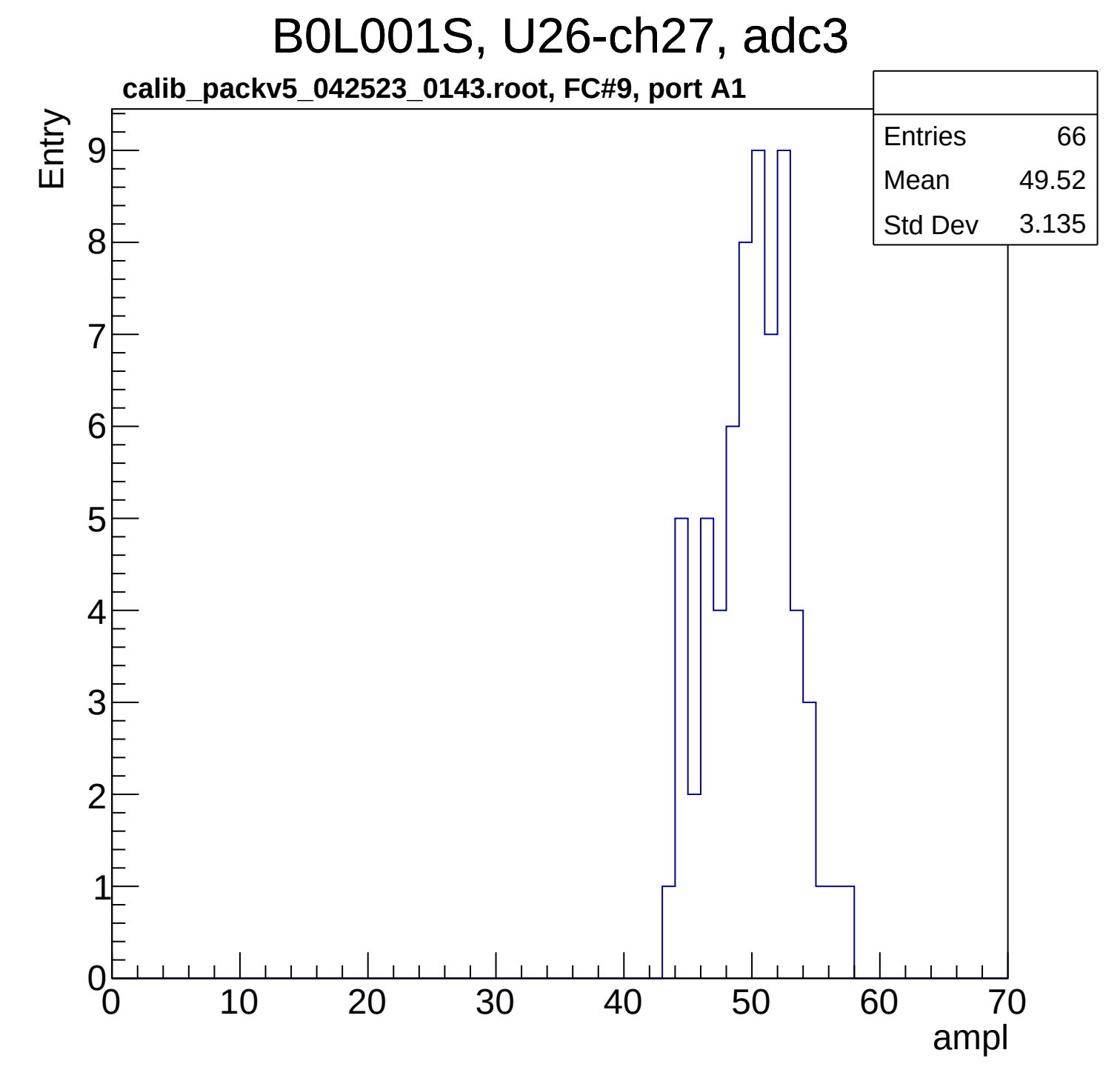
B0L001S, U26-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.52
Std Dev 3.135
Entry
ampl
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4
5
6
7
8
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0
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60
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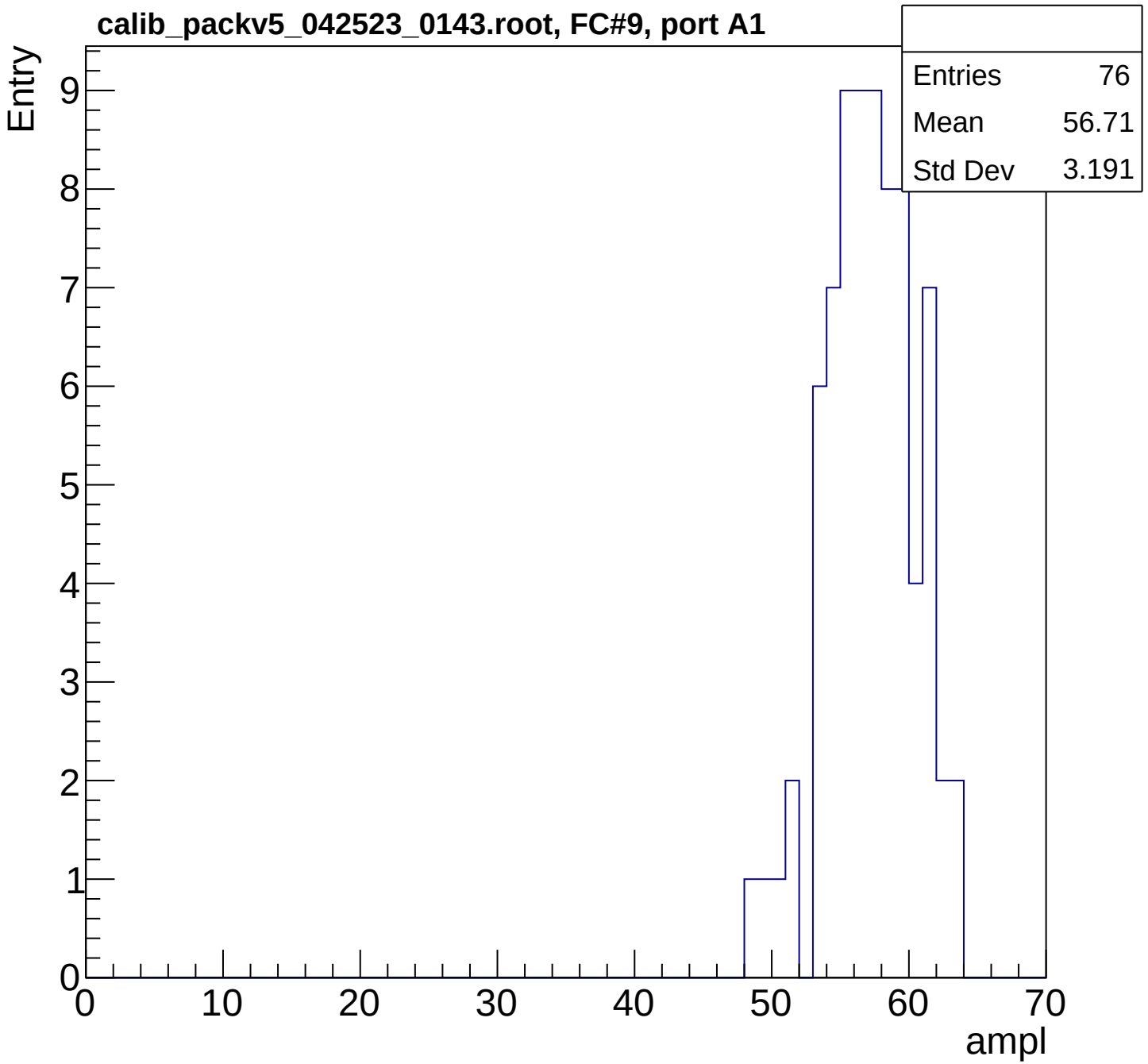
B0L001S, U26-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 56.71
Std Dev 3.191
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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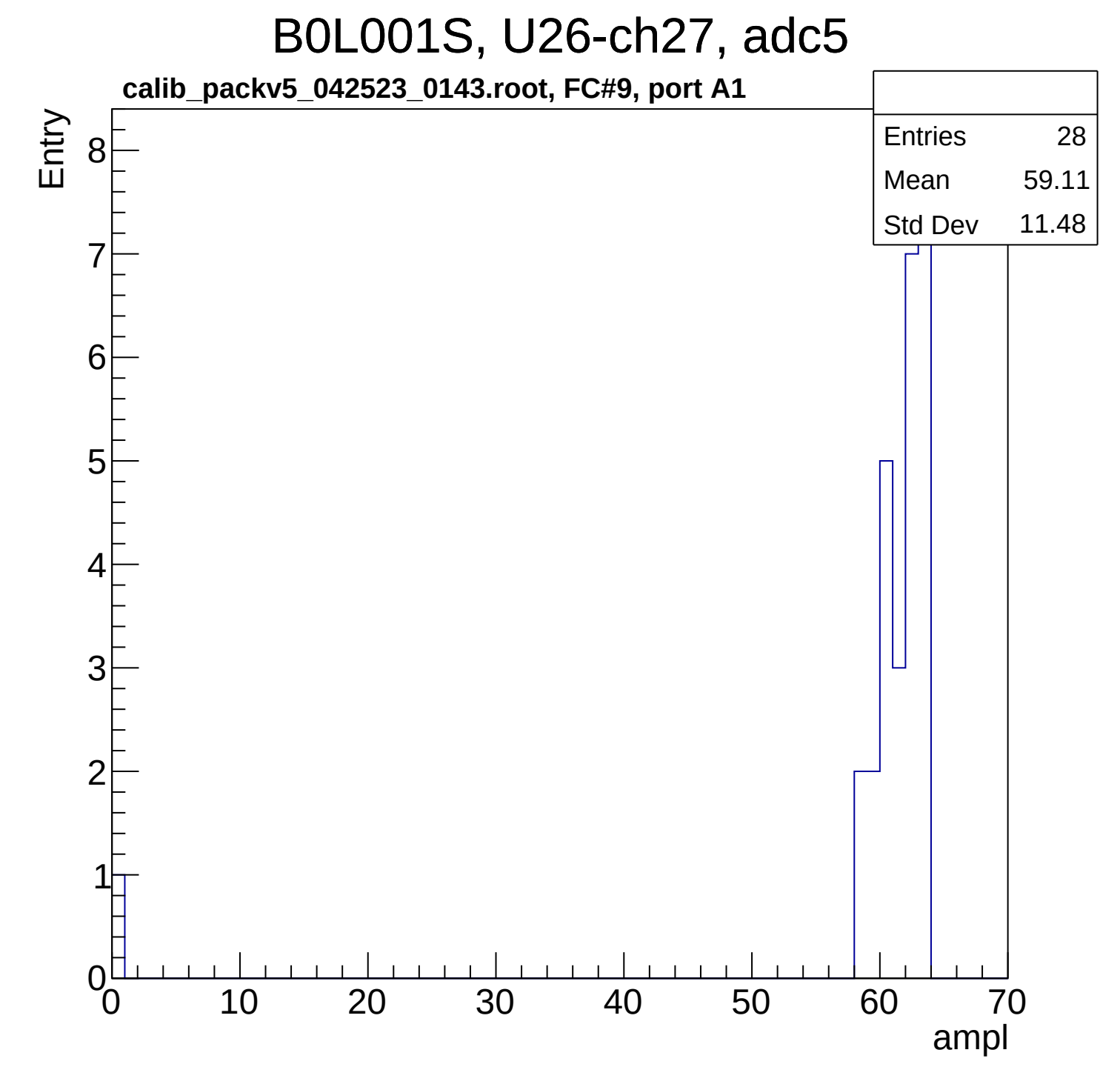
B0L001S, U26-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 59.11
Std Dev 11.48
Entry
0
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2
3
4
5
6
7
8
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10
20
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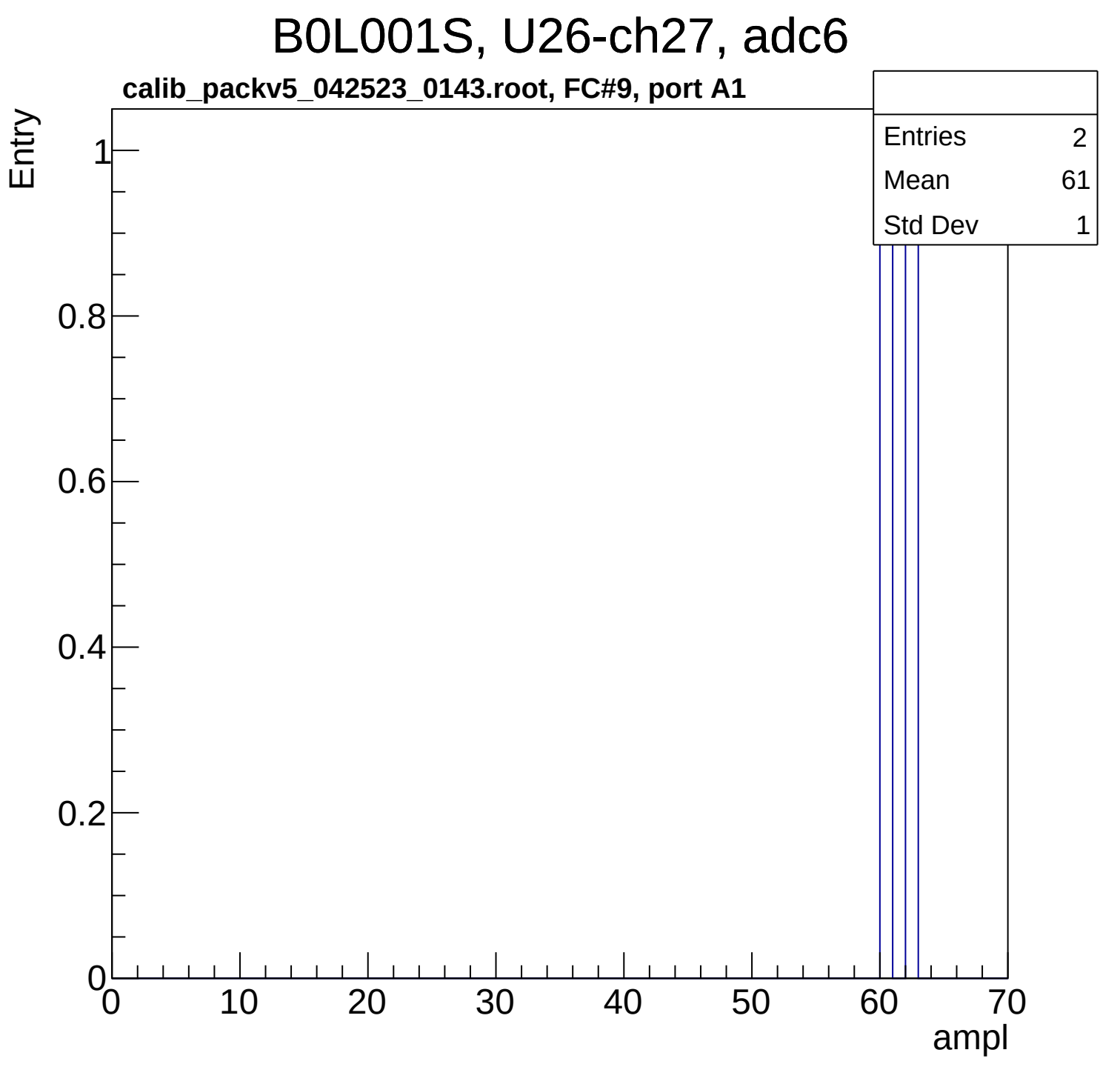
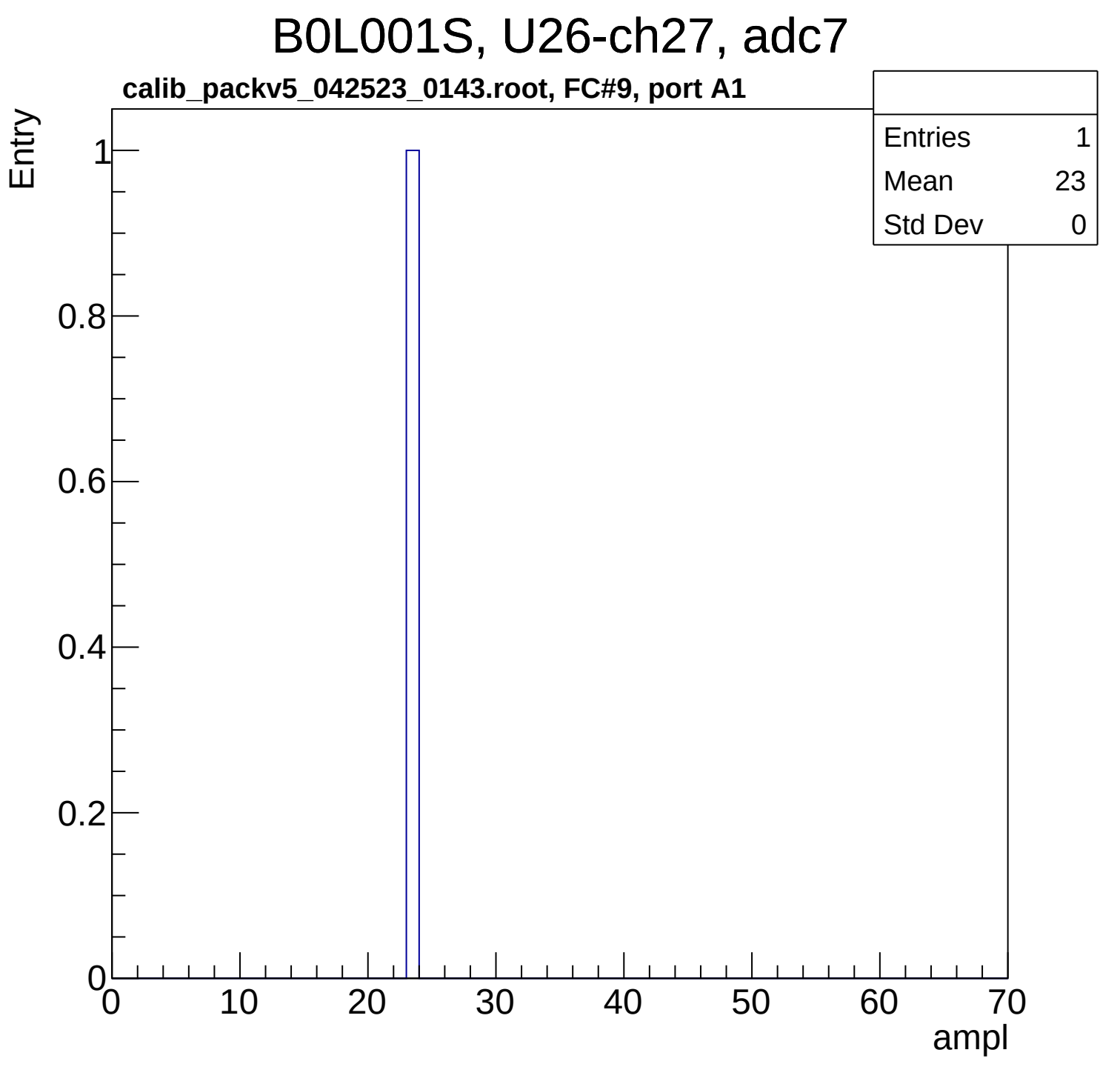
50
60
70
ampl


B0L001S, U26-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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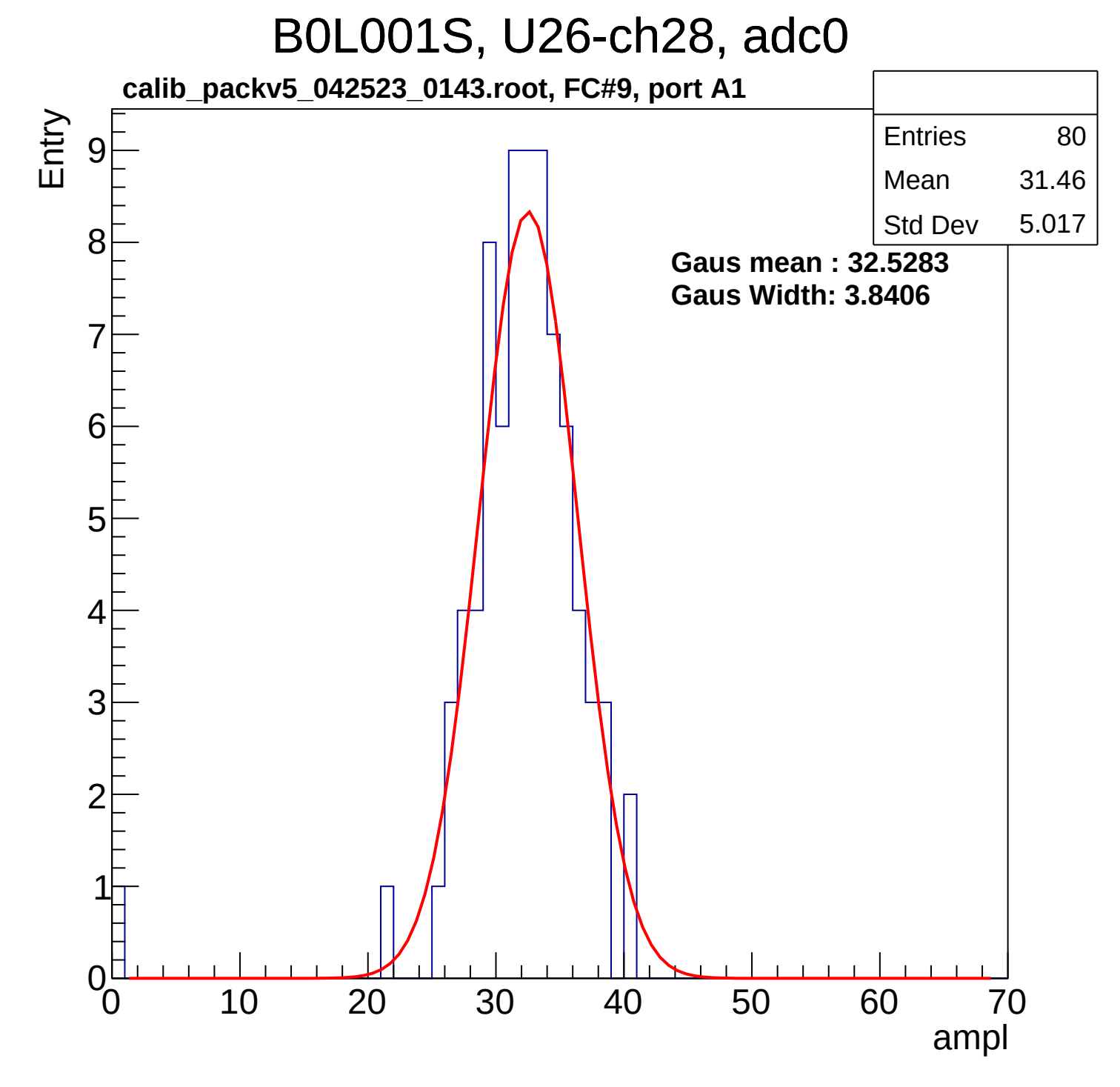
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 31.46
Std Dev 5.017
Gaus mean : 32.5283
Gaus Width: 3.8406
Entry
ampl
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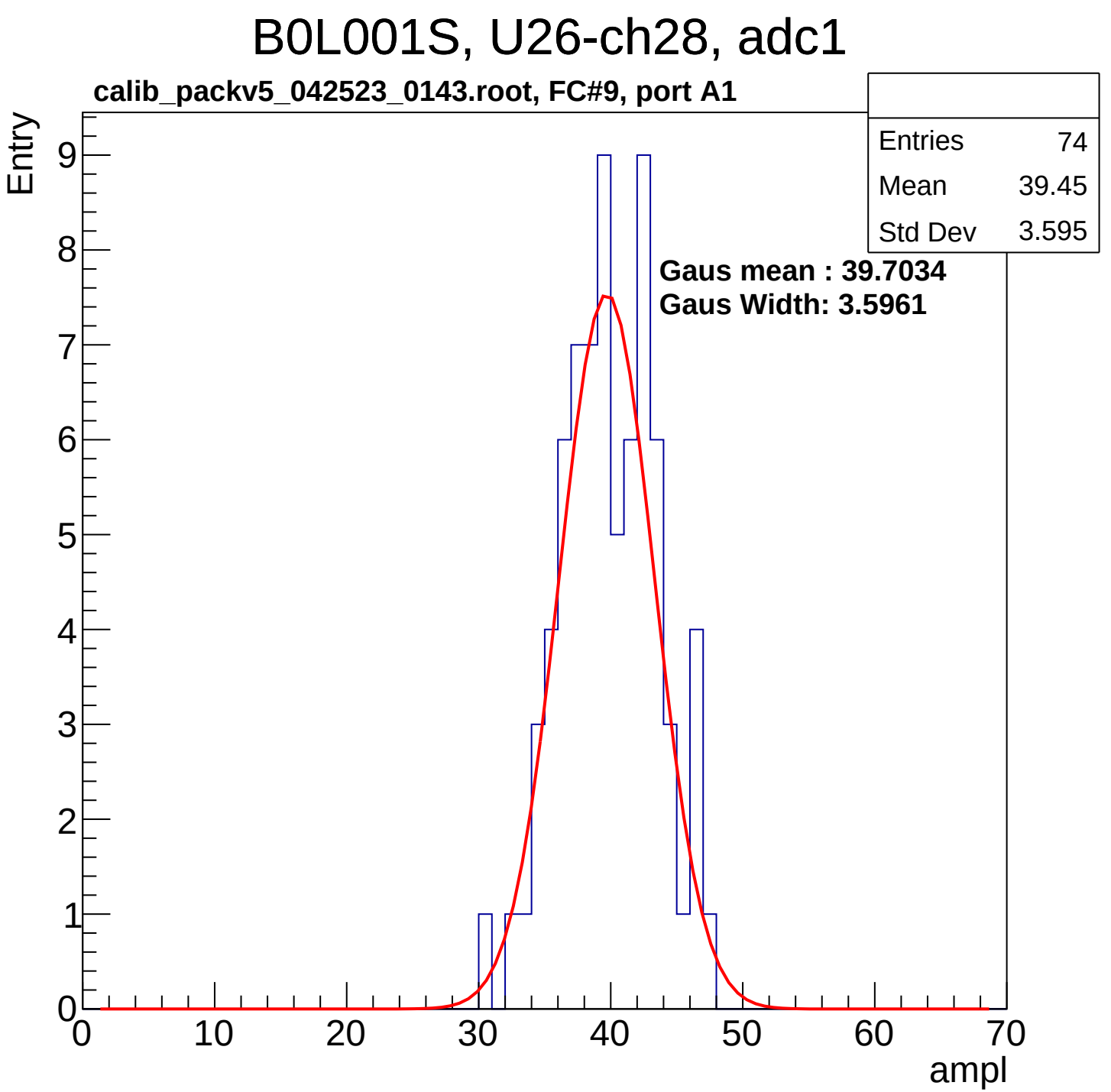
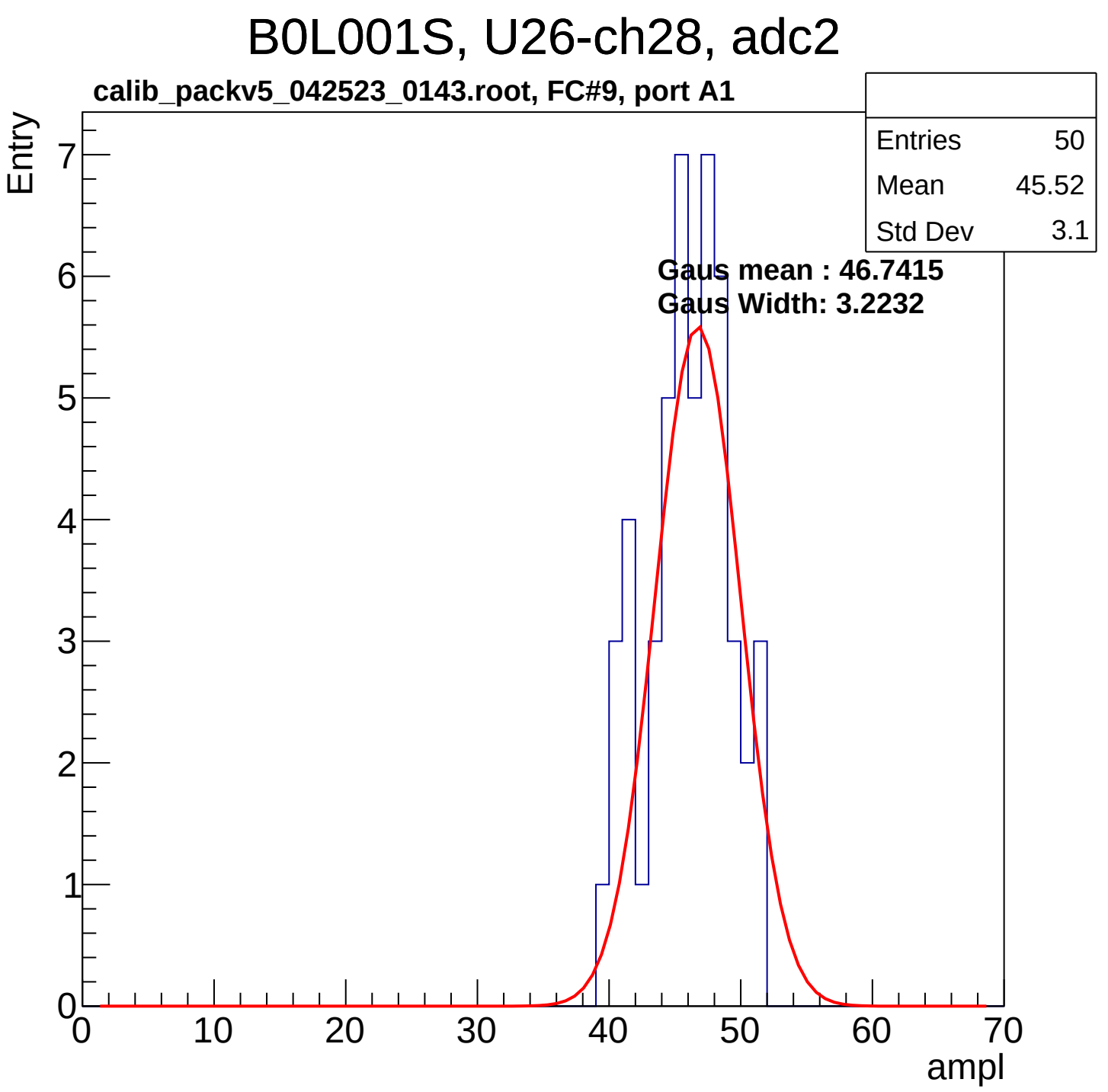
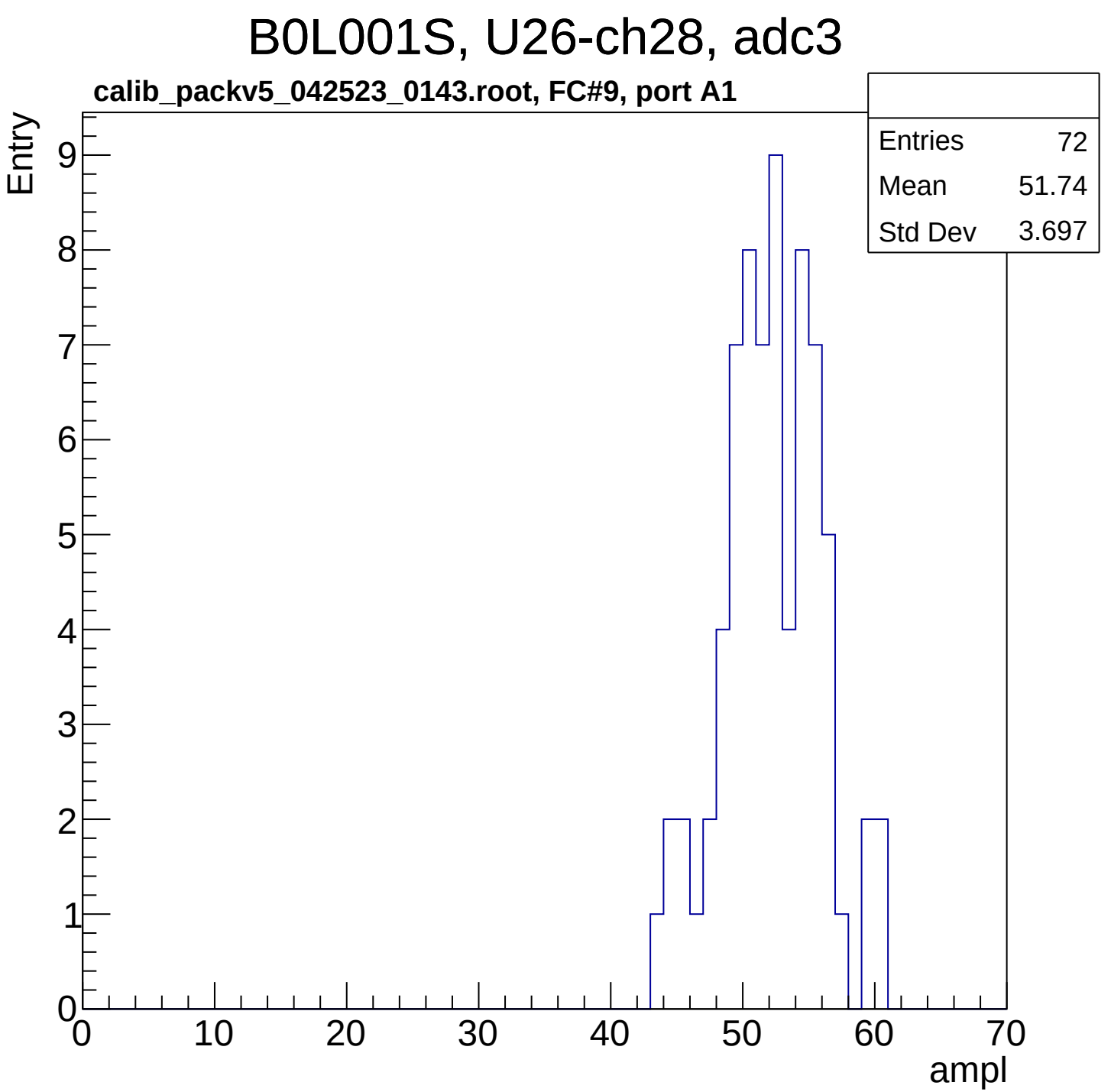
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 39.45
Std Dev 3.595
Gaus mean : 39.7034
Gaus Width: 3.5961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 45.52
Std Dev 3.1
Gaus mean : 46.7415
Gaus Width: 3.2232
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.74
Std Dev 3.697
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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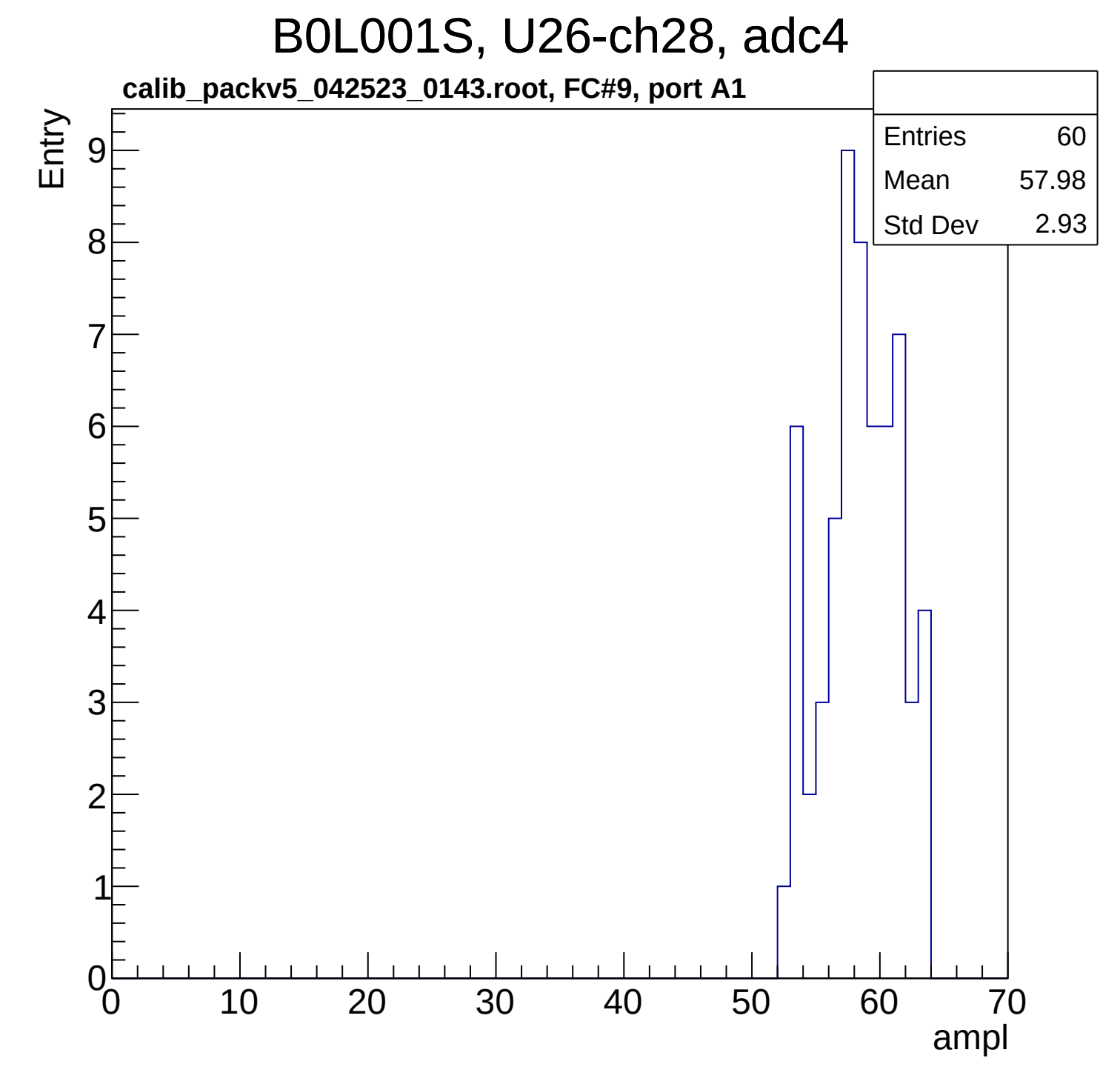
50
60
70
ampl


B0L001S, U26-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.98
Std Dev 2.93
Entry
ampl
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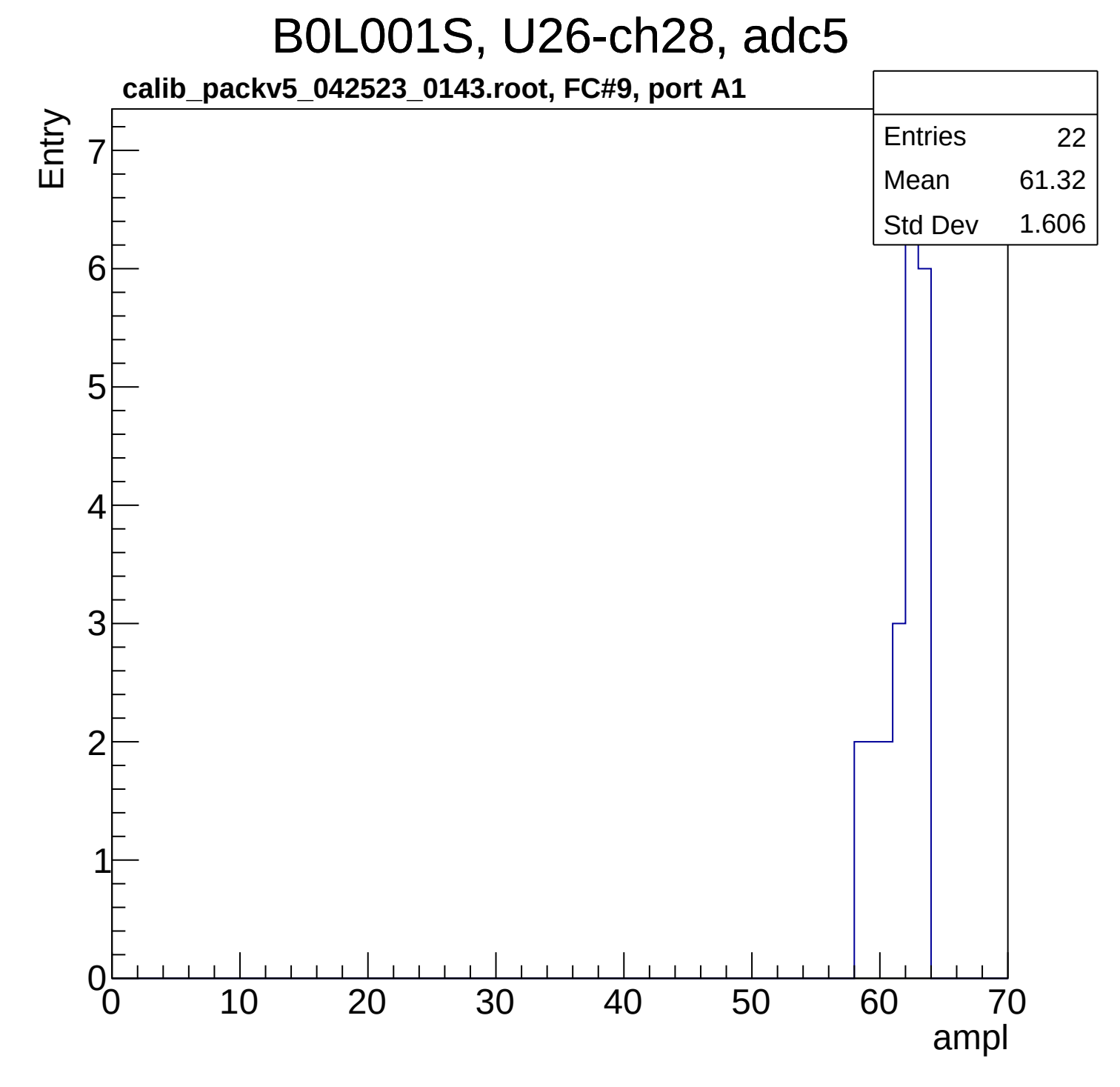
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 61.32
Std Dev 1.606
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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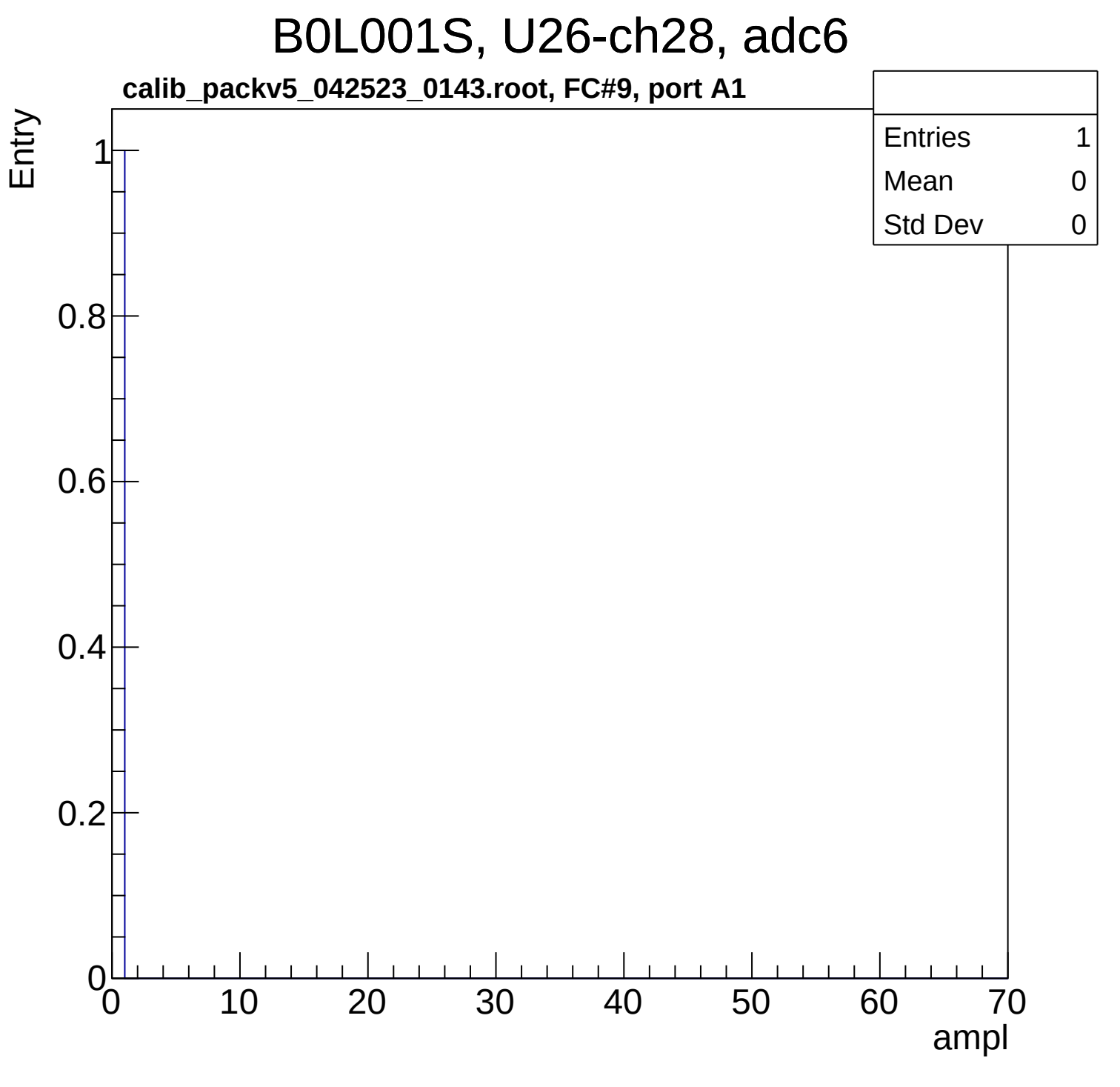
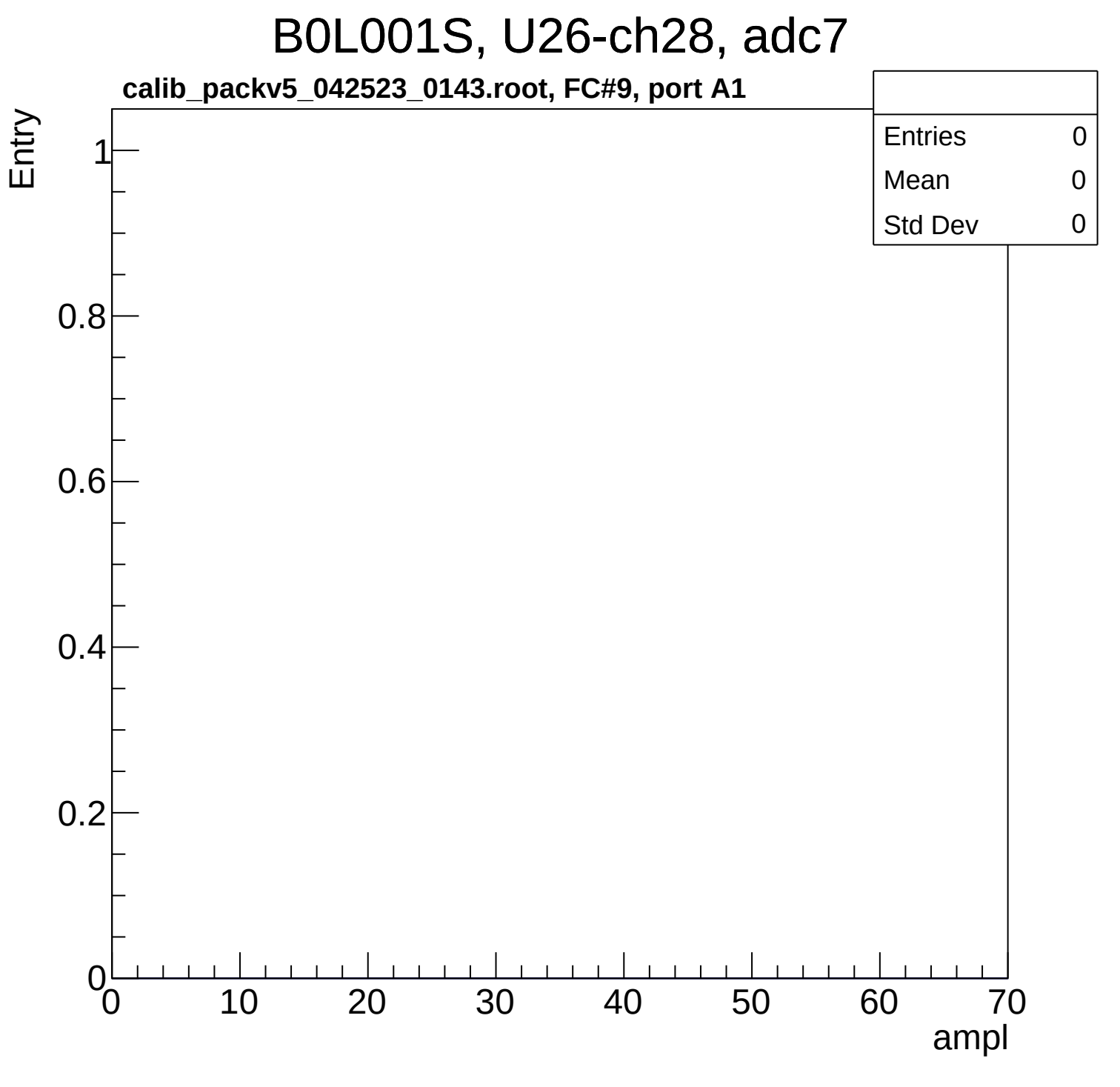
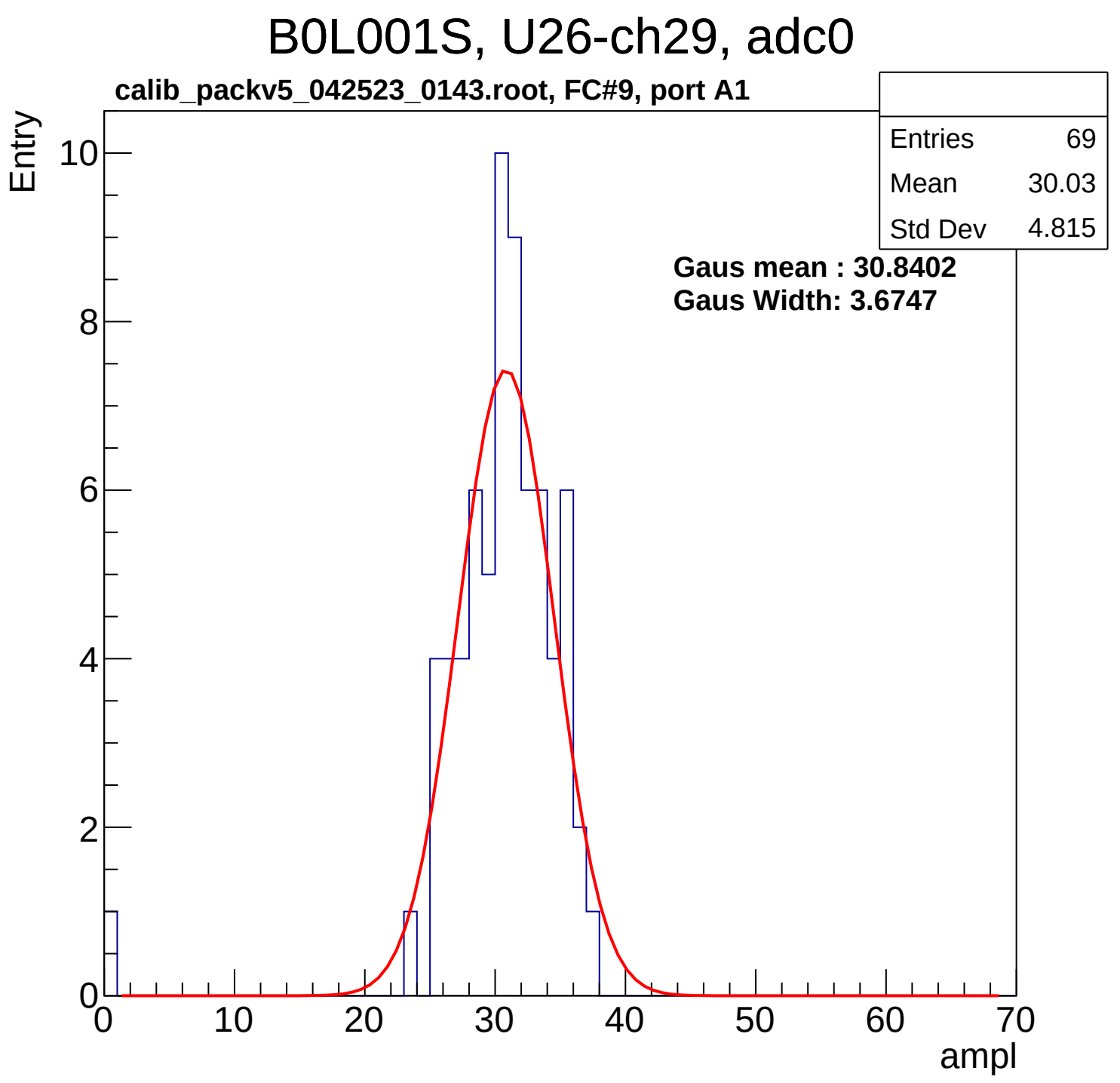
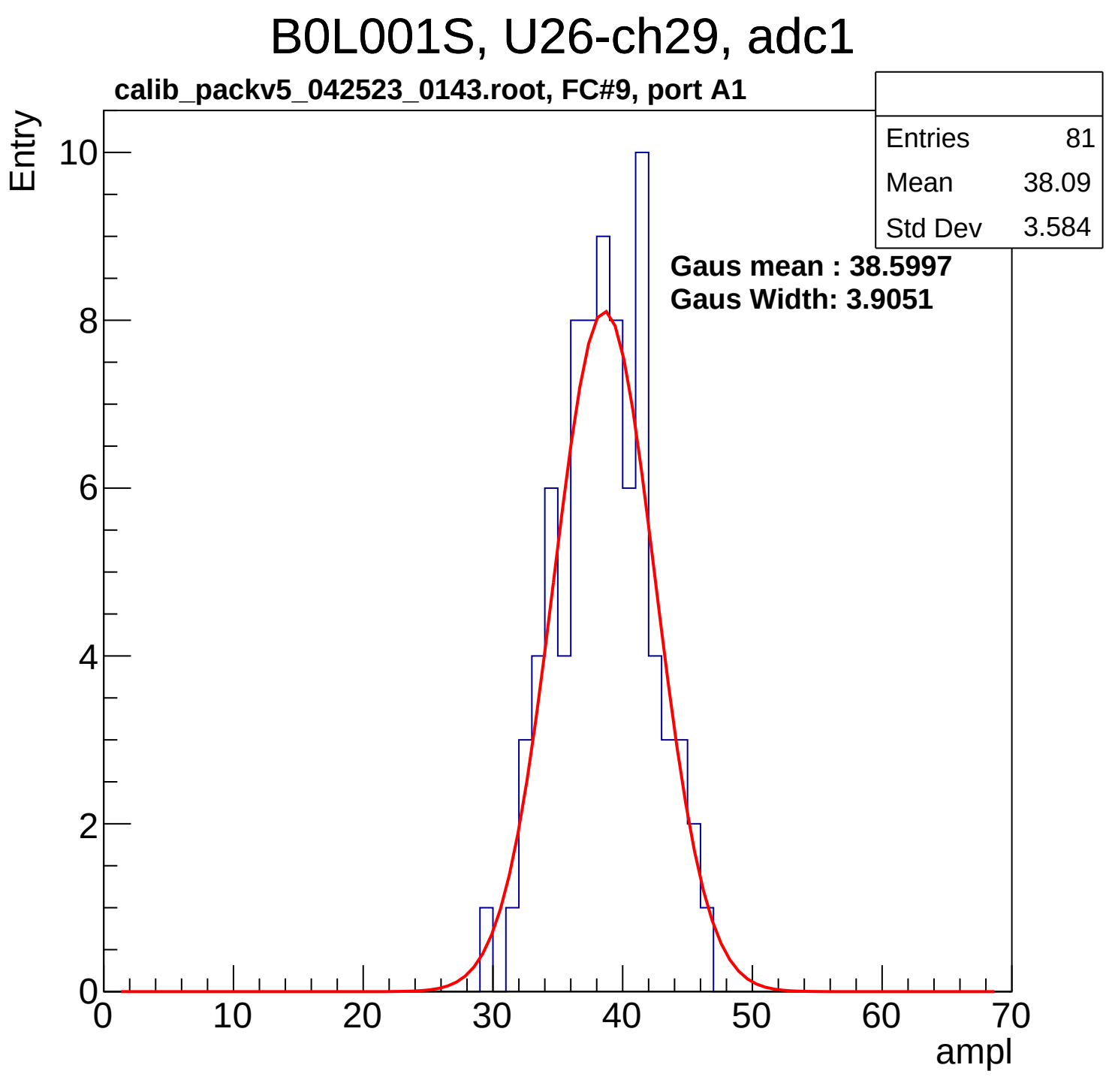
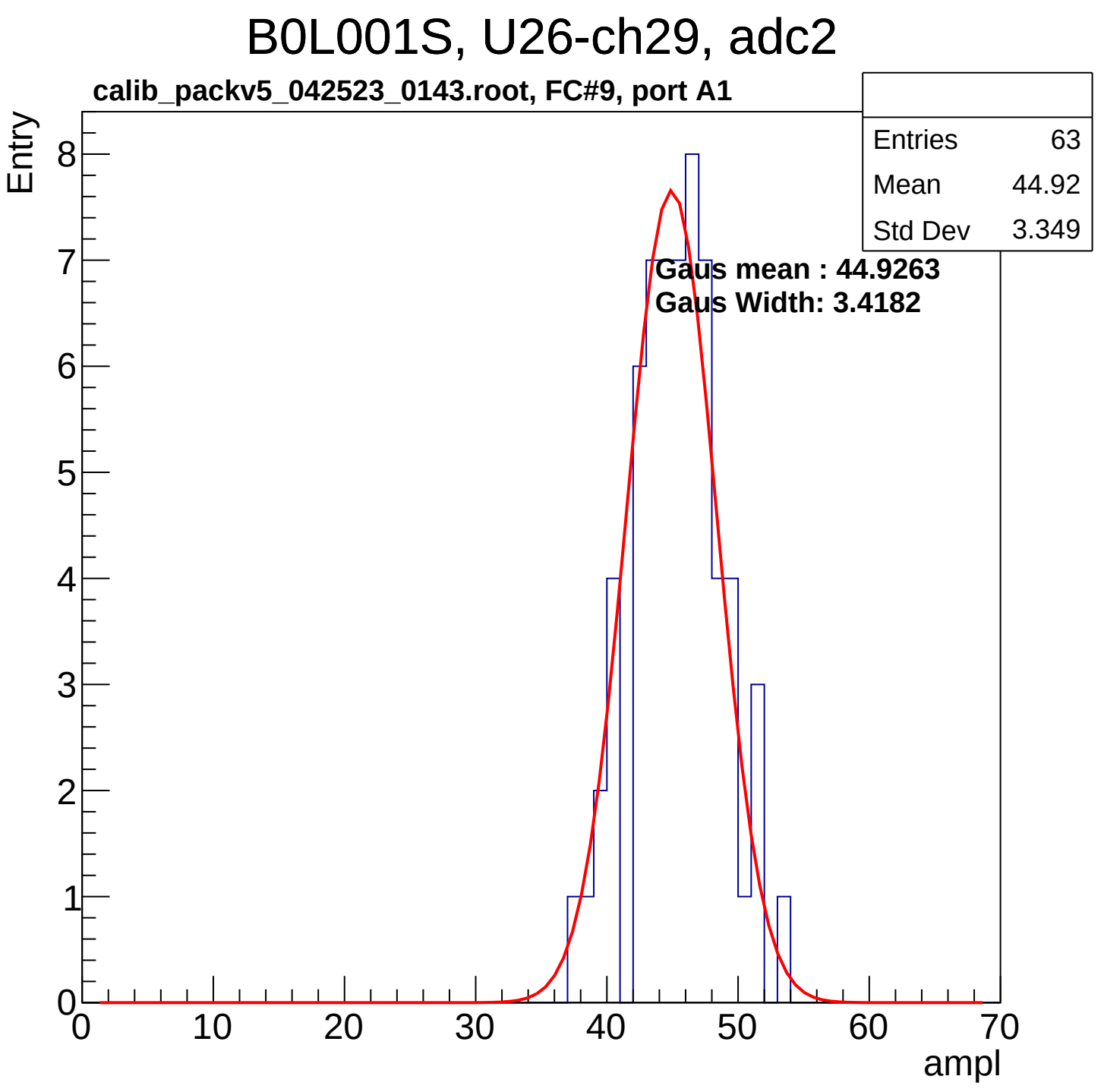
40
50
60
70


B0L001S, U26-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.03
Std Dev 4.815
Gaus mean : 30.8402
Gaus Width: 3.6747
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 38.09
Std Dev 3.584
Gaus mean : 38.5997
Gaus Width: 3.9051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.92
Std Dev 3.349
Gaus mean : 44.9263
Gaus Width: 3.4182
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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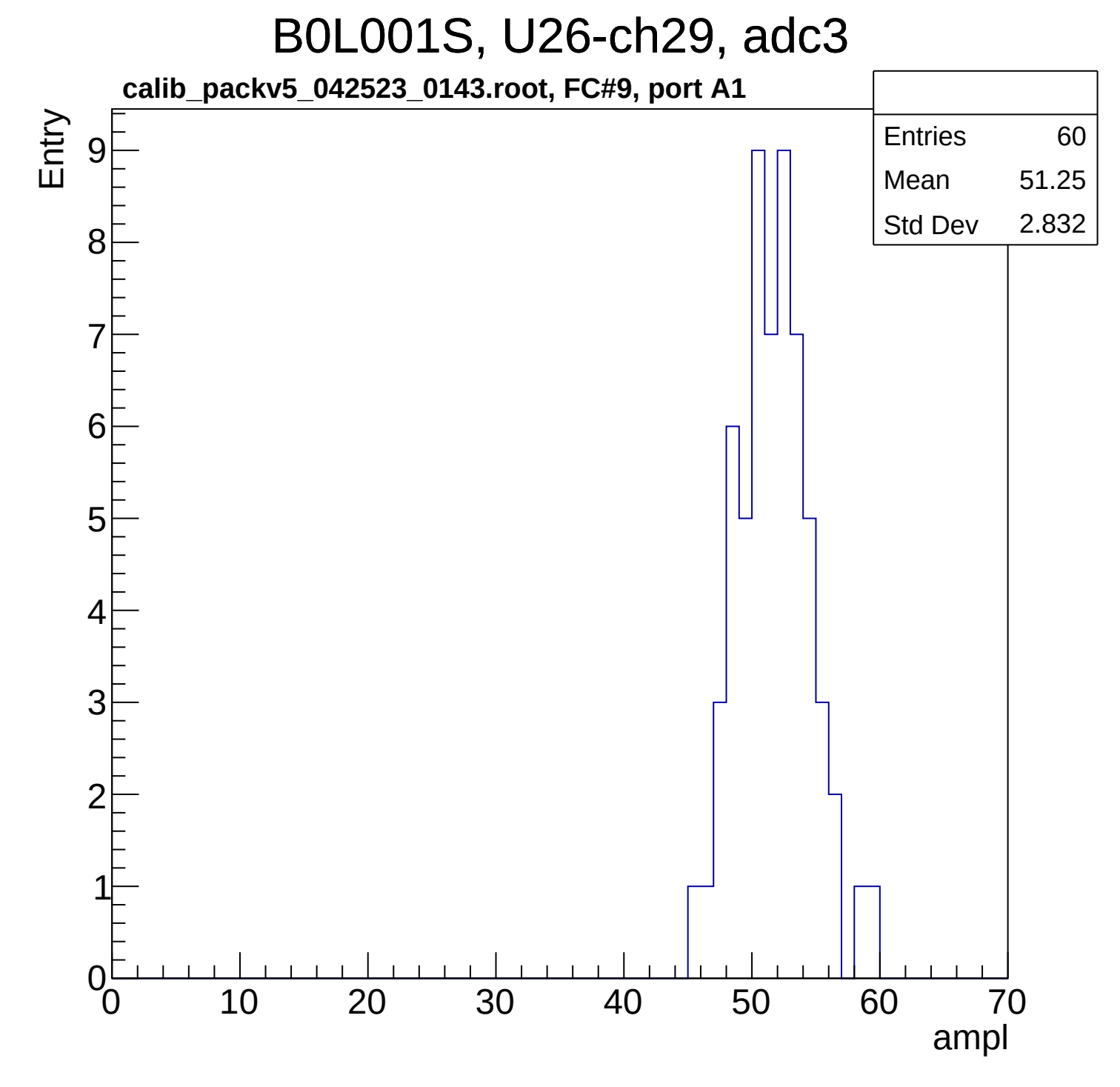
B0L001S, U26-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.25
Std Dev 2.832
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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40
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60
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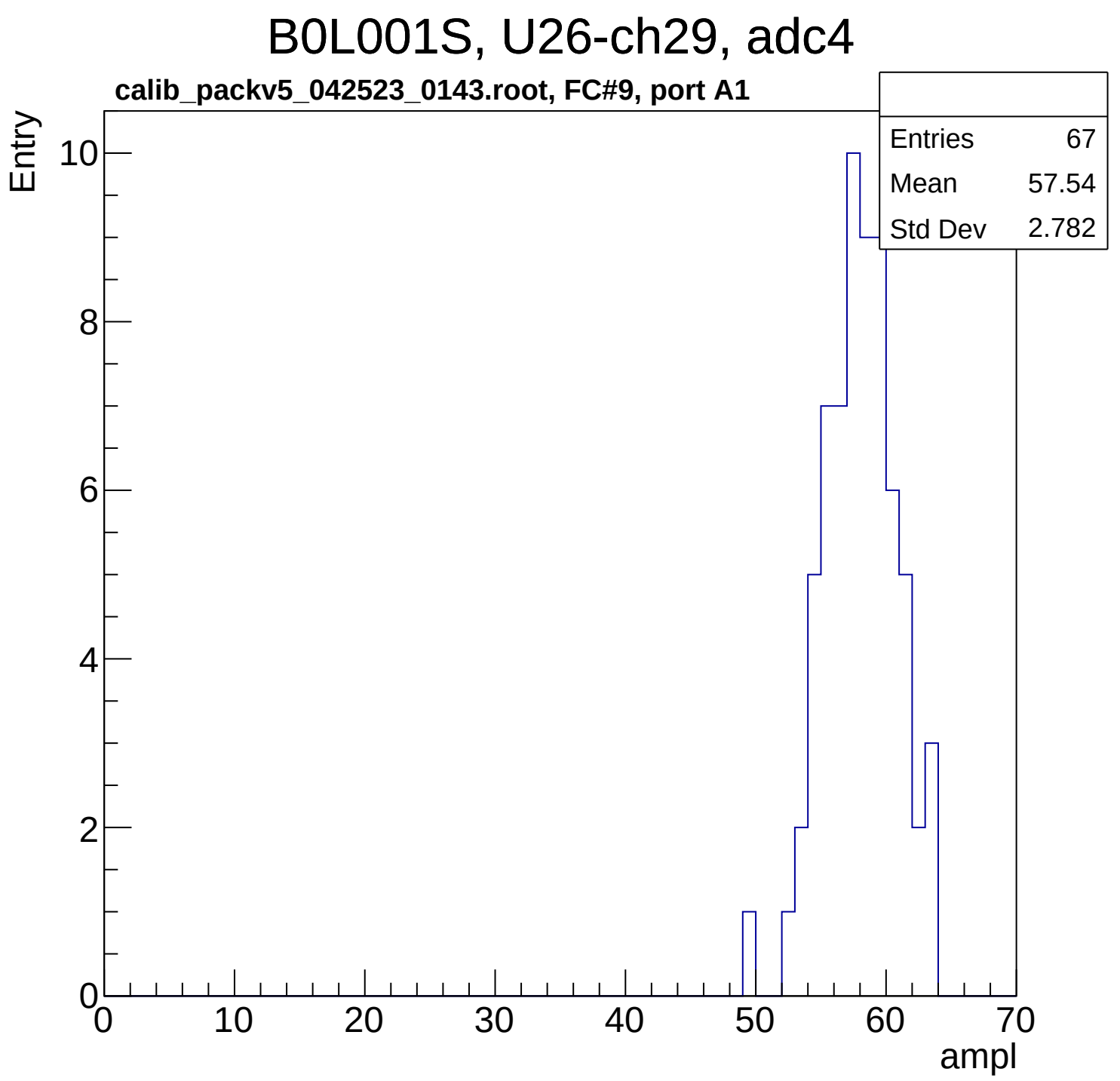
B0L001S, U26-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57.54
Std Dev 2.782
Entry
0
2
4
6
8
10
0
10
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30
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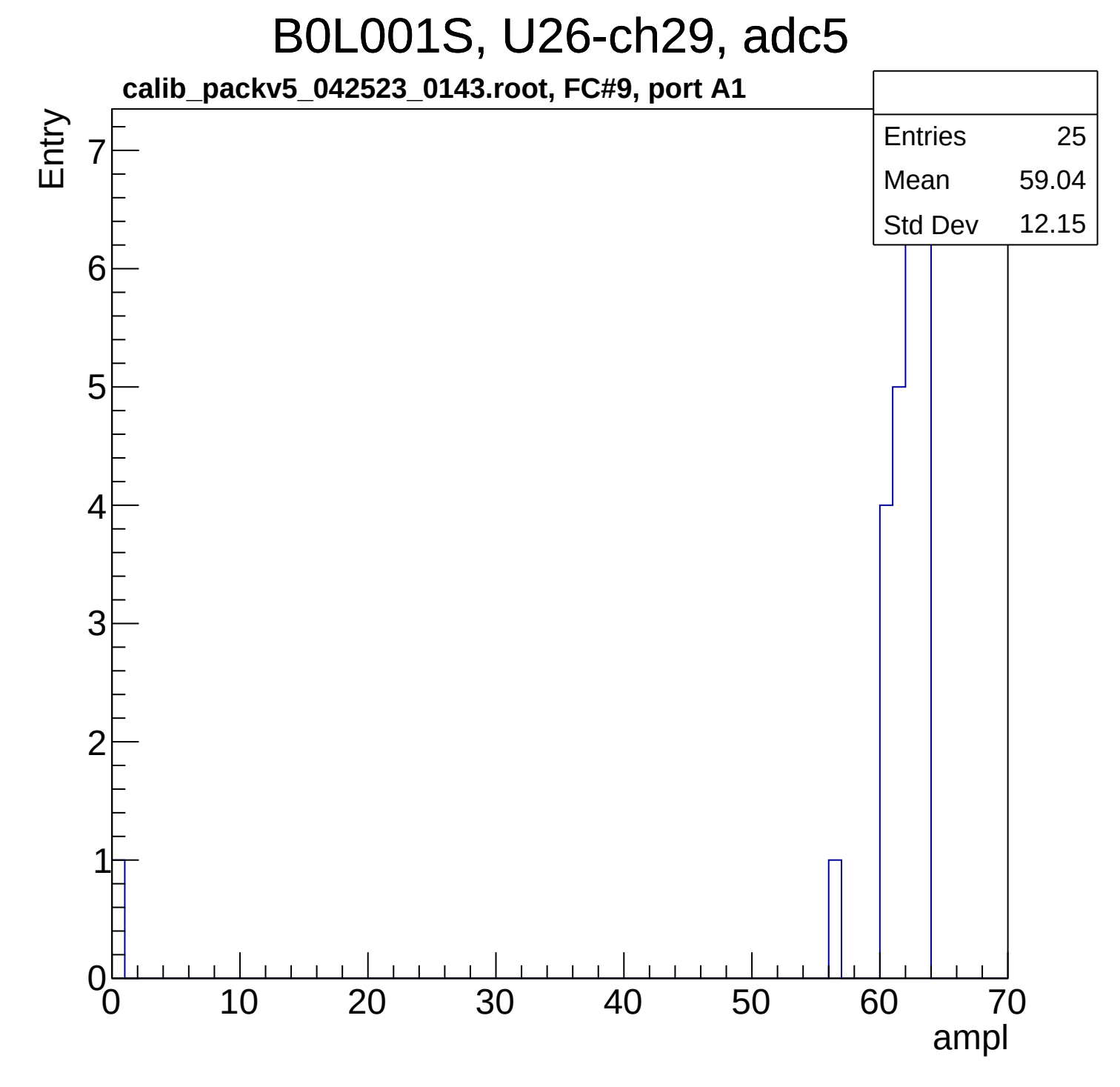
50
60
70
ampl


B0L001S, U26-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 59.04
Std Dev 12.15
Entry
ampl
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4
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0
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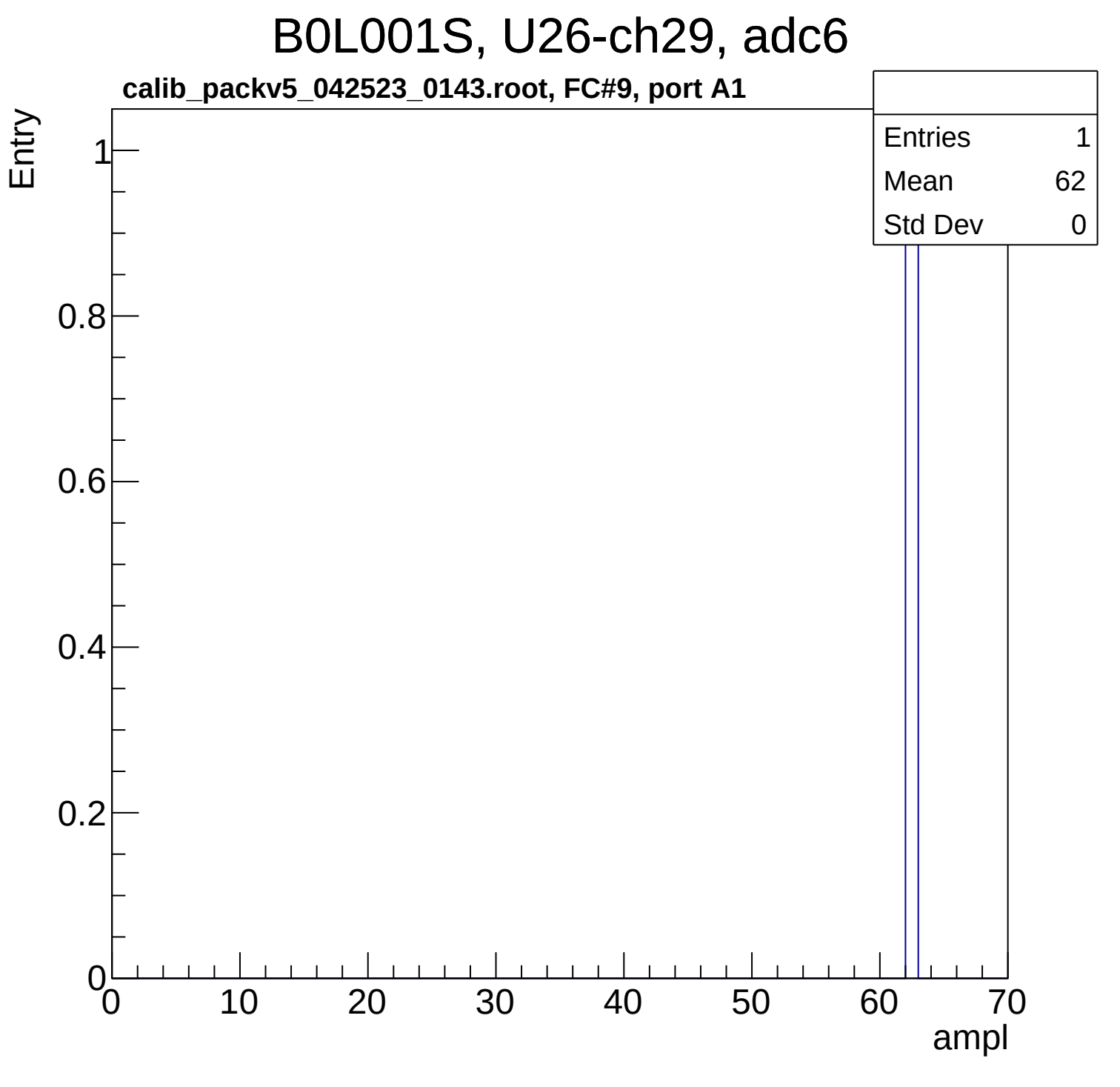
B0L001S, U26-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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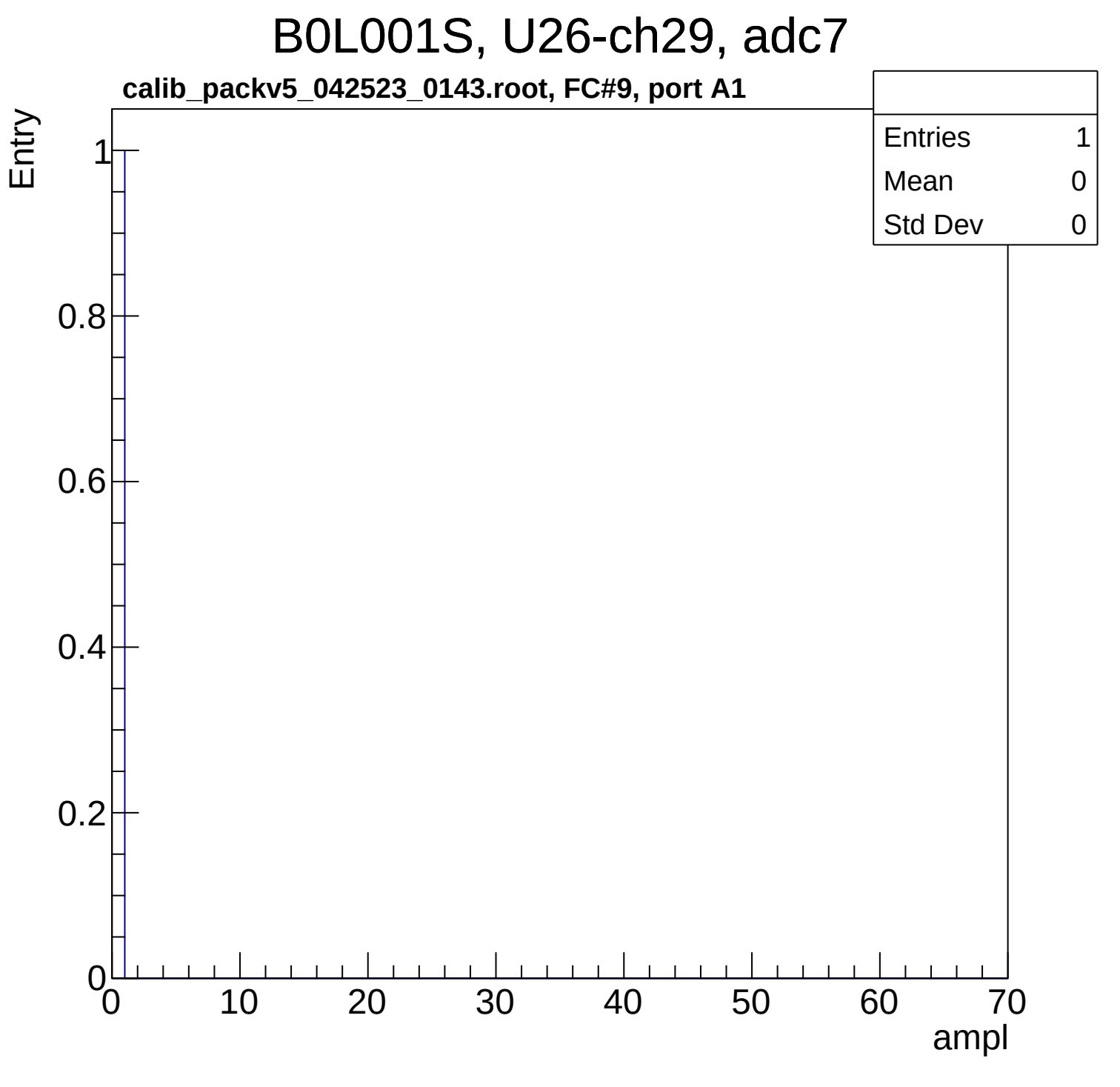
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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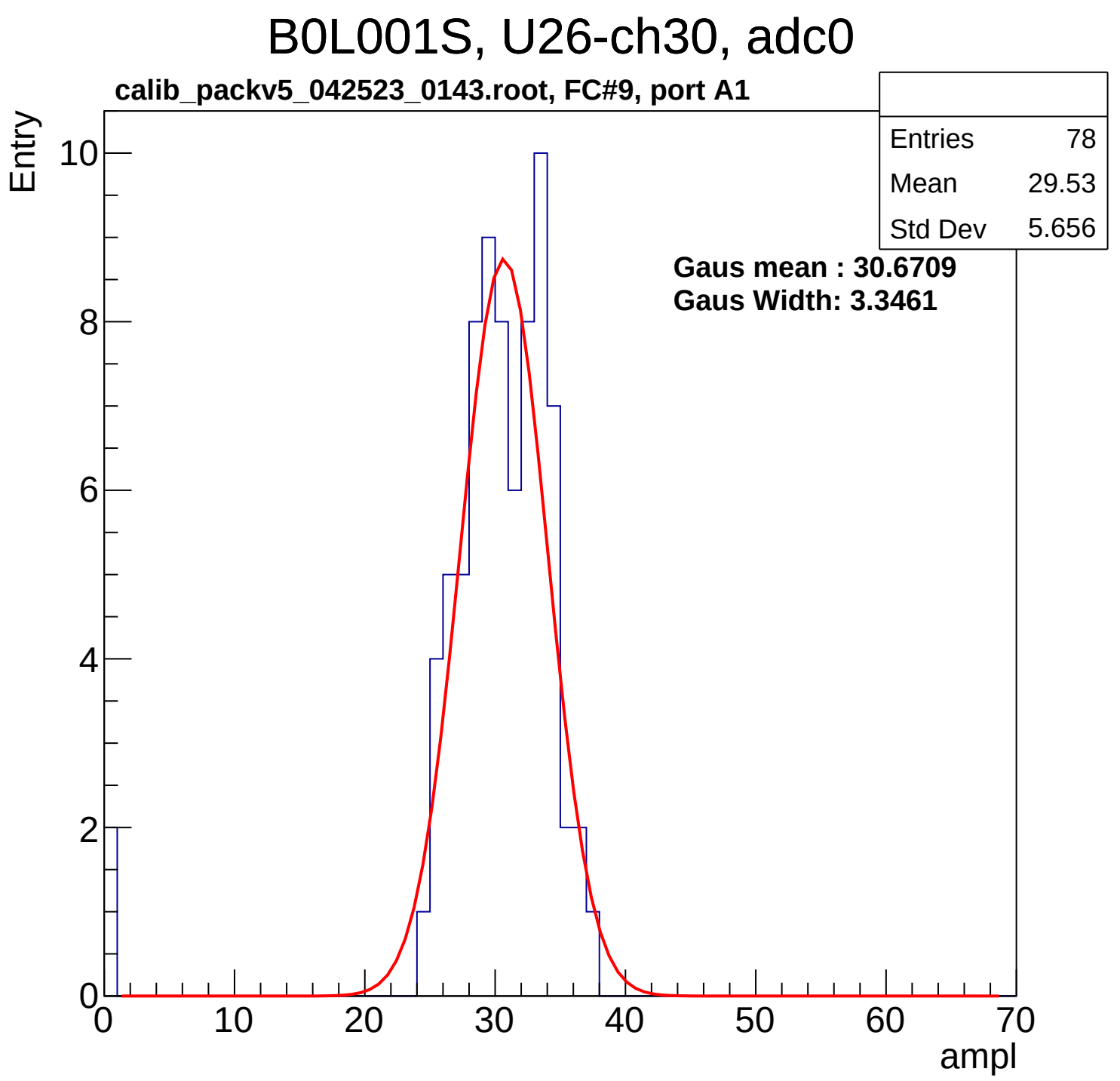
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.53
Std Dev 5.656
Gaus mean : 30.6709
Gaus Width: 3.3461
Entry
ampl
0
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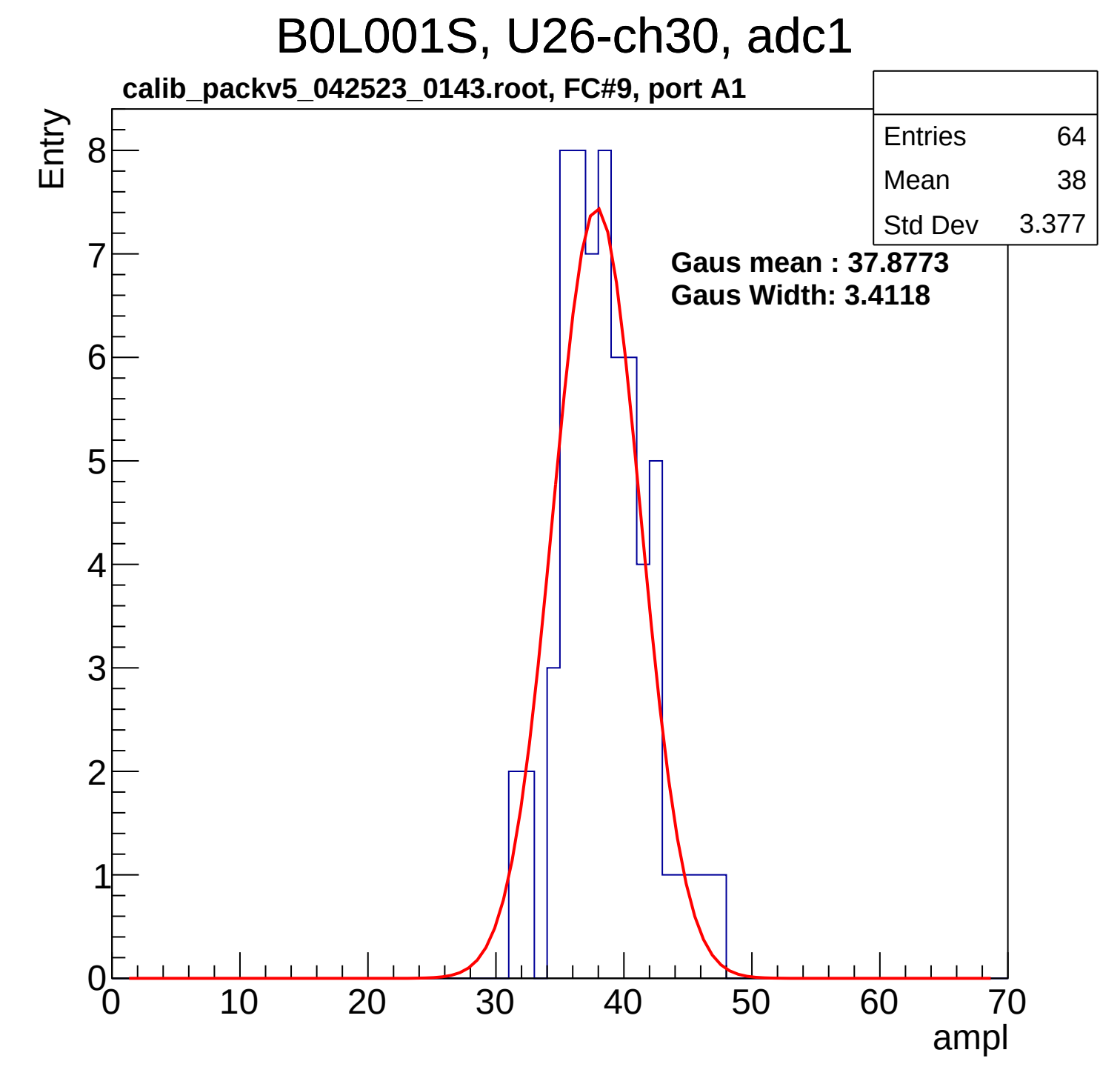
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38
Std Dev 3.377
Gaus mean : 37.8773
Gaus Width: 3.4118
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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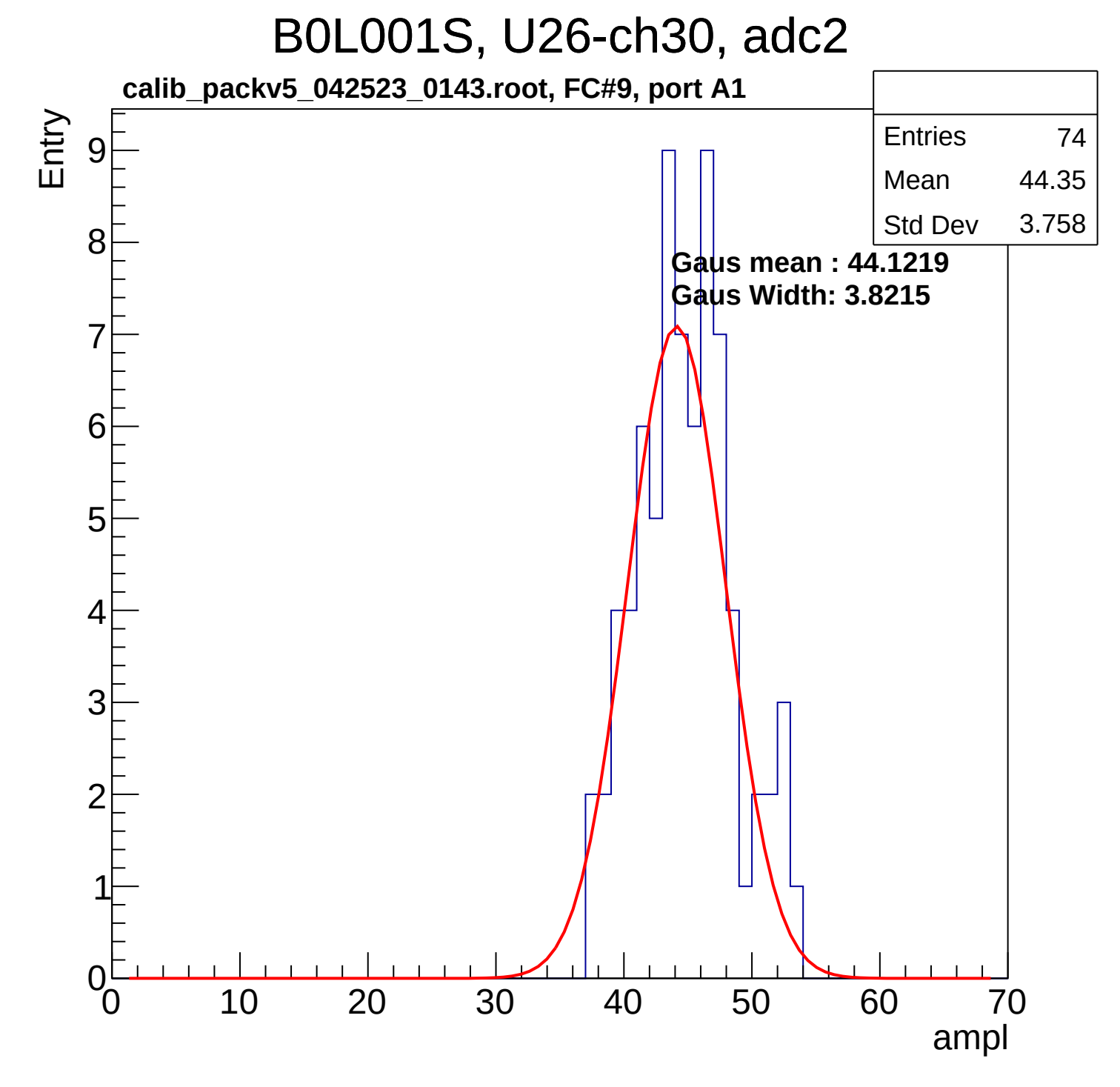
B0L001S, U26-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44.35
Std Dev 3.758
Gaus mean : 44.1219
Gaus Width: 3.8215
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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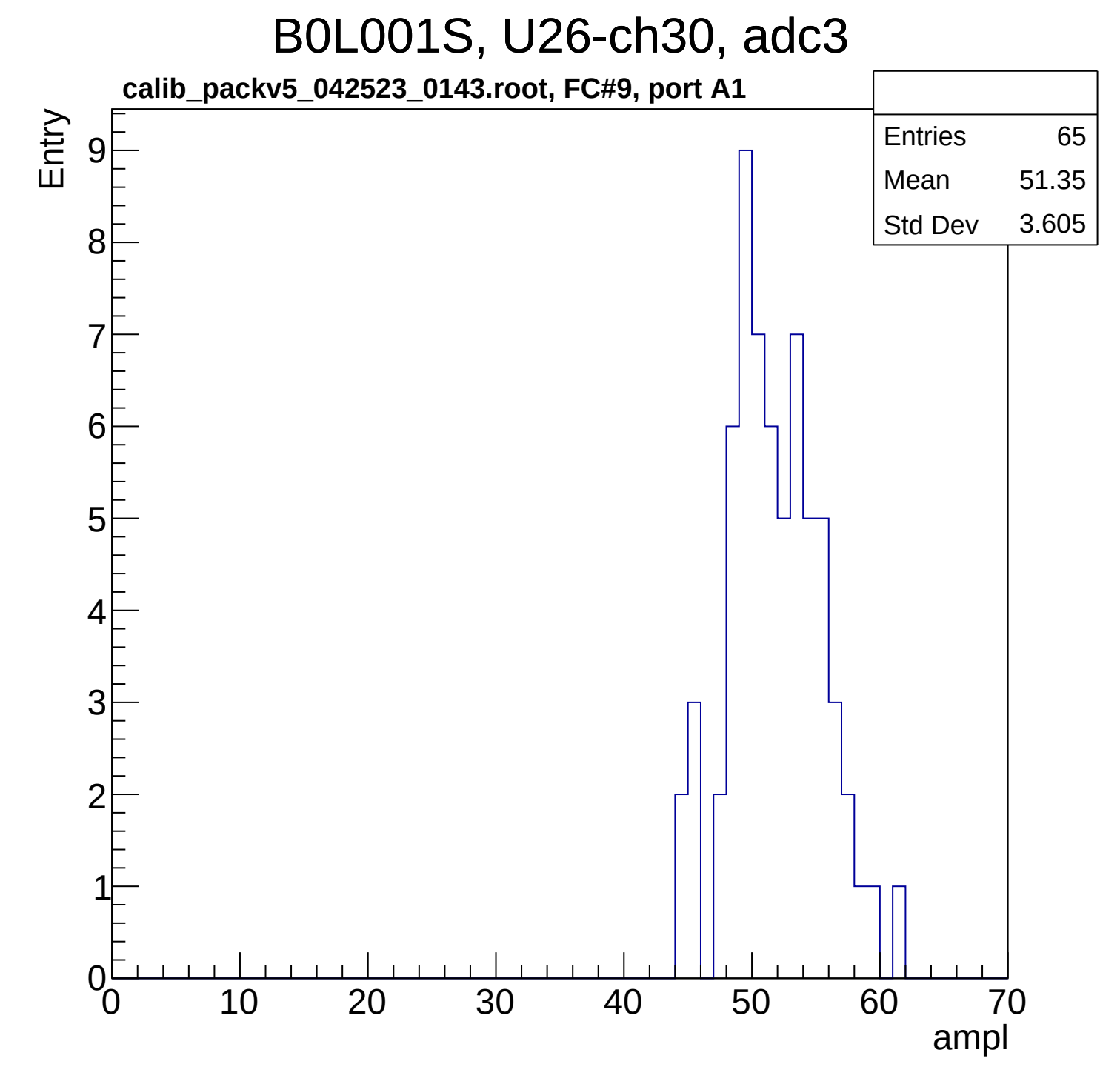
60
70


B0L001S, U26-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.35
Std Dev 3.605
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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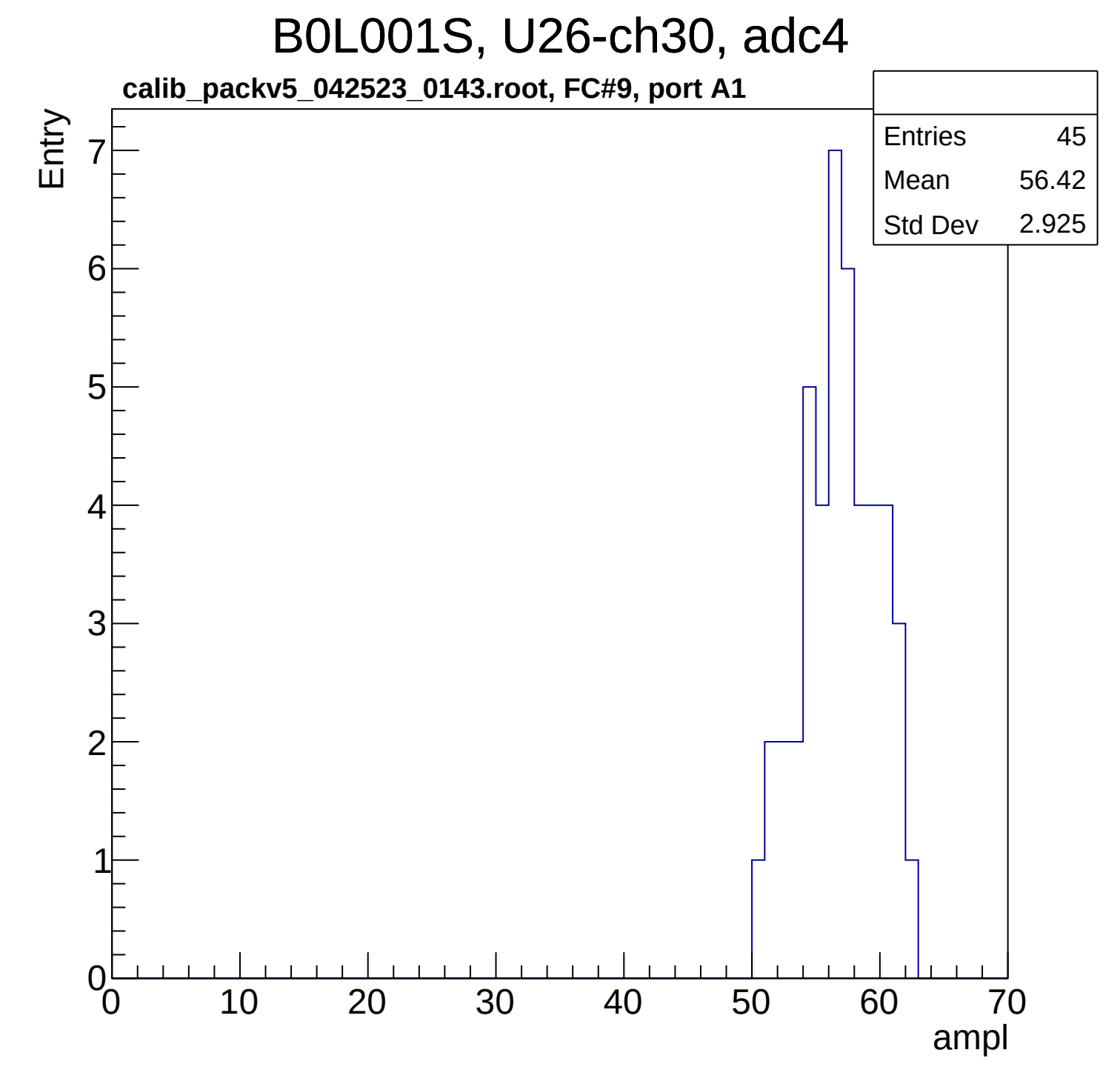
B0L001S, U26-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.42
Std Dev 2.925
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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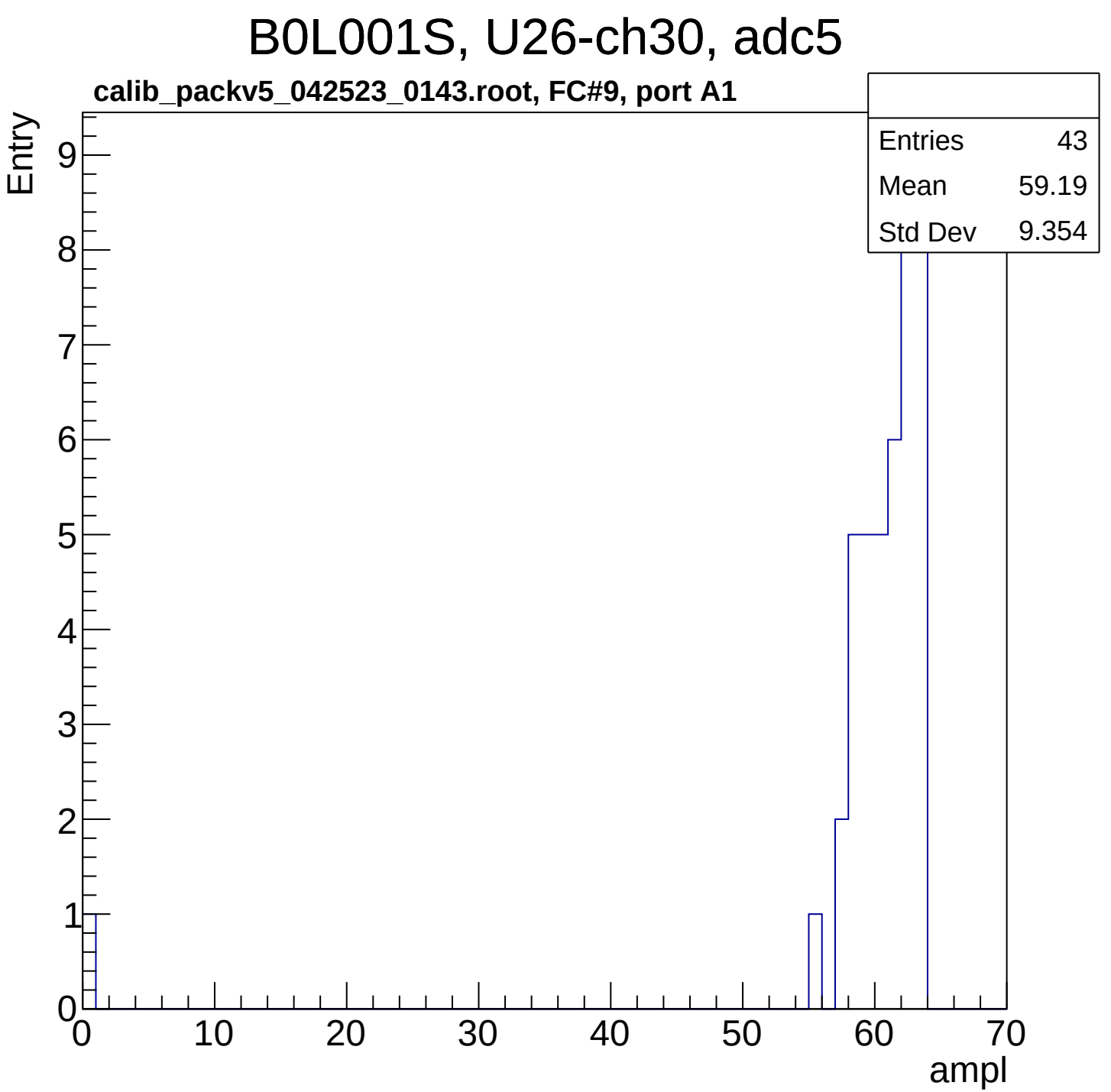
B0L001S, U26-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.19
Std Dev 9.354
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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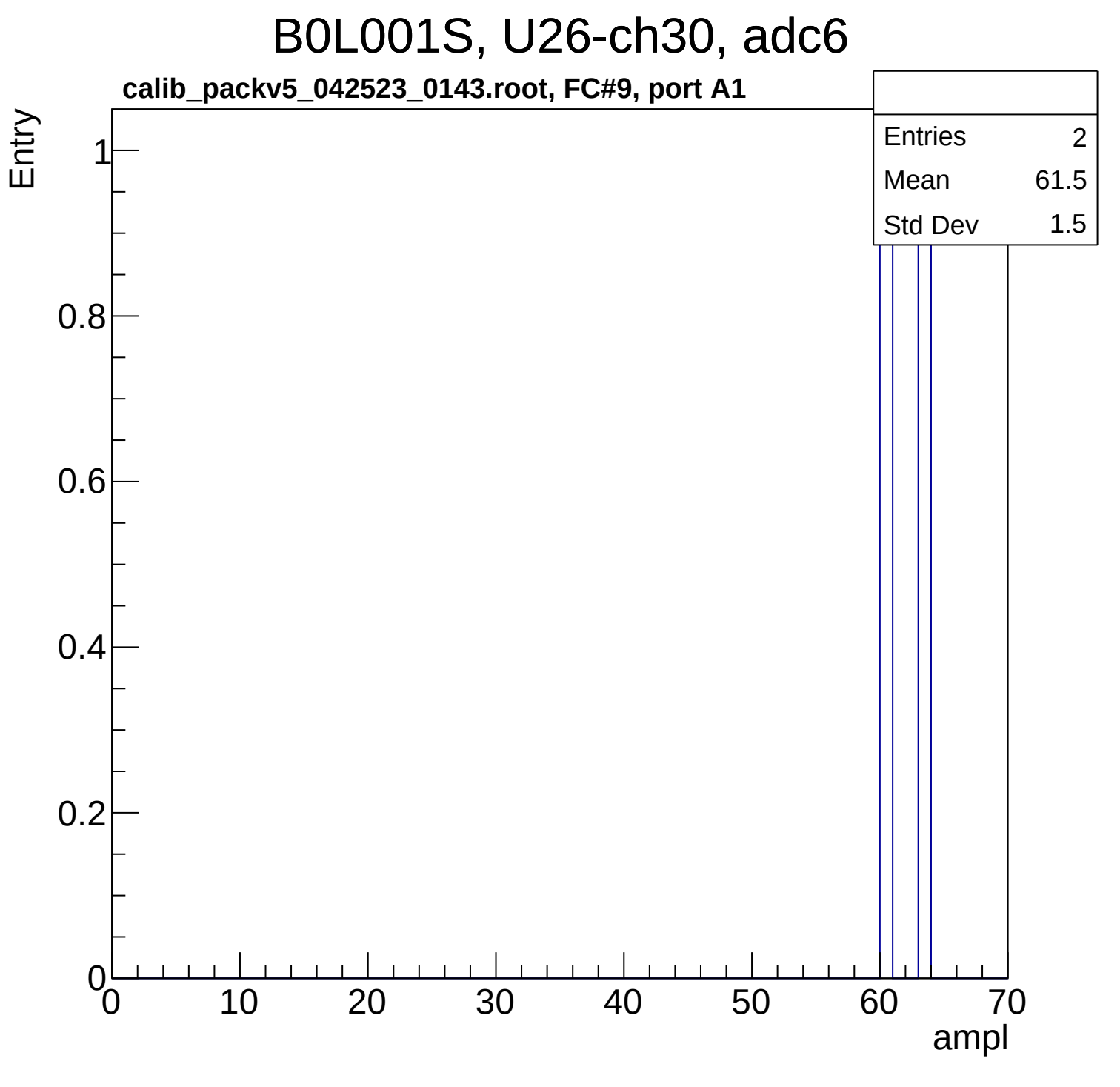
B0L001S, U26-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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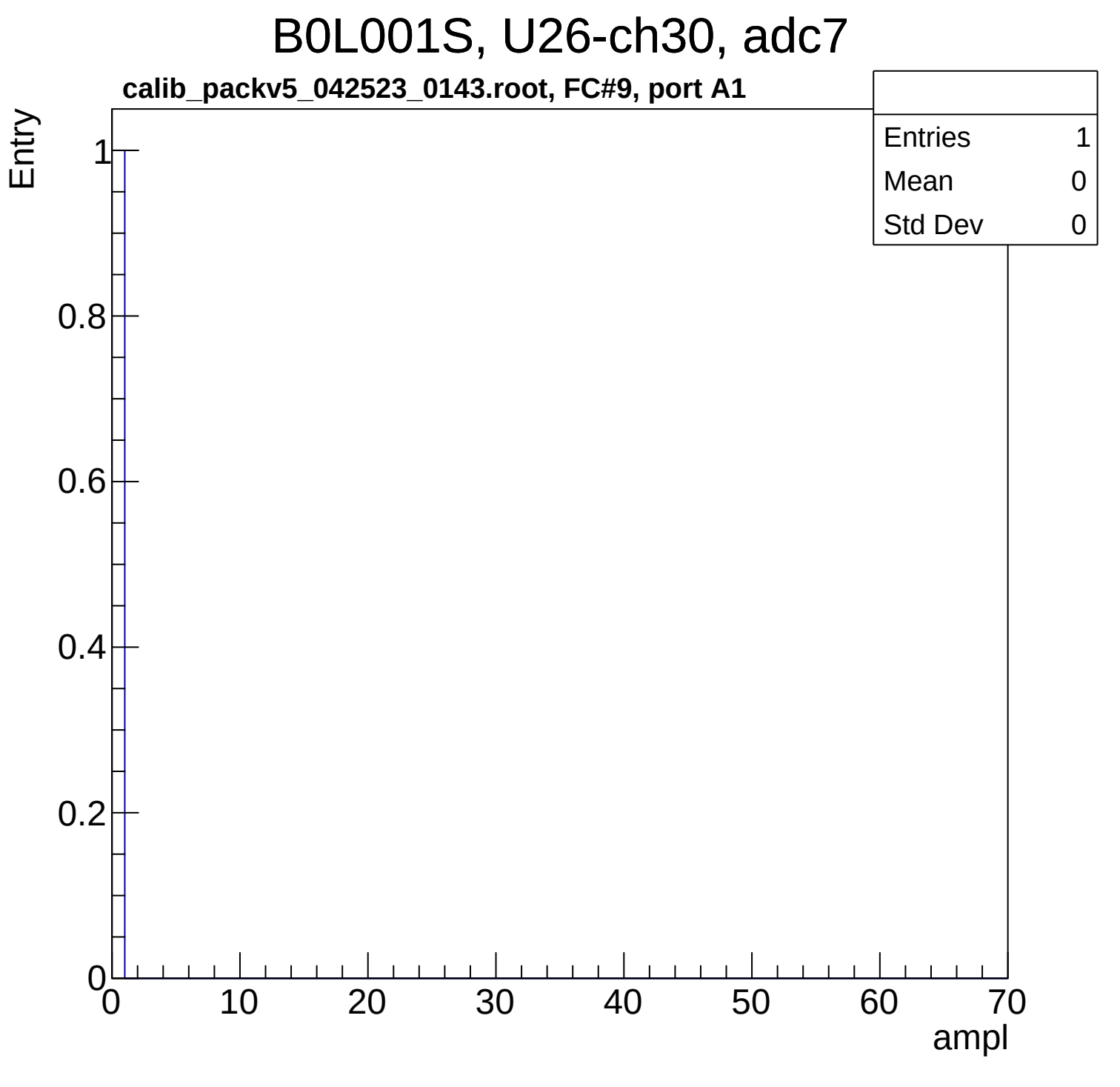
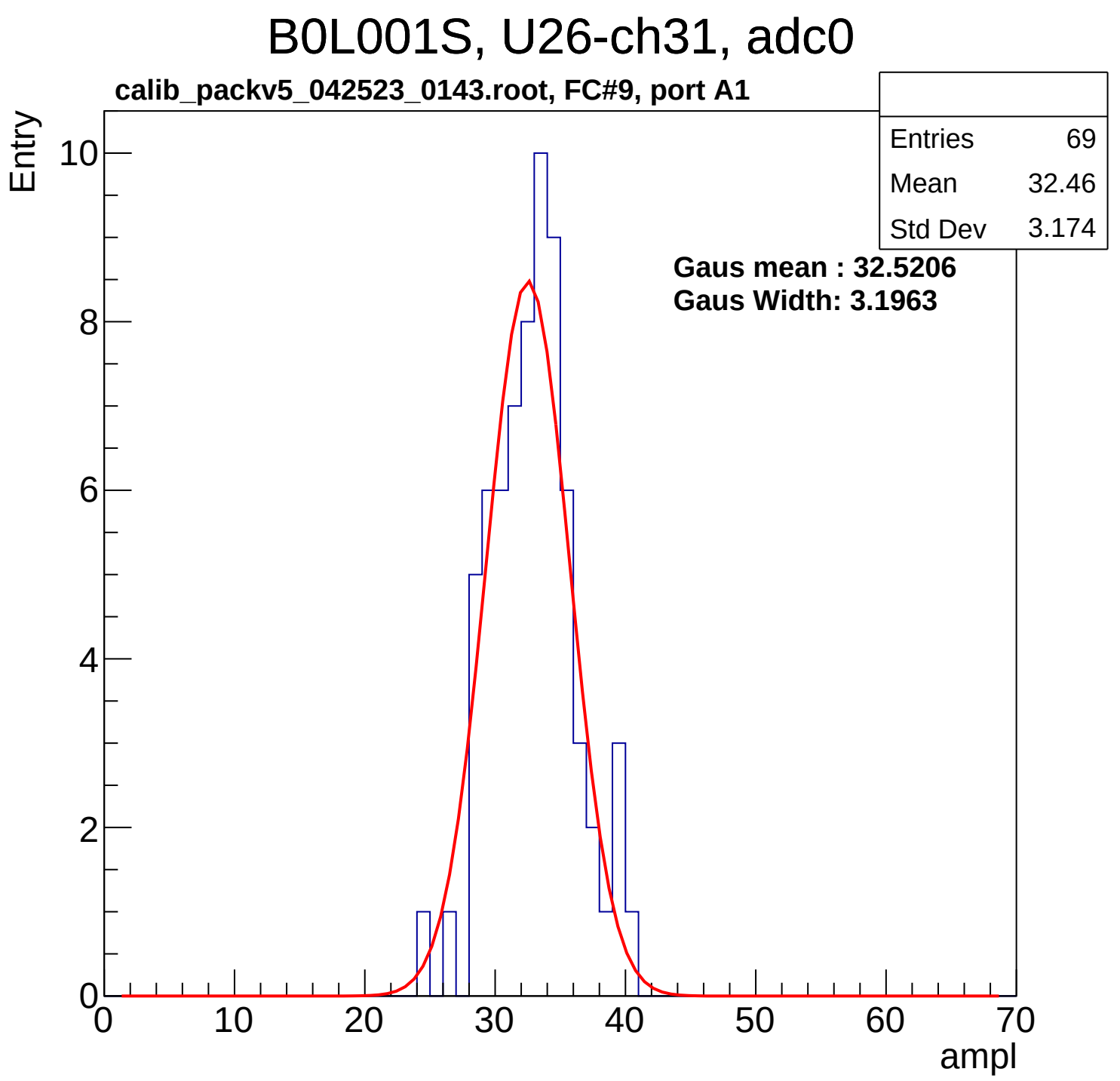
40
50
60
70


B0L001S, U26-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 32.46
Std Dev 3.174
Gaus mean : 32.5206
Gaus Width: 3.1963
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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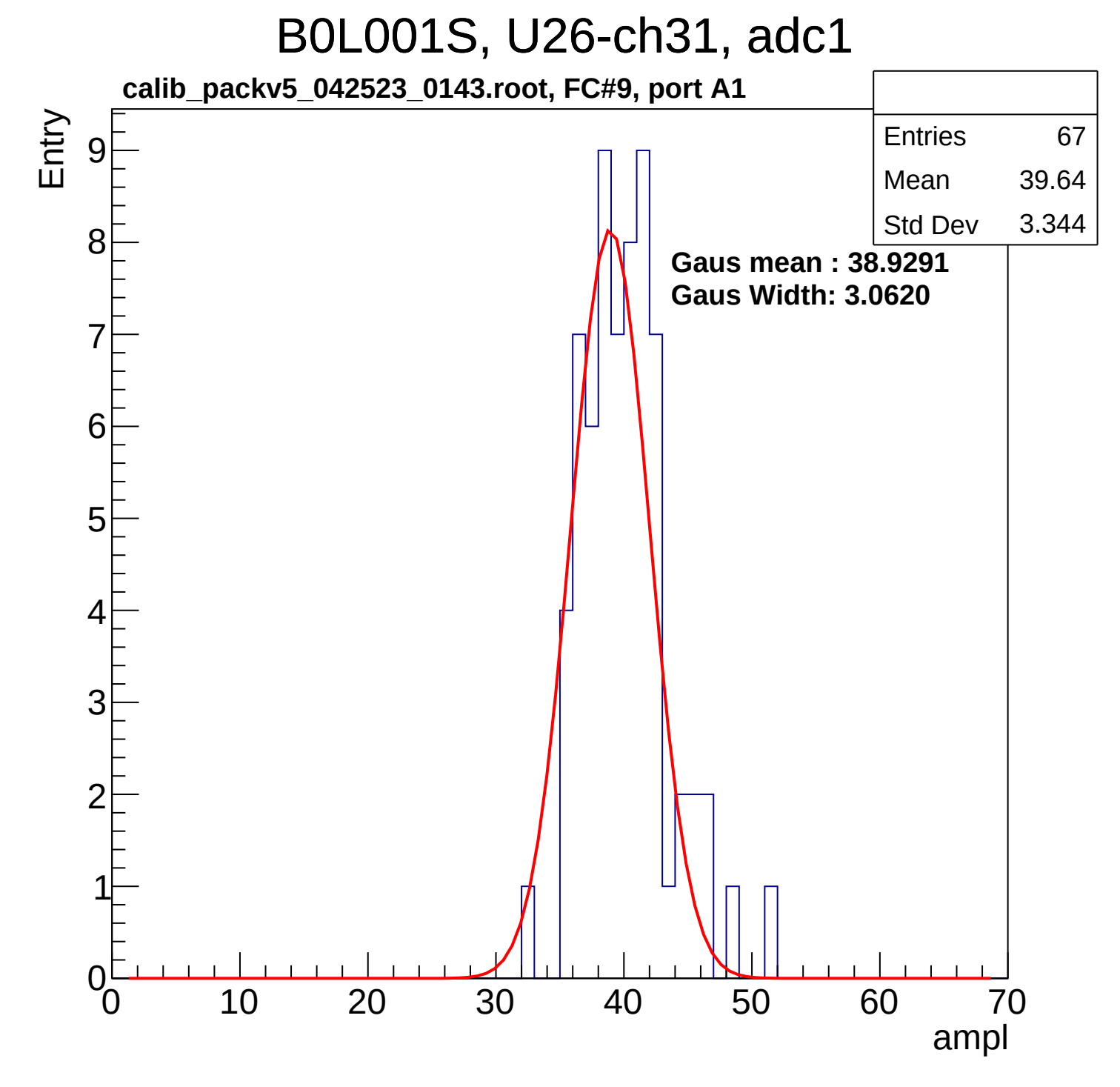
B0L001S, U26-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 39.64
Std Dev 3.344
Gaus mean : 38.9291
Gaus Width: 3.0620
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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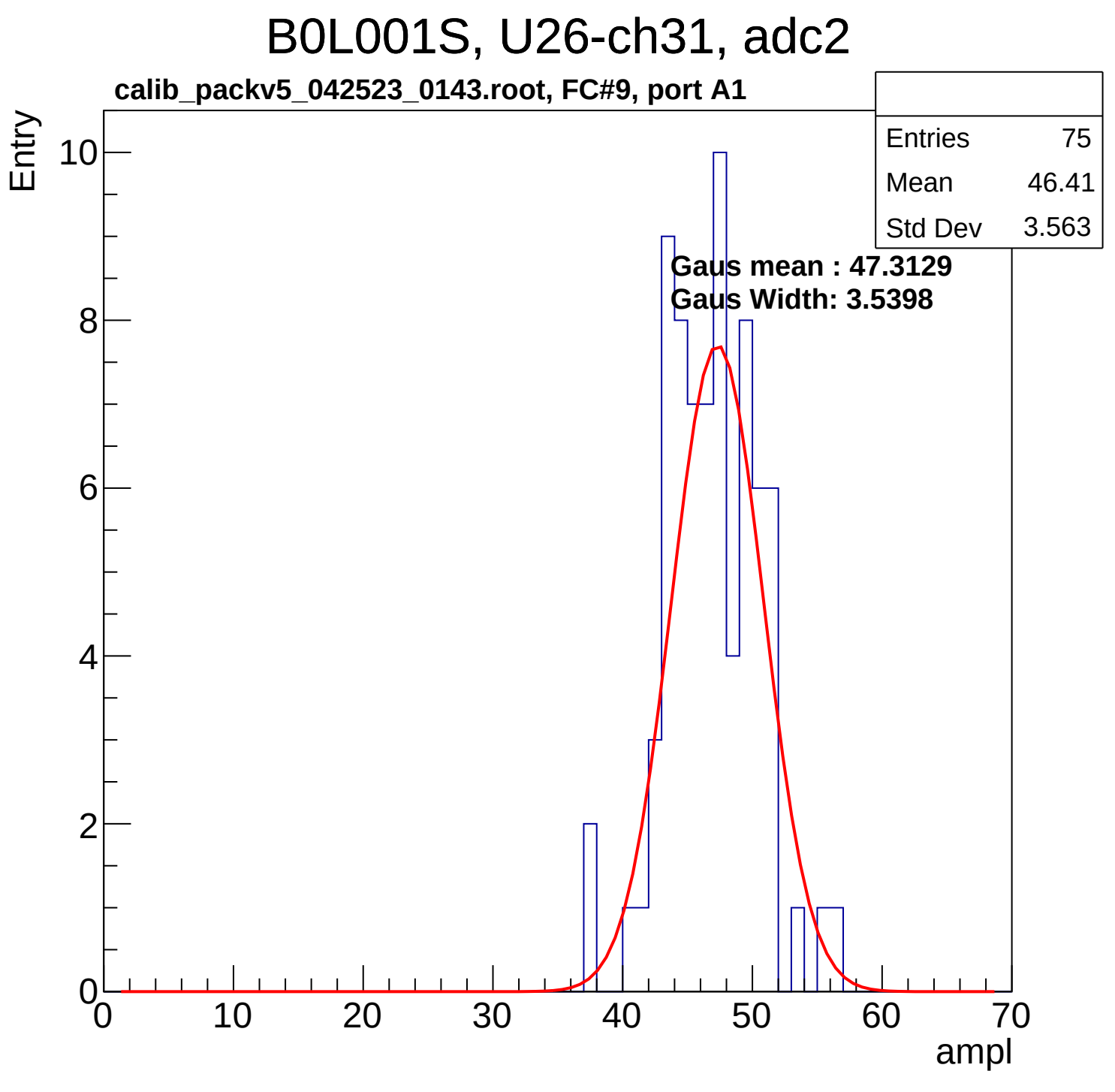
B0L001S, U26-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 46.41
Std Dev 3.563
Gaus mean : 47.3129
Gaus Width: 3.5398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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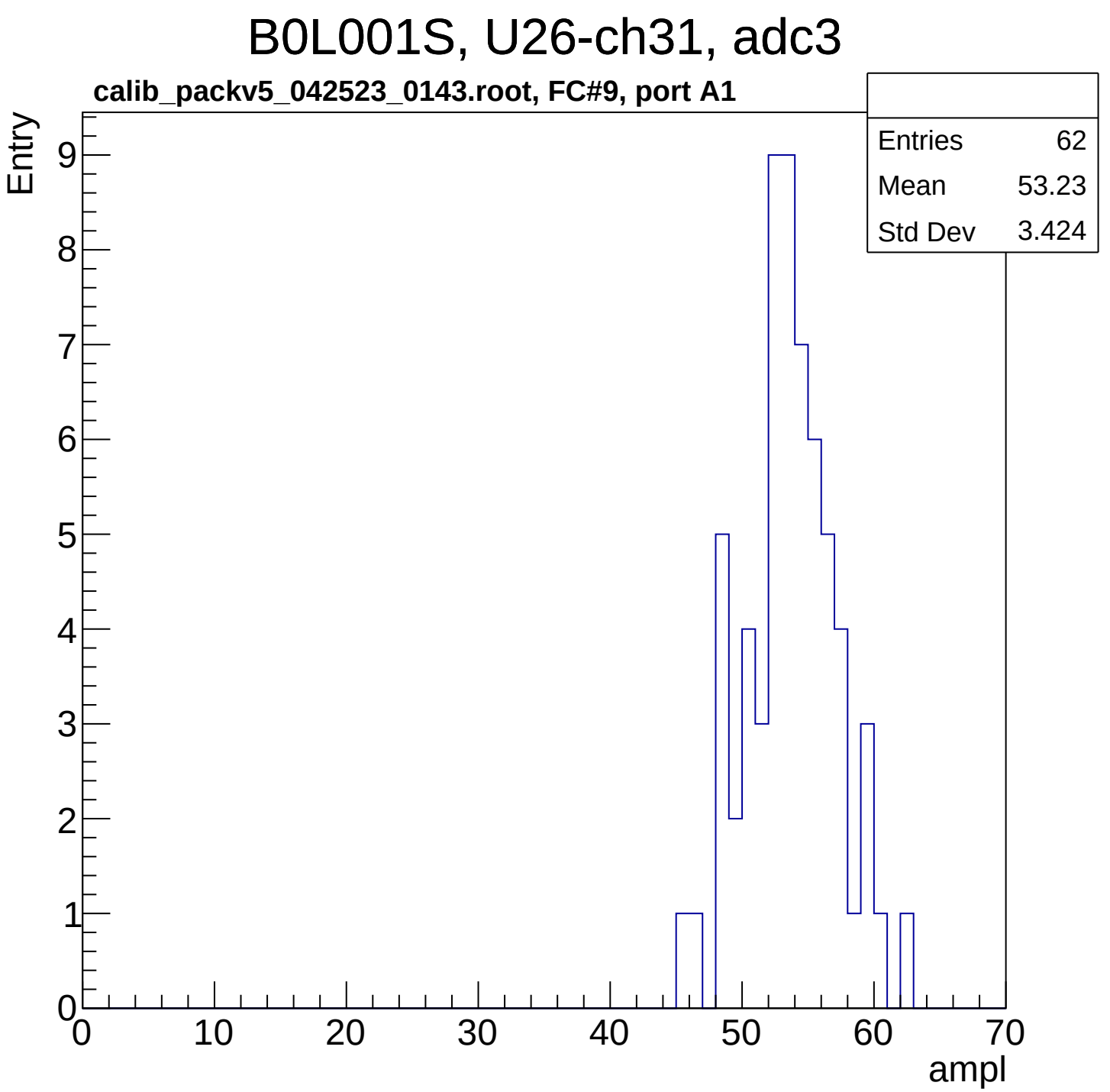
B0L001S, U26-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 53.23
Std Dev 3.424
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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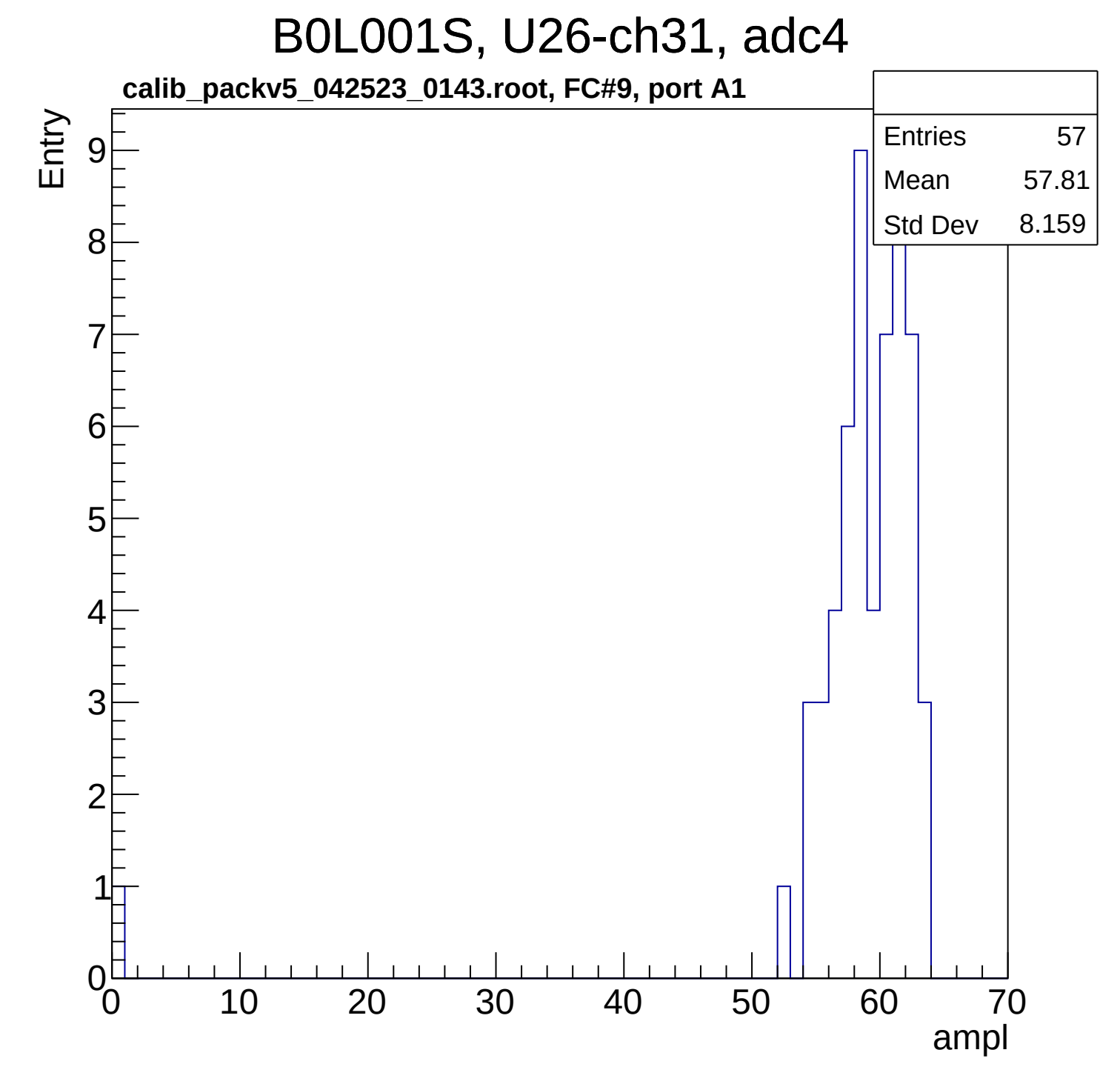
B0L001S, U26-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.81
Std Dev 8.159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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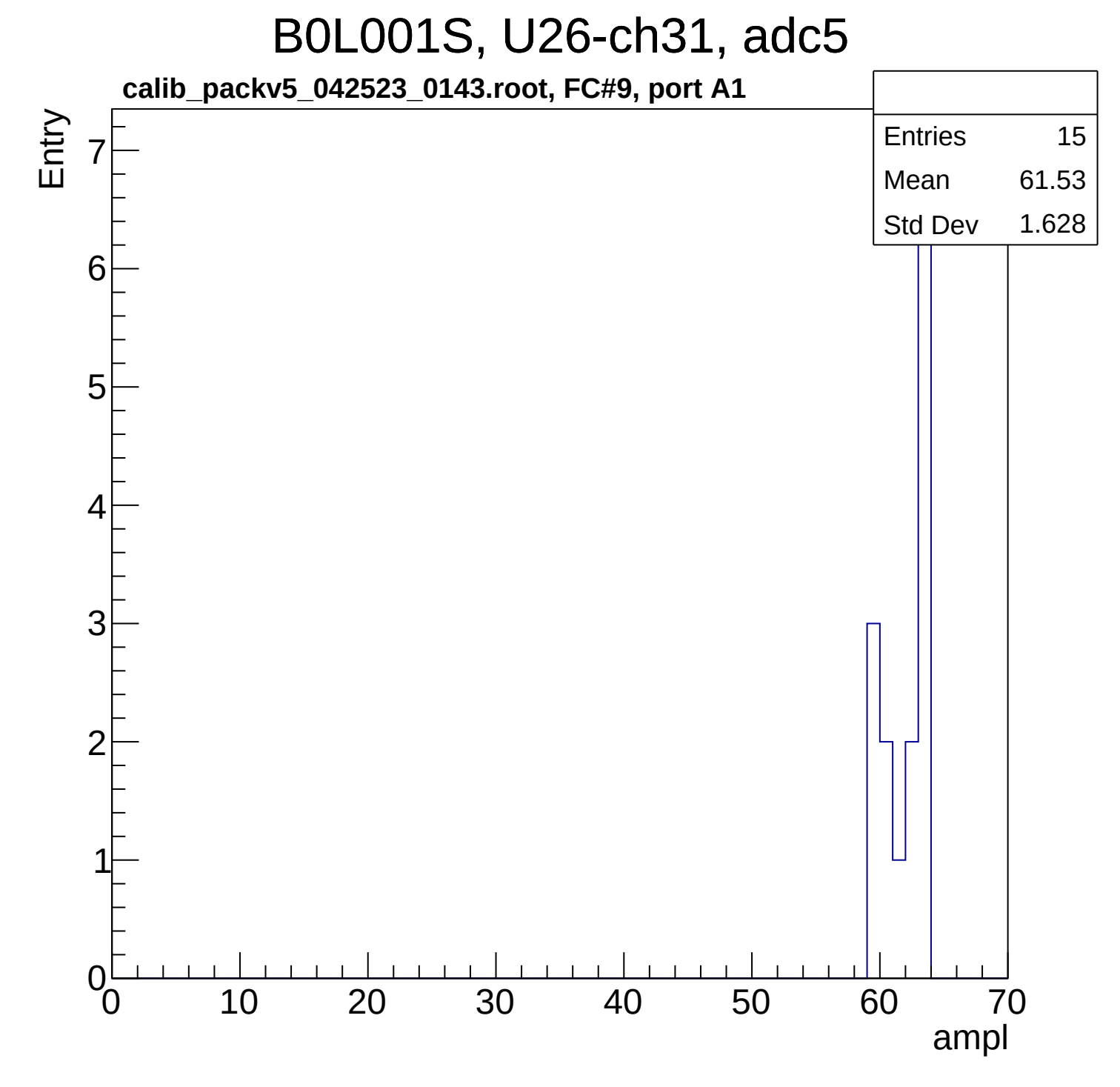
B0L001S, U26-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 61.53
Std Dev 1.628
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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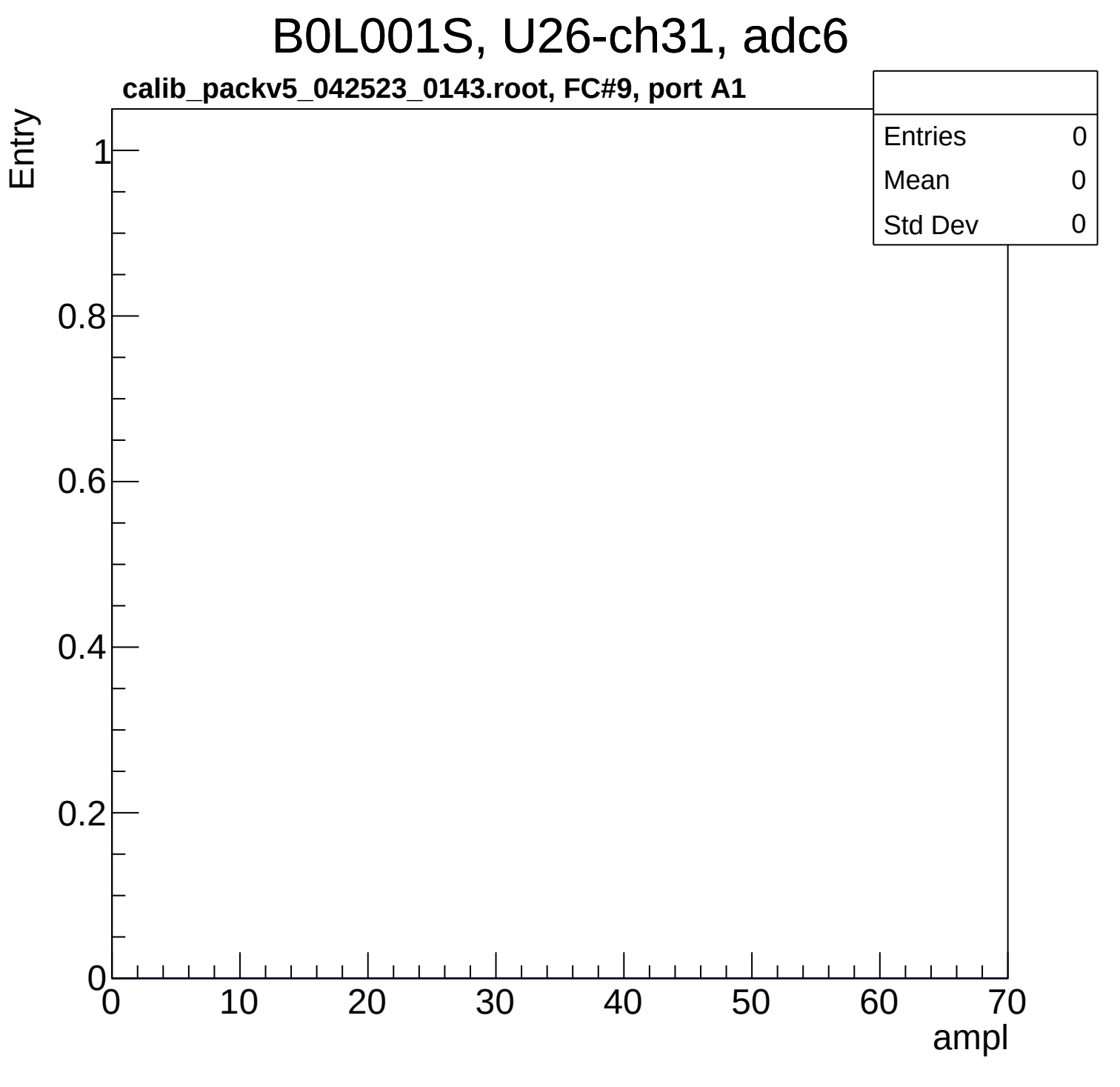
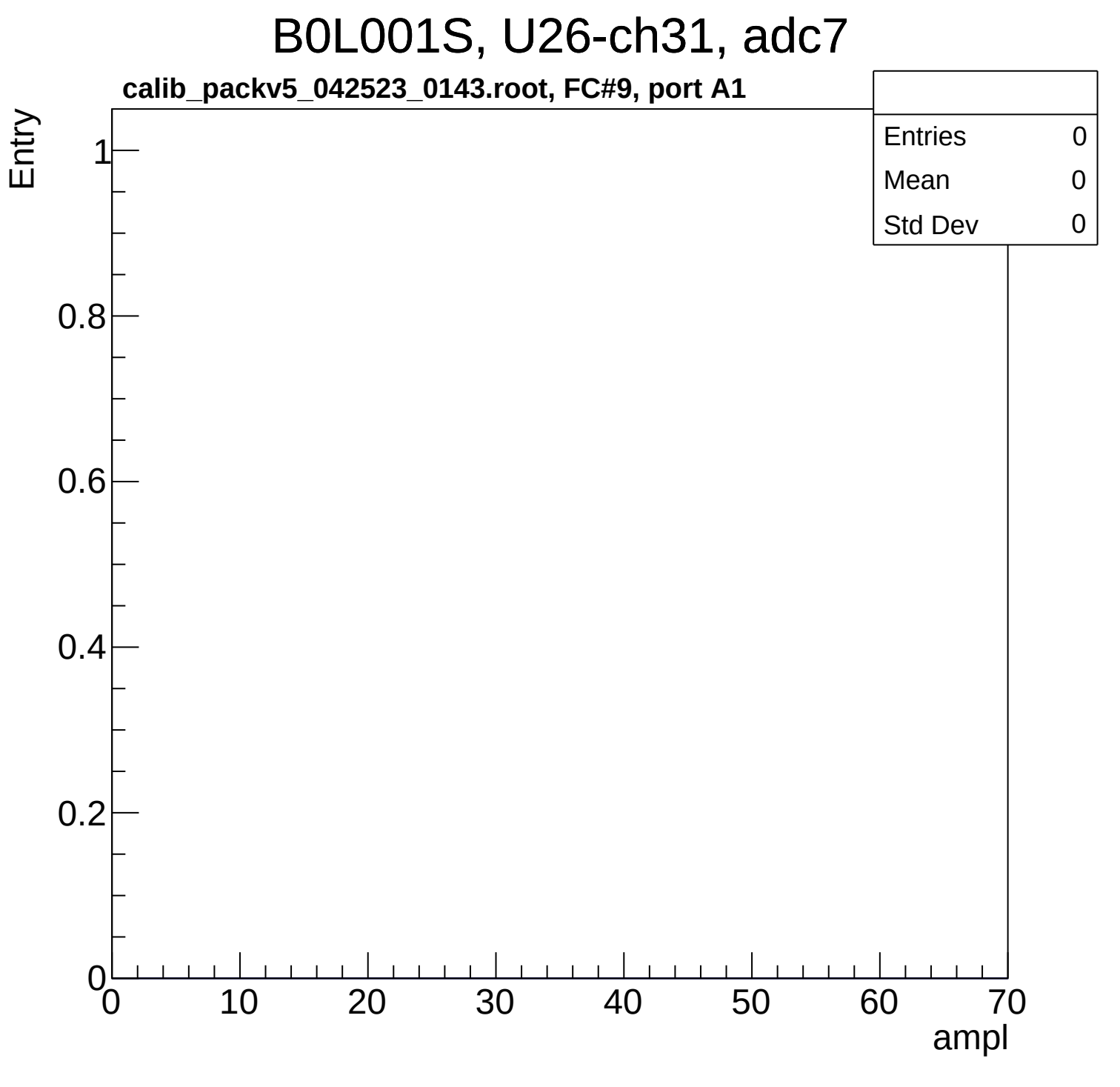

B0L001S, U26-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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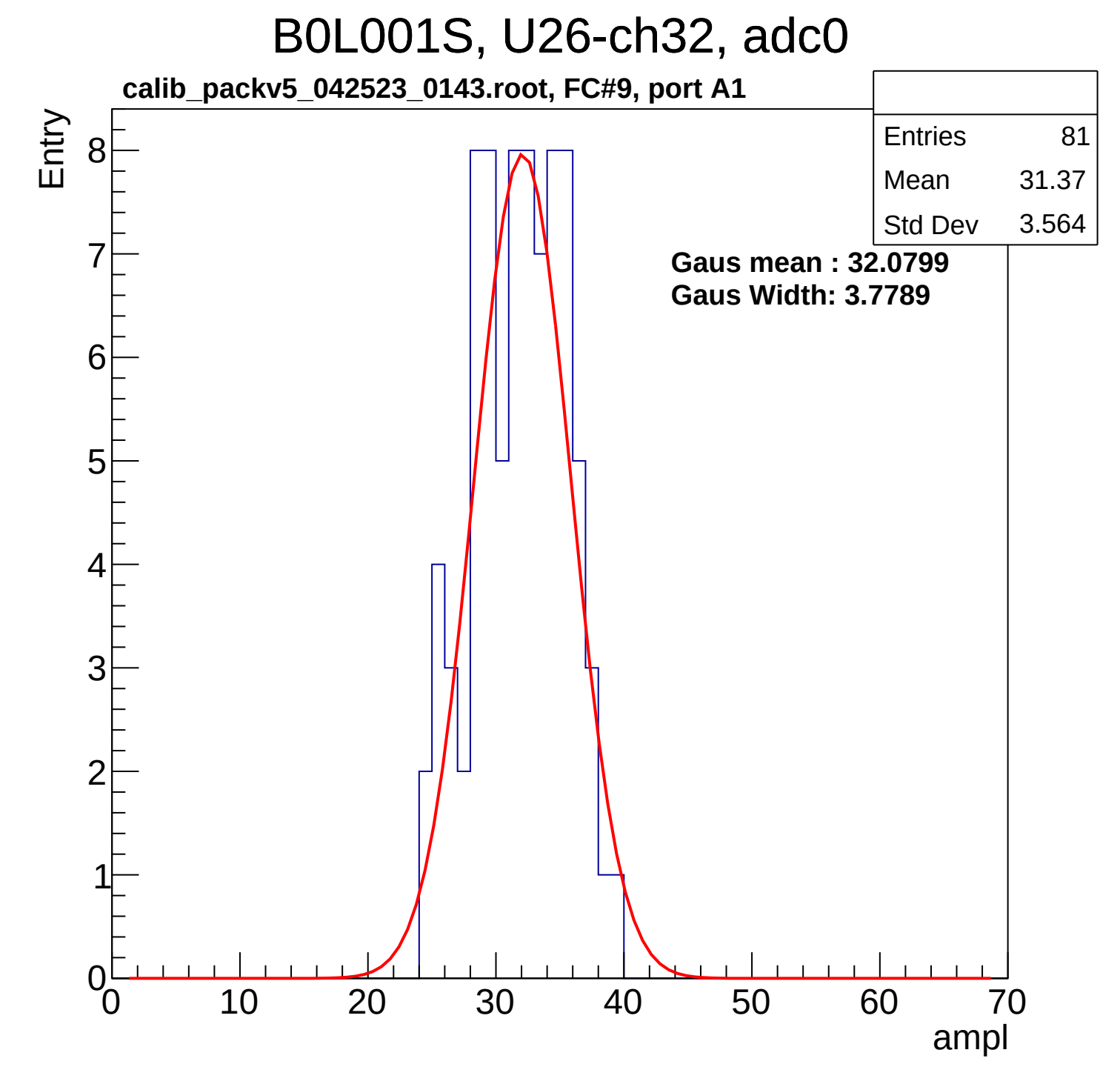
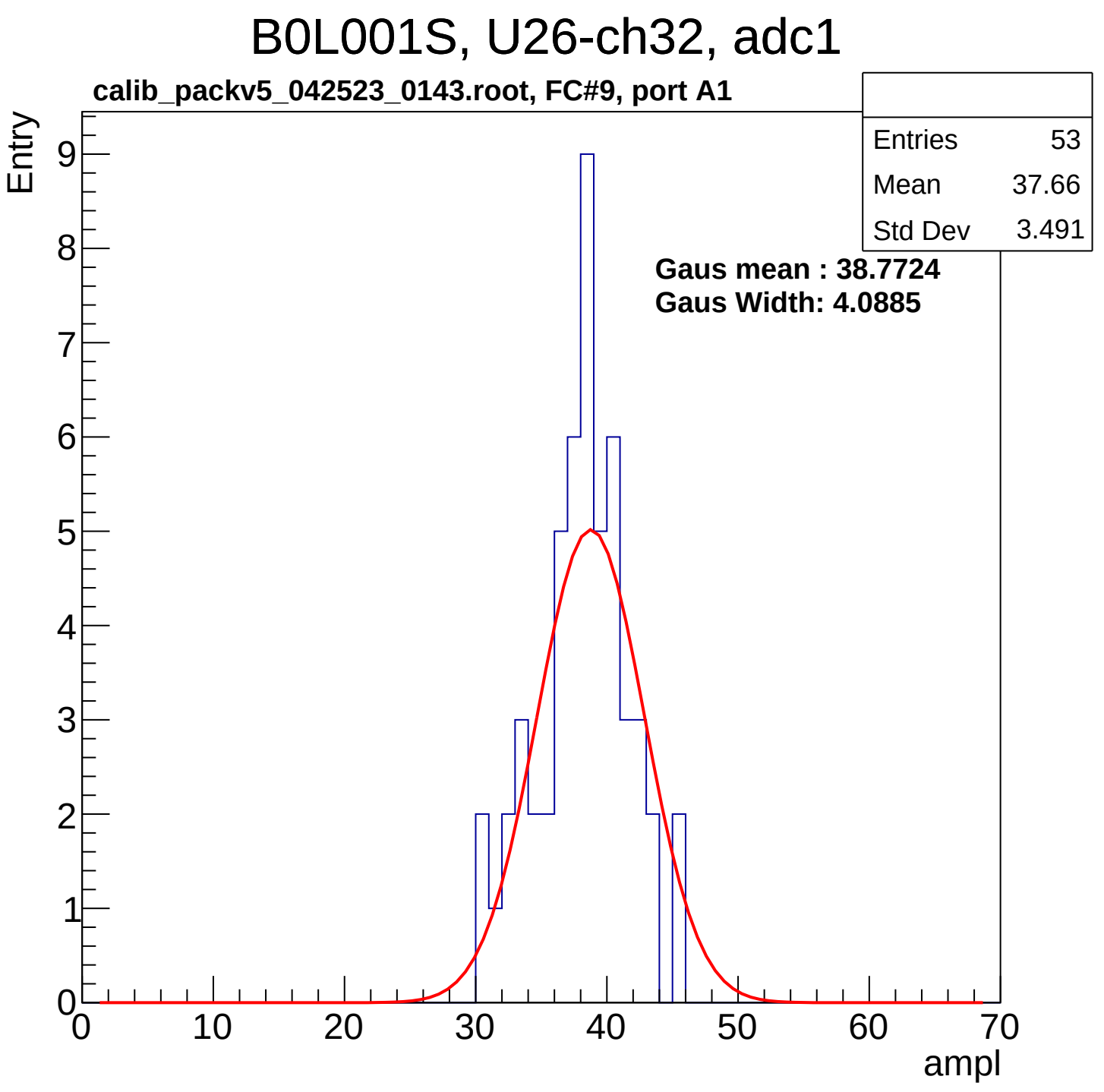
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 31.37
Std Dev 3.564
Gaus mean : 32.0799
Gaus Width: 3.7789
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 37.66
Std Dev 3.491
Gaus mean : 38.7724
Gaus Width: 4.0885
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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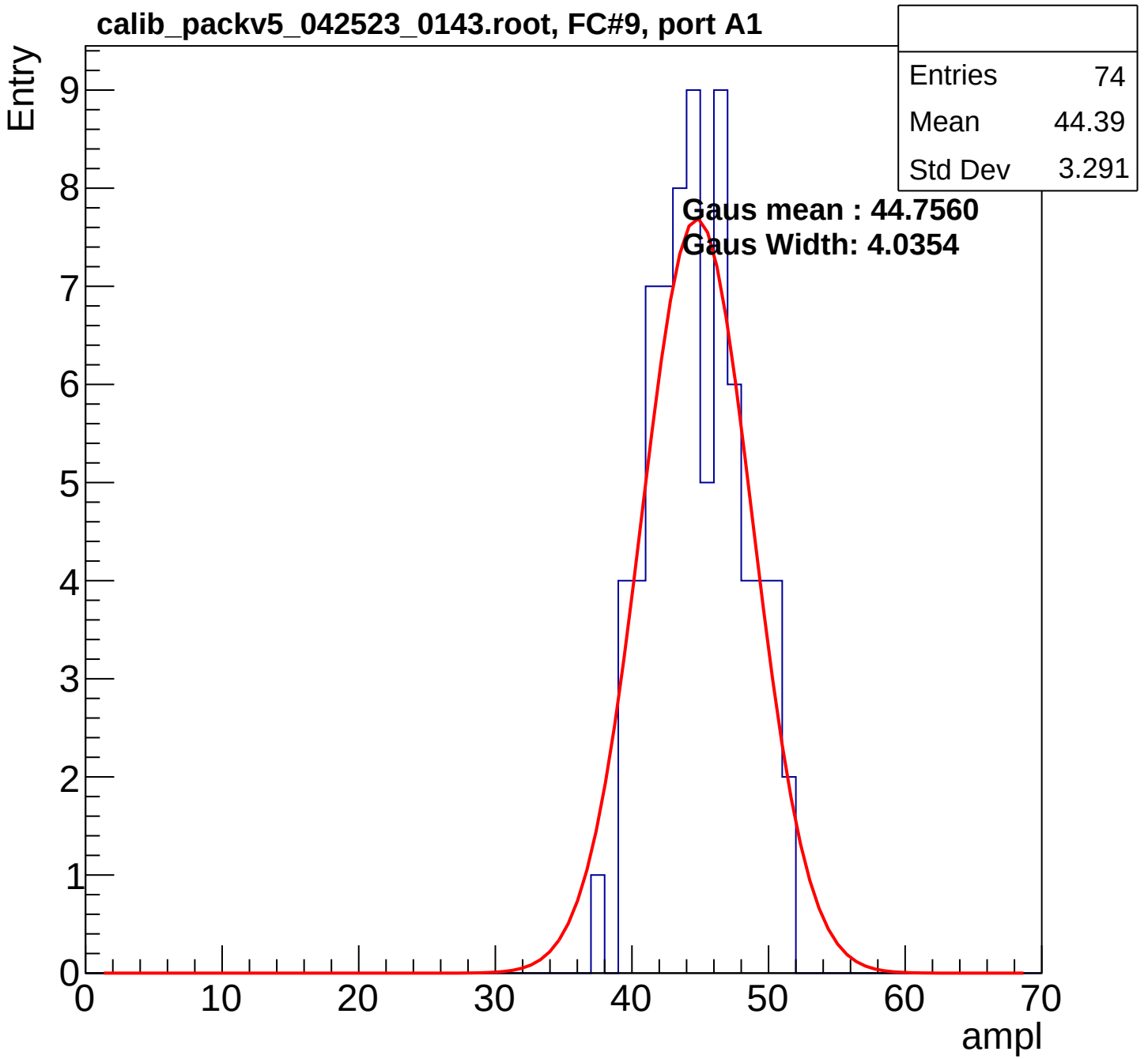
B0L001S, U26-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44.39
Std Dev 3.291
Gaus mean : 44.7560
Gaus Width: 4.0354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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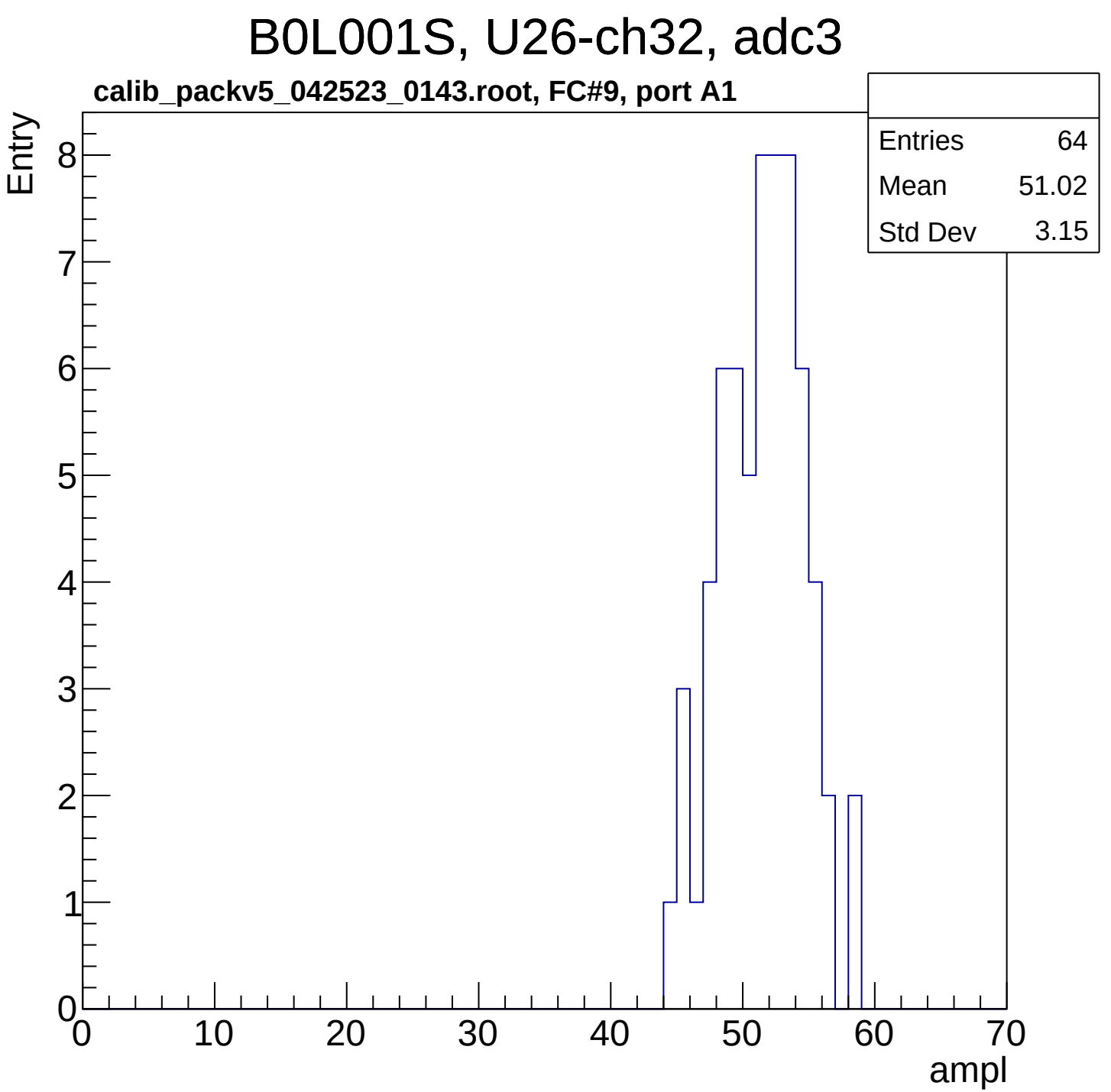
B0L001S, U26-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.02
Std Dev 3.15
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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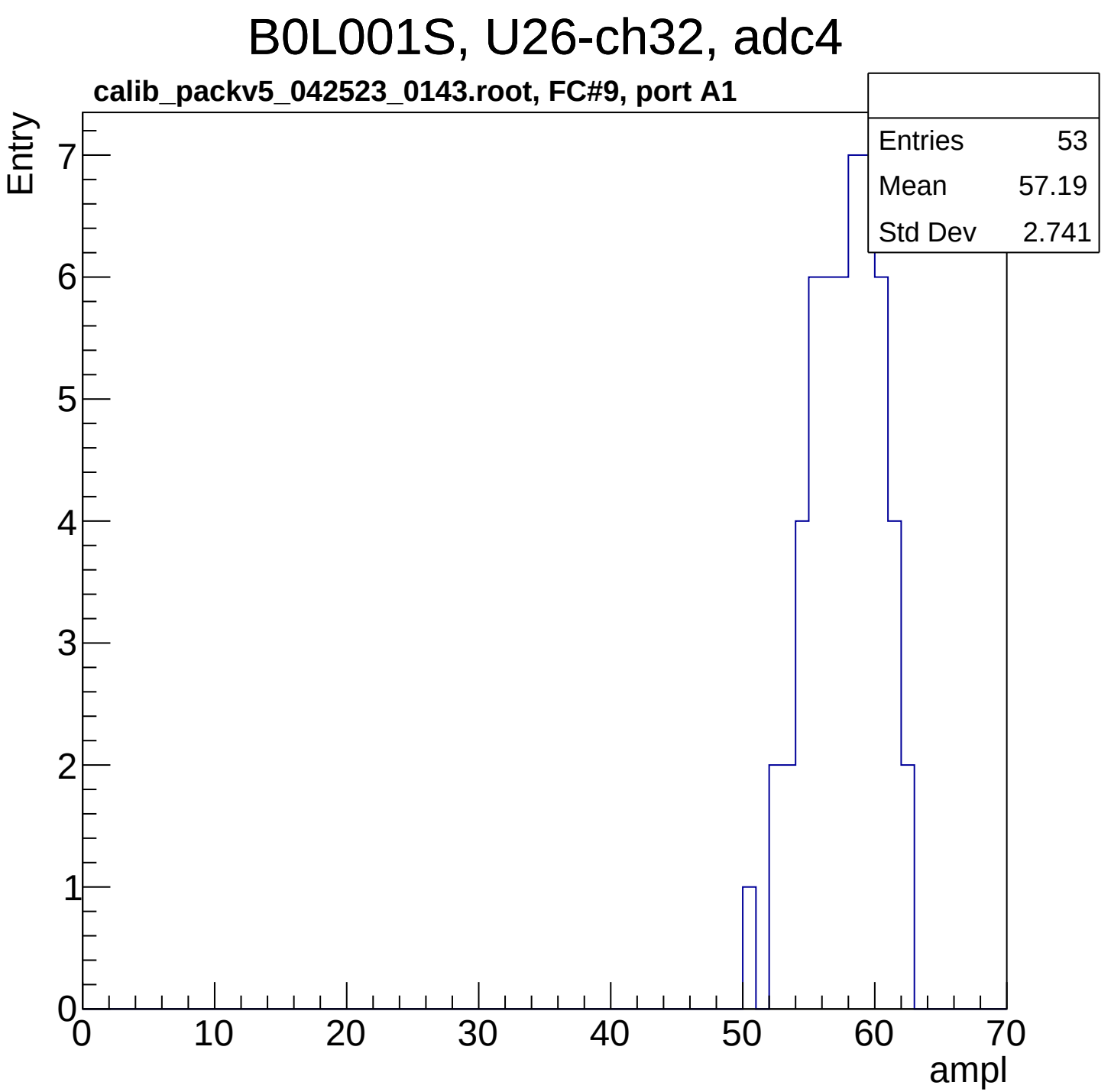
B0L001S, U26-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.19
Std Dev 2.741
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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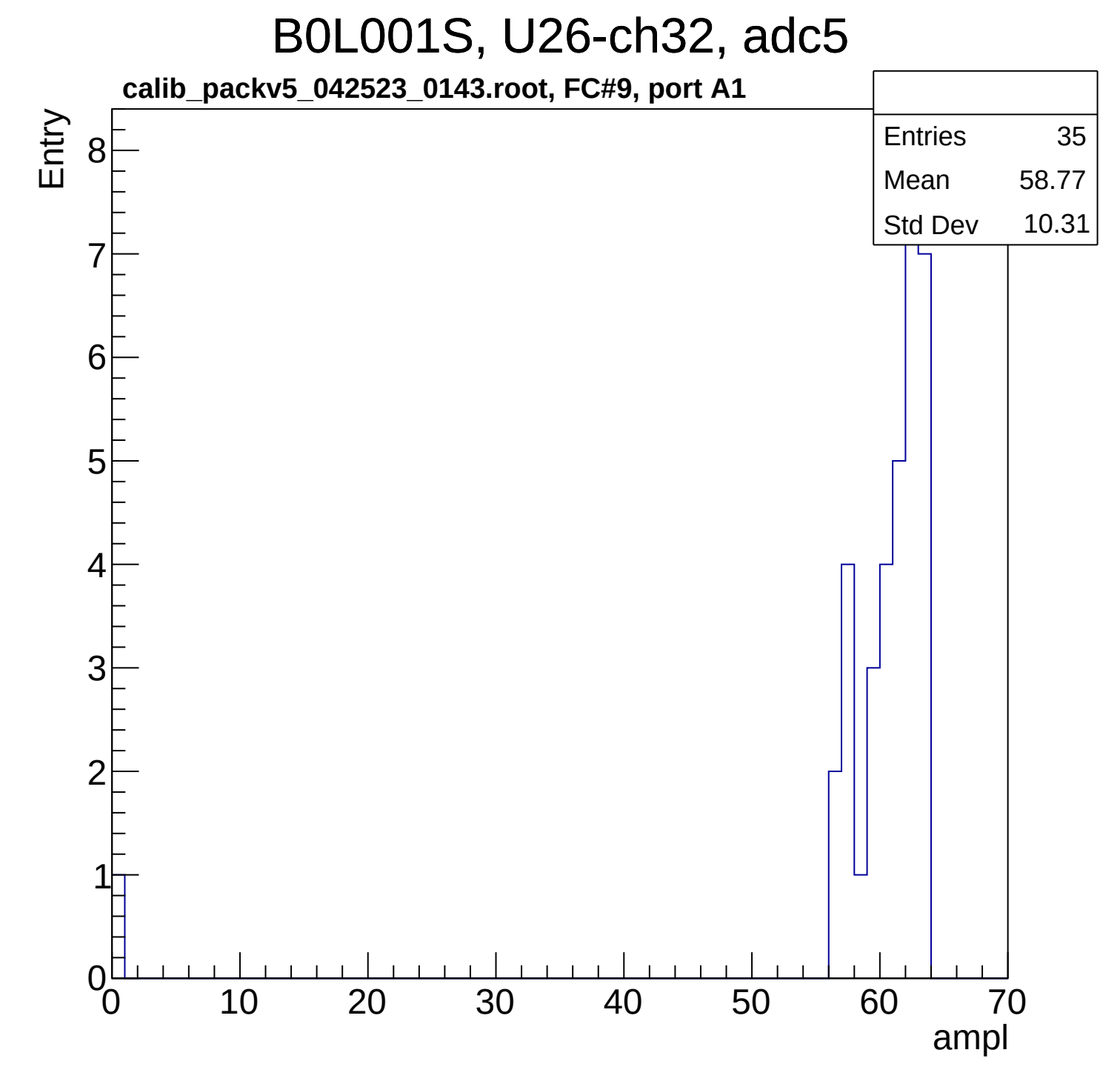
B0L001S, U26-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.77
Std Dev 10.31
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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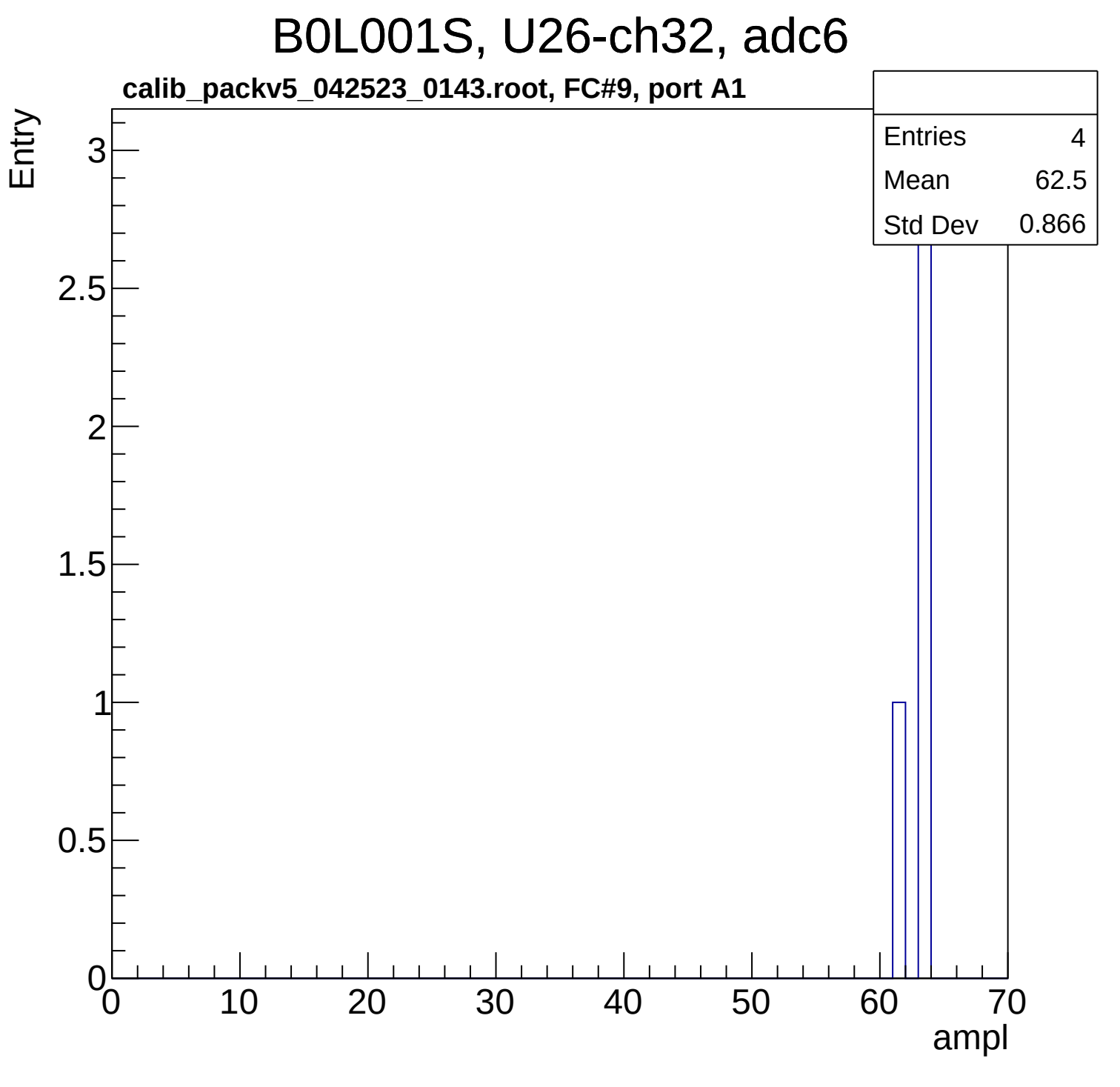
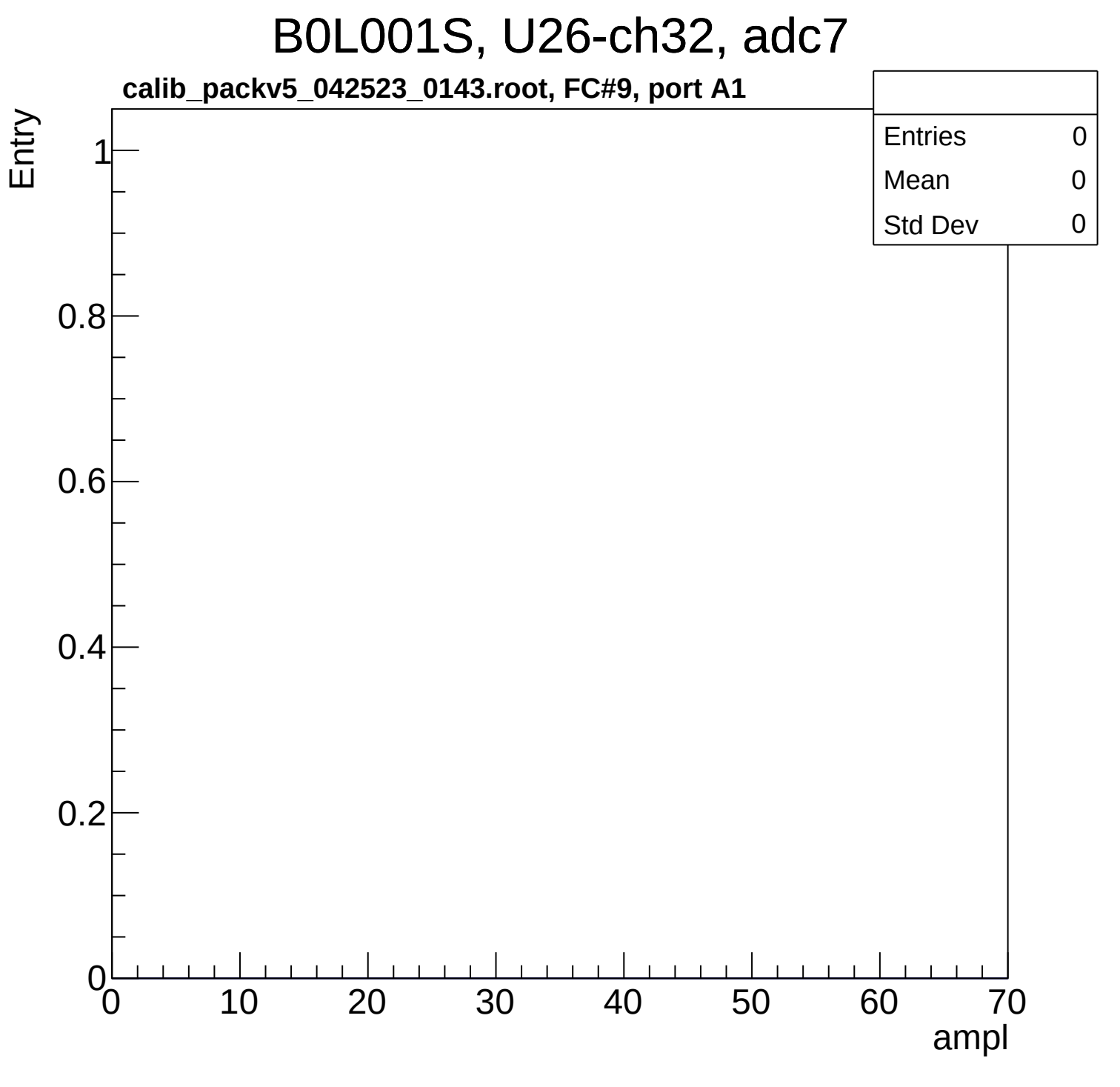

B0L001S, U26-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U26-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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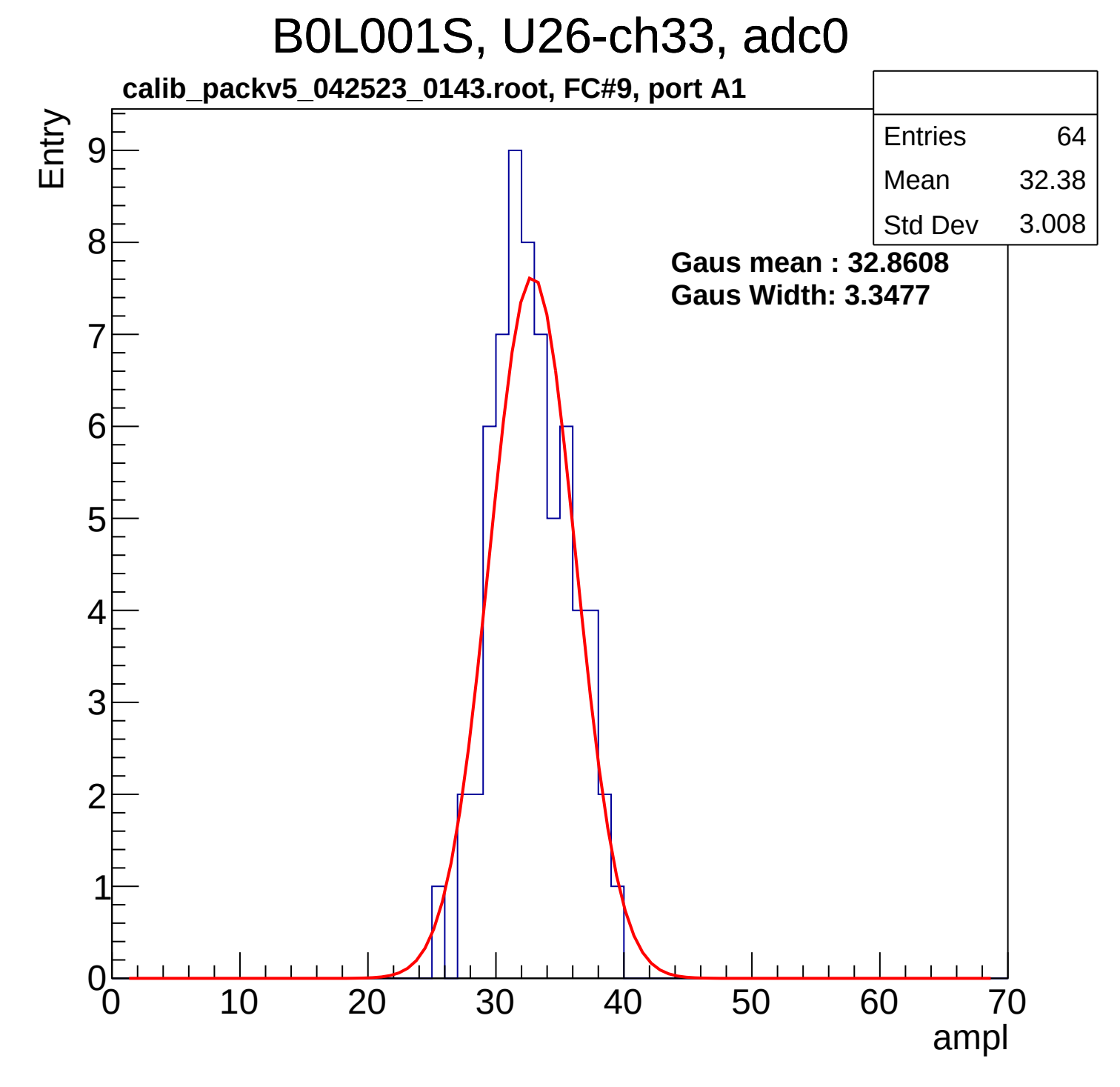
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 32.38
Std Dev 3.008
Gaus mean : 32.8608
Gaus Width: 3.3477
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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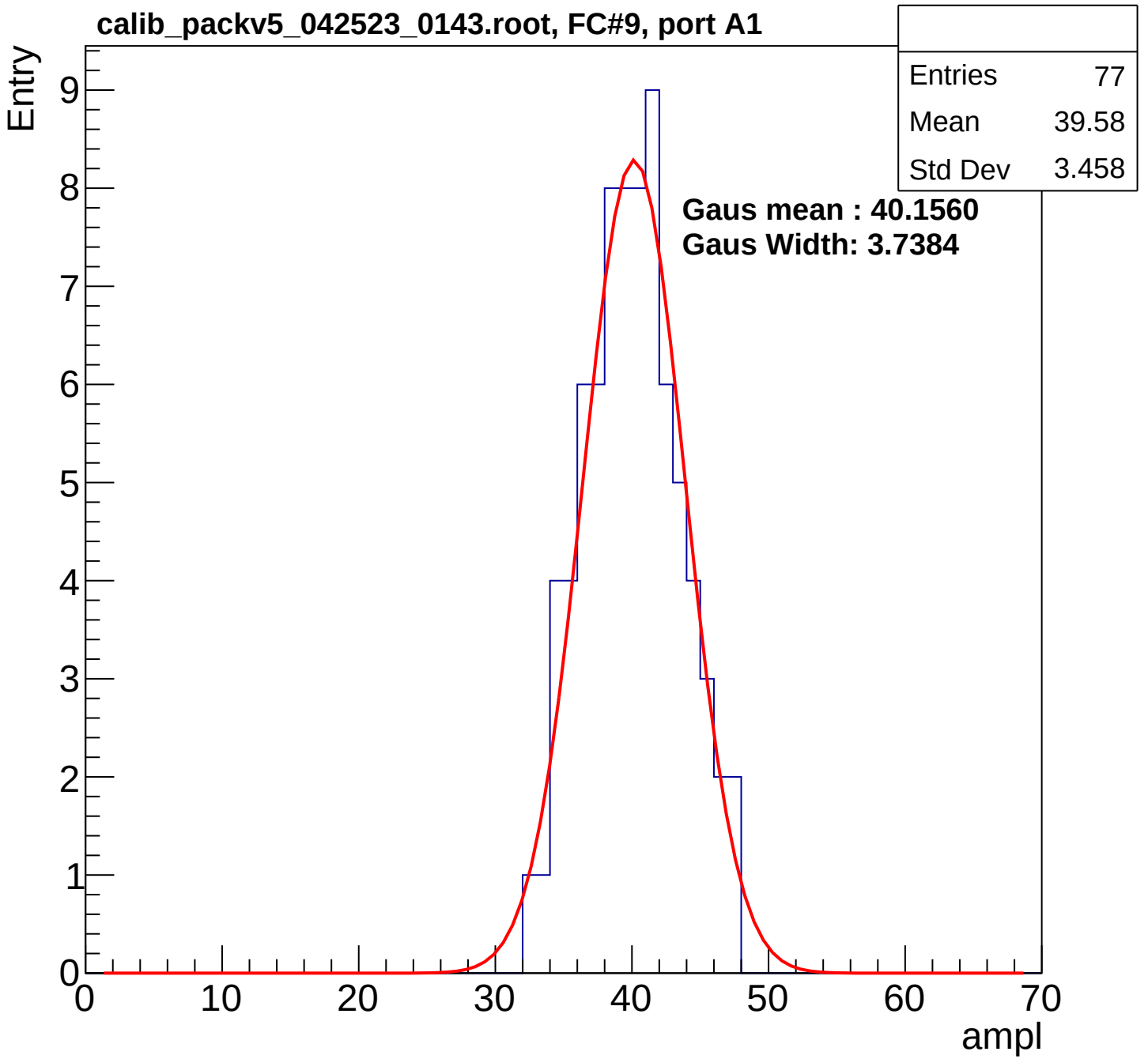
40
50
60
70


B0L001S, U26-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 39.58
Std Dev 3.458
Gaus mean : 40.1560
Gaus Width: 3.7384
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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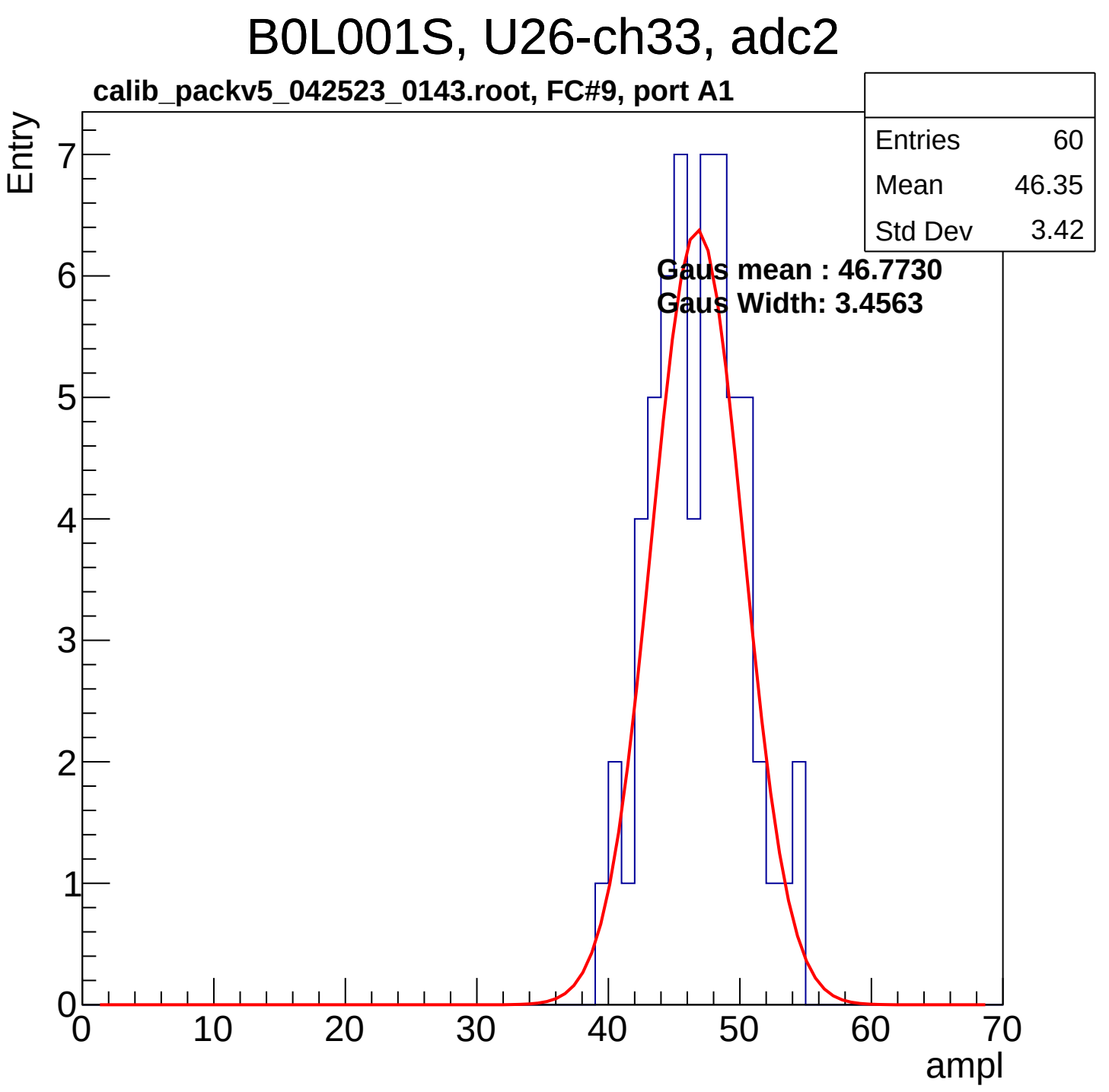
B0L001S, U26-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 46.35
Std Dev 3.42
Gaus mean : 46.7730
Gaus Width: 3.4563
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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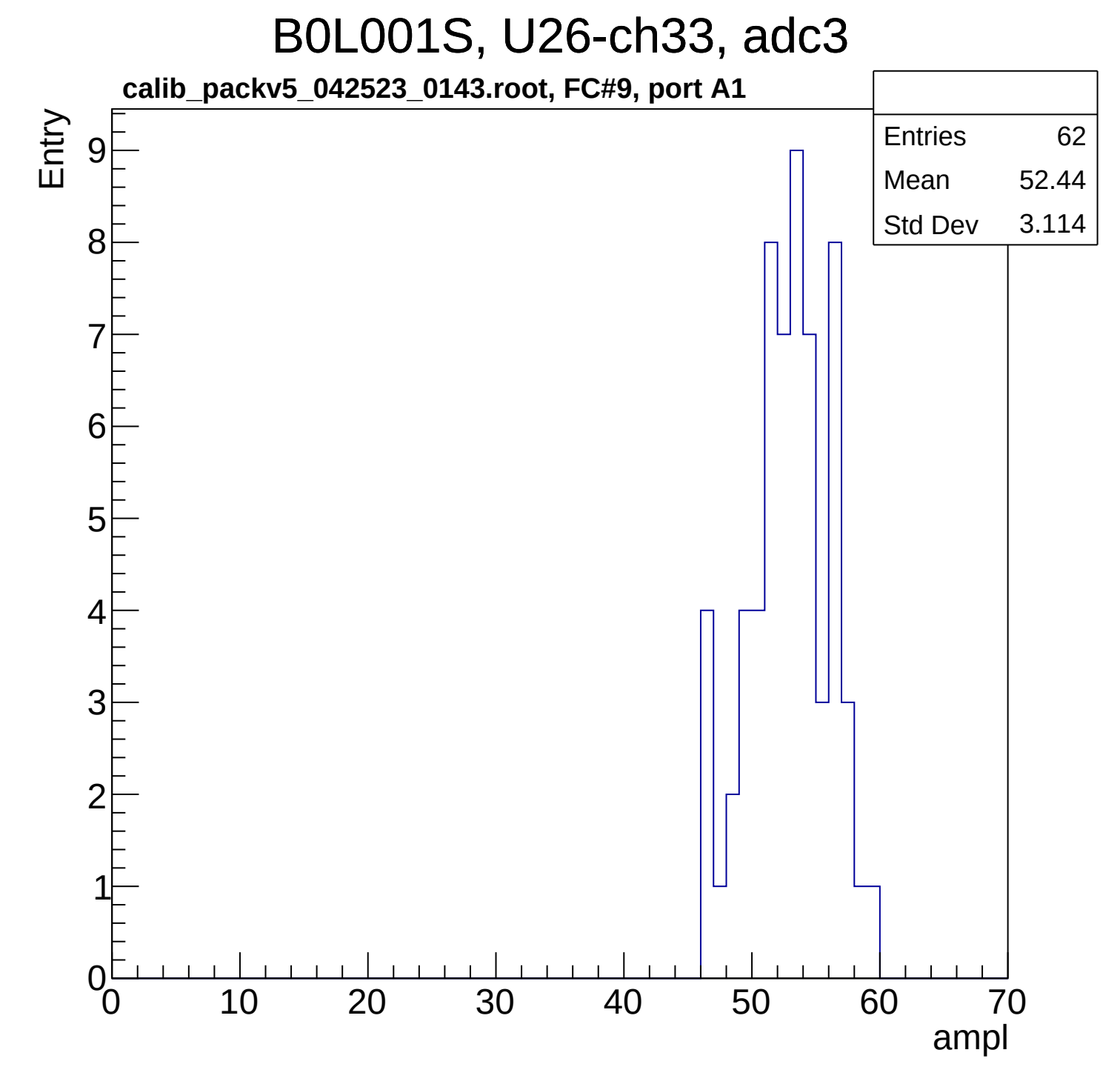
B0L001S, U26-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 52.44
Std Dev 3.114
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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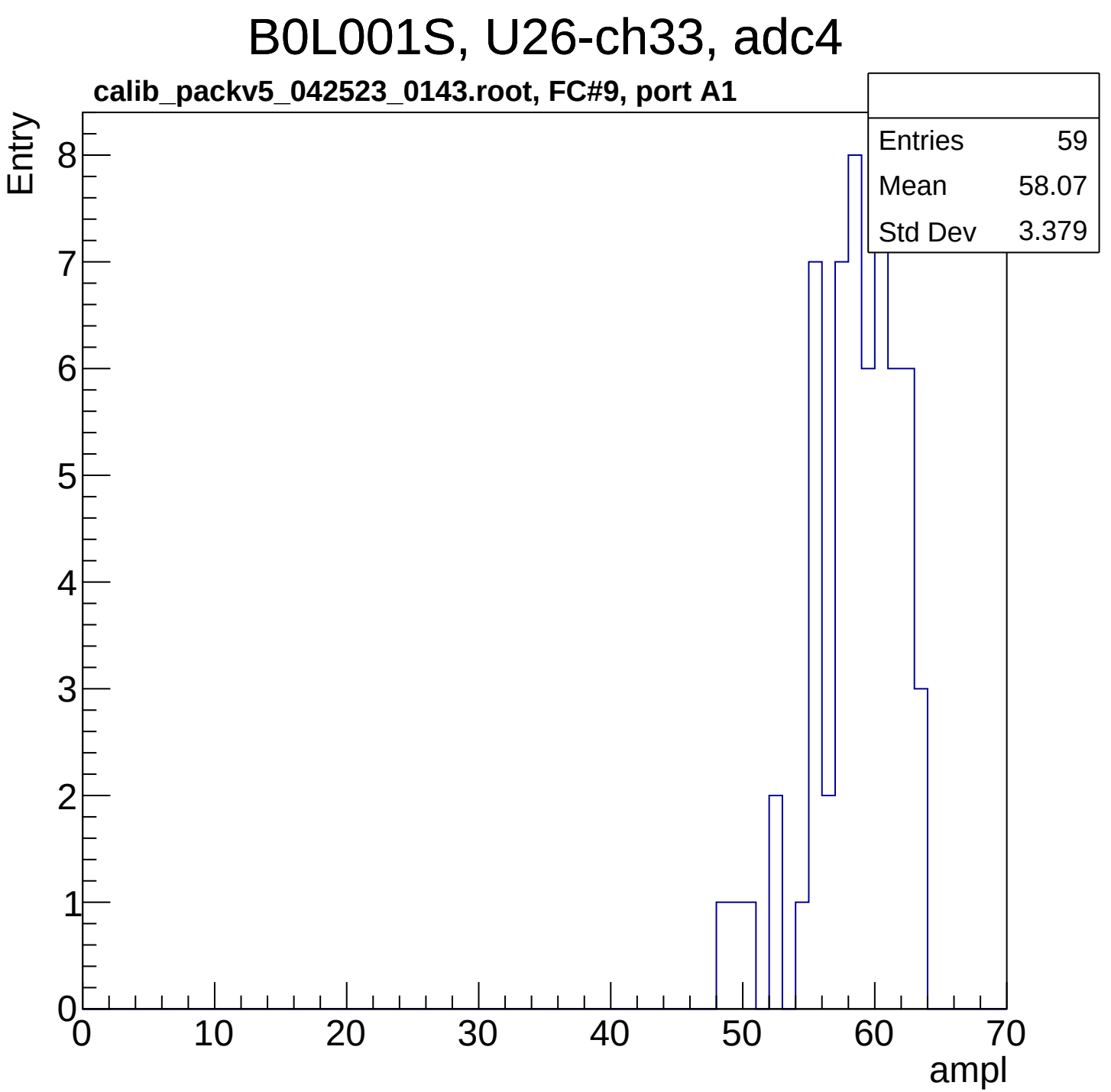
B0L001S, U26-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.07
Std Dev 3.379
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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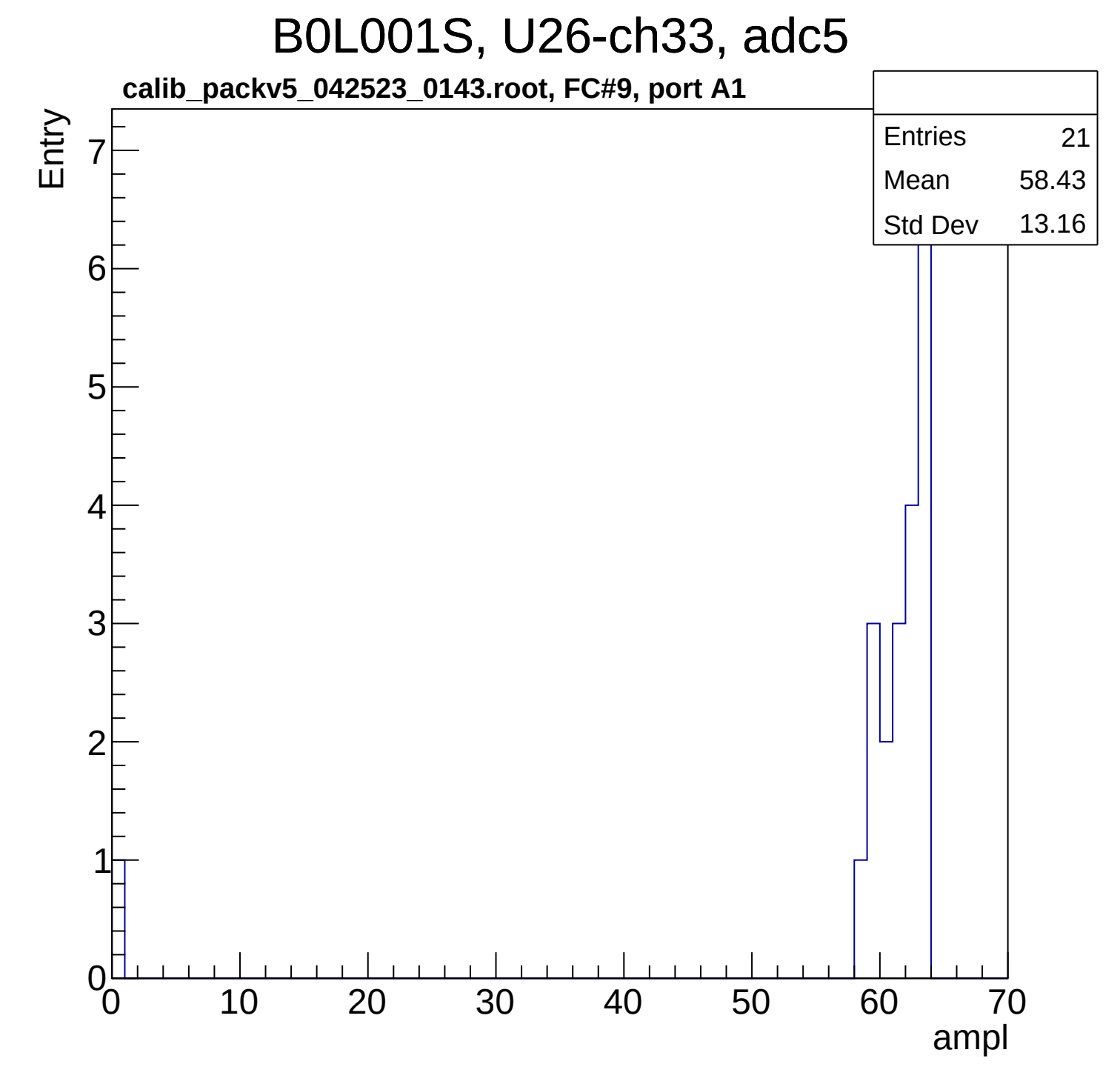
B0L001S, U26-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.43
Std Dev 13.16
Entry
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1
2
3
4
5
6
7
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10
20
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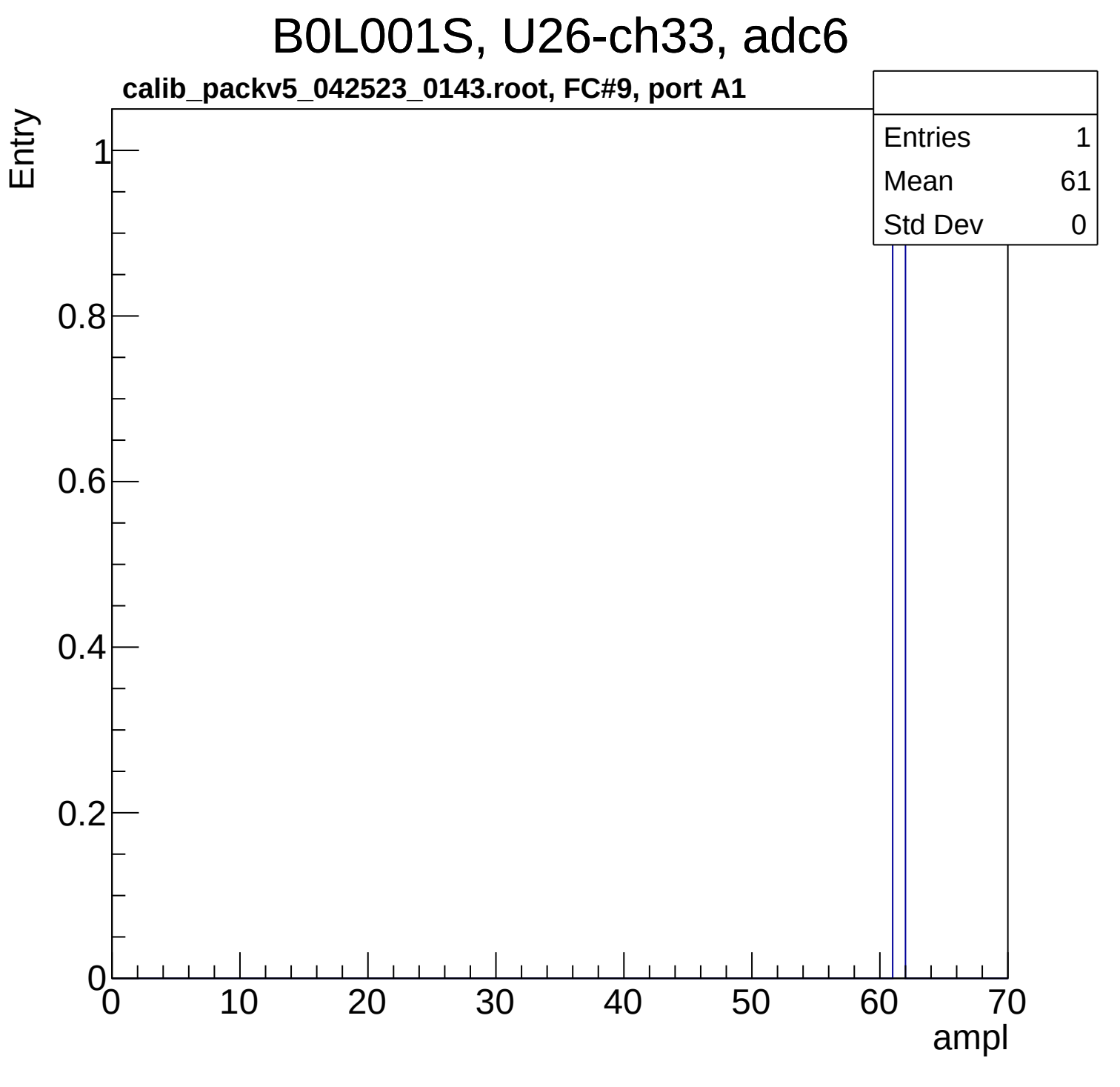
50
60
70
ampl


B0L001S, U26-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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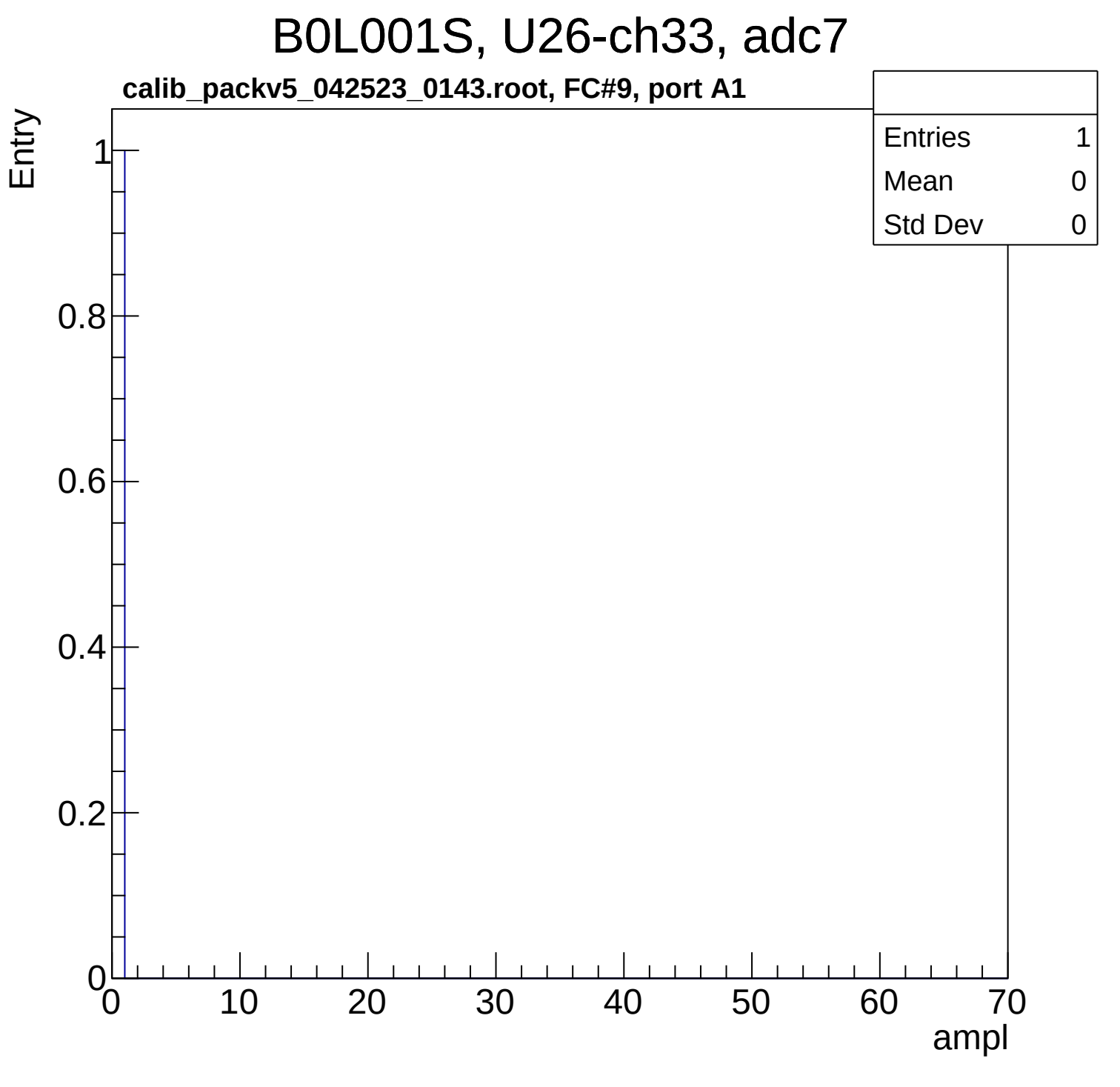
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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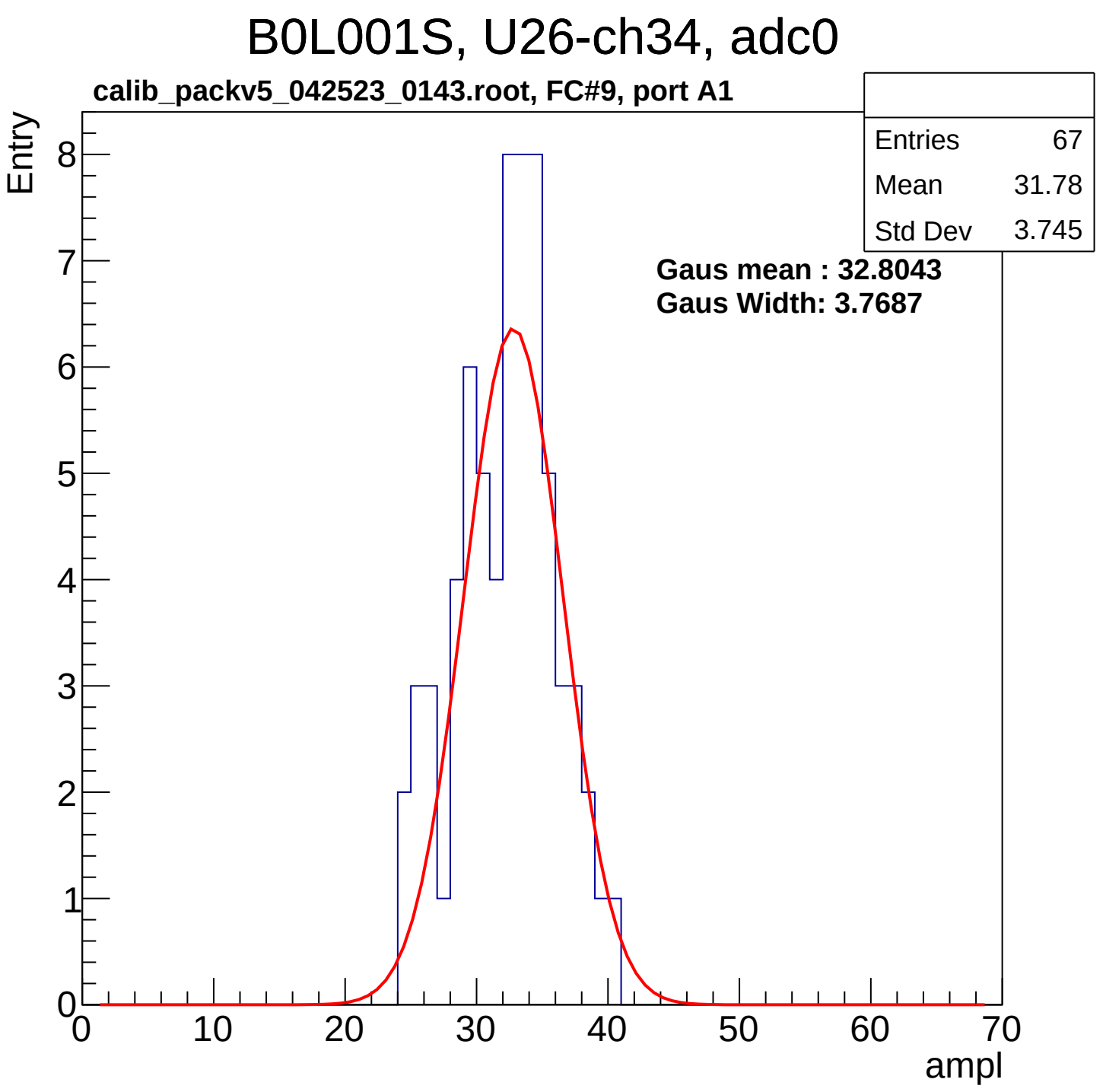
50
60
70
ampl


B0L001S, U26-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.78
Std Dev 3.745
Gaus mean : 32.8043
Gaus Width: 3.7687
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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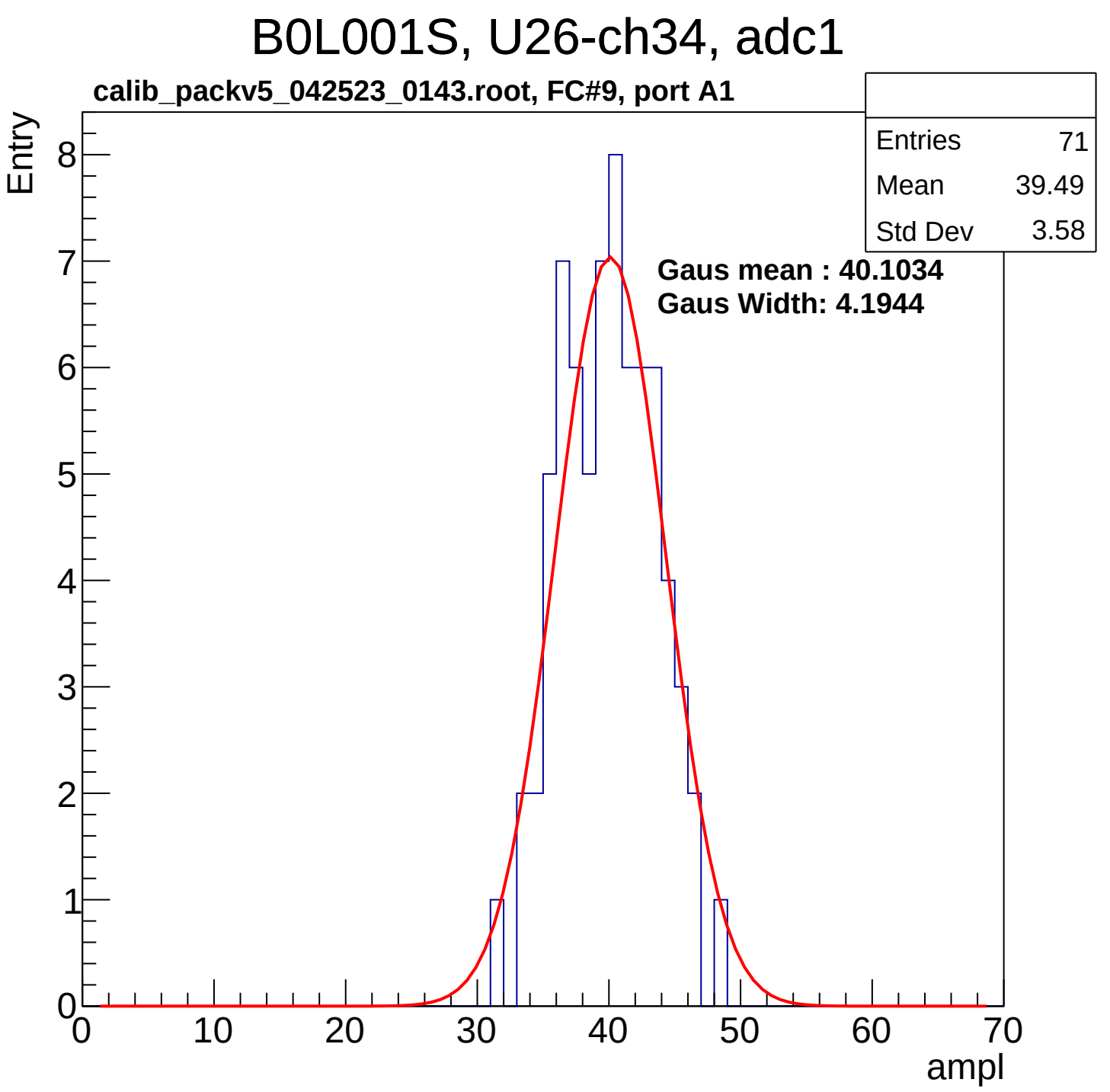
B0L001S, U26-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 39.49
Std Dev 3.58
Gaus mean : 40.1034
Gaus Width: 4.1944
Entry
ampl
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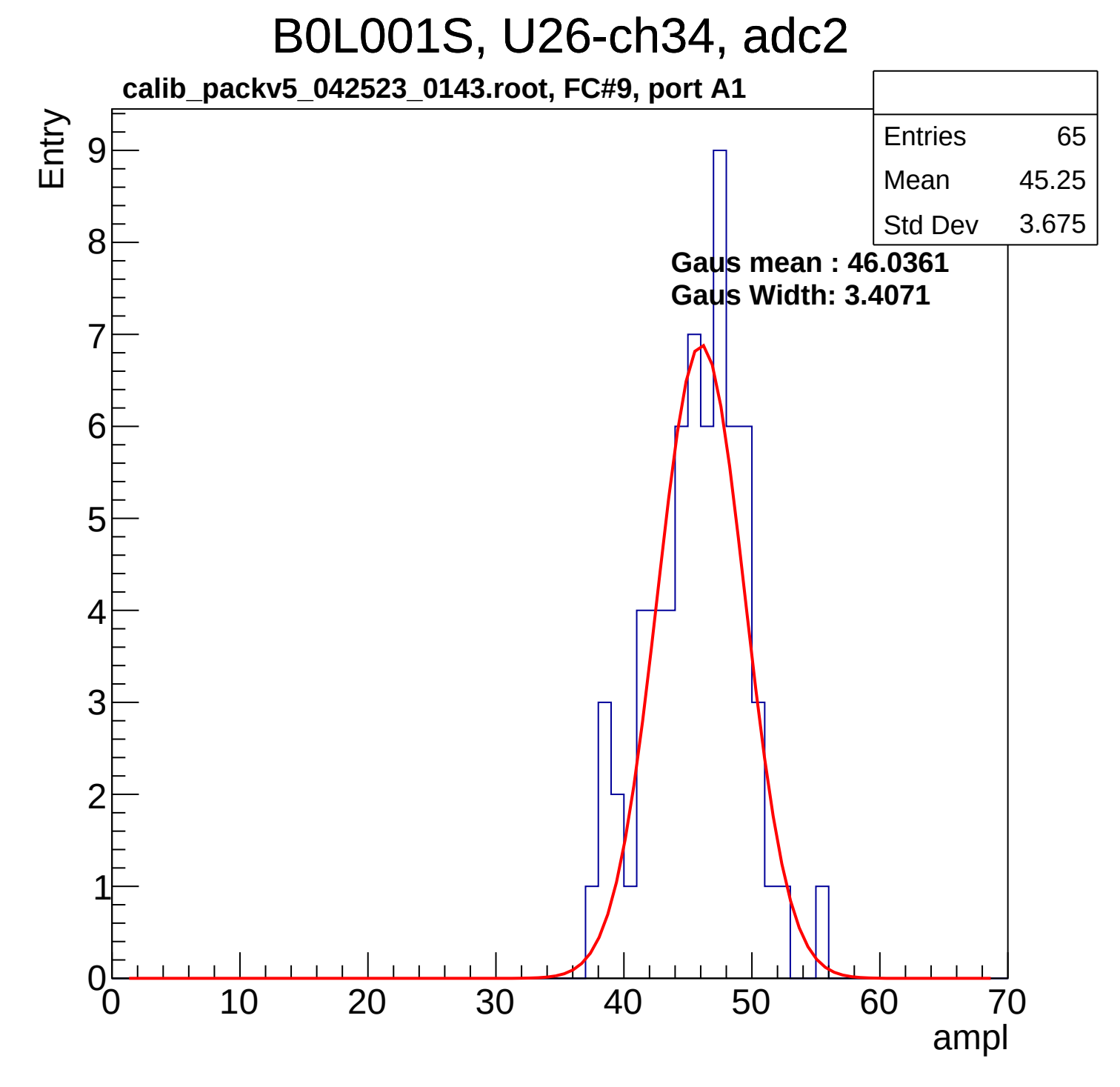
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.25
Std Dev 3.675
Gaus mean : 46.0361
Gaus Width: 3.4071
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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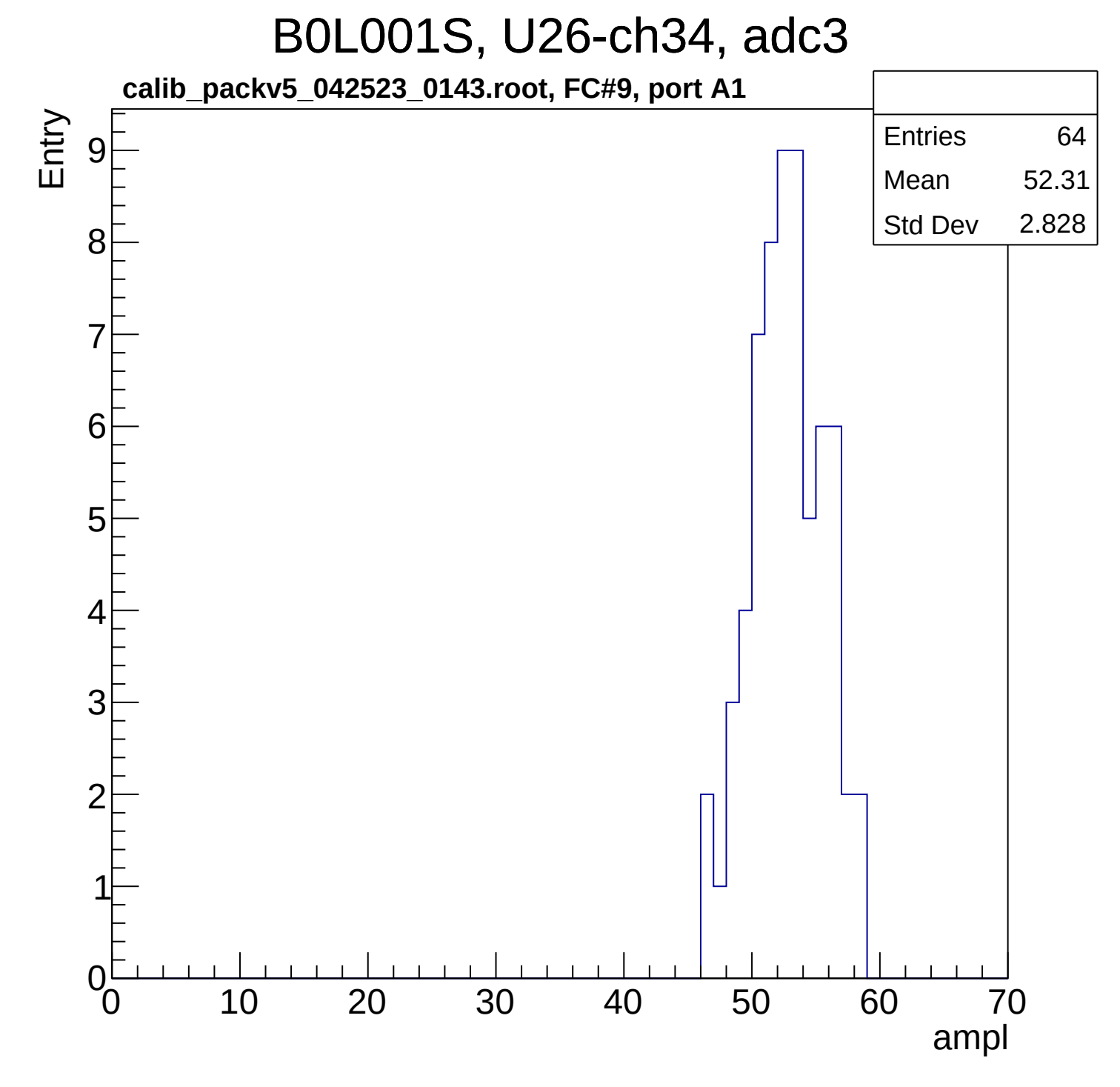
B0L001S, U26-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.31
Std Dev 2.828
Entry
ampl
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2
3
4
5
6
7
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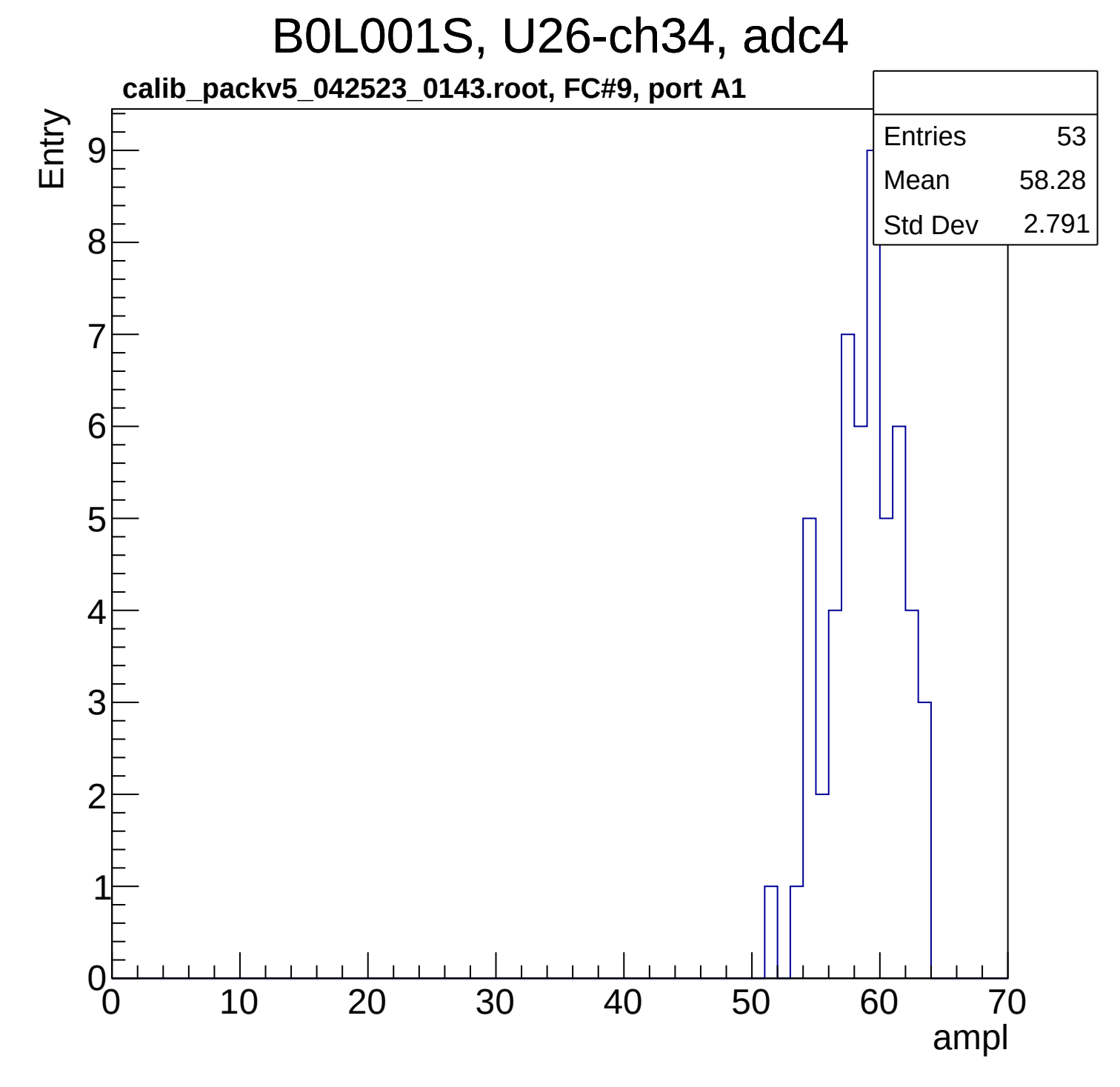
B0L001S, U26-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.28
Std Dev 2.791
Entry
ampl
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3
4
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7
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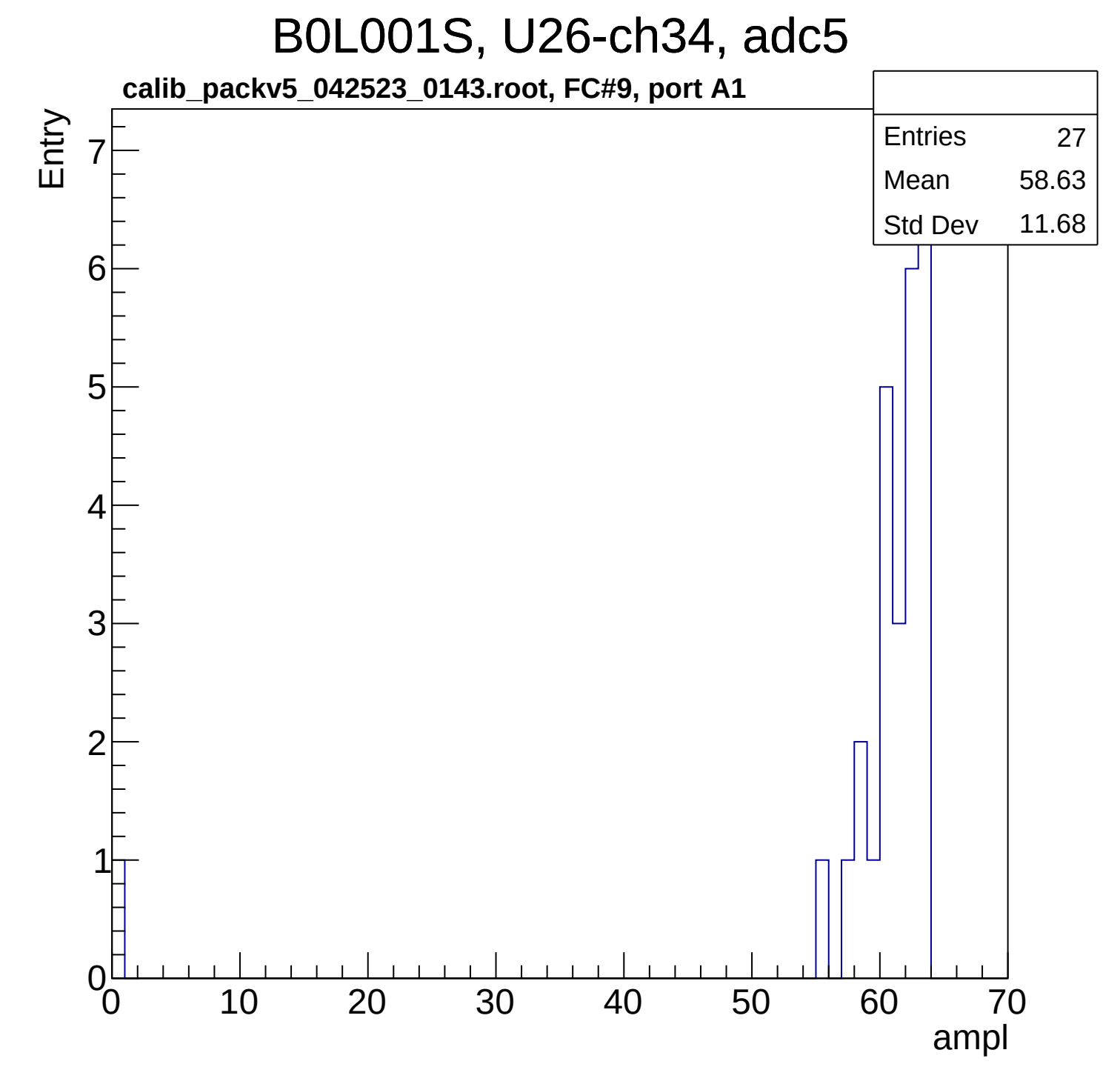
B0L001S, U26-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.63
Std Dev 11.68
Entry
ampl
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2
3
4
5
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0
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60
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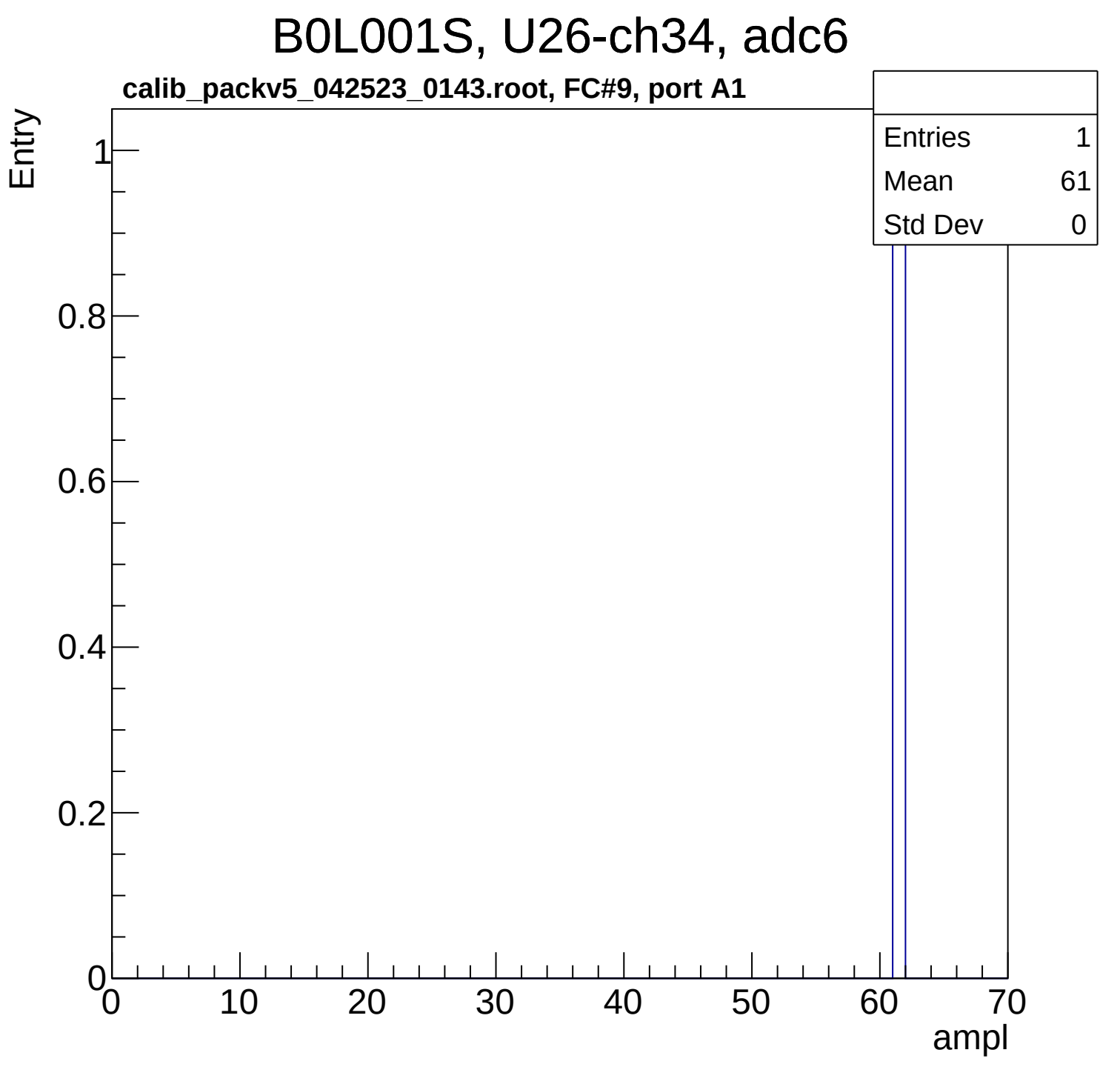
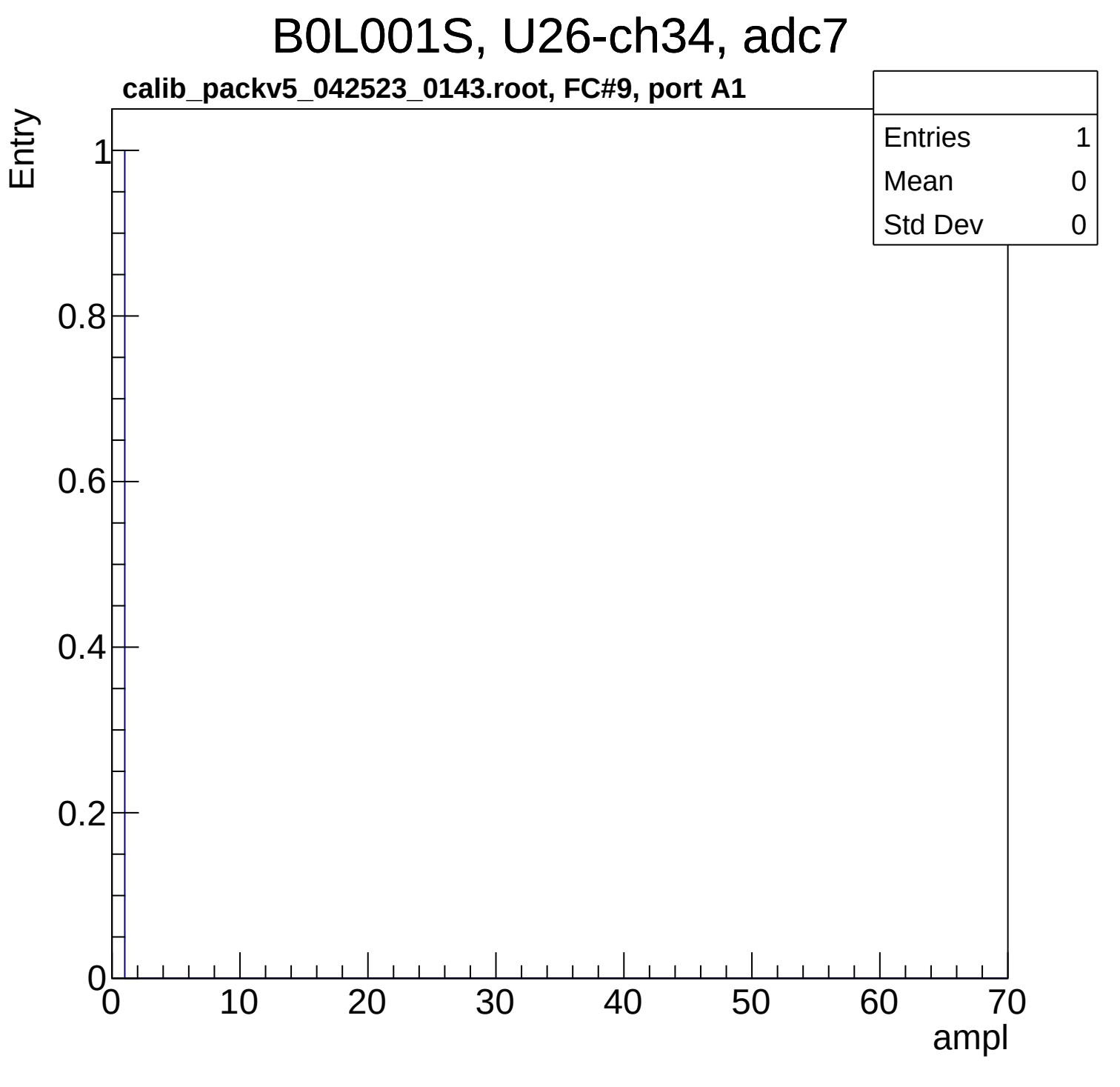

B0L001S, U26-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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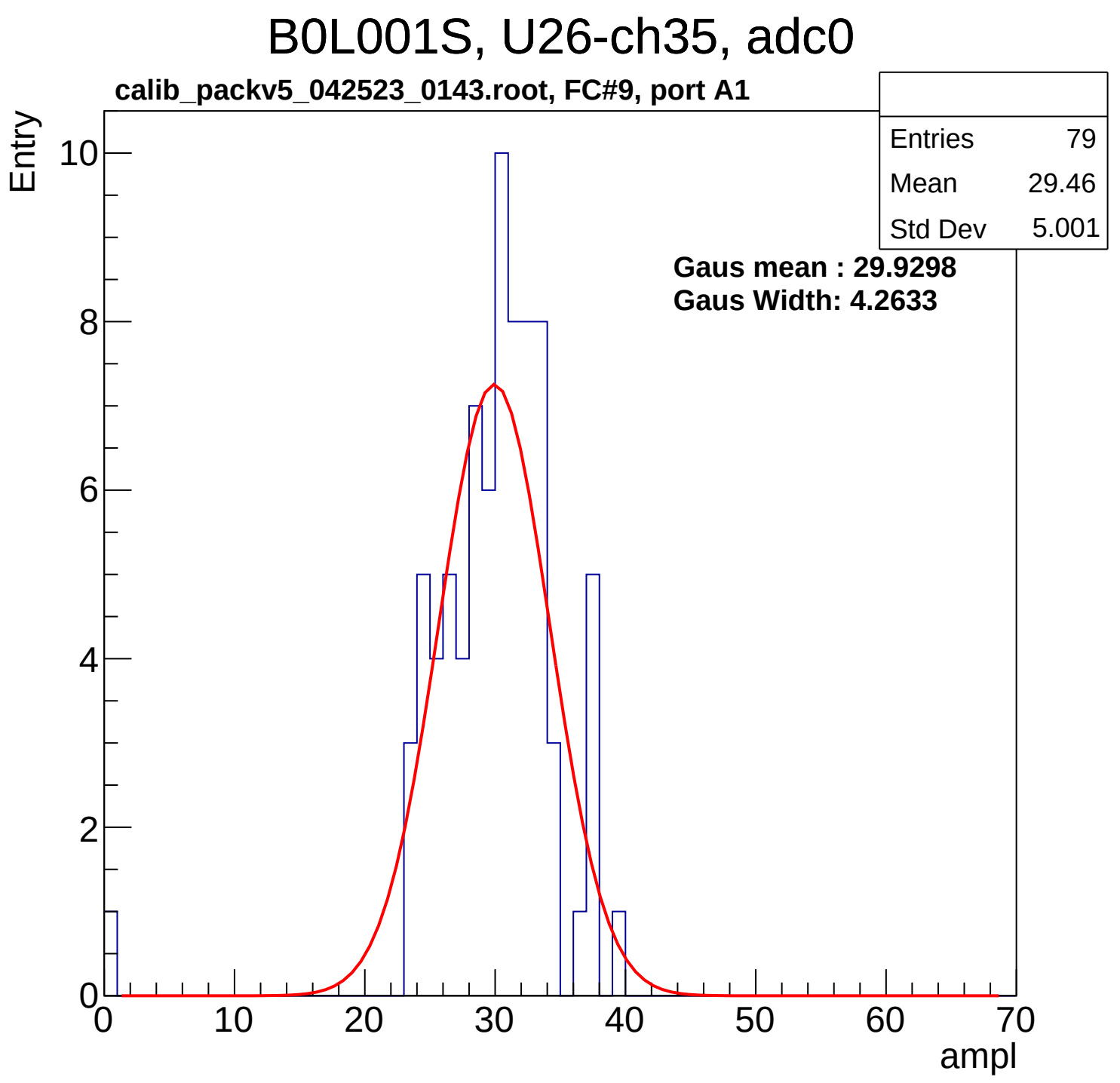
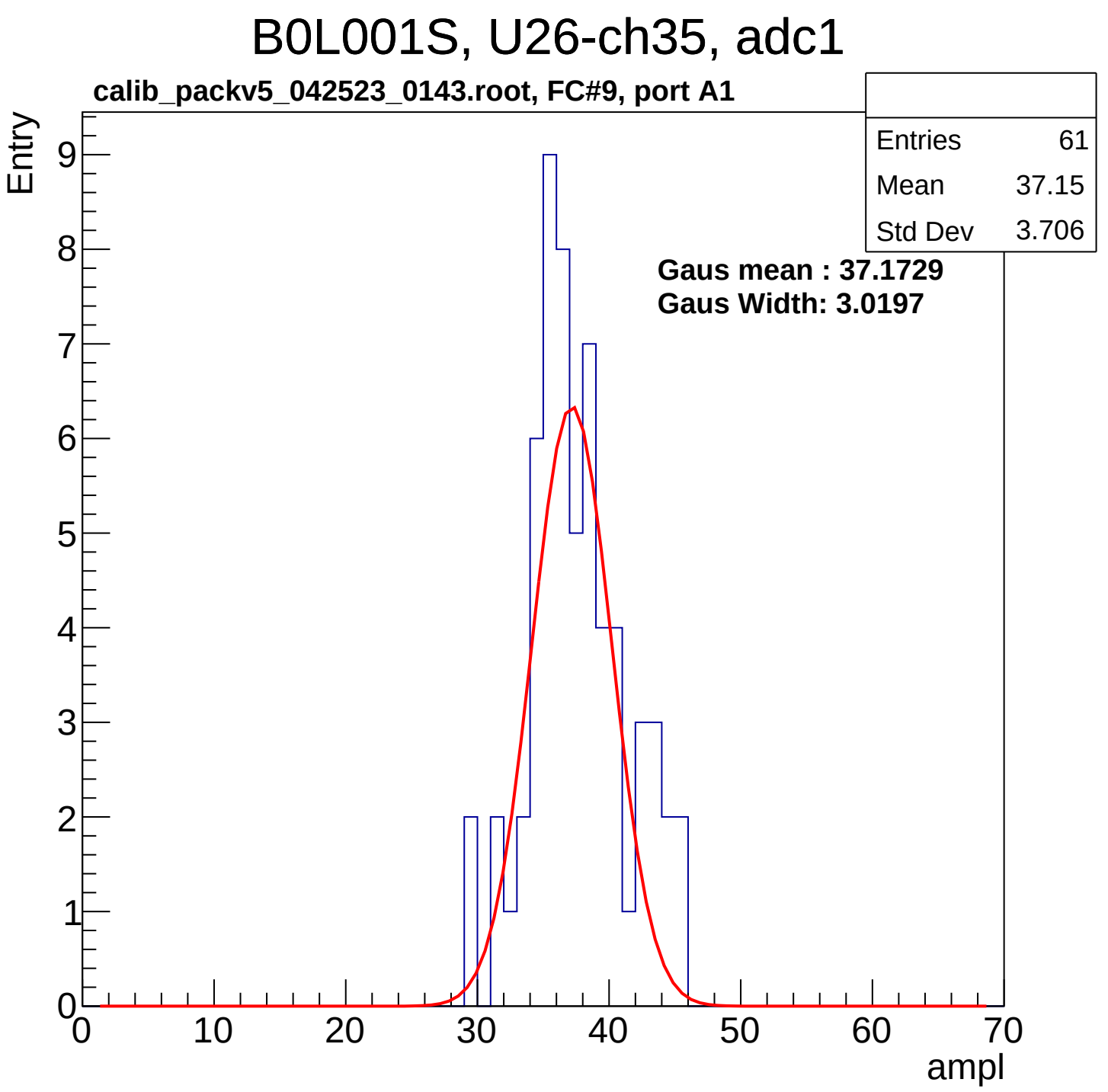
50
60
70
ampl


B0L001S, U26-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 29.46
Std Dev 5.001
Gaus mean : 29.9298
Gaus Width: 4.2633
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.15
Std Dev 3.706
Gaus mean : 37.1729
Gaus Width: 3.0197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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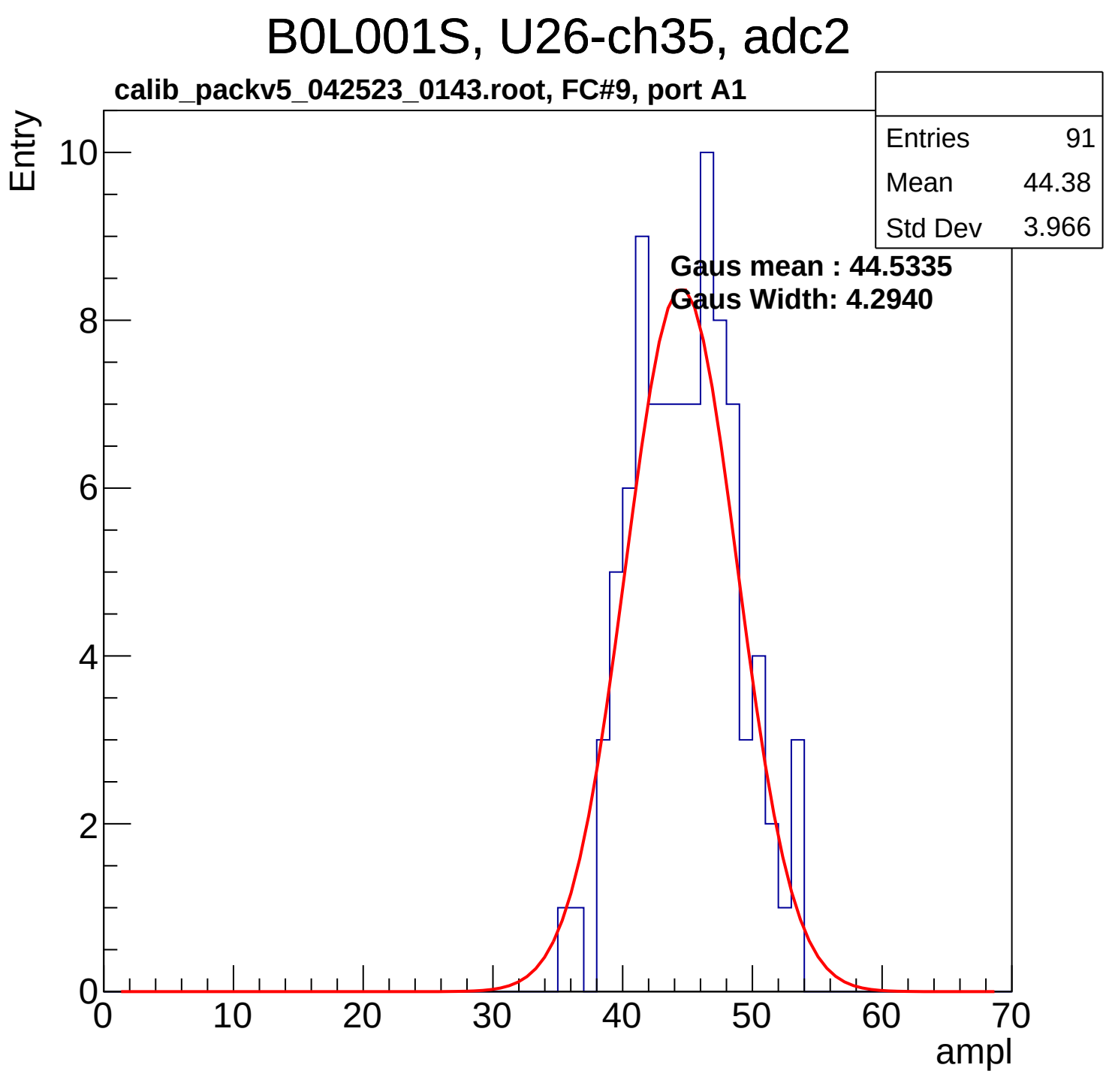
B0L001S, U26-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 44.38
Std Dev 3.966
Gaus mean : 44.5335
Gaus Width: 4.2940
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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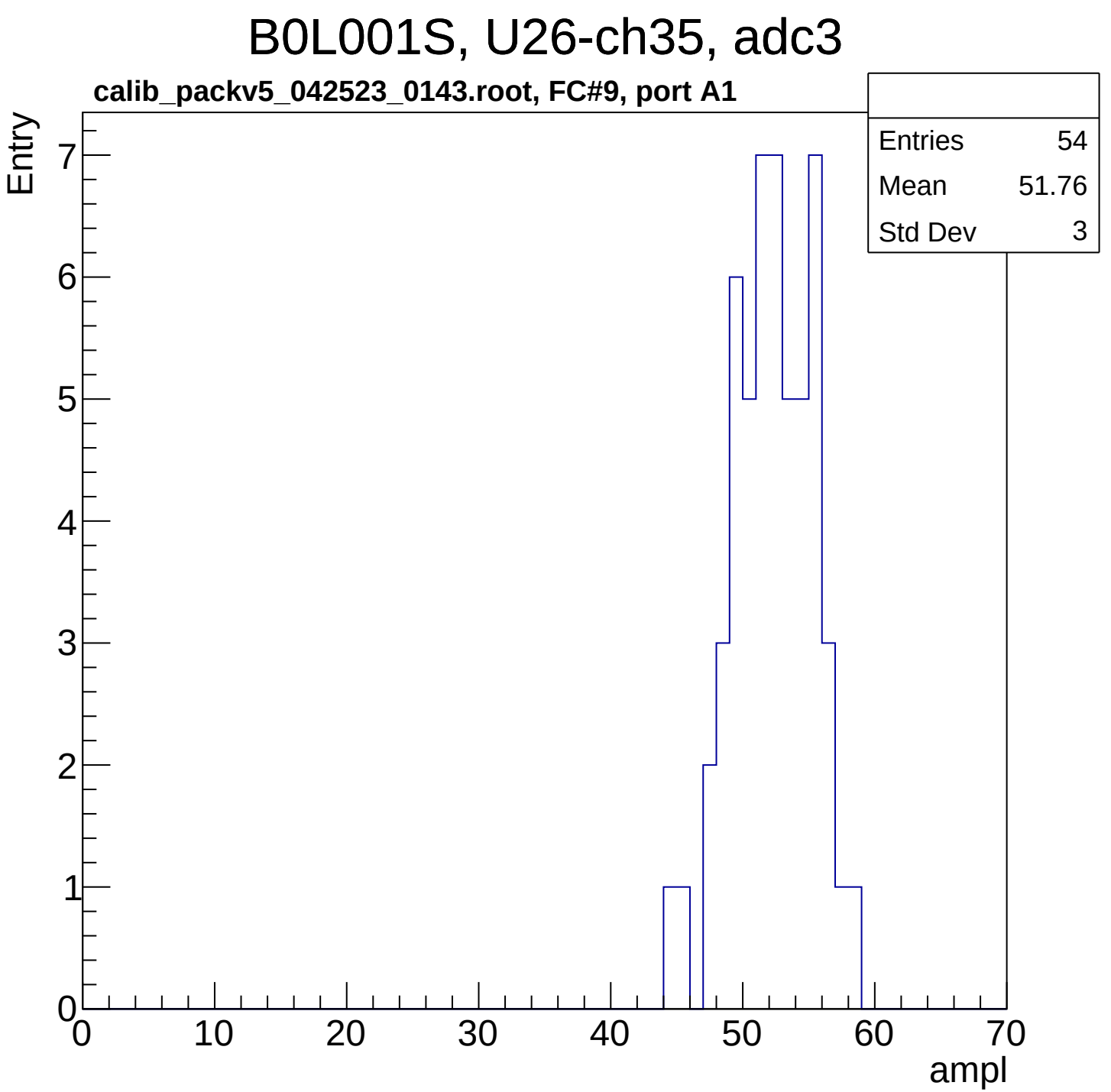
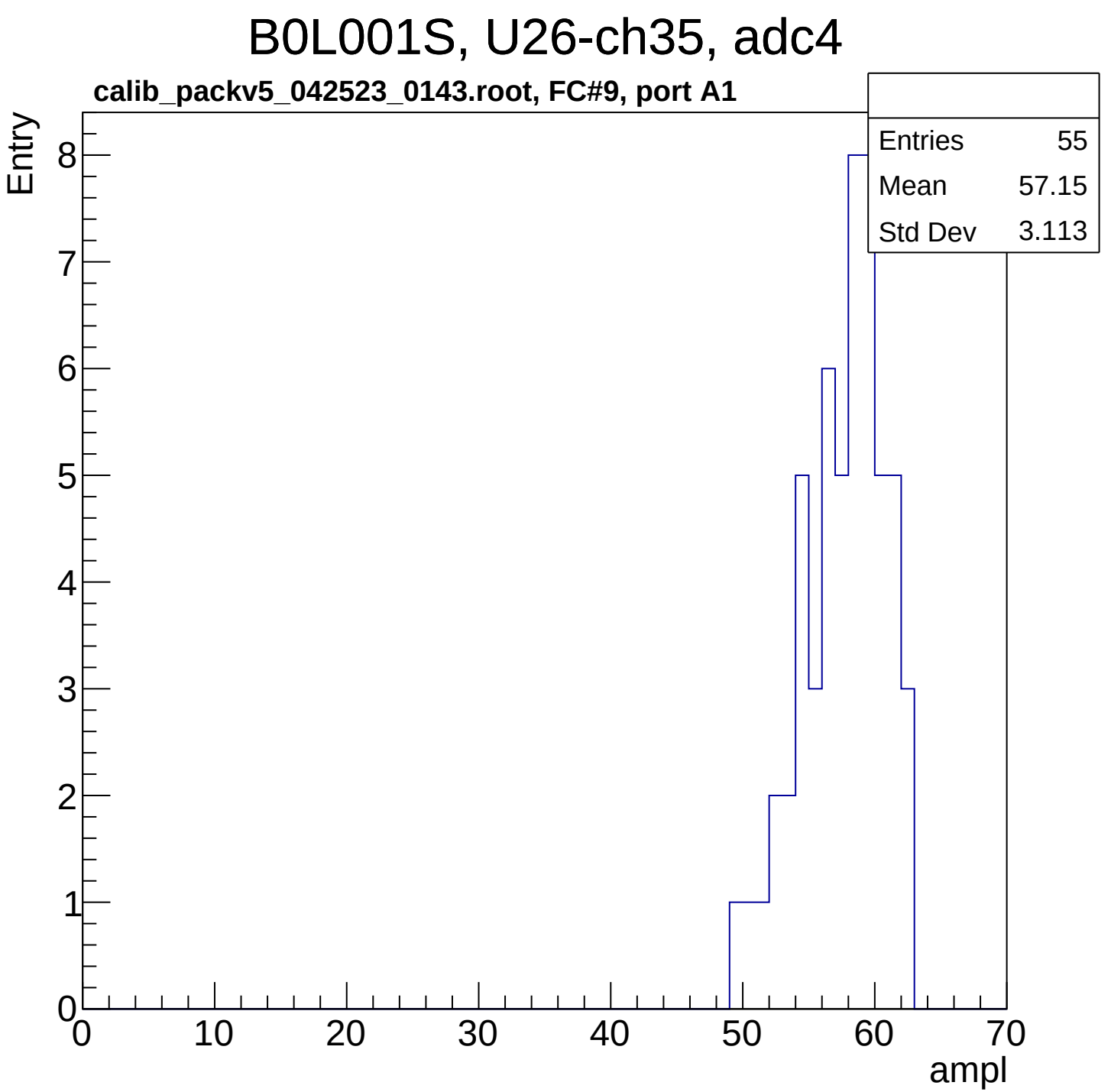
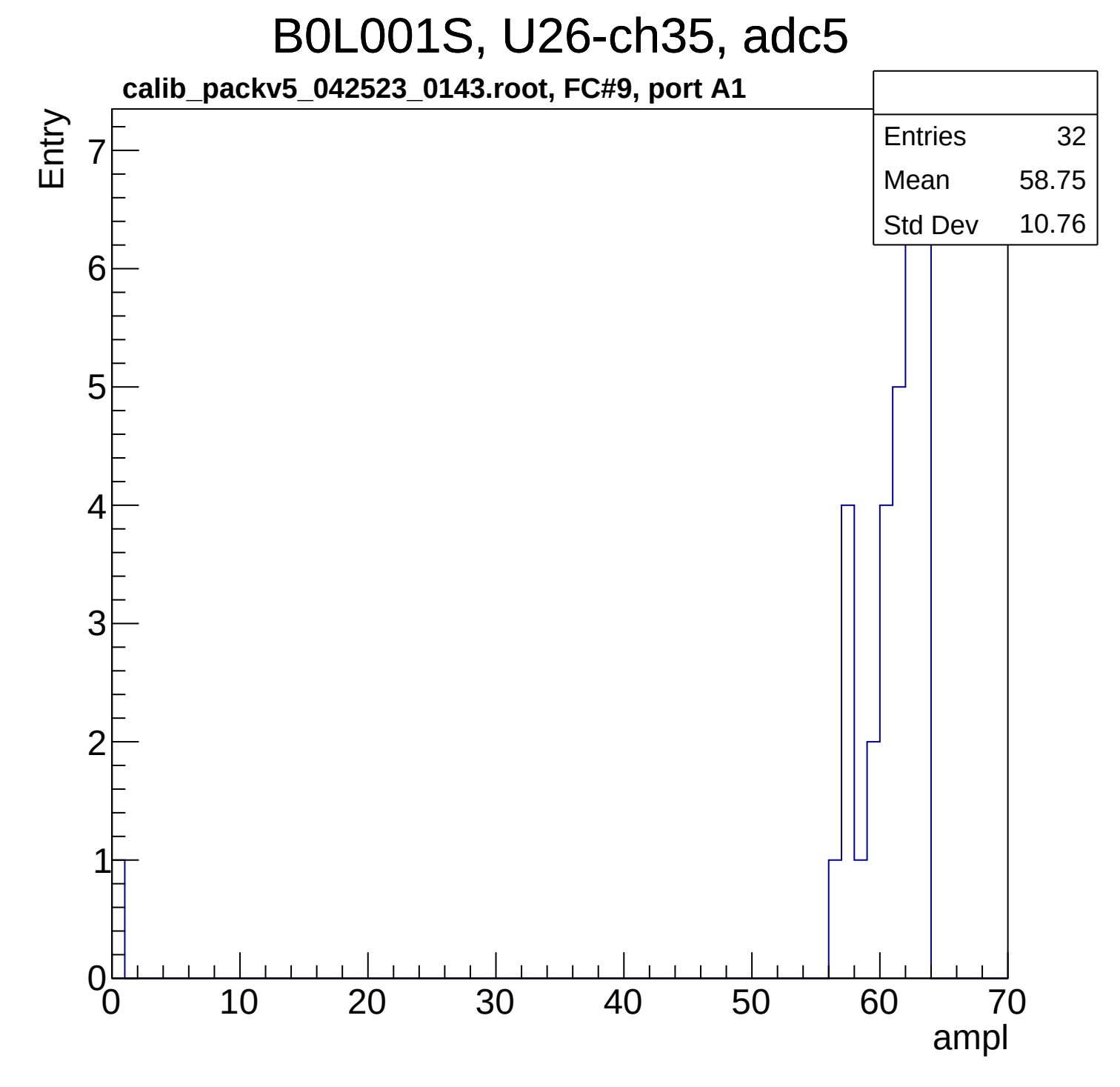
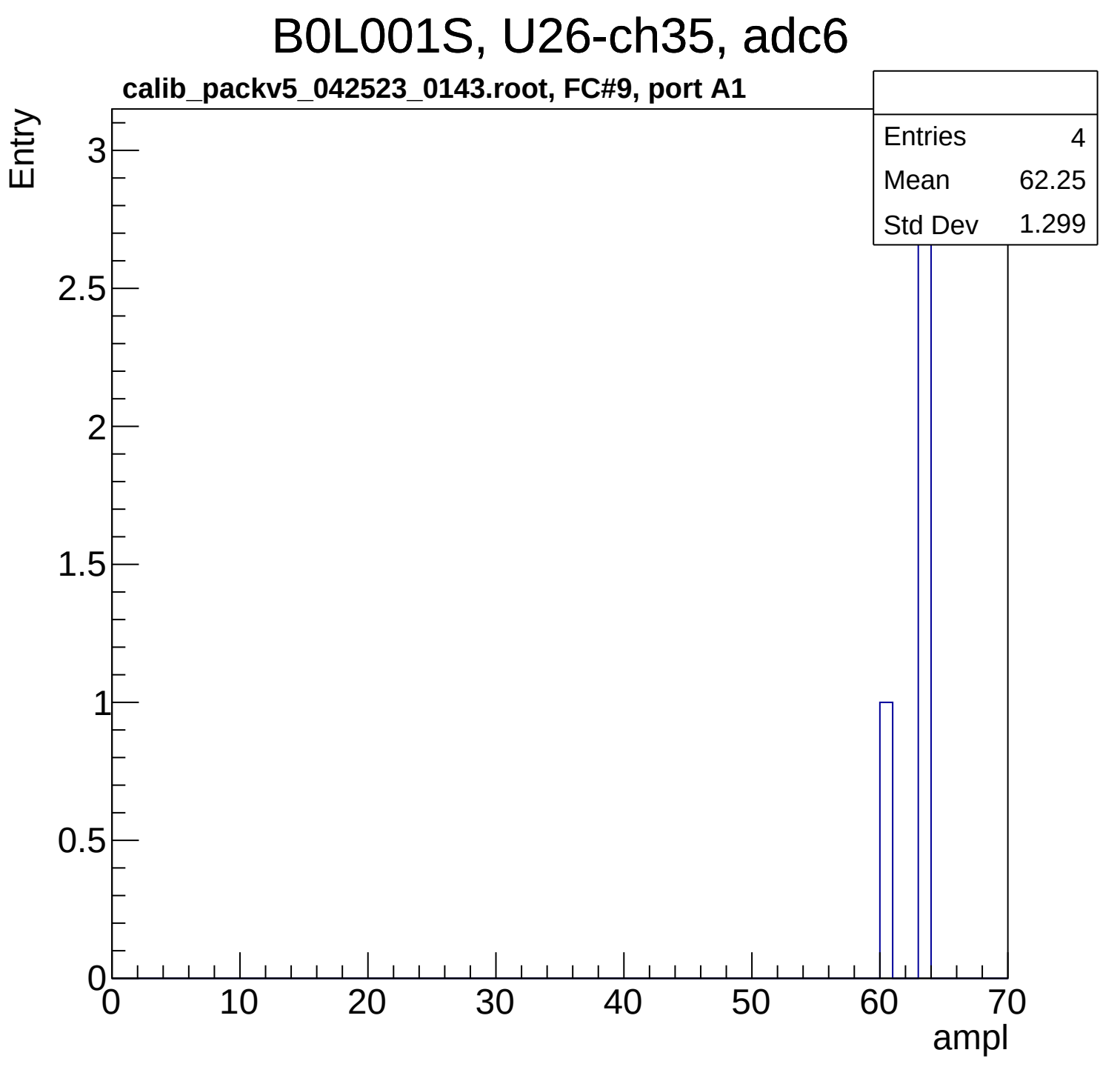
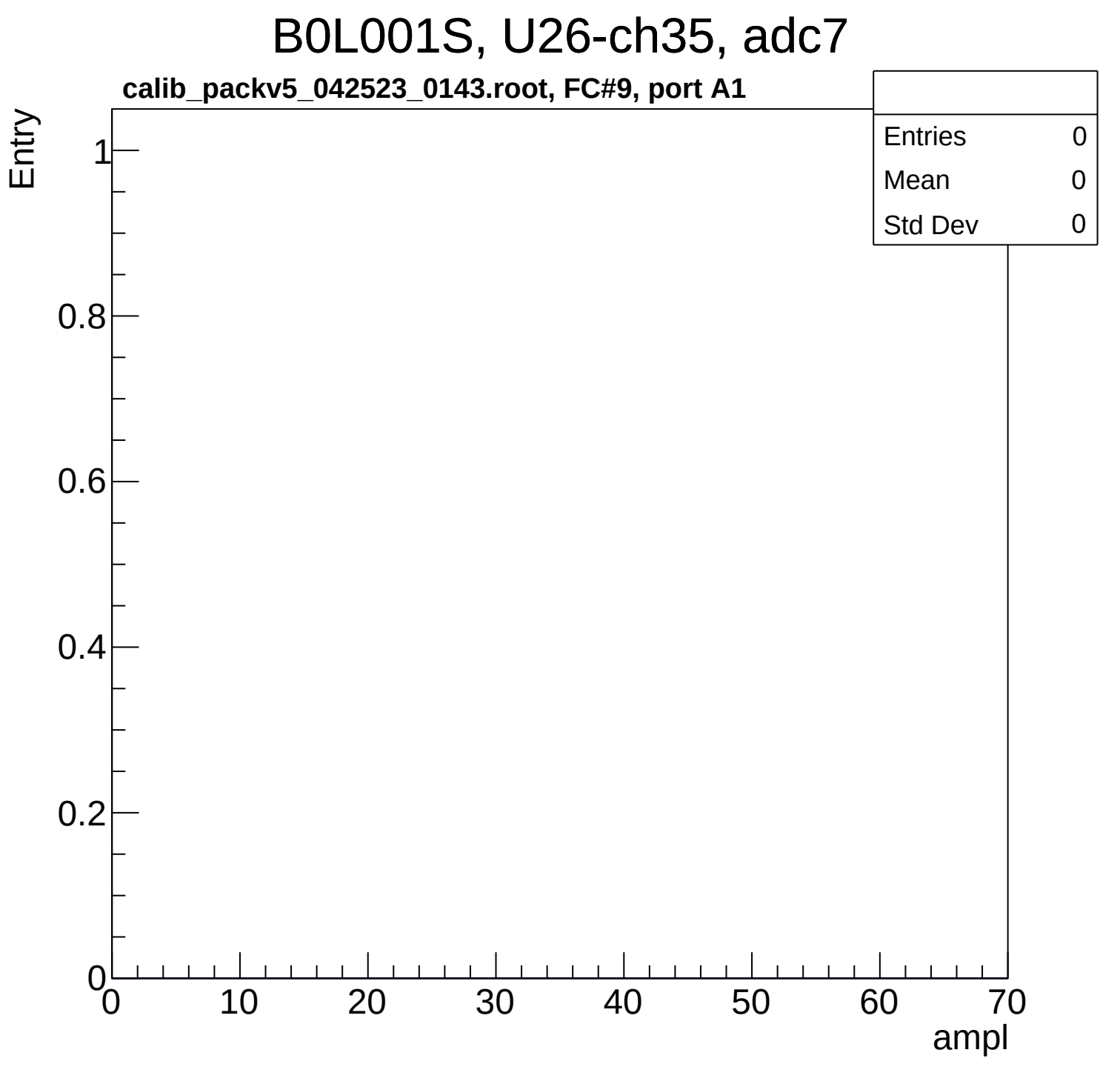
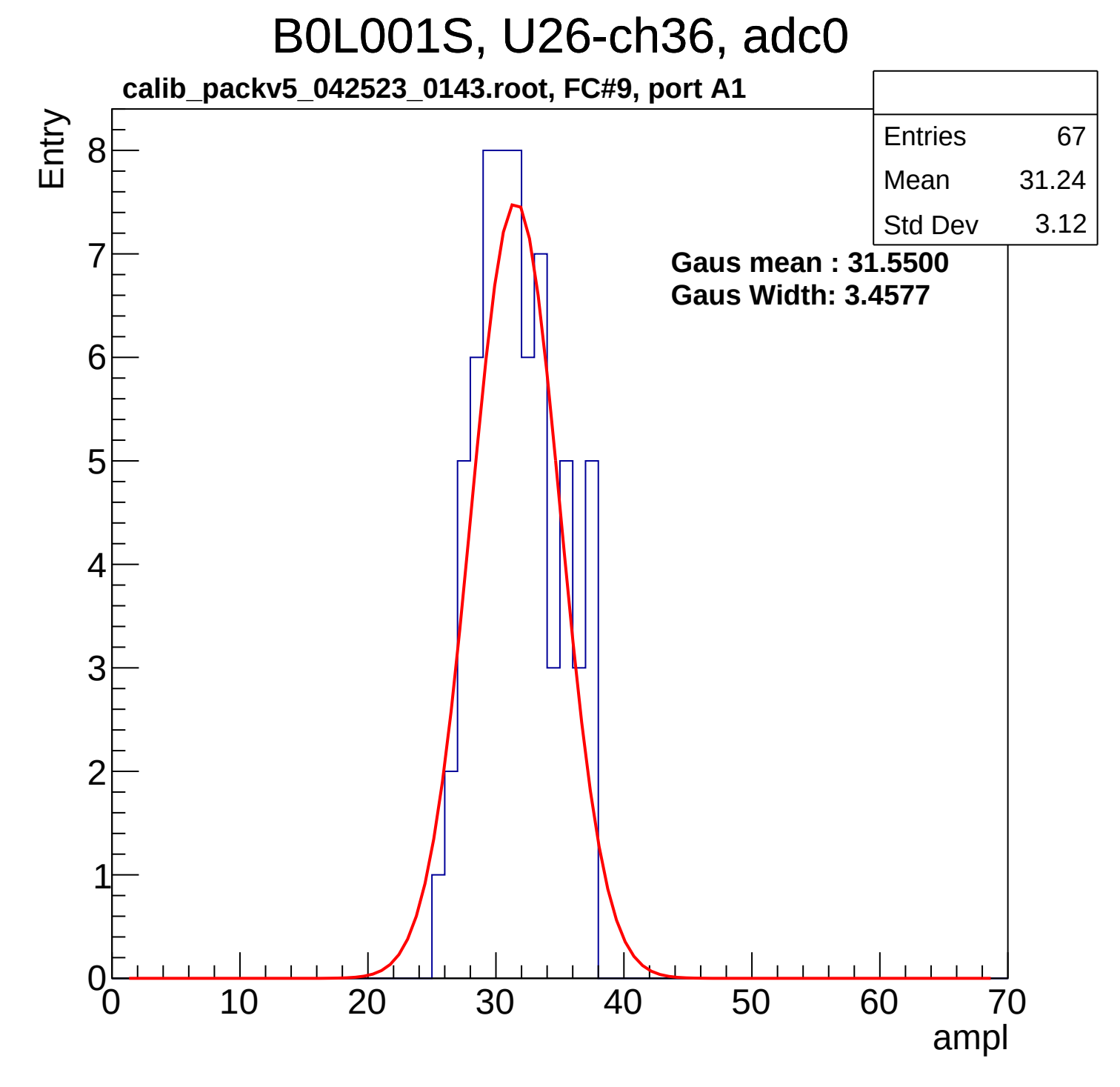

B0L001S, U26-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.76
Std Dev 3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

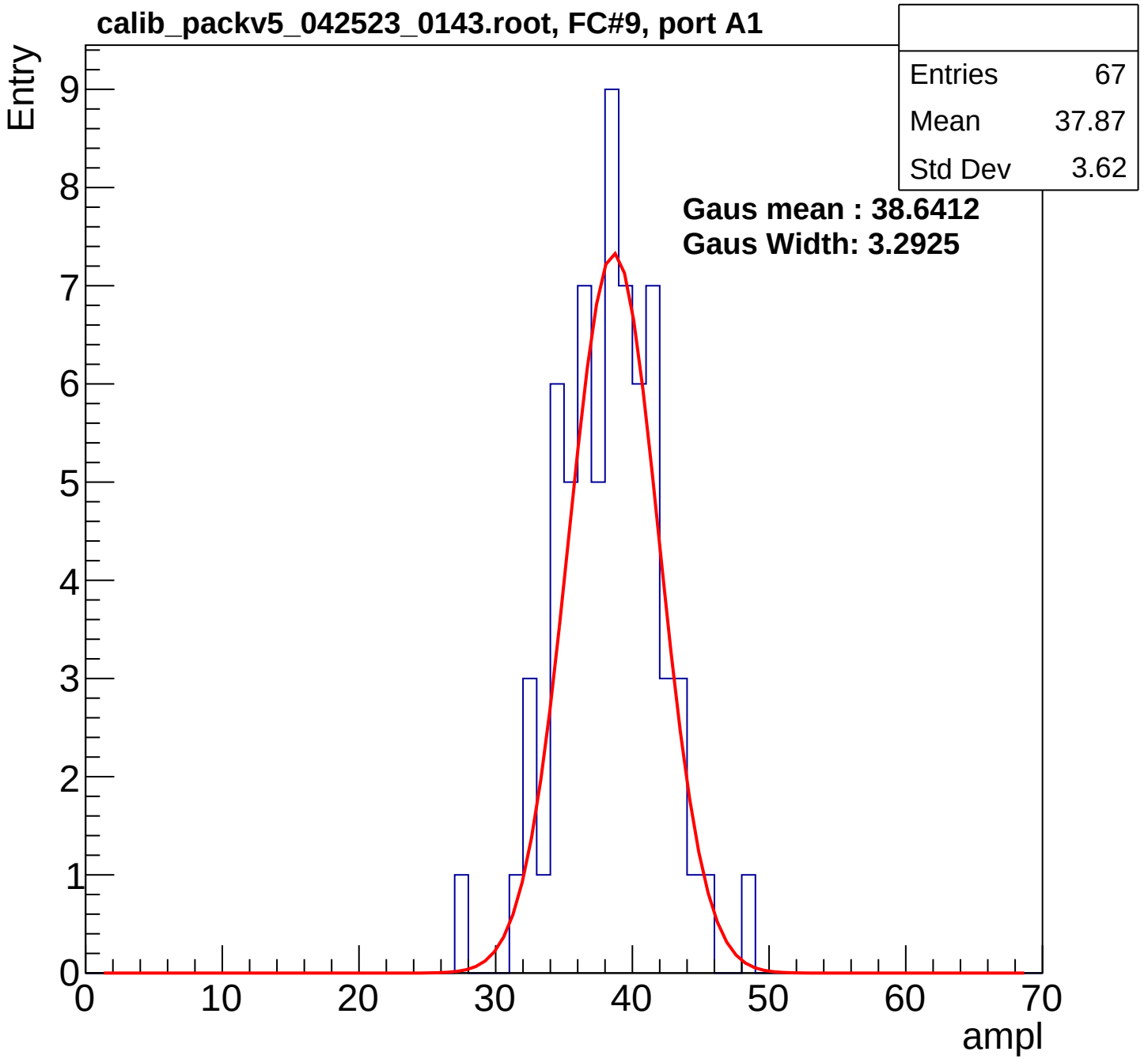

B0L001S, U26-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.15
Std Dev 3.113
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

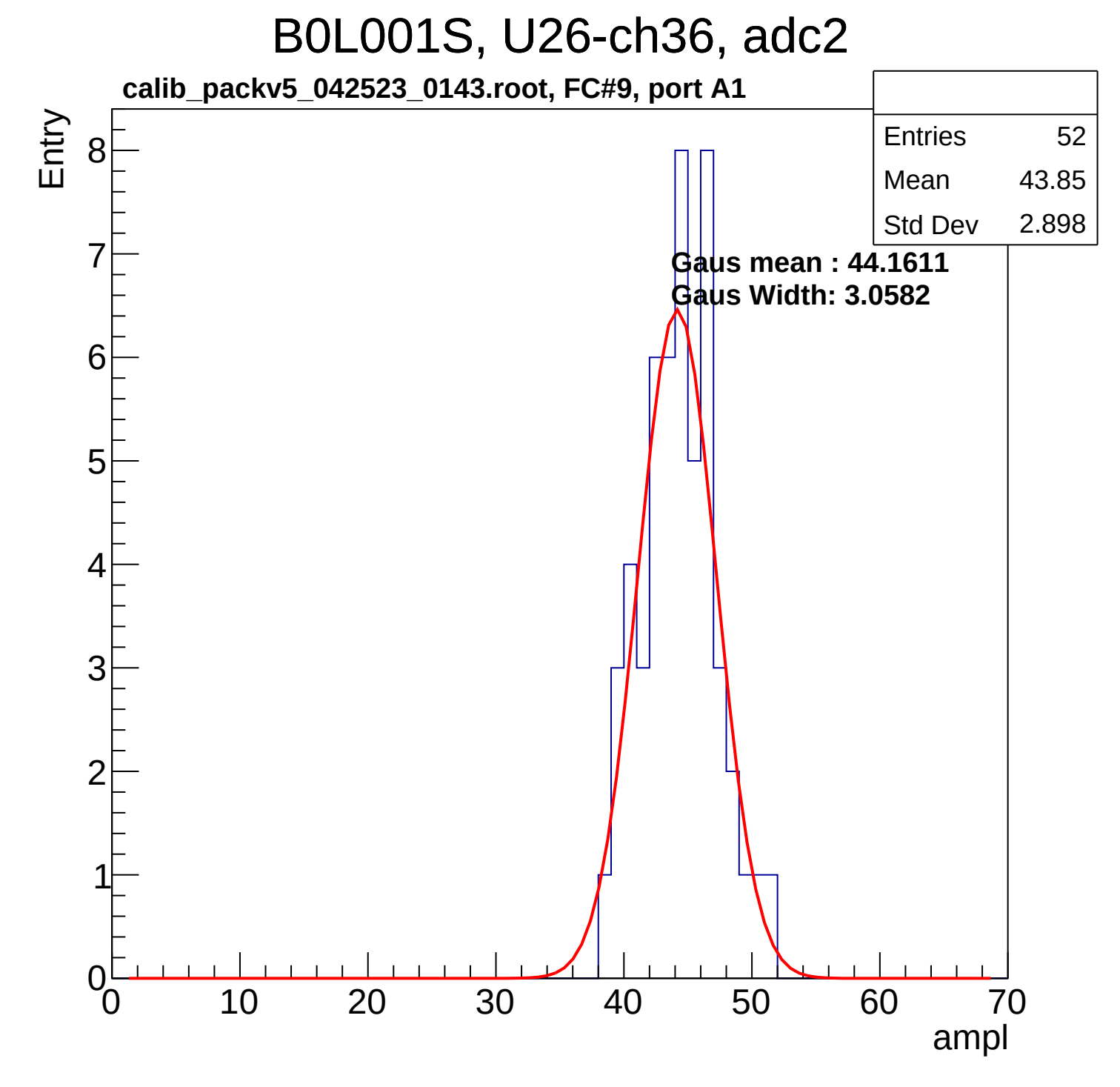

B0L001S, U26-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.75
Std Dev 10.76
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

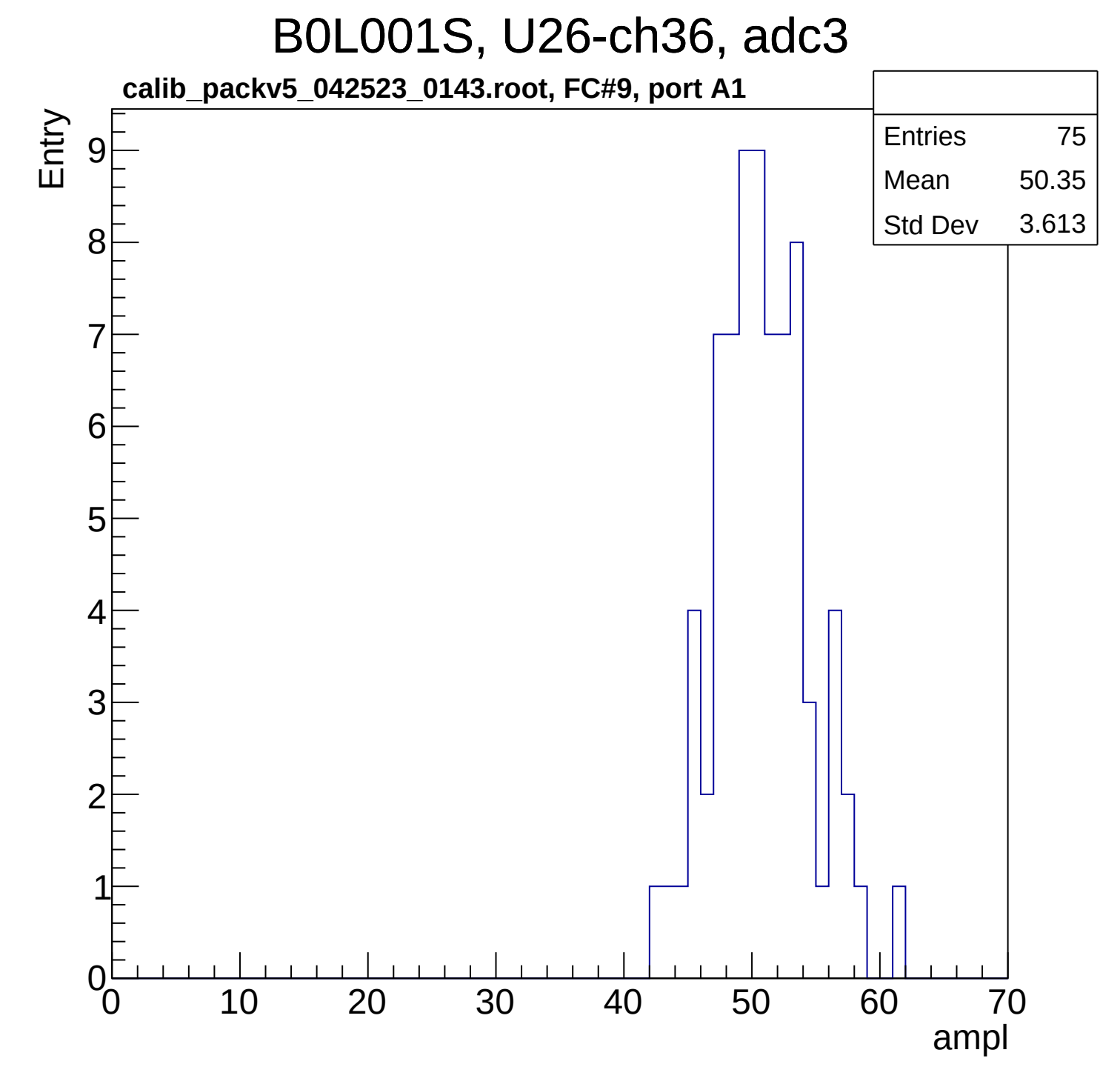

B0L001S, U26-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

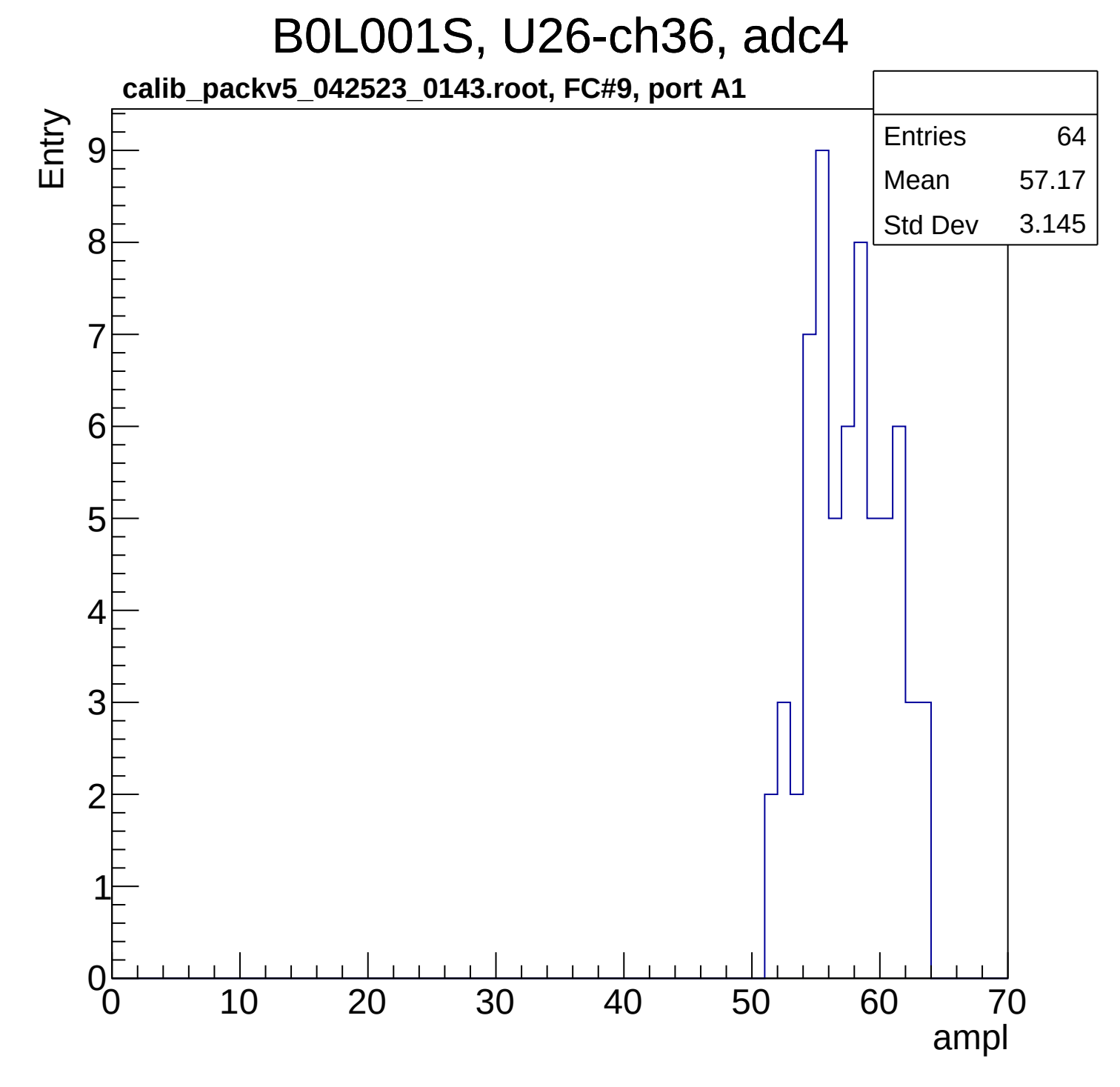

B0L001S, U26-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

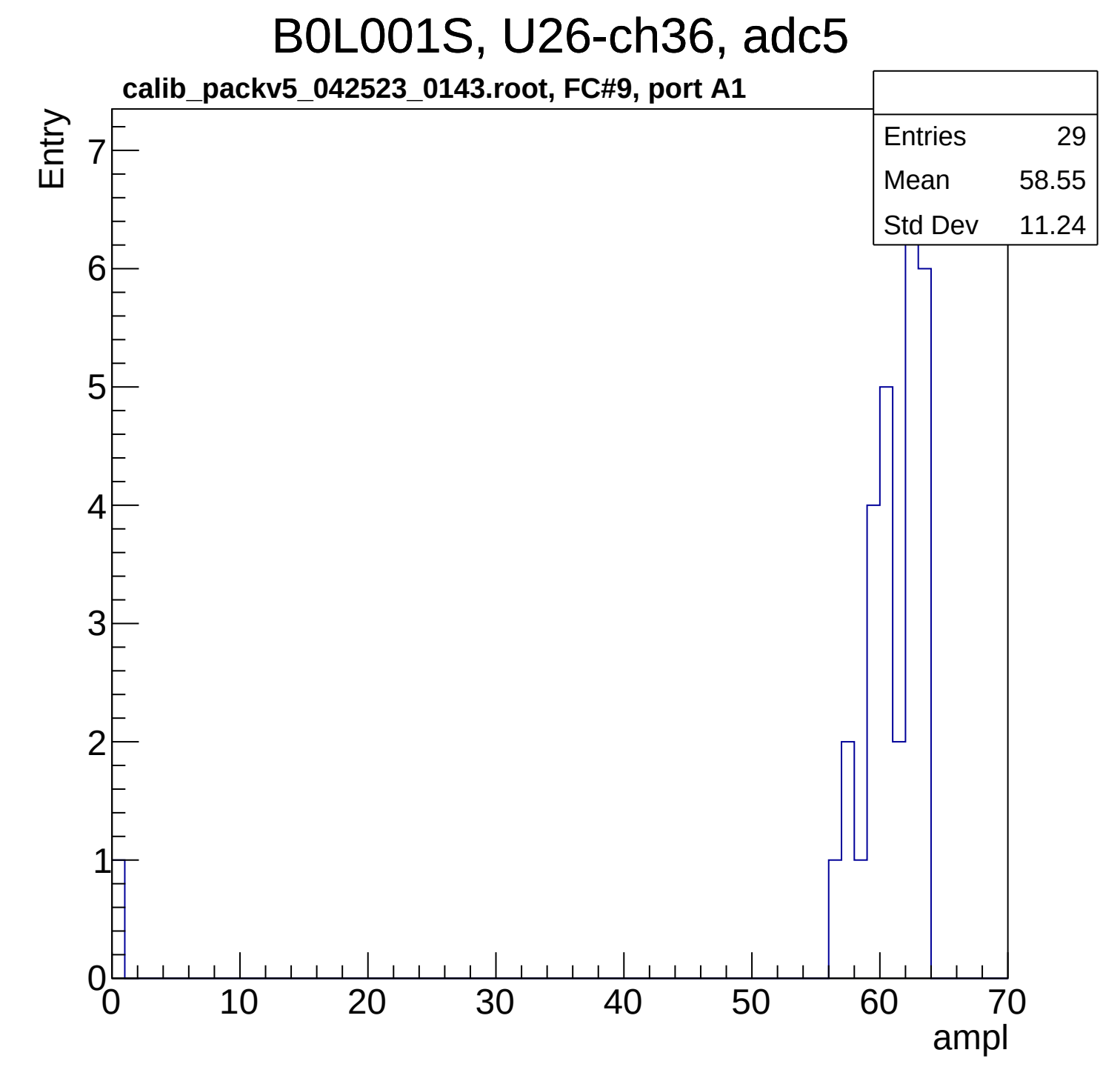

B0L001S, U26-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.24
Std Dev 3.12
Gaus mean : 31.5500
Gaus Width: 3.4577
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

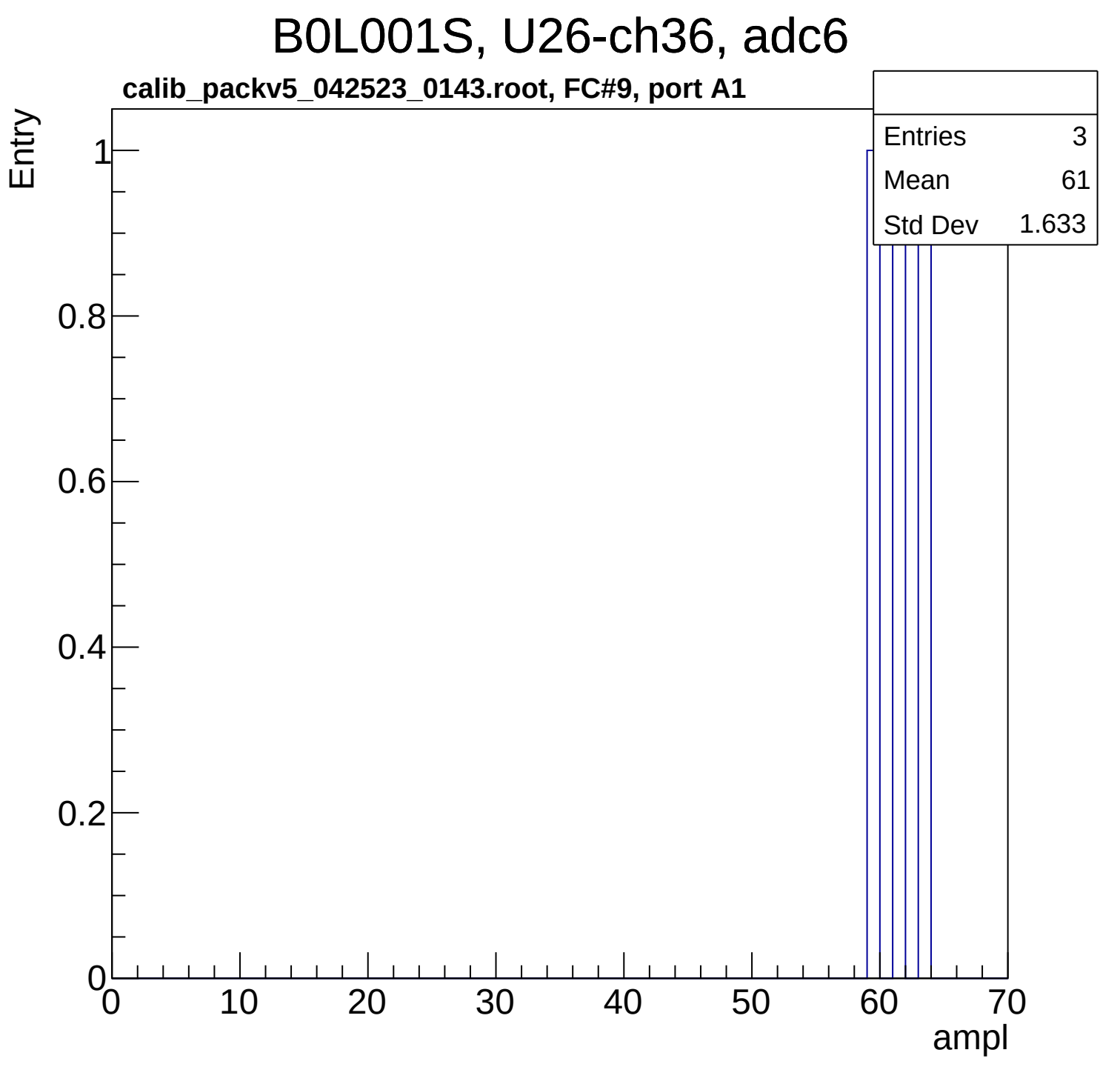

B0L001S, U26-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.87
Std Dev 3.62
Gaus mean : 38.6412
Gaus Width: 3.2925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.85
Std Dev 2.898
Gaus mean : 44.1611
Gaus Width: 3.0582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.35
Std Dev 3.613
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.17
Std Dev 3.145
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.55
Std Dev 11.24
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 1.633
Entry
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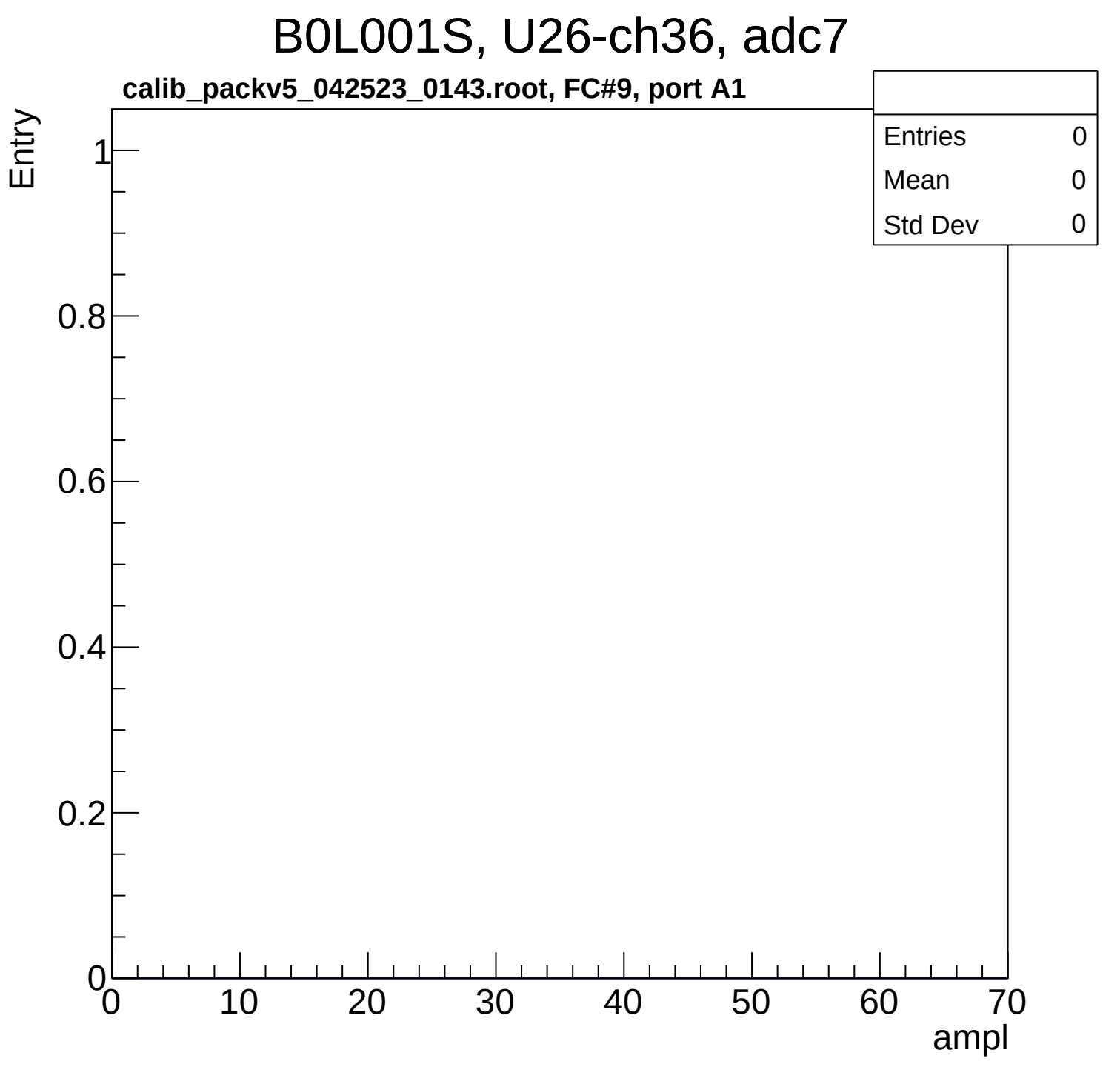
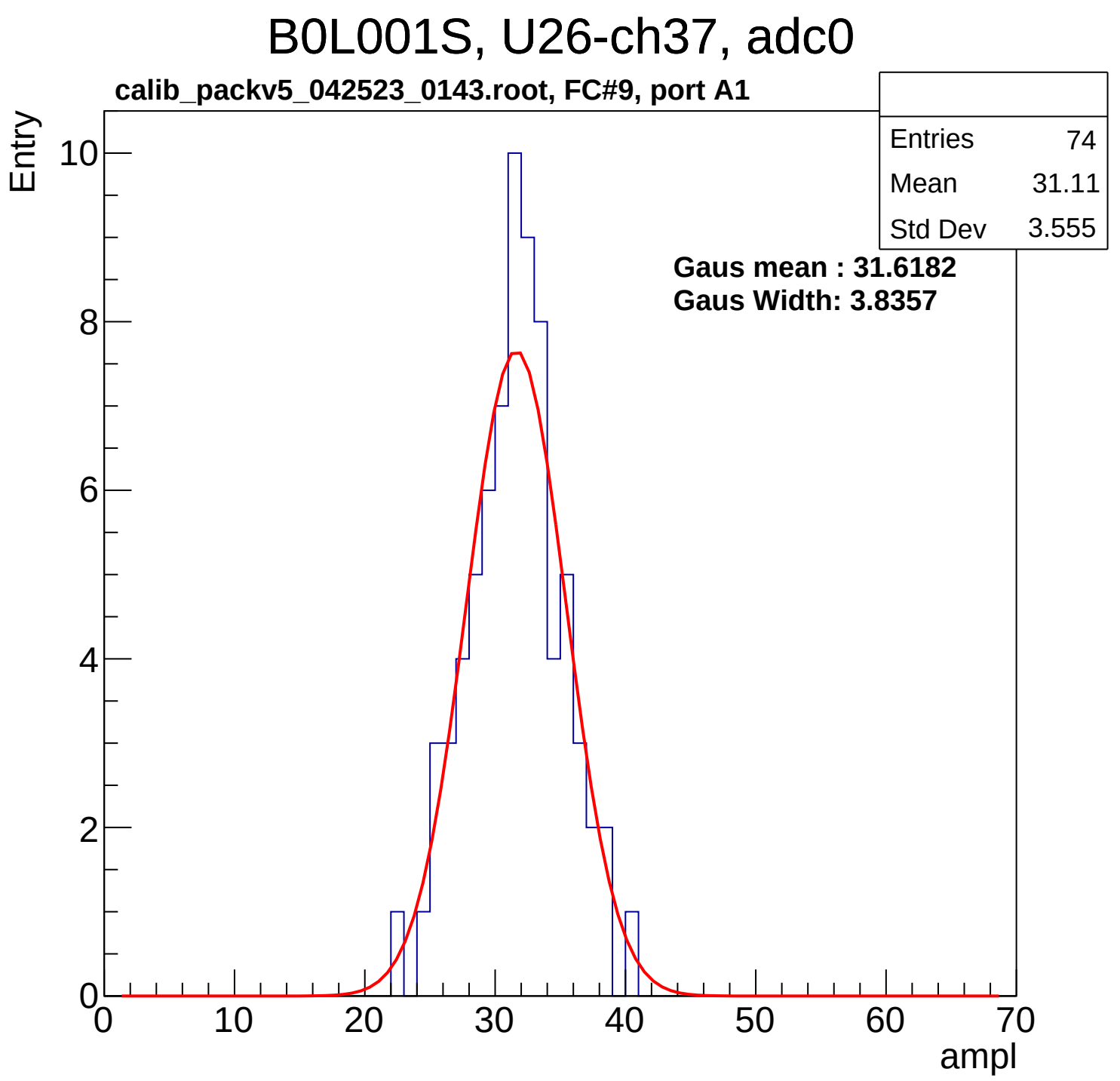
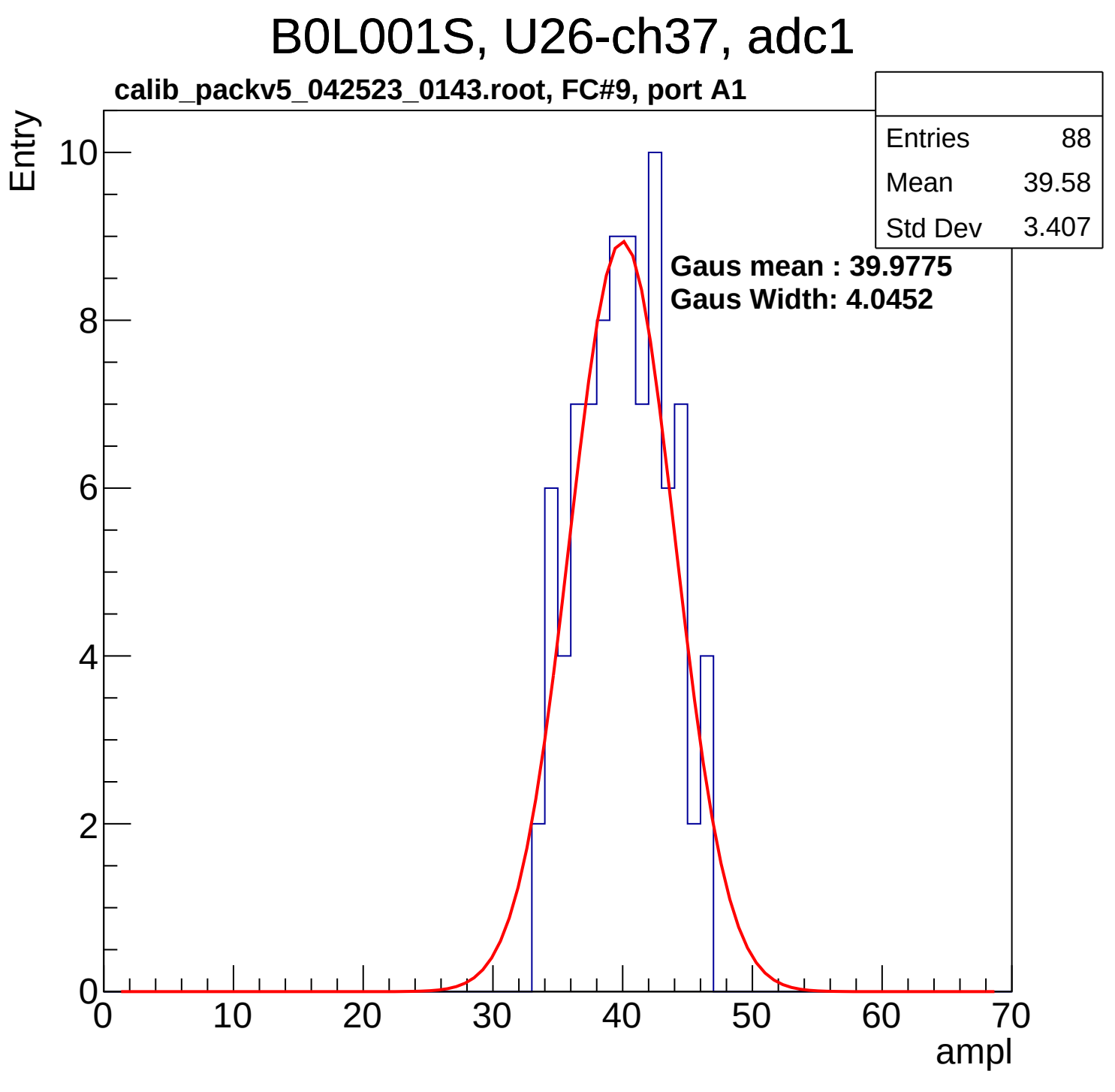
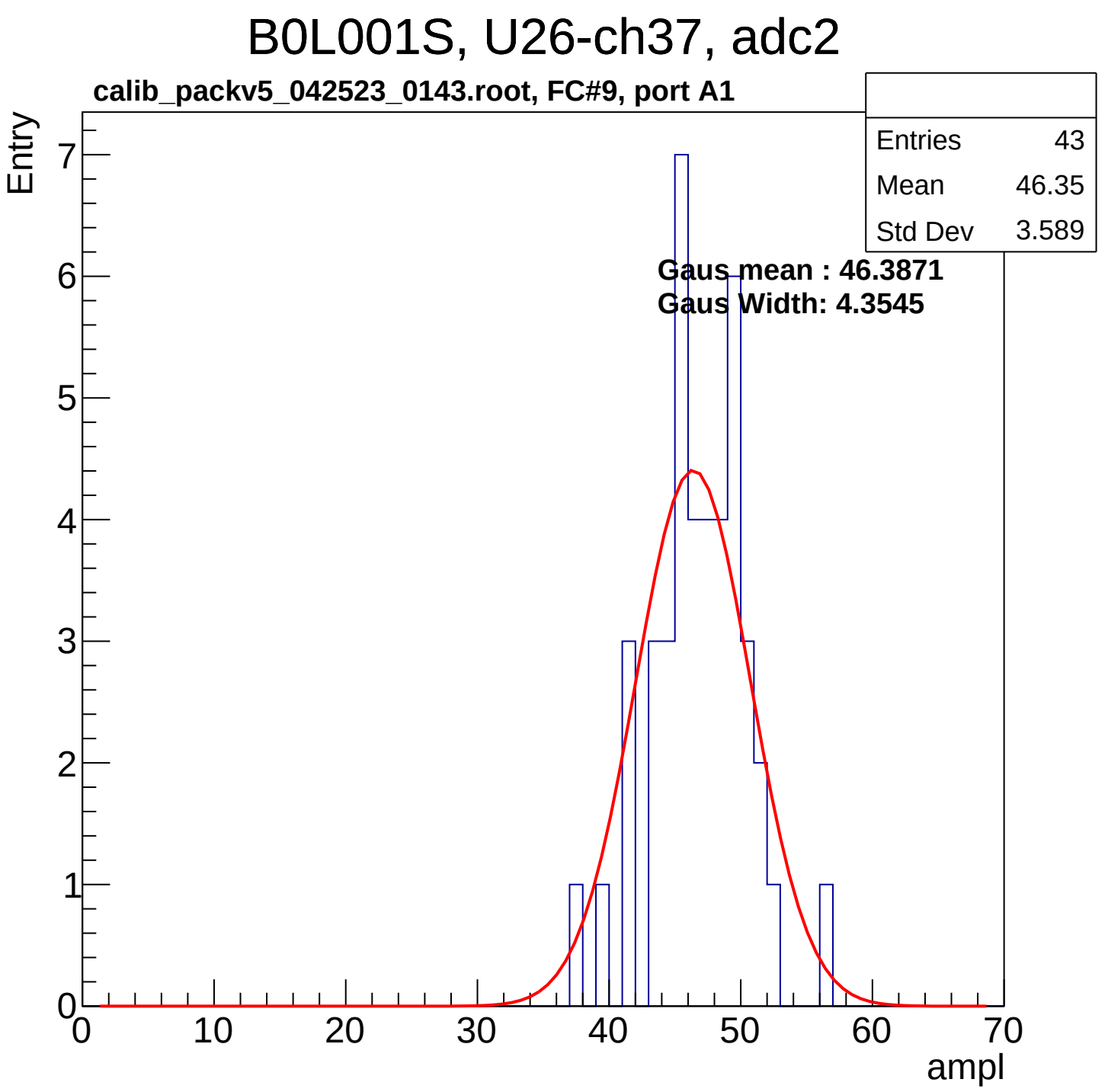
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 31.11
Std Dev 3.555
Gaus mean : 31.6182
Gaus Width: 3.8357
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 39.58
Std Dev 3.407
Gaus mean : 39.9775
Gaus Width: 4.0452
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 46.35
Std Dev 3.589
Gaus mean : 46.3871
Gaus Width: 4.3545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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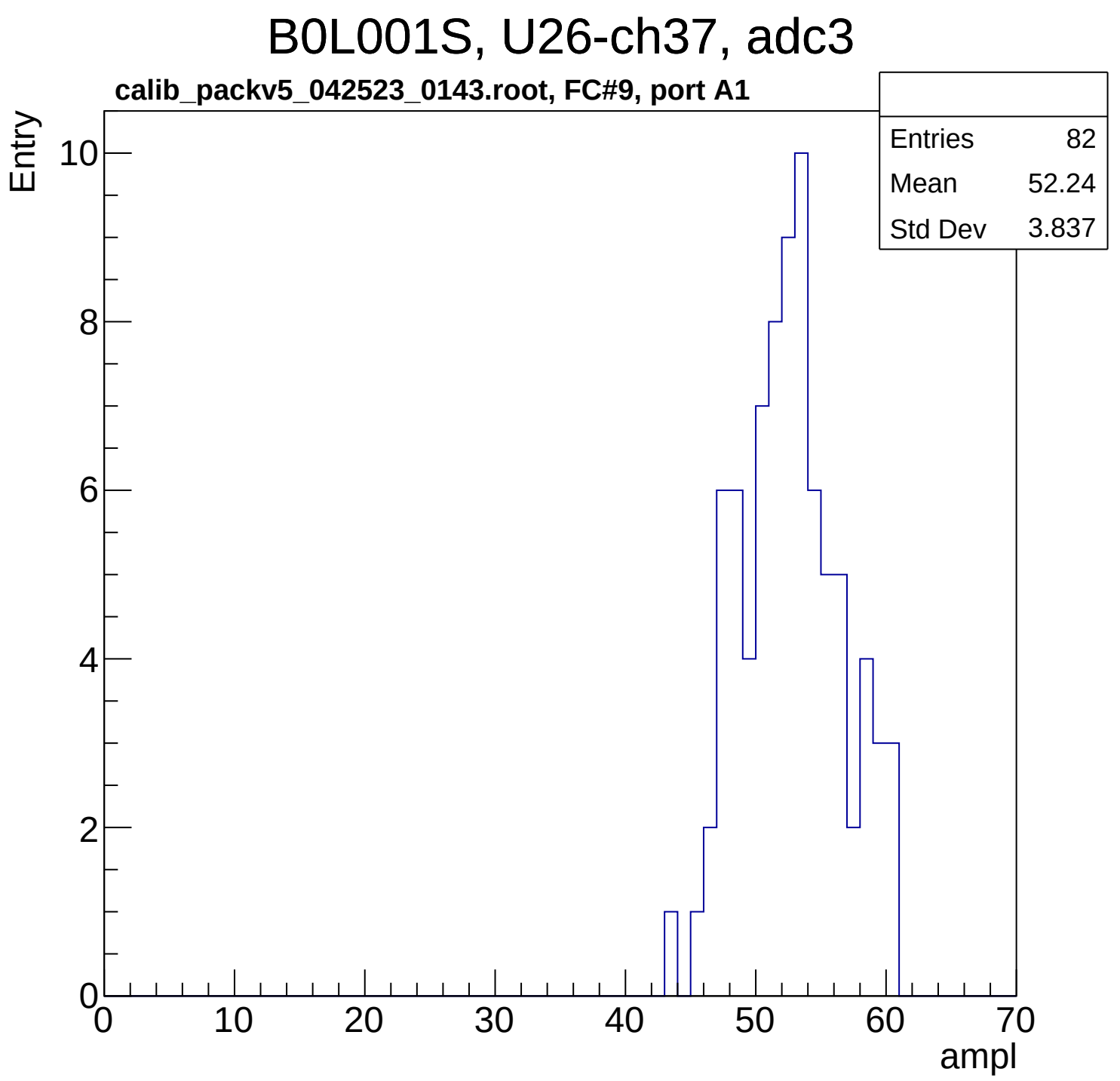
40
50
60
70


B0L001S, U26-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 52.24
Std Dev 3.837
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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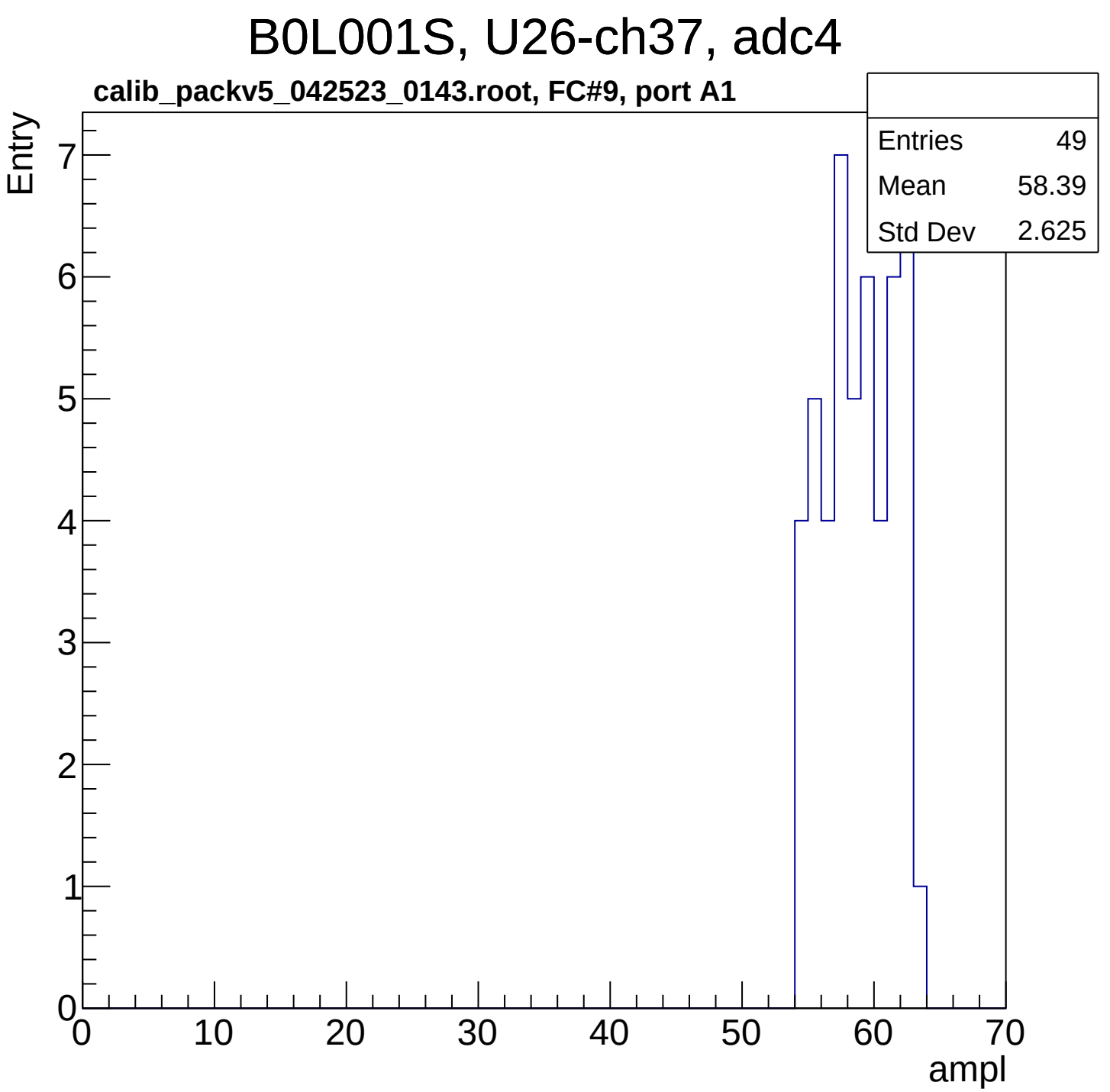
B0L001S, U26-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.39
Std Dev 2.625
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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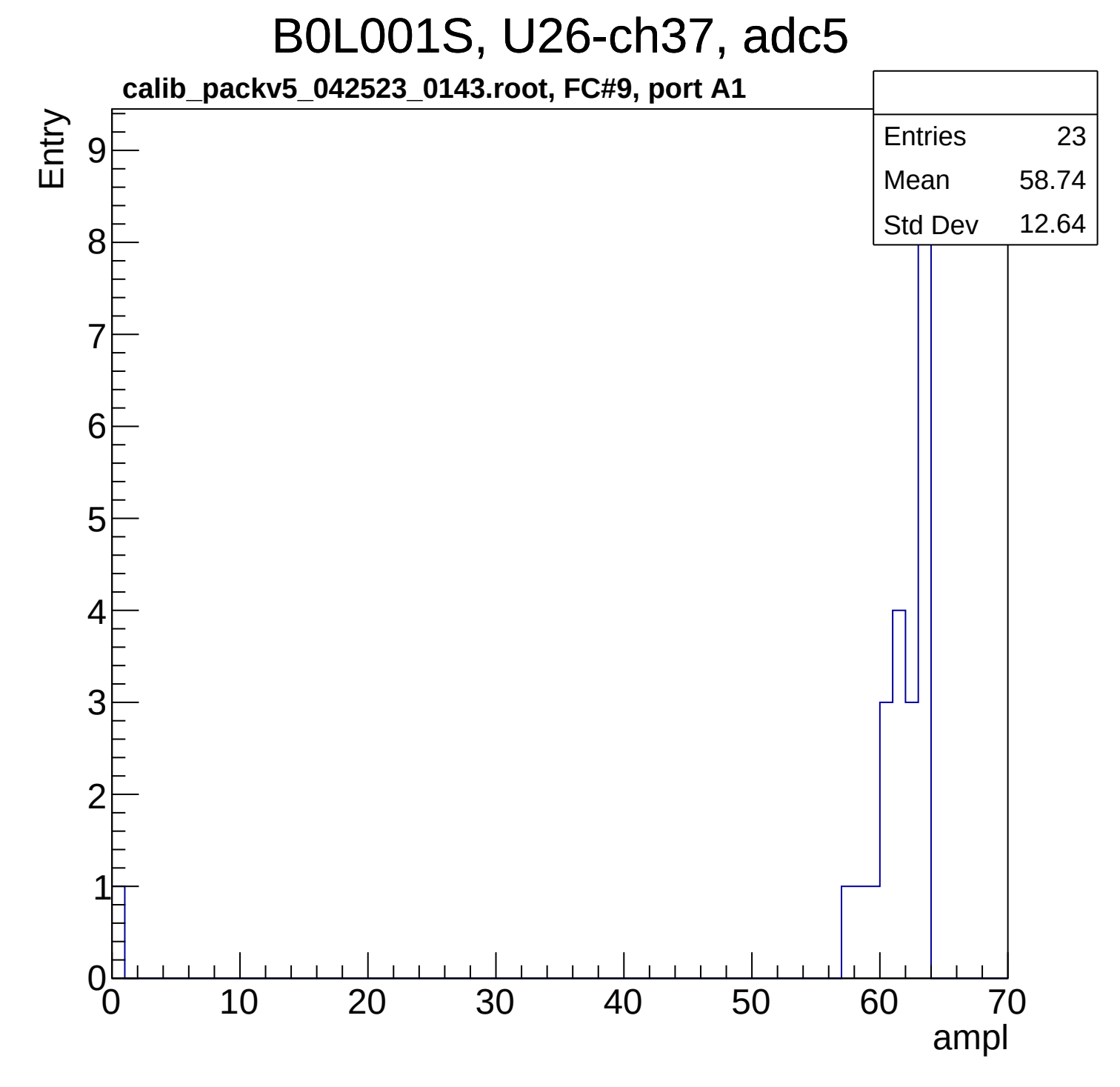
40
50
60
70


B0L001S, U26-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 58.74
Std Dev 12.64
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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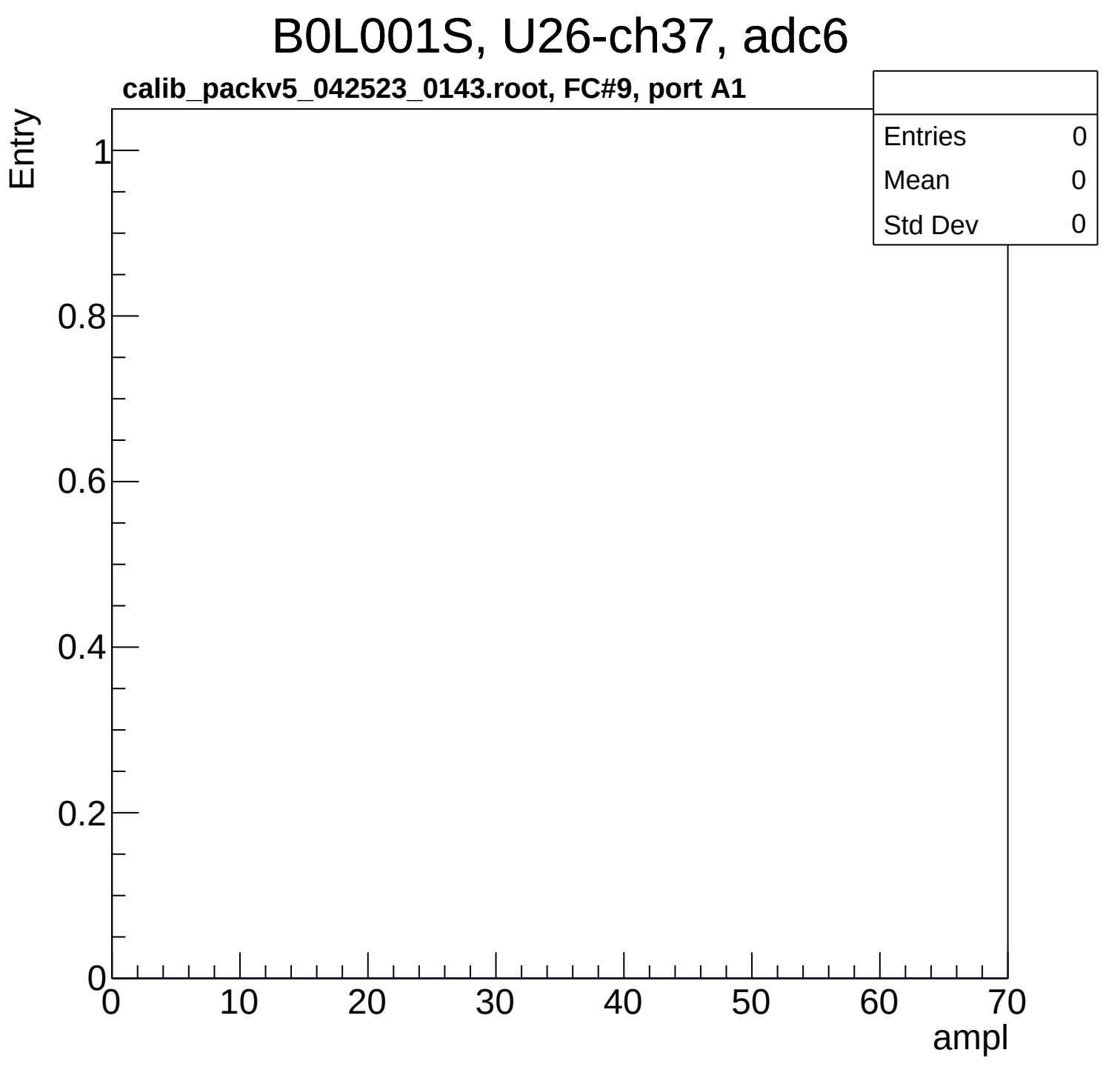
B0L001S, U26-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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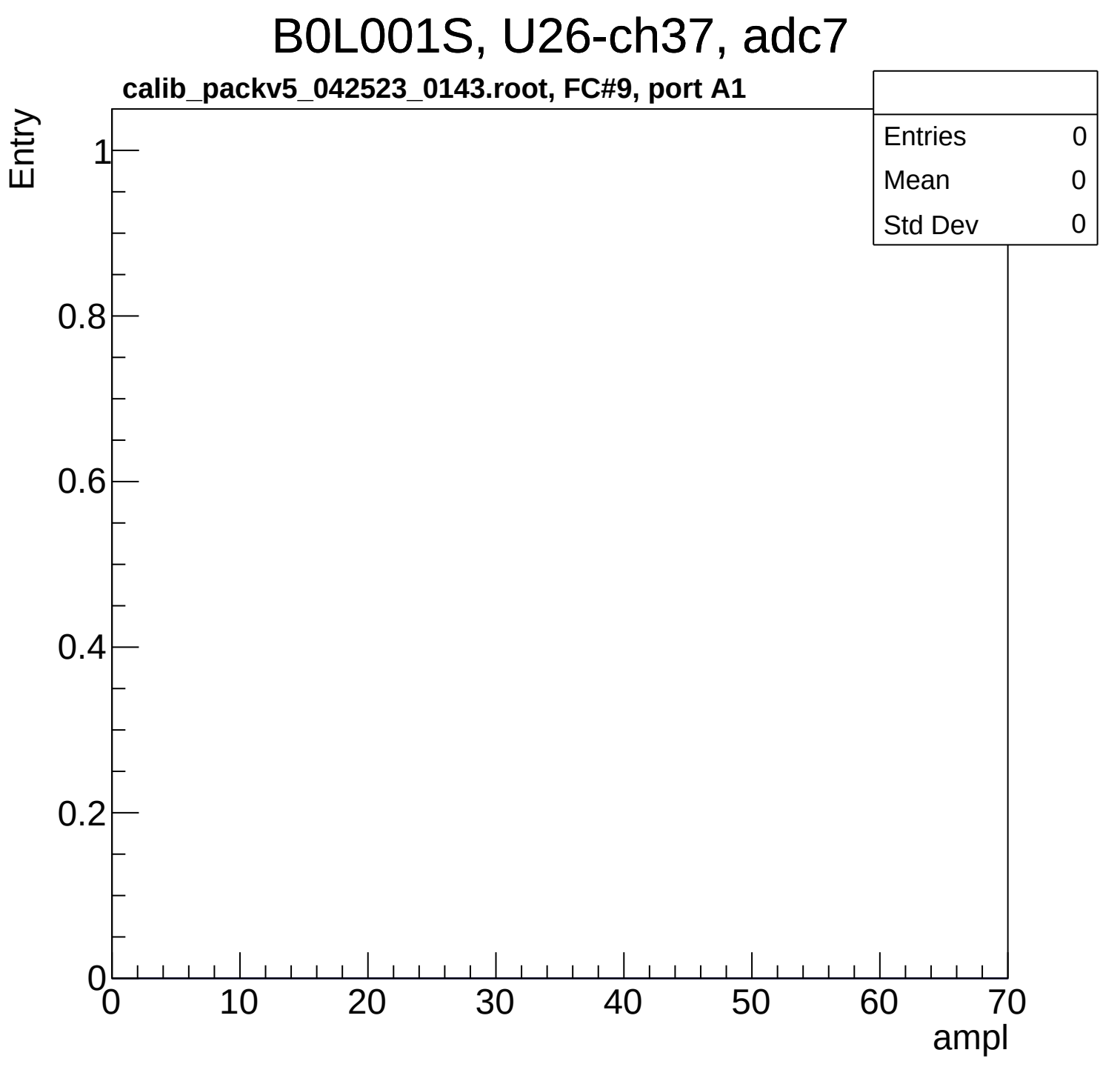
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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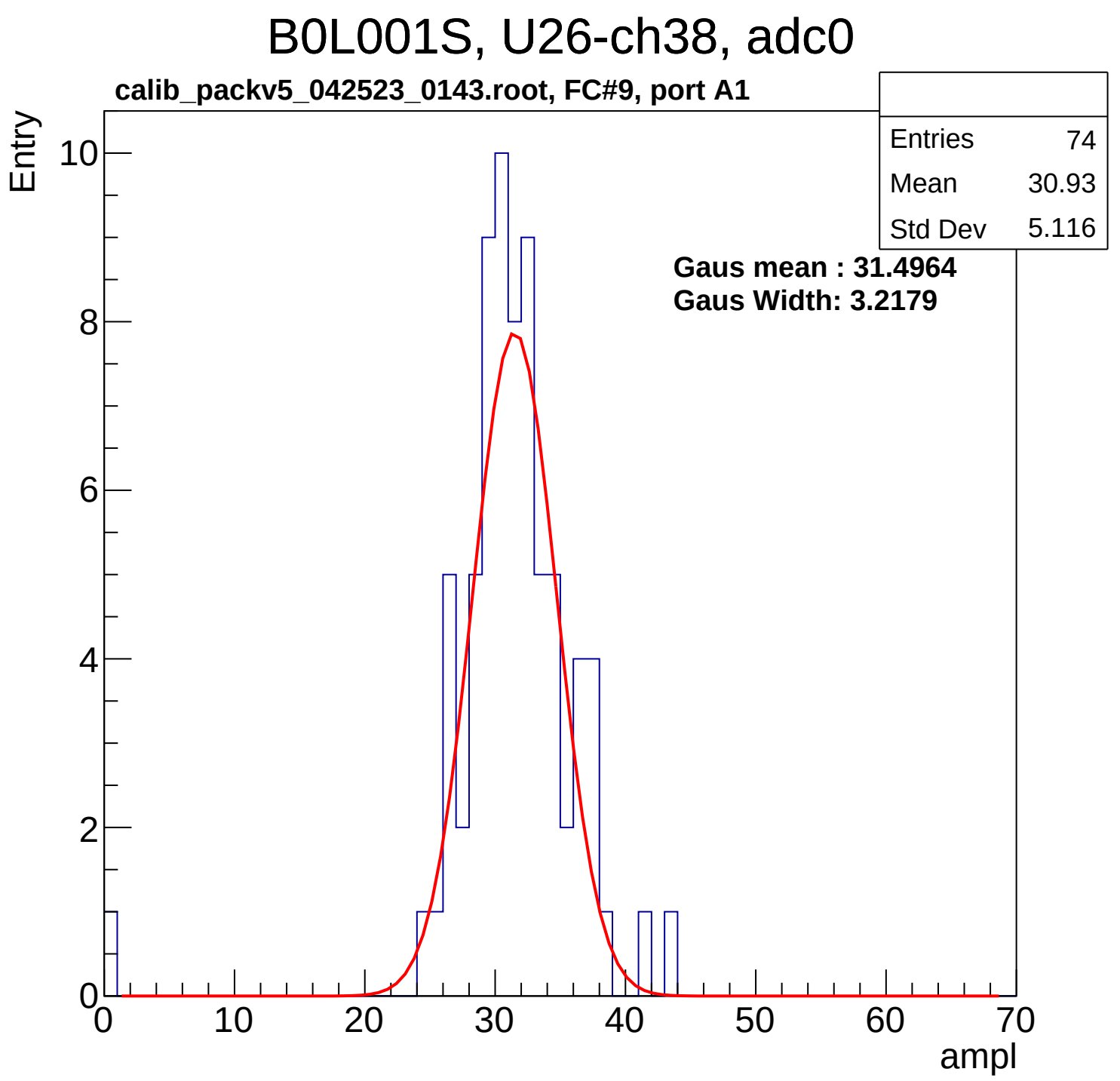
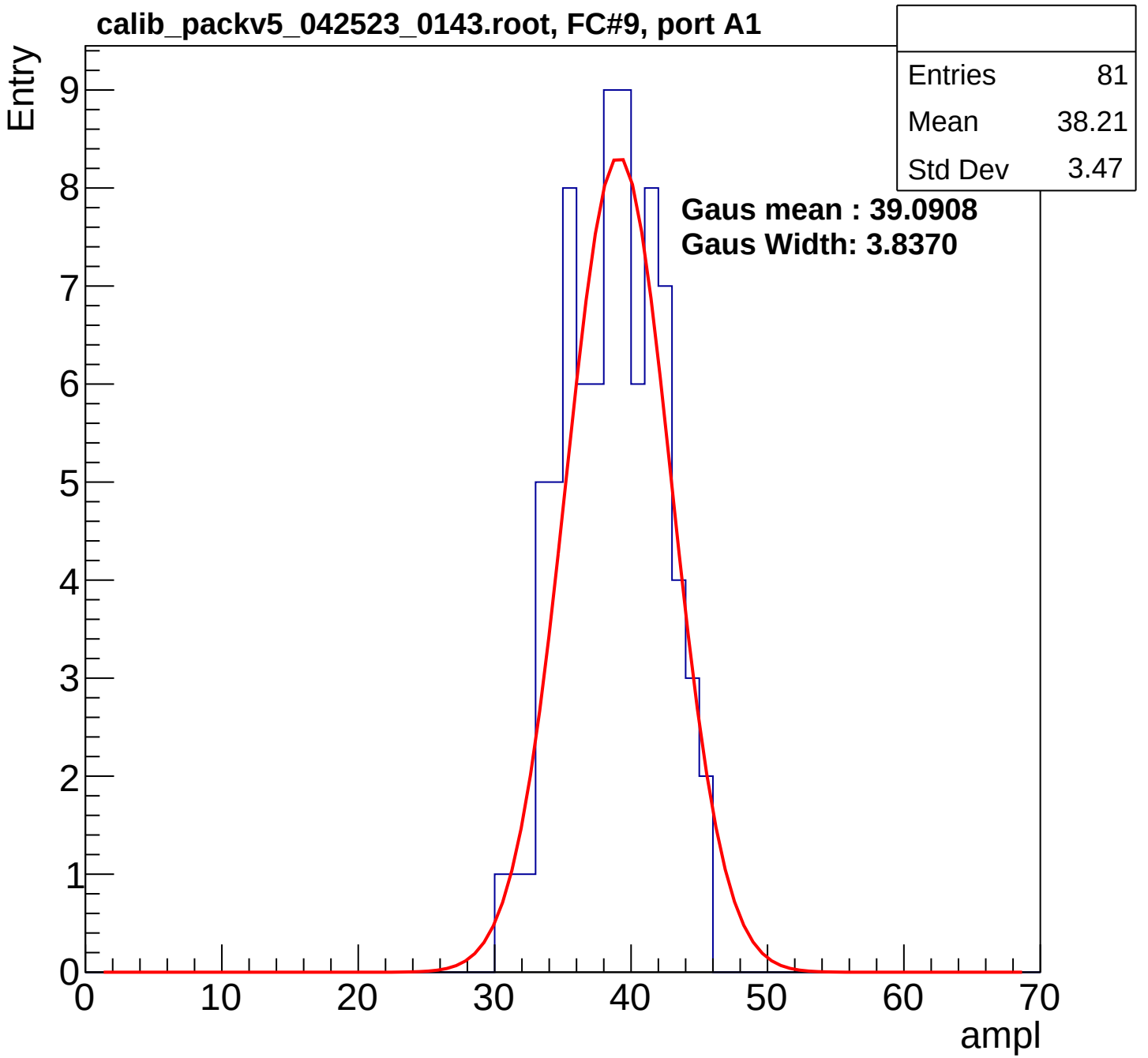
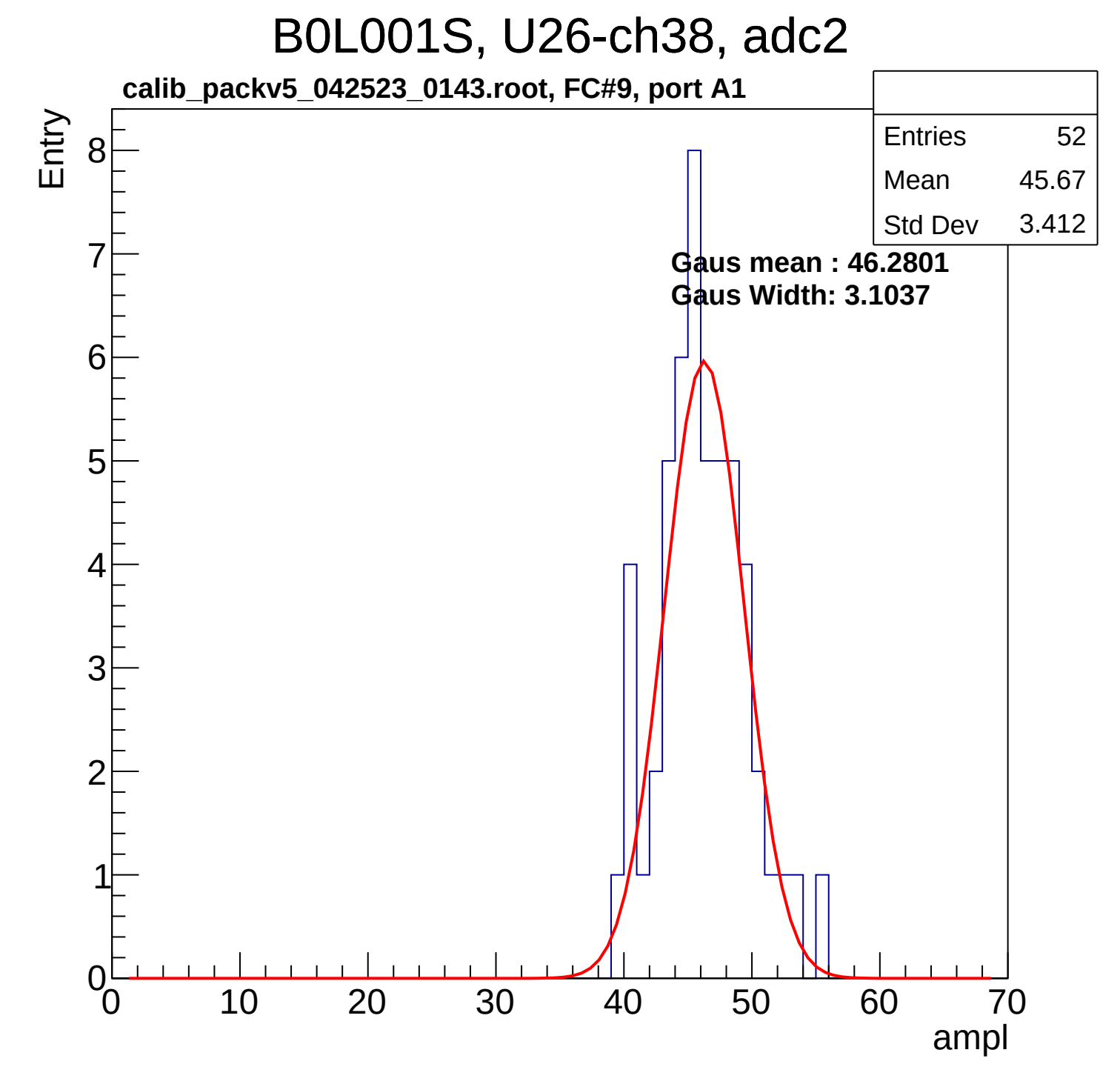
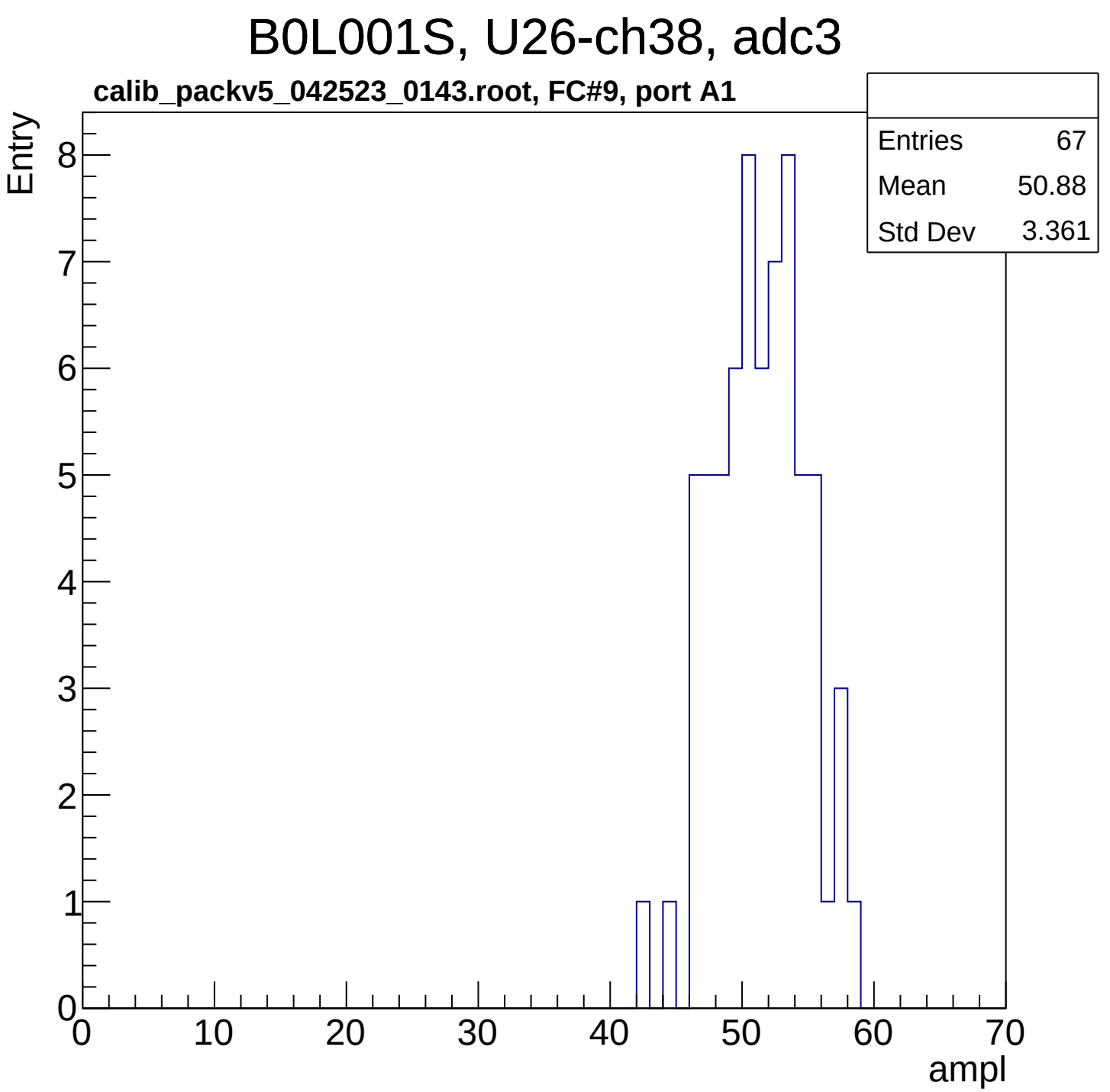
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.93
Std Dev 5.116
Gaus mean : 31.4964
Gaus Width: 3.2179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 38.21
Std Dev 3.47
Gaus mean : 39.0908
Gaus Width: 3.8370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 45.67
Std Dev 3.412
Gaus mean : 46.2801
Gaus Width: 3.1037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.88
Std Dev 3.361
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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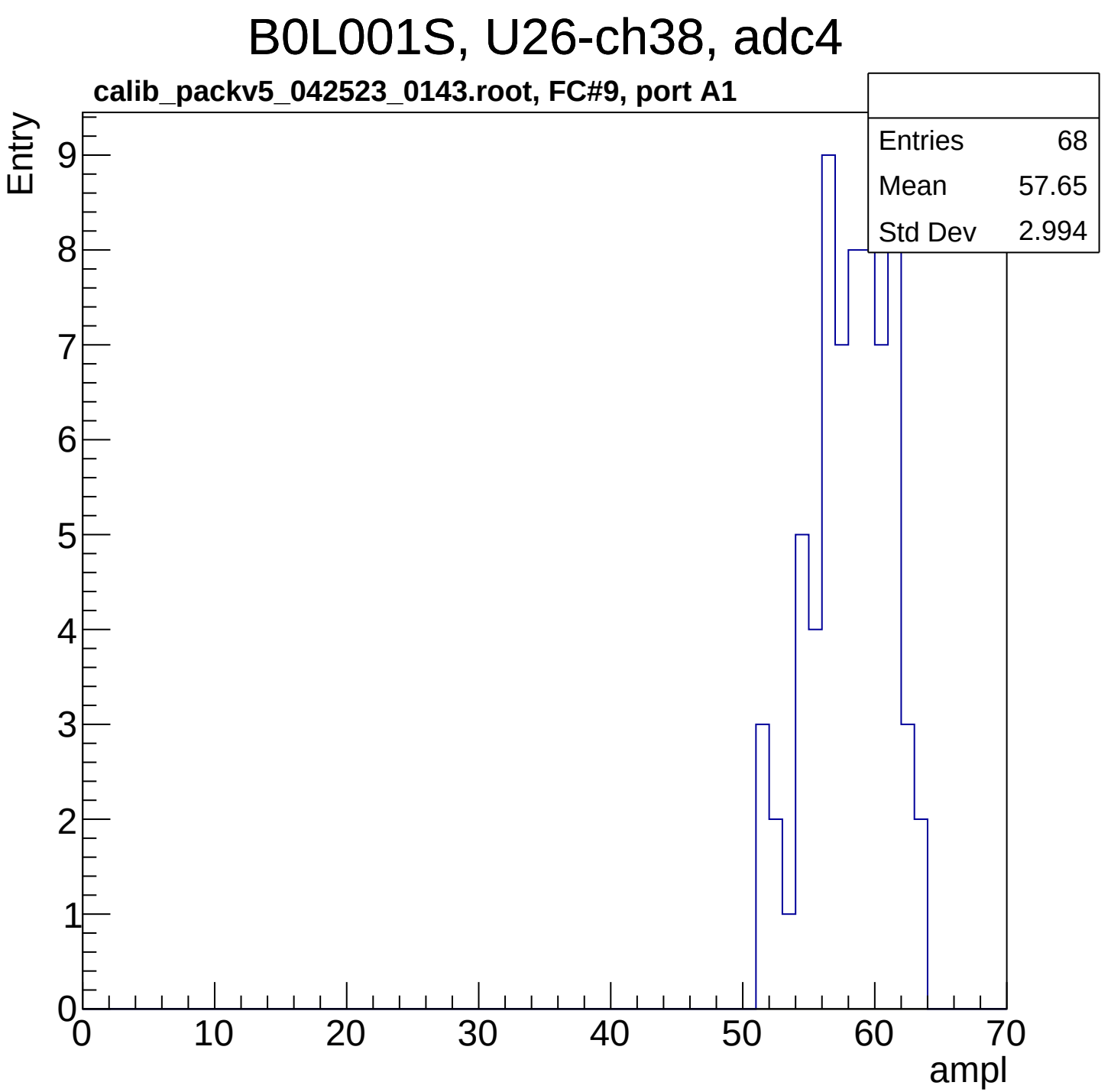
B0L001S, U26-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.65
Std Dev 2.994
Entry
ampl
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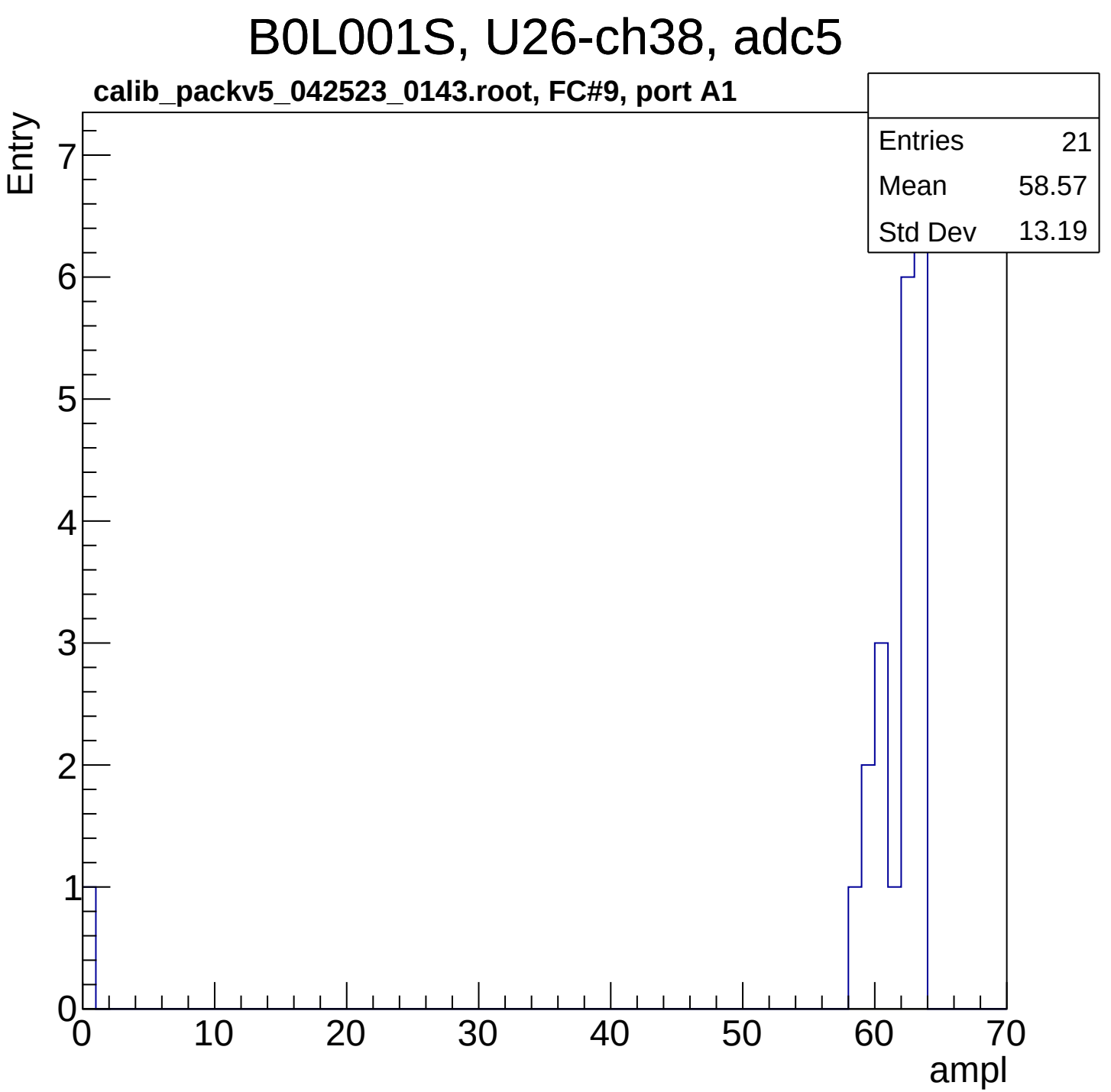
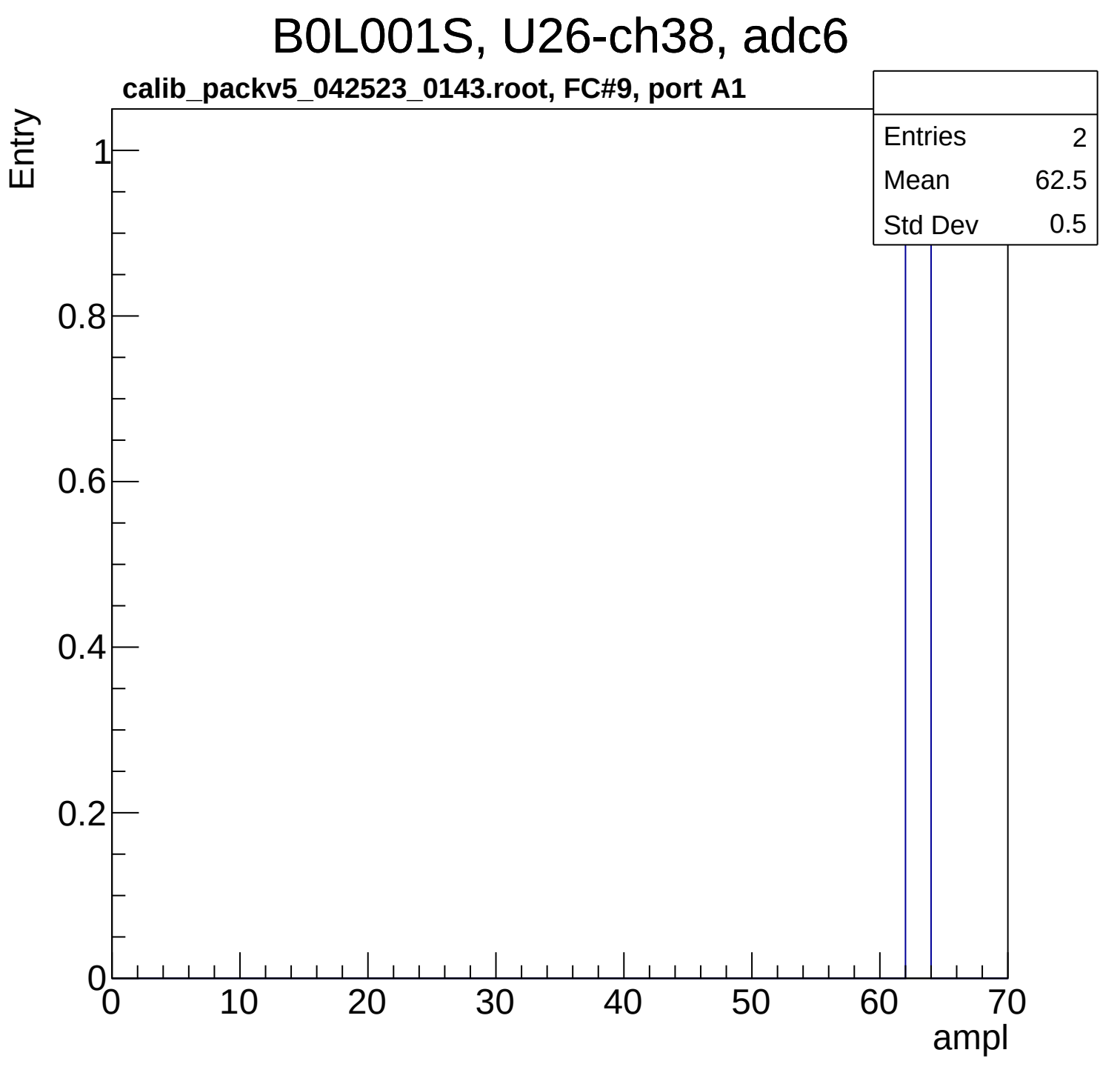
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.57
Std Dev 13.19
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
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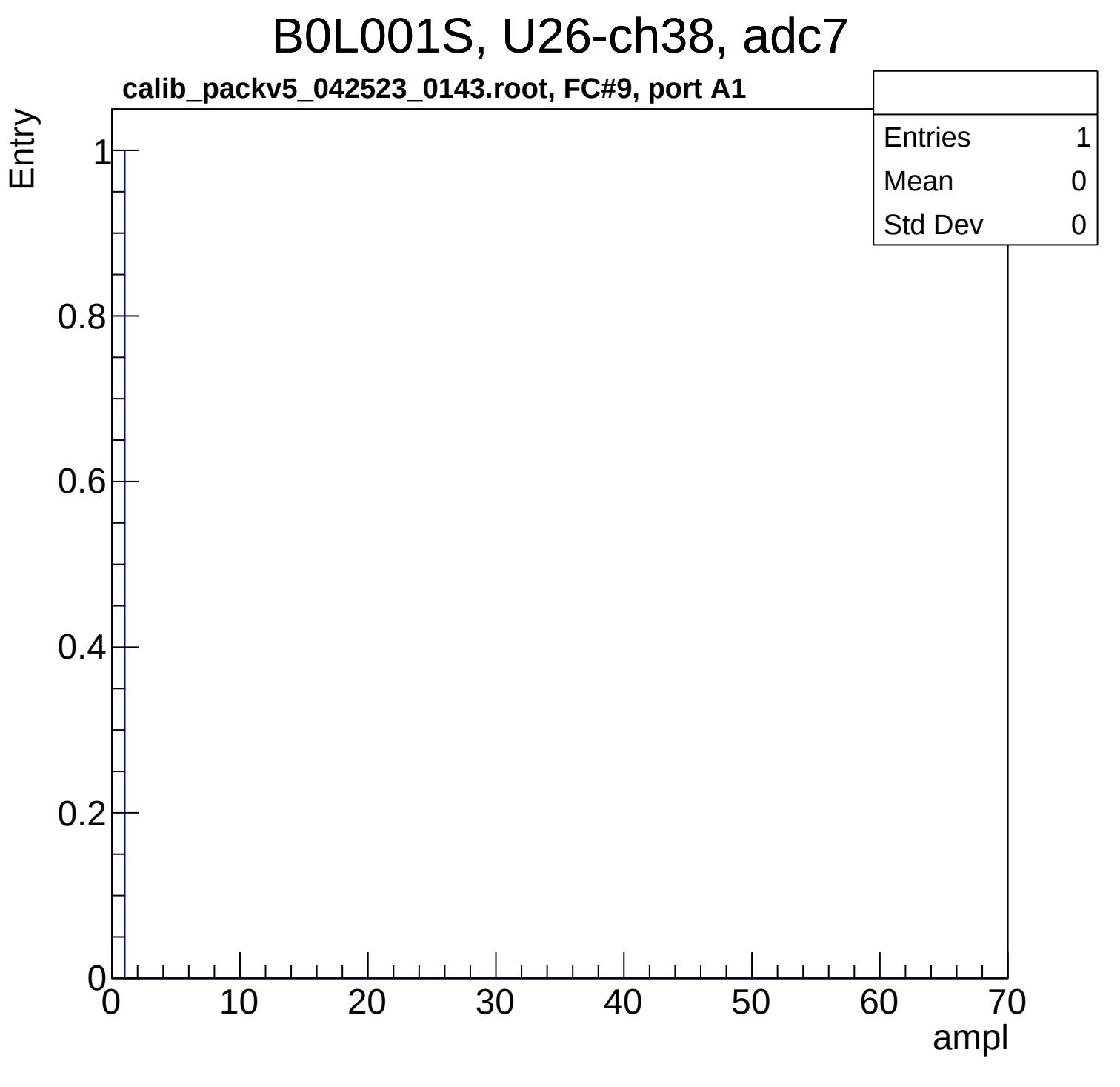
70
ampl


B0L001S, U26-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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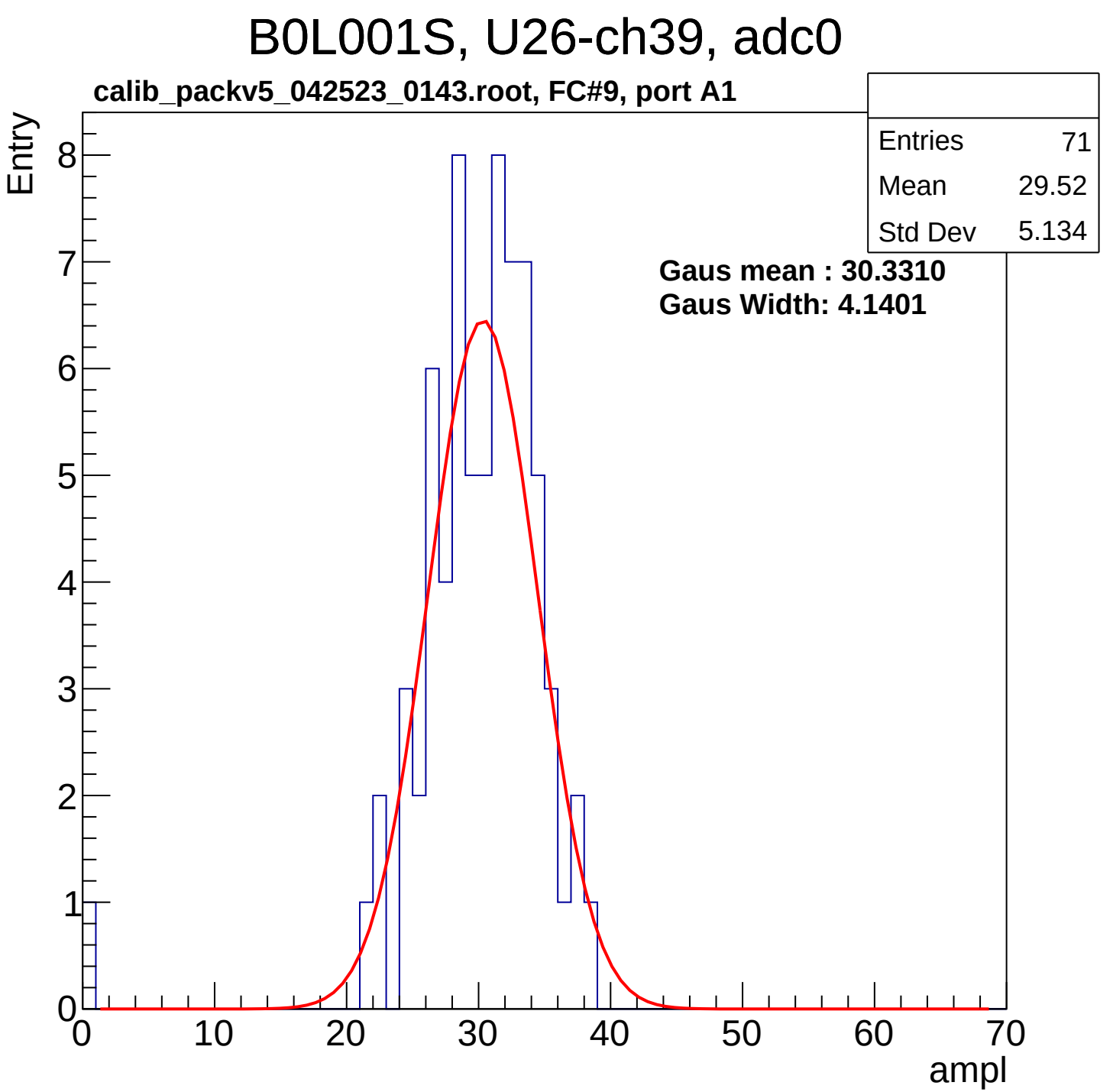
50
60
70
ampl


B0L001S, U26-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.52
Std Dev 5.134
Gaus mean : 30.3310
Gaus Width: 4.1401
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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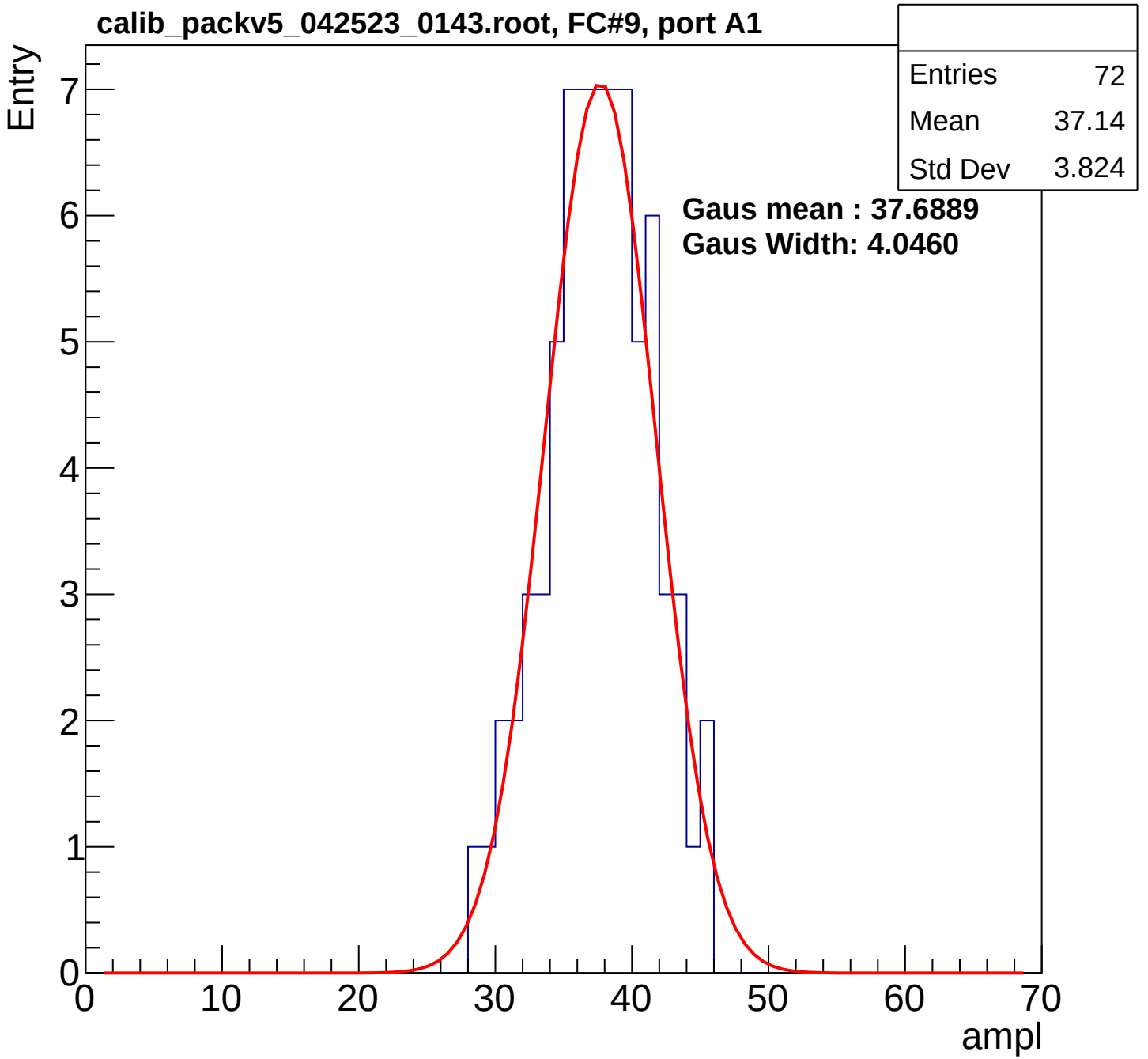
B0L001S, U26-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.14
Std Dev 3.824
Gaus mean : 37.6889
Gaus Width: 4.0460
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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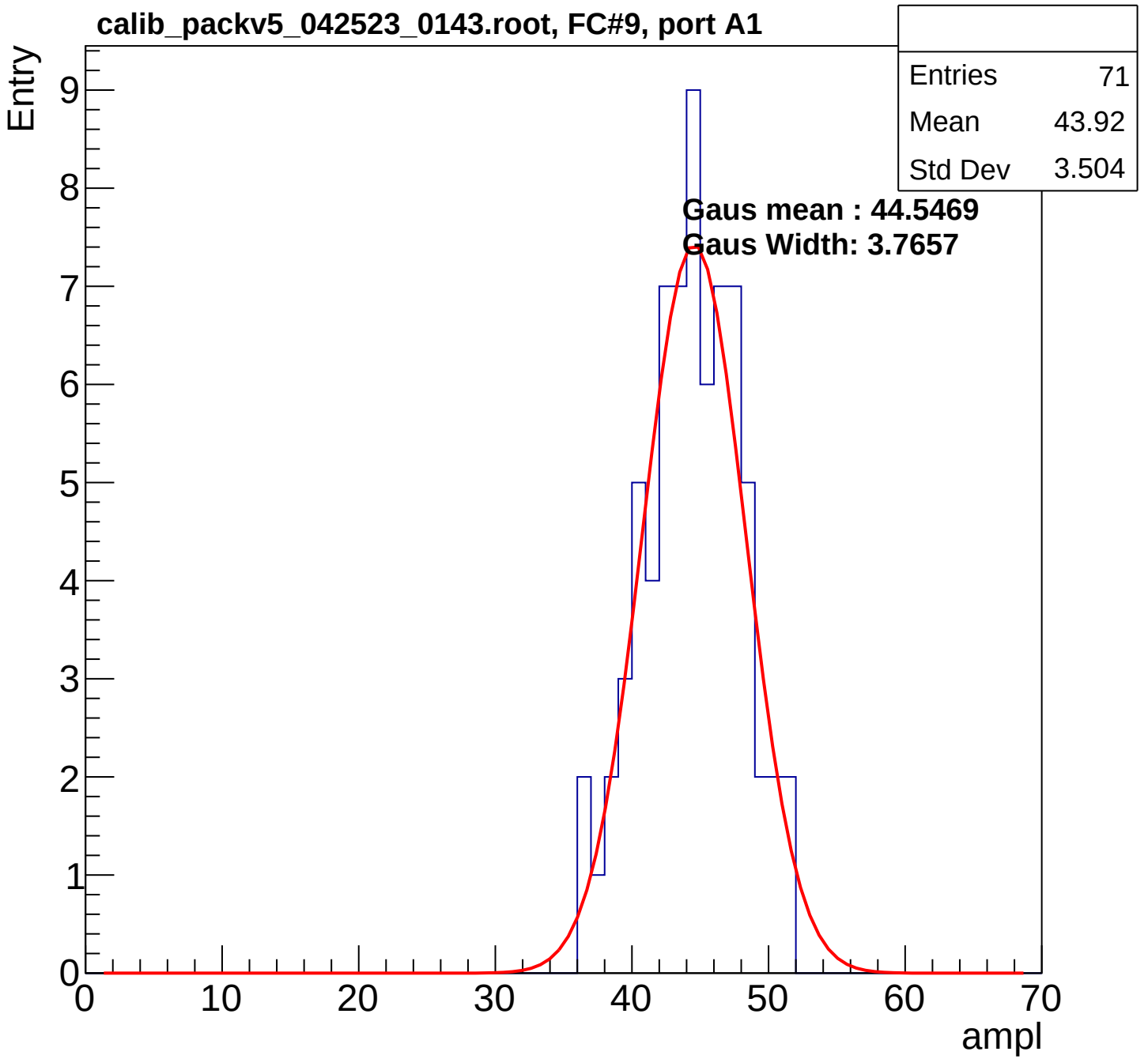
B0L001S, U26-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.92
Std Dev 3.504
Gaus mean : 44.5469
Gaus Width: 3.7657
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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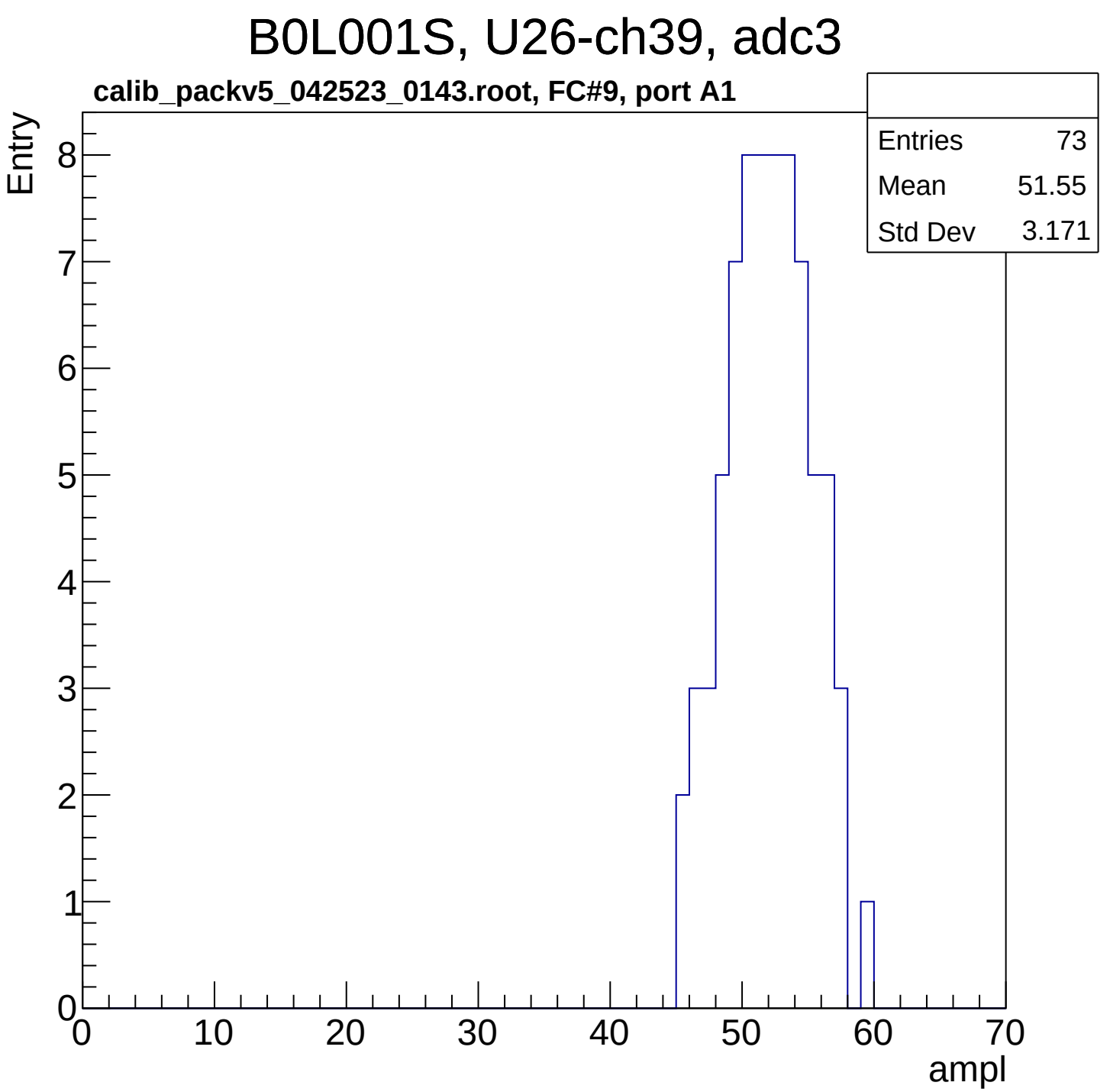
B0L001S, U26-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 51.55
Std Dev 3.171
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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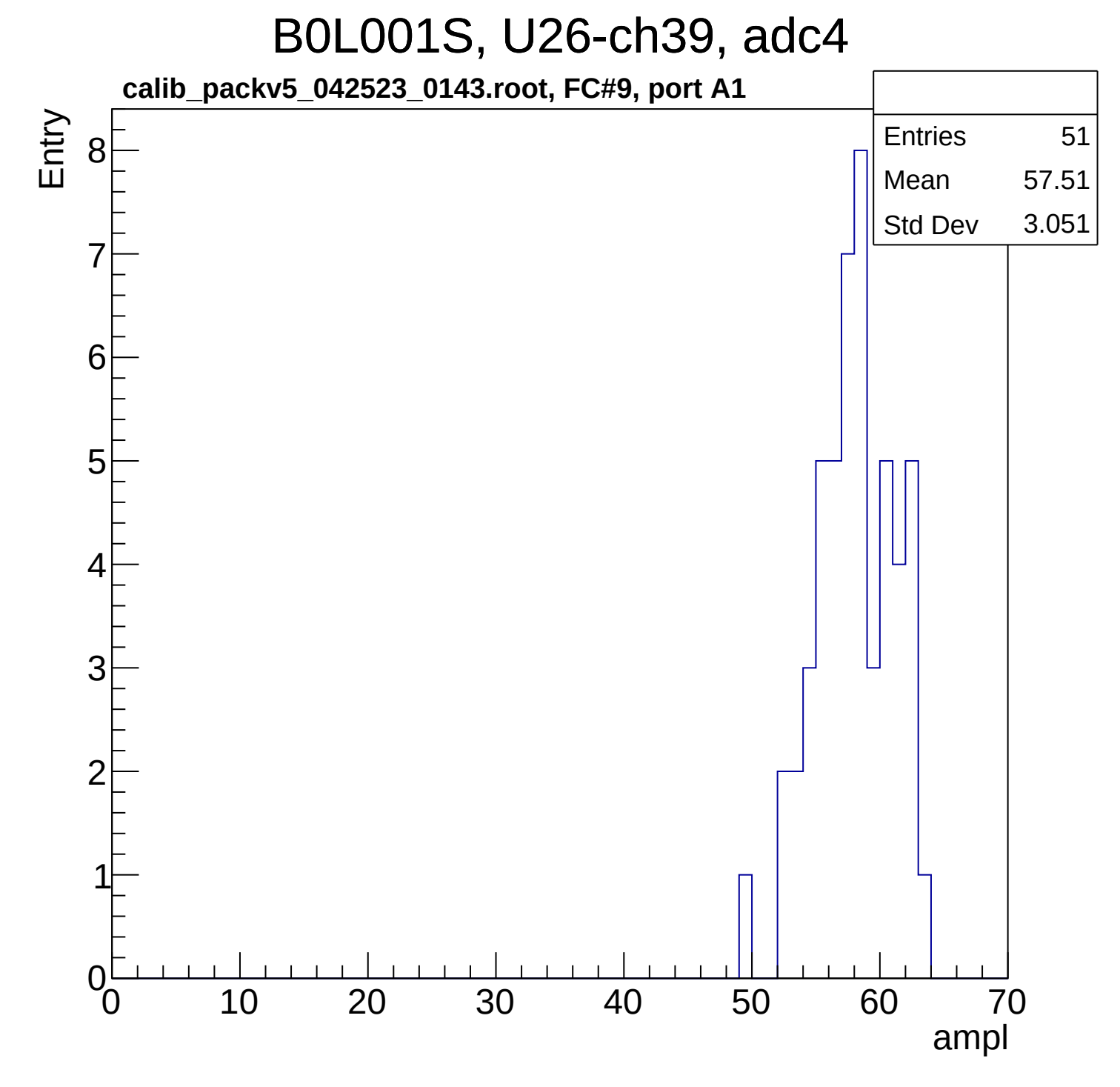
B0L001S, U26-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.51
Std Dev 3.051
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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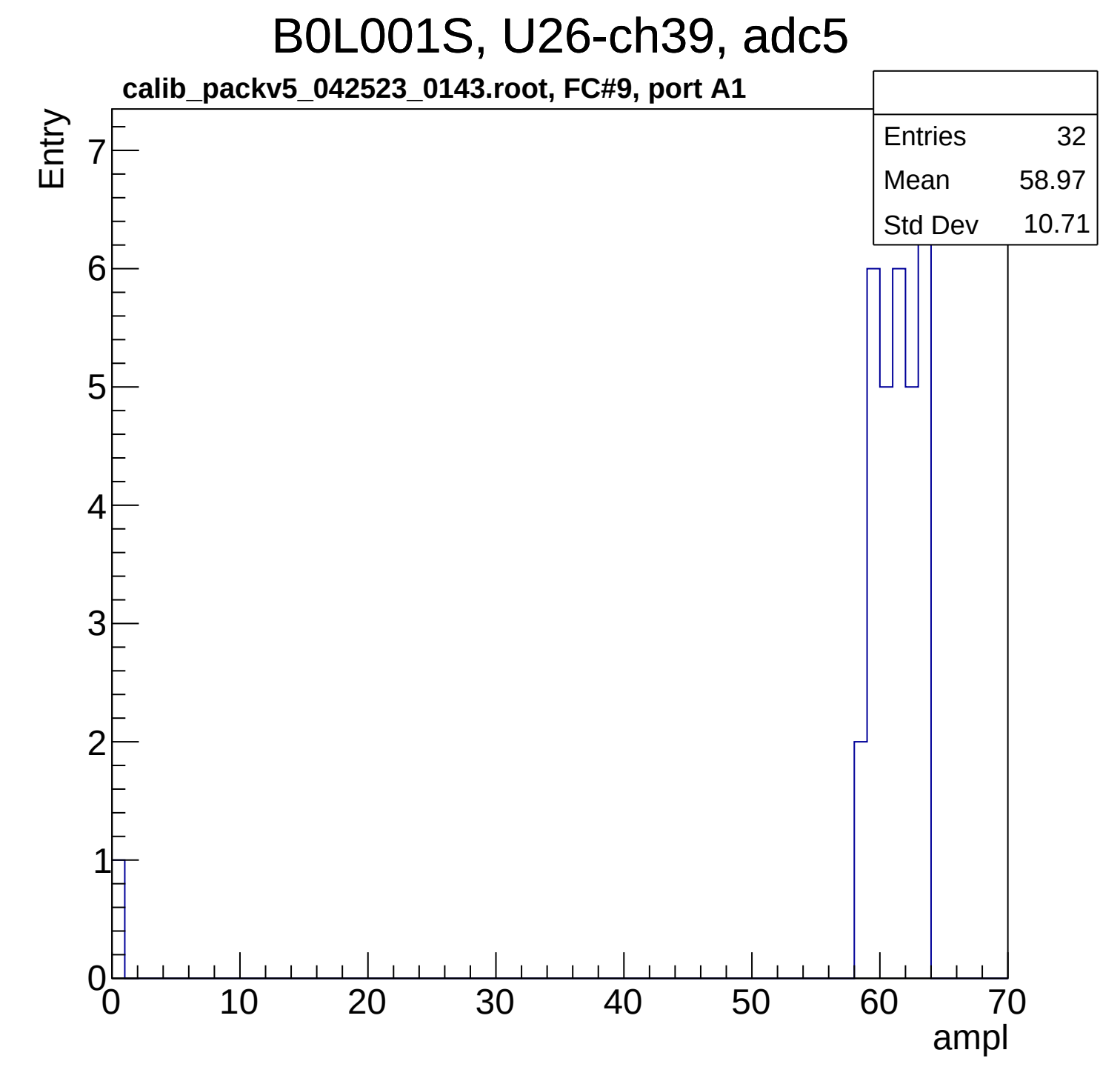
B0L001S, U26-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.97
Std Dev 10.71
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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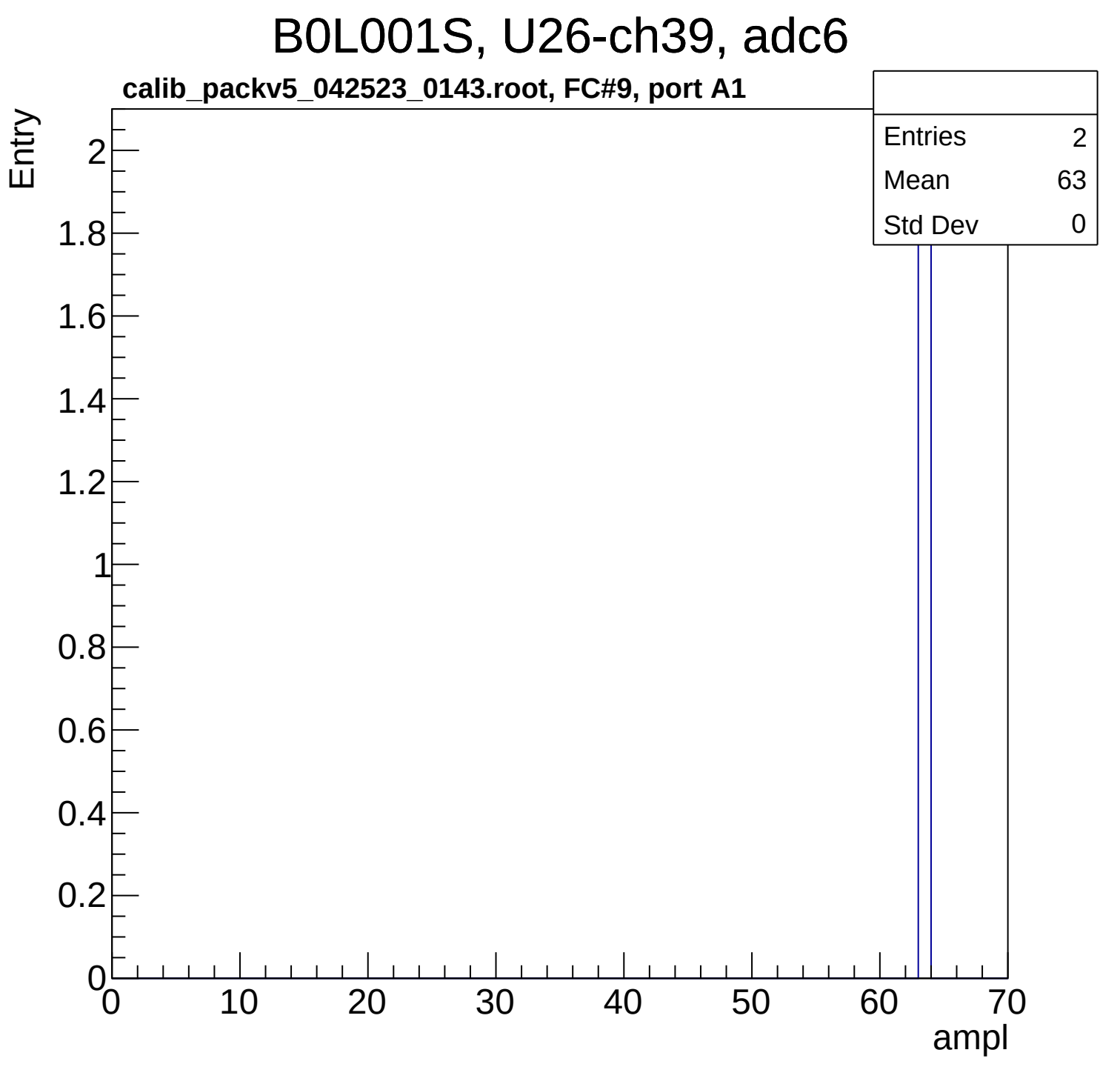
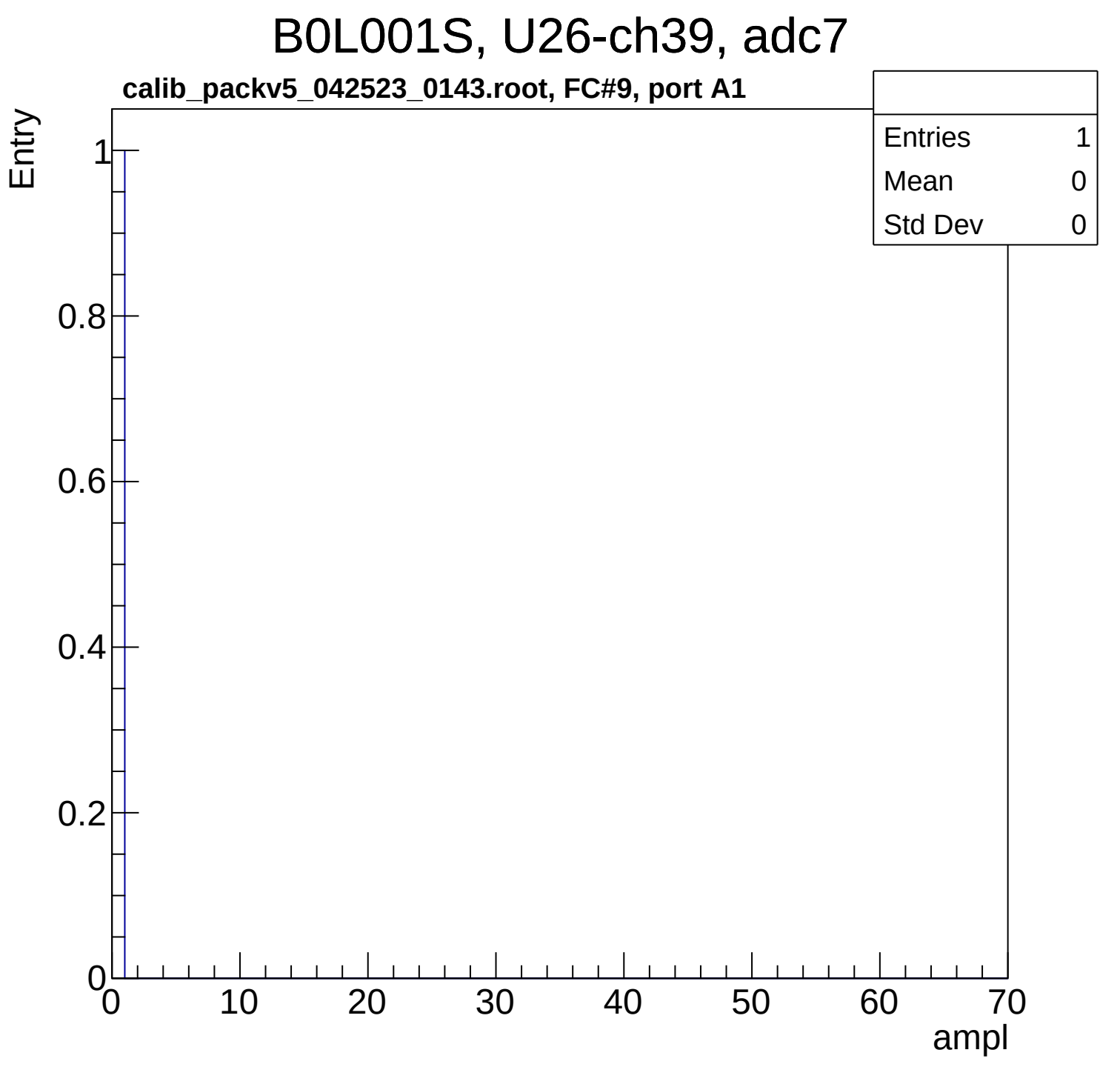
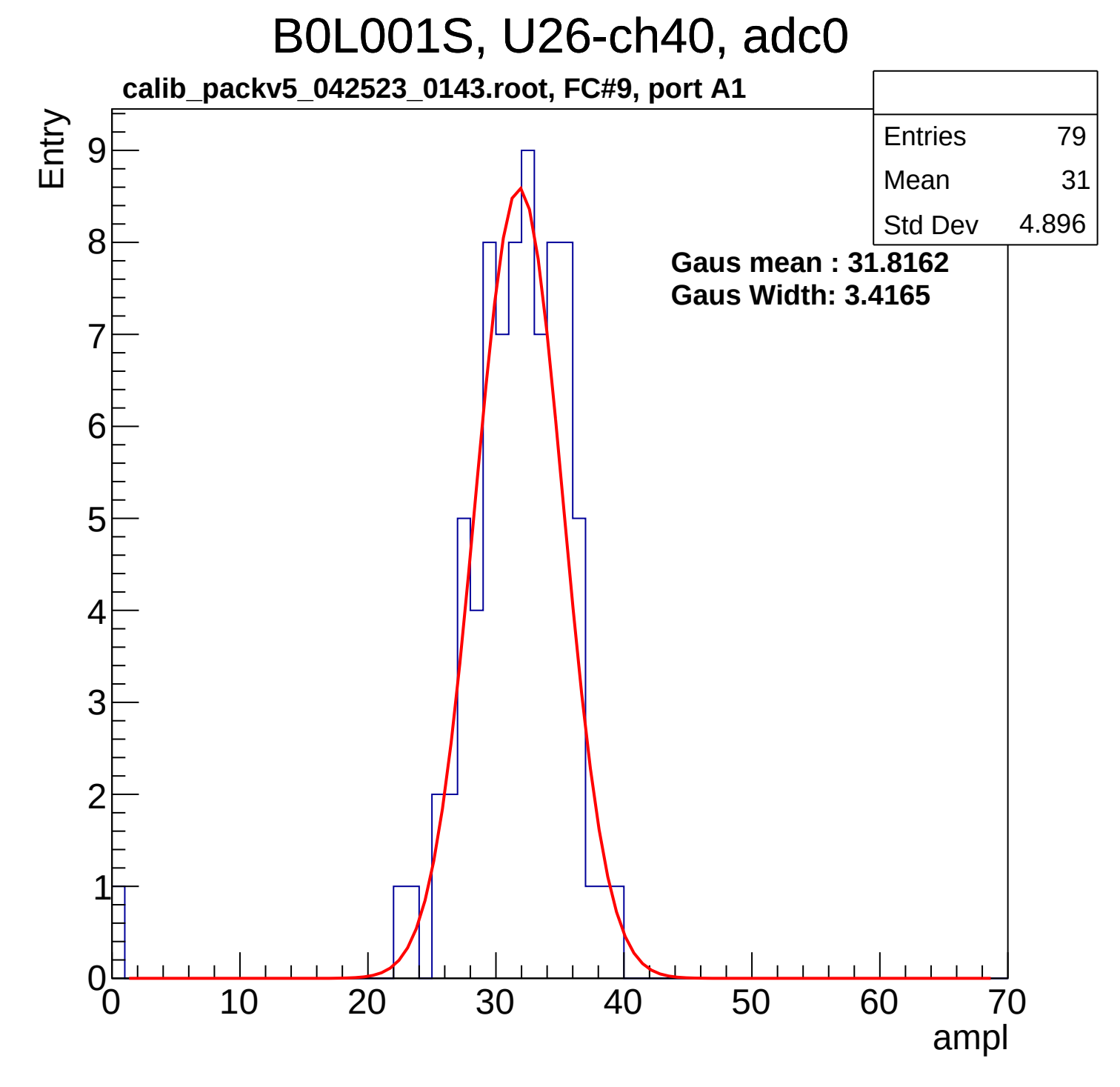
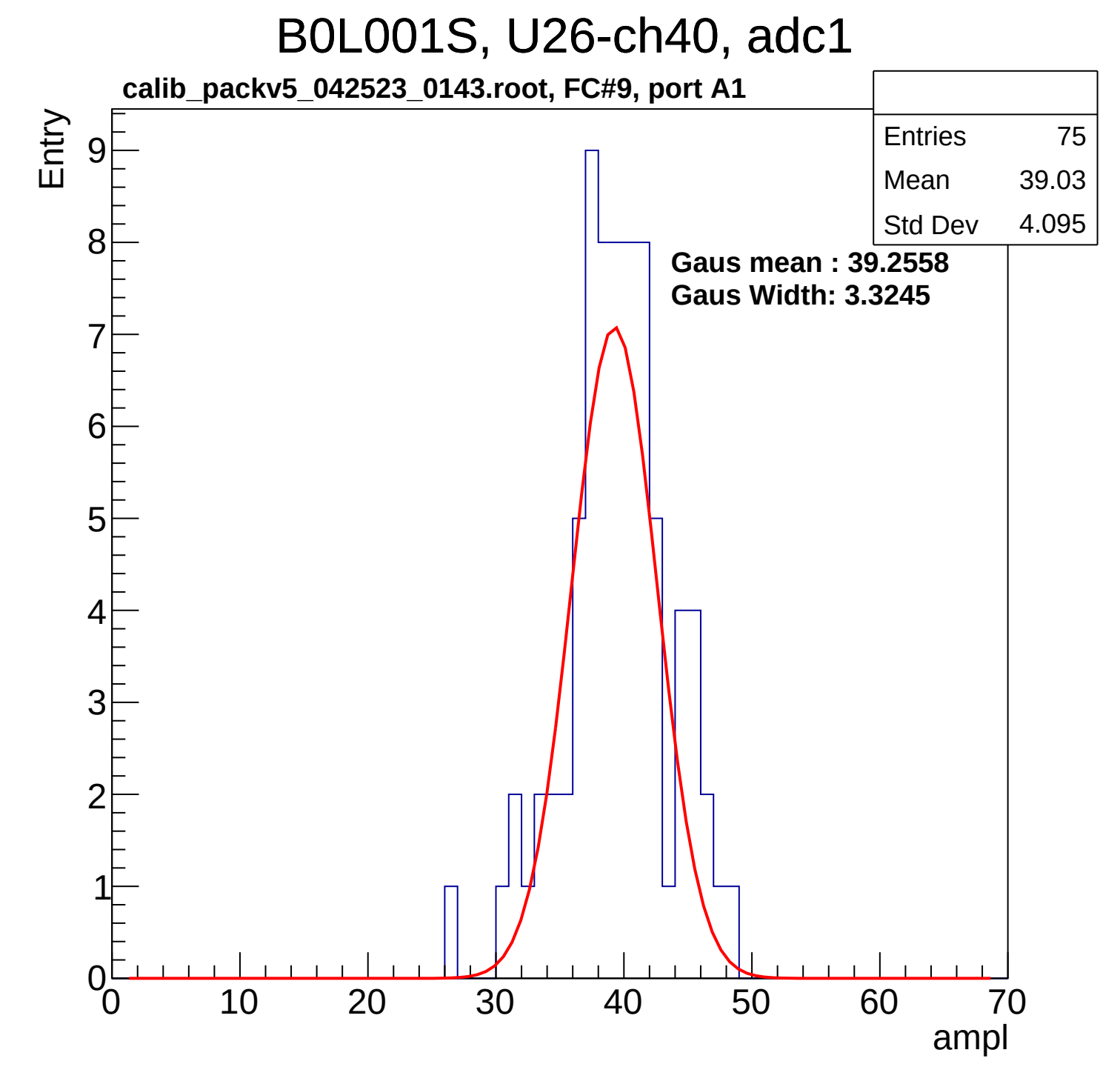

B0L001S, U26-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 31
Std Dev 4.896
Gaus mean : 31.8162
Gaus Width: 3.4165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 39.03
Std Dev 4.095
Gaus mean : 39.2558
Gaus Width: 3.3245
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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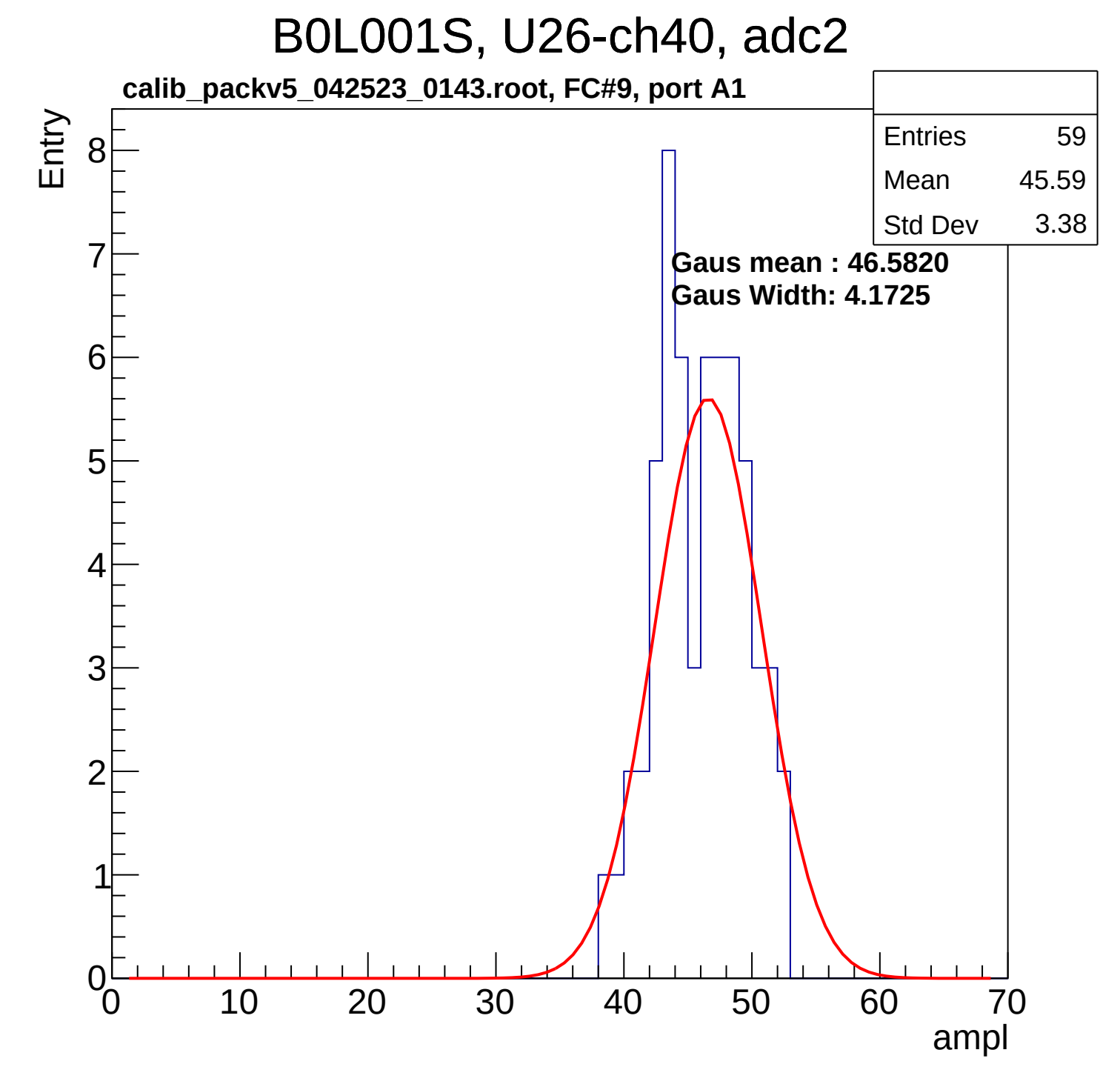
B0L001S, U26-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.59
Std Dev 3.38
Gaus mean : 46.5820
Gaus Width: 4.1725
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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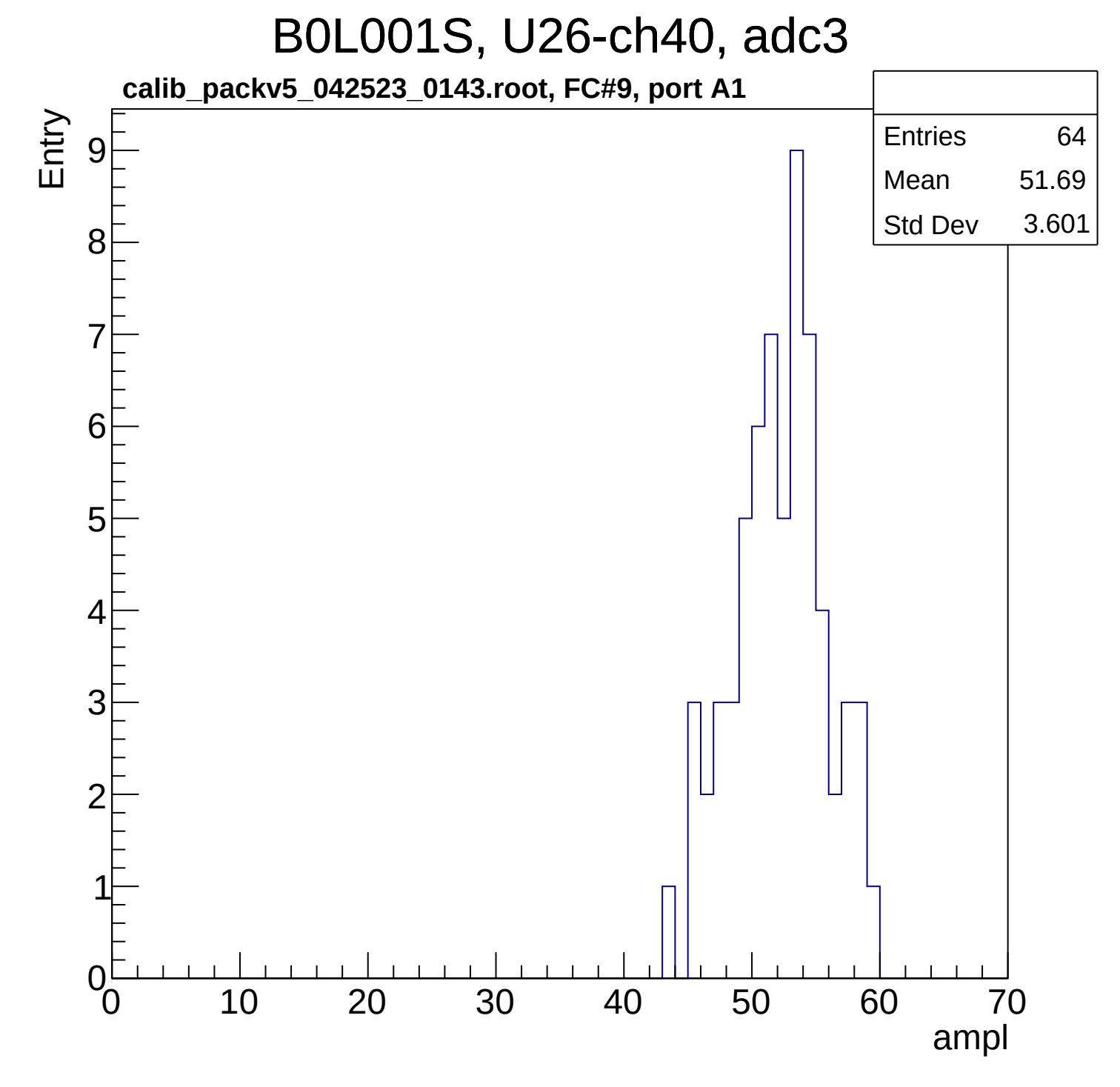
B0L001S, U26-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.69
Std Dev 3.601
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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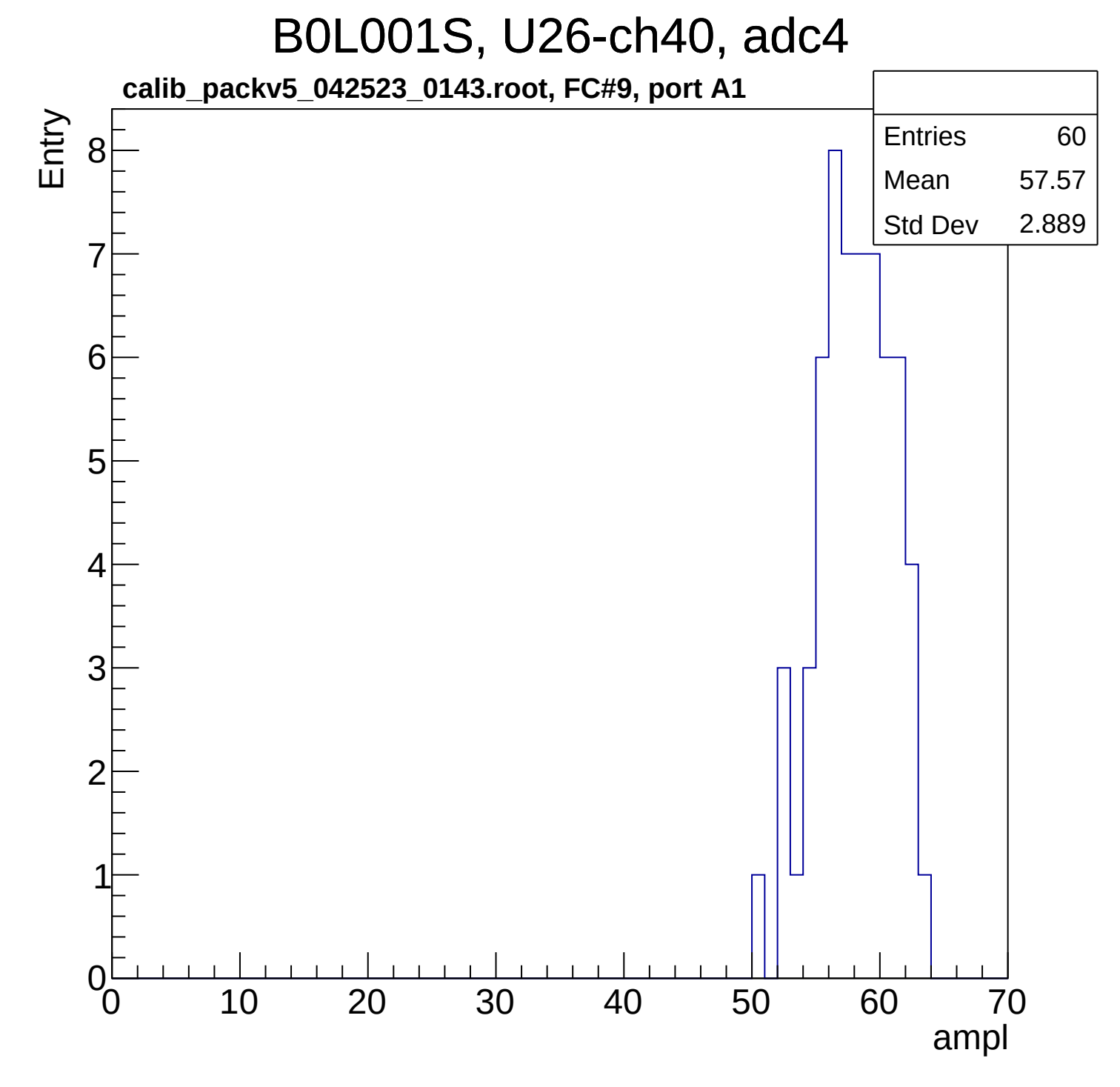
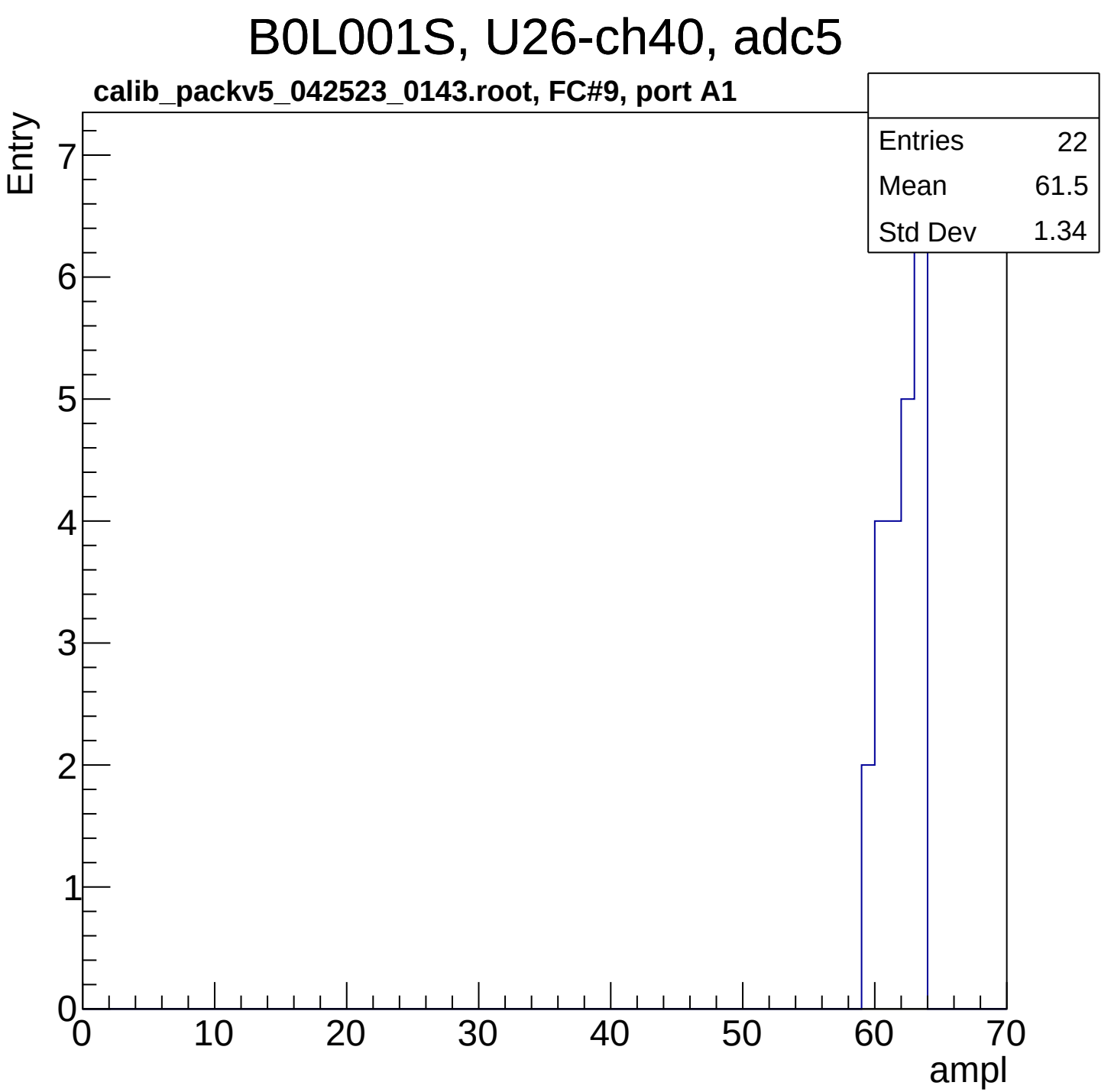

B0L001S, U26-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.57
Std Dev 2.889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 61.5
Std Dev 1.34
Entry
ampl
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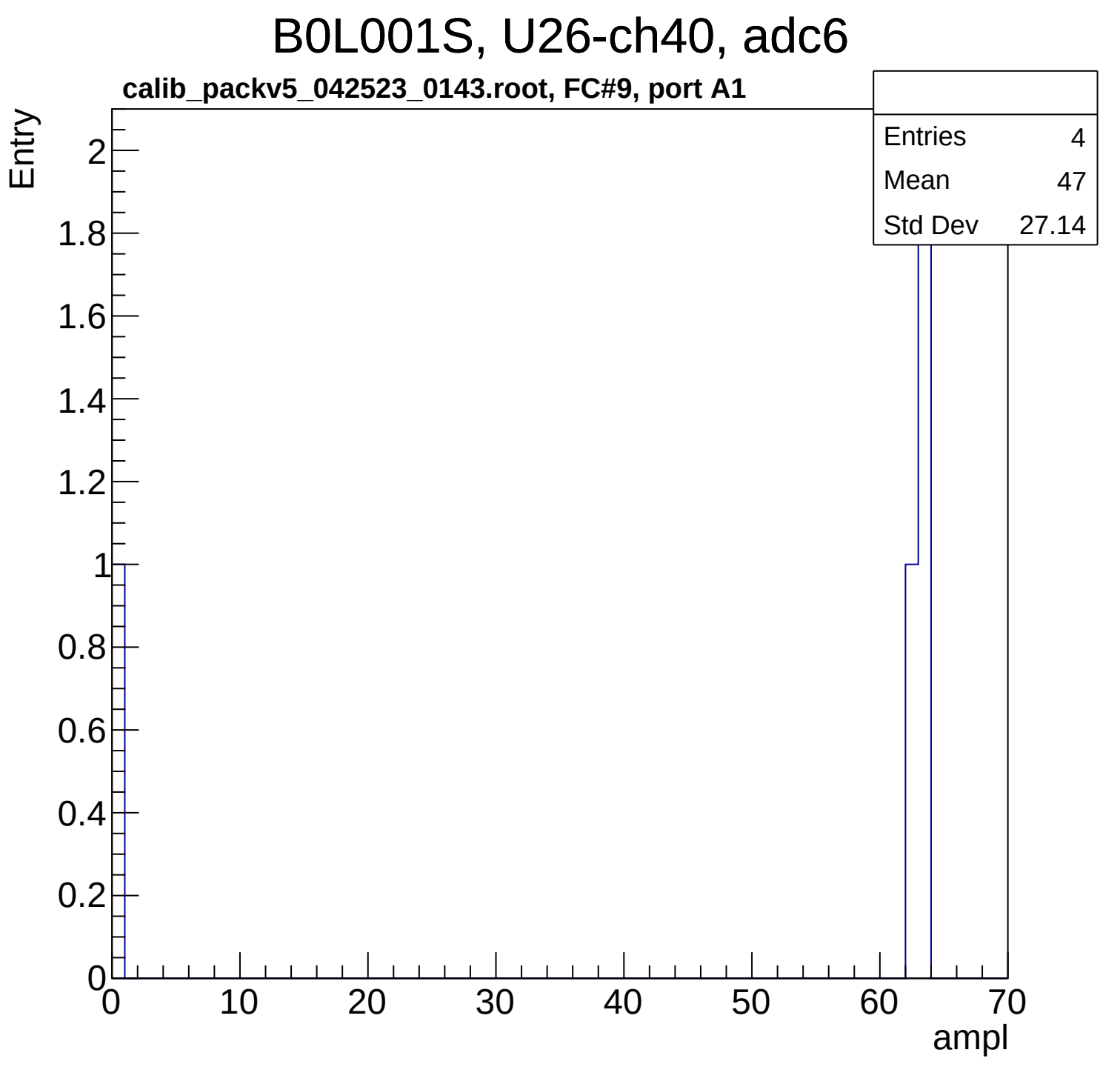
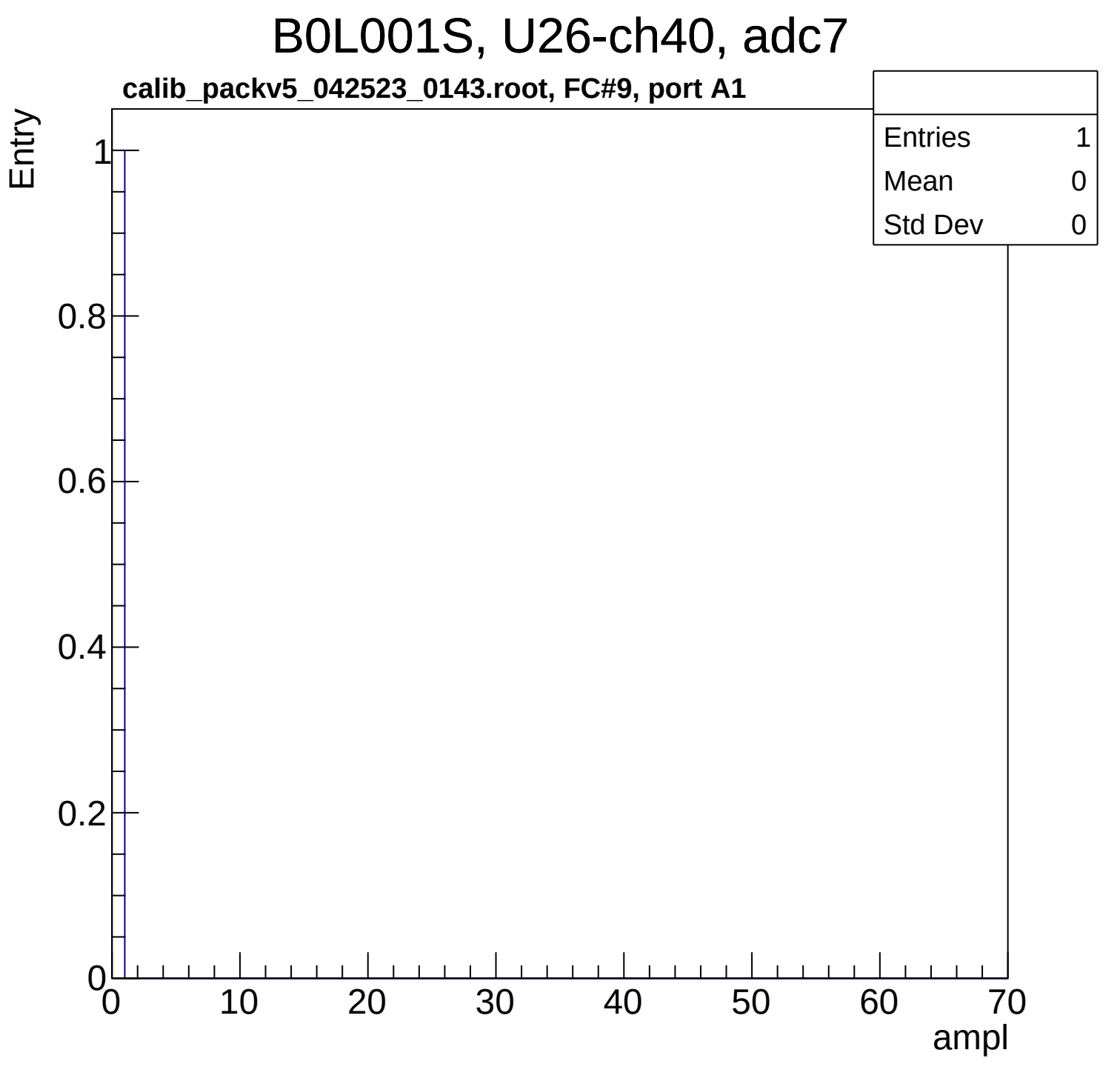
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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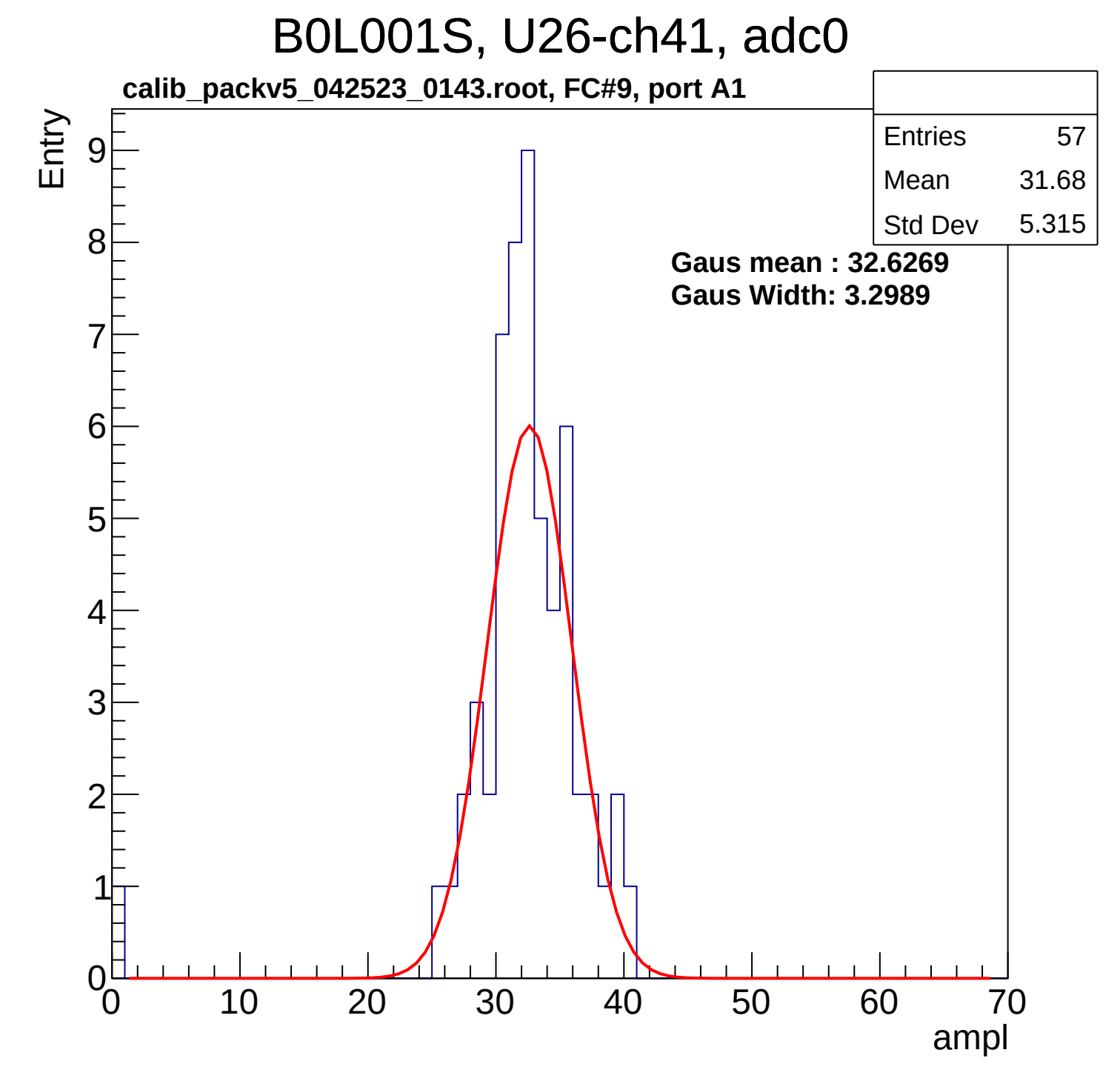
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 31.68
Std Dev 5.315
Gaus mean : 32.6269
Gaus Width: 3.2989
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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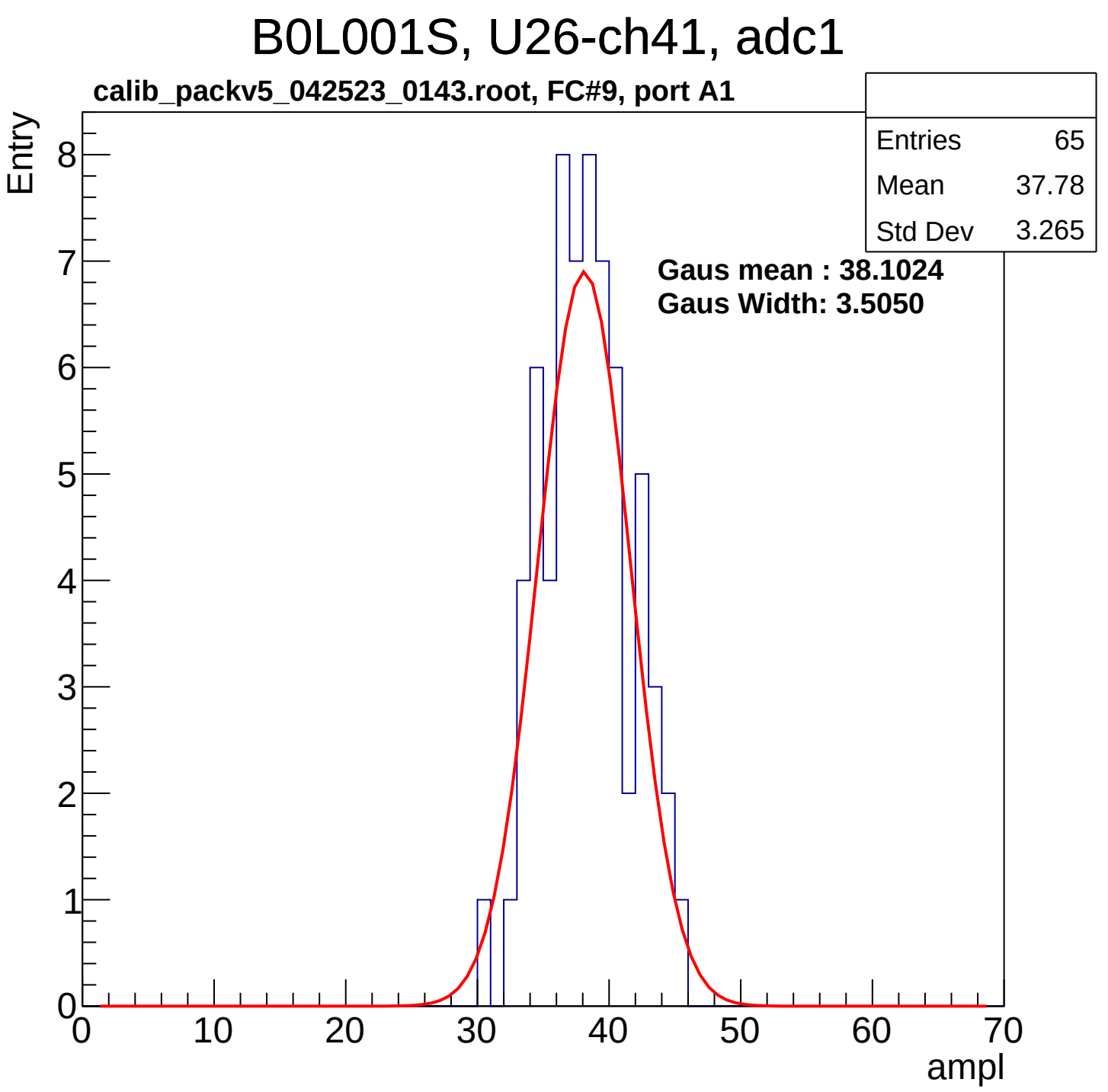
60
70


B0L001S, U26-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.78
Std Dev 3.265
Gaus mean : 38.1024
Gaus Width: 3.5050
Entry
ampl
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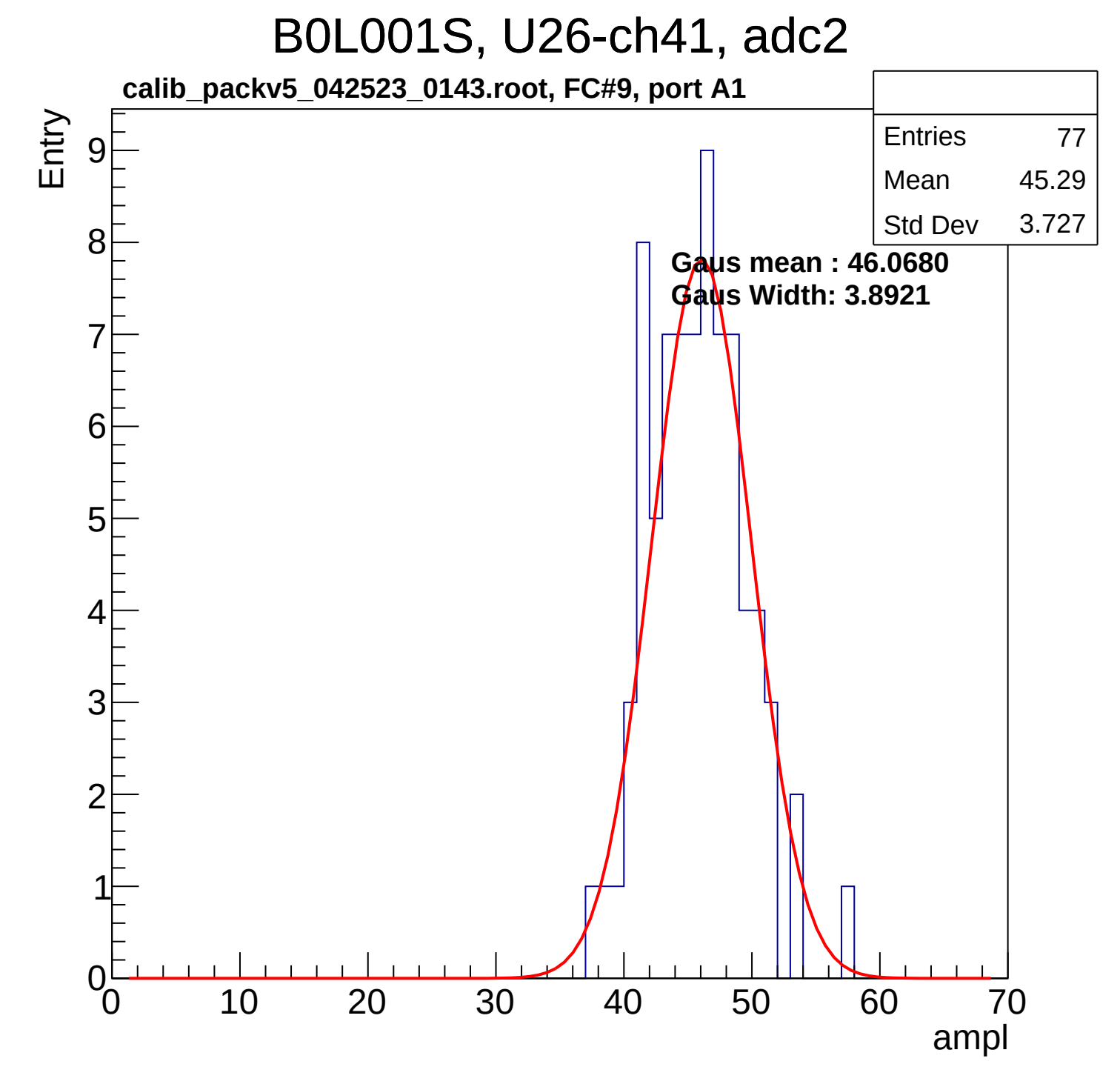
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 45.29
Std Dev 3.727
Gaus mean : 46.0680
Gaus Width: 3.8921
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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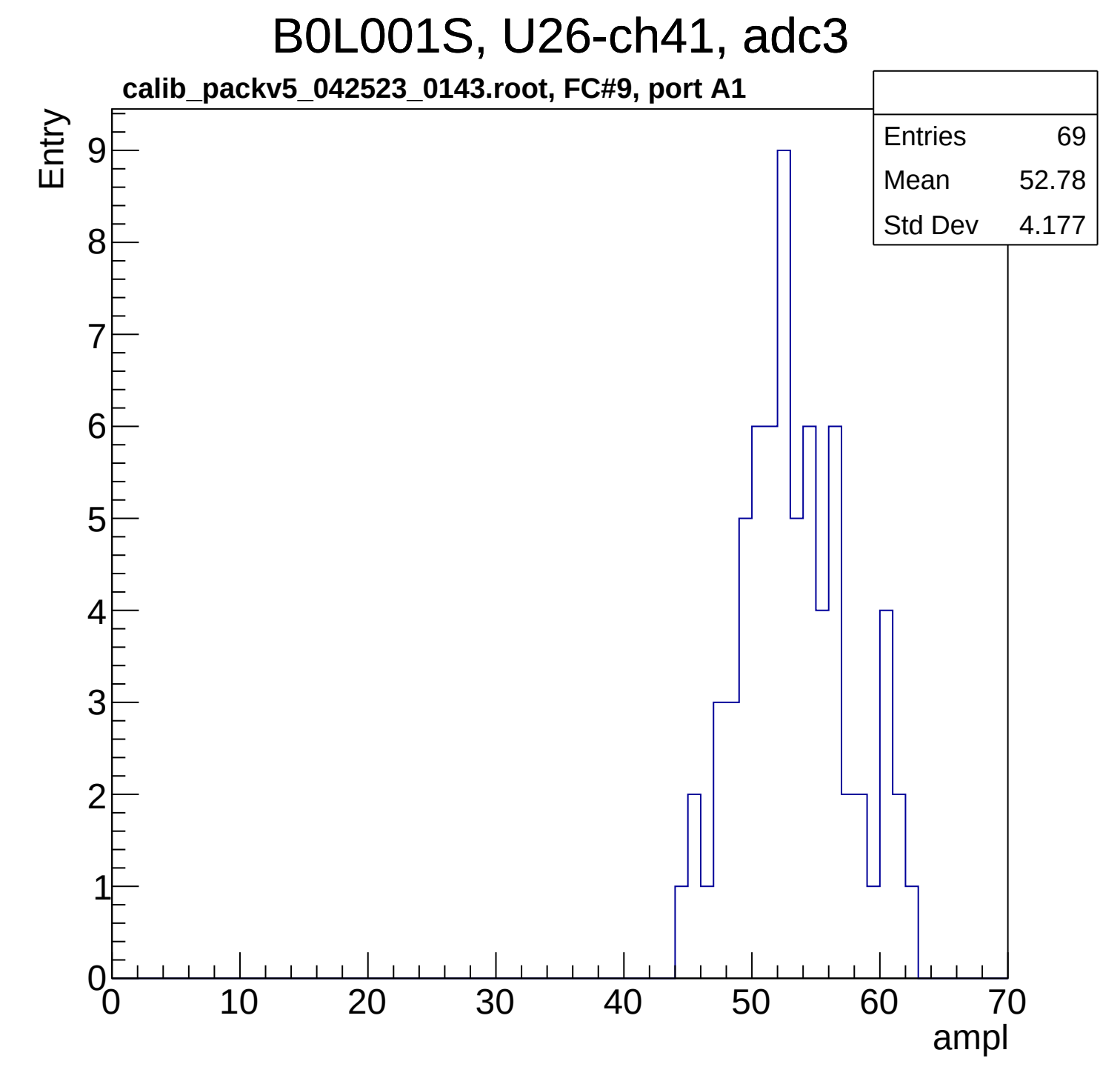
B0L001S, U26-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 52.78
Std Dev 4.177
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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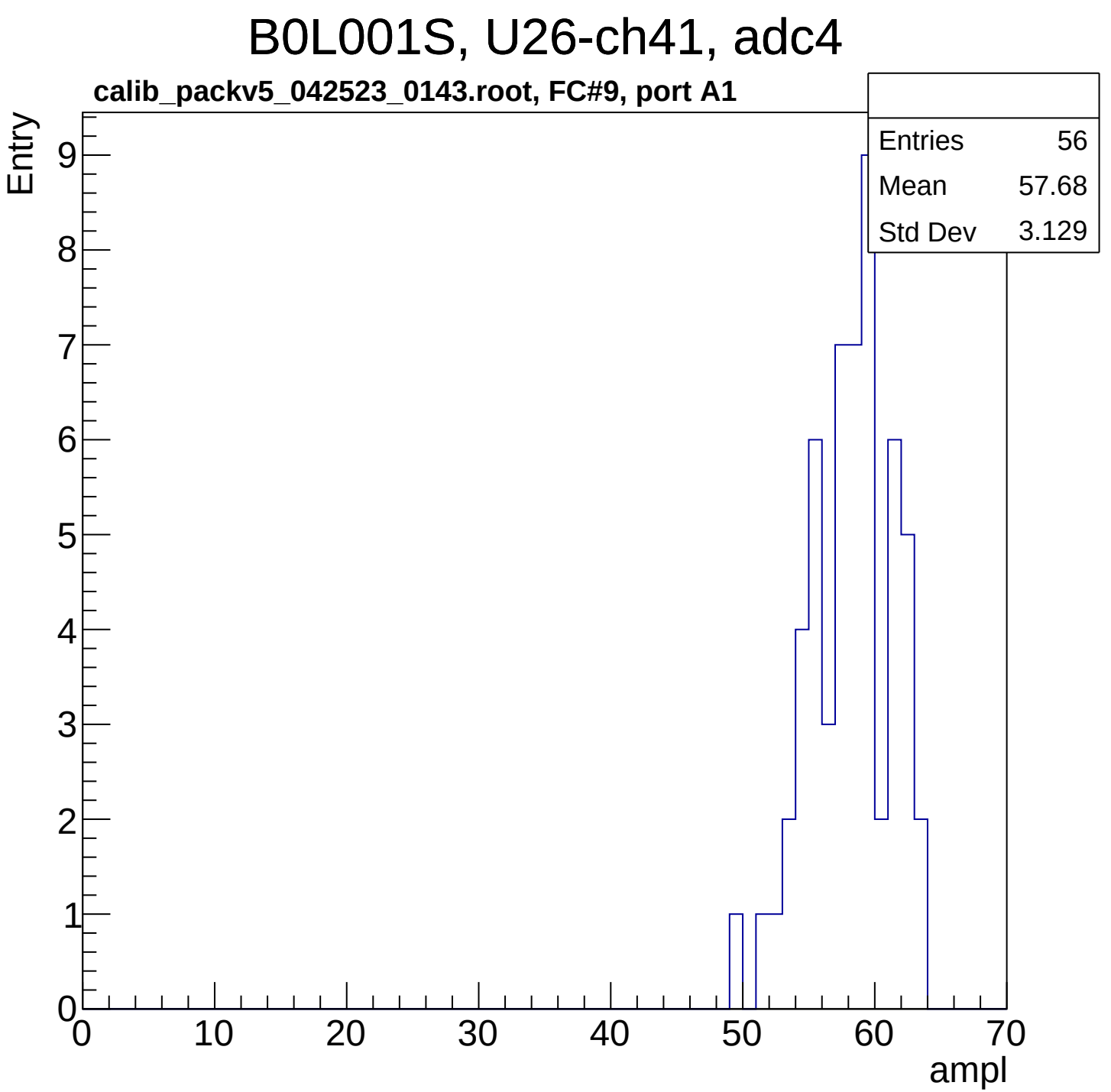
B0L001S, U26-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.68
Std Dev 3.129
Entry
ampl
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7
8
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0
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20
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40
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60
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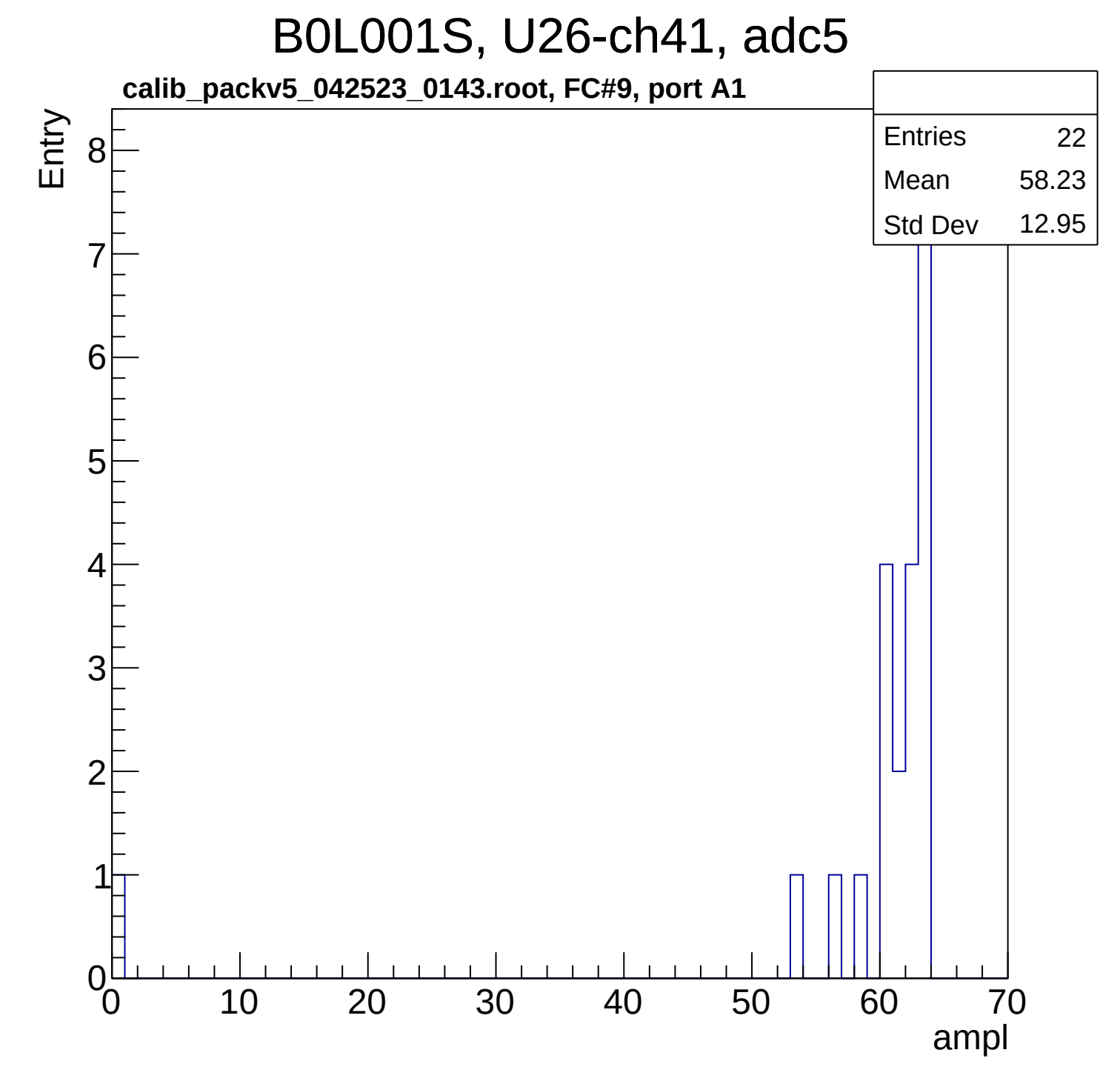
B0L001S, U26-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.23
Std Dev 12.95
Entry
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2
3
4
5
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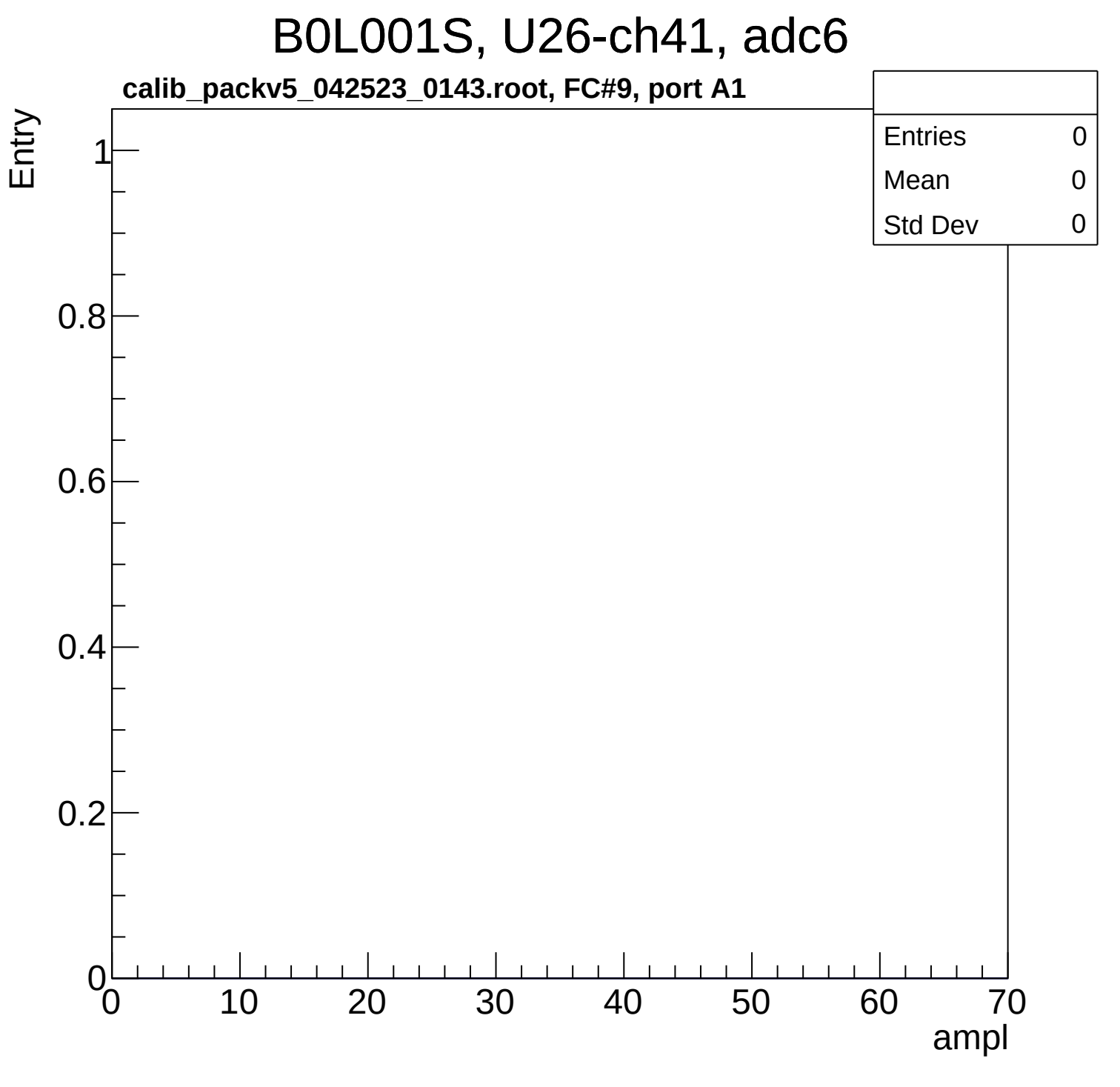
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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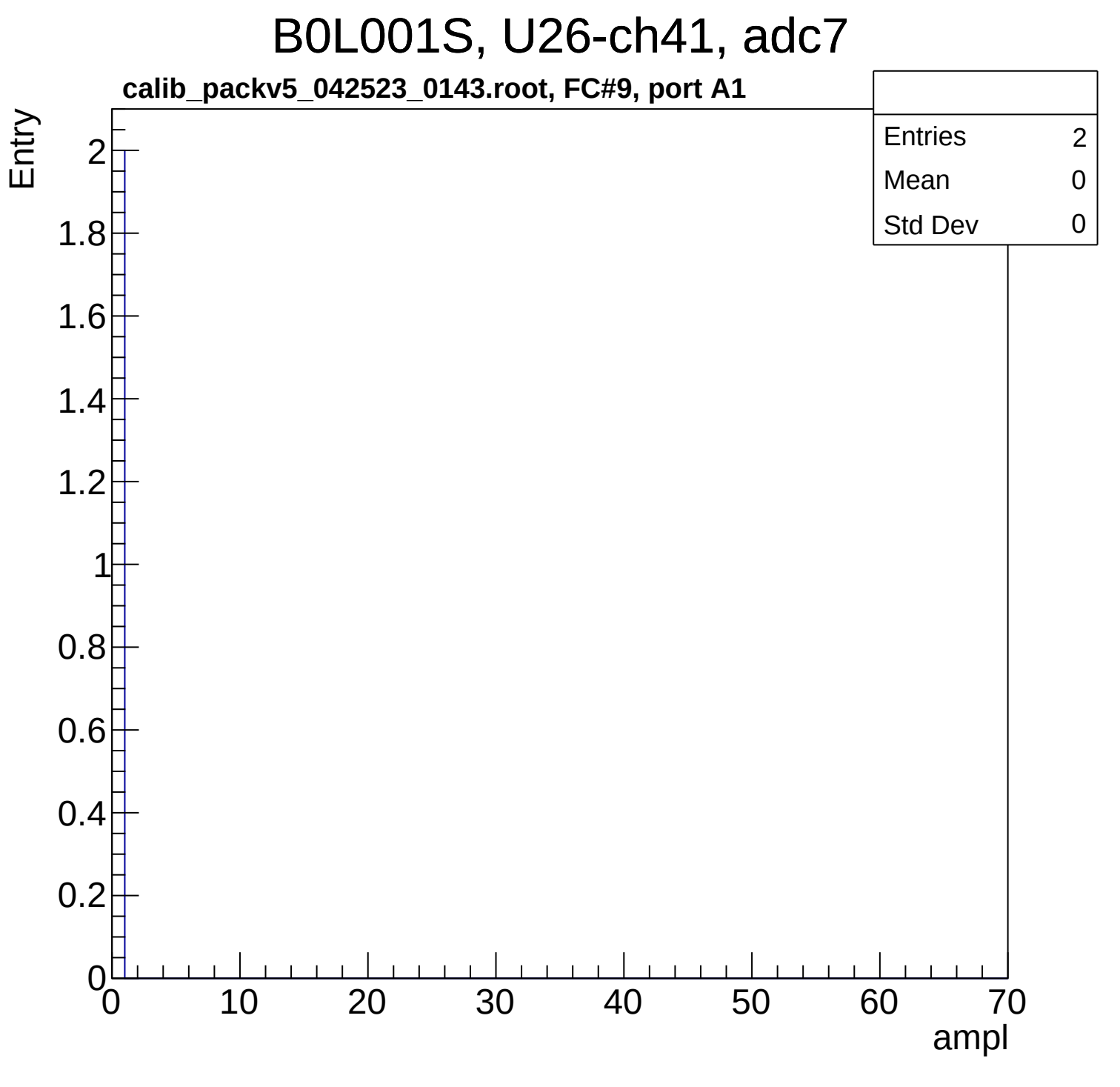
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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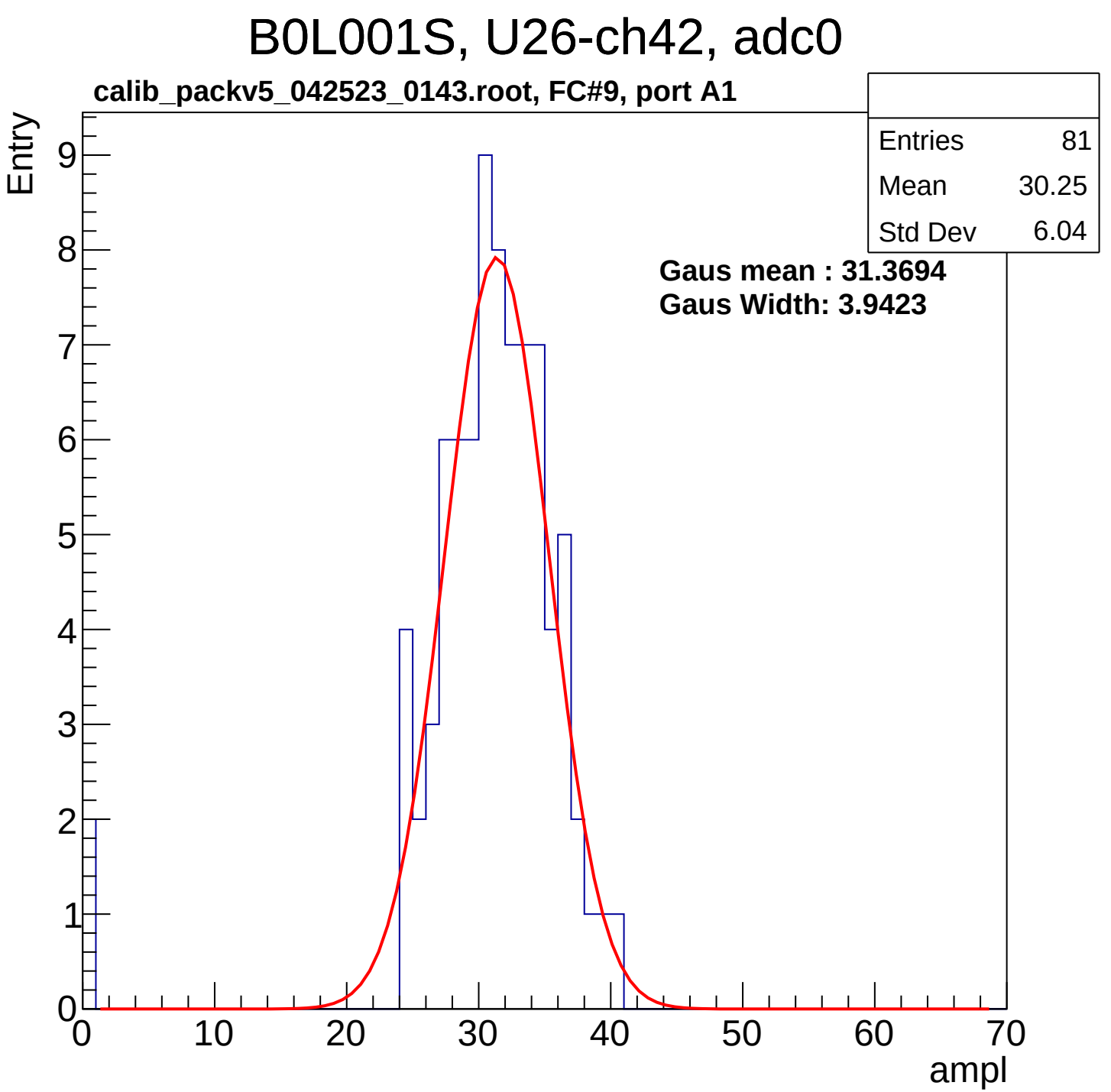
B0L001S, U26-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 30.25
Std Dev 6.04
Gaus mean : 31.3694
Gaus Width: 3.9423
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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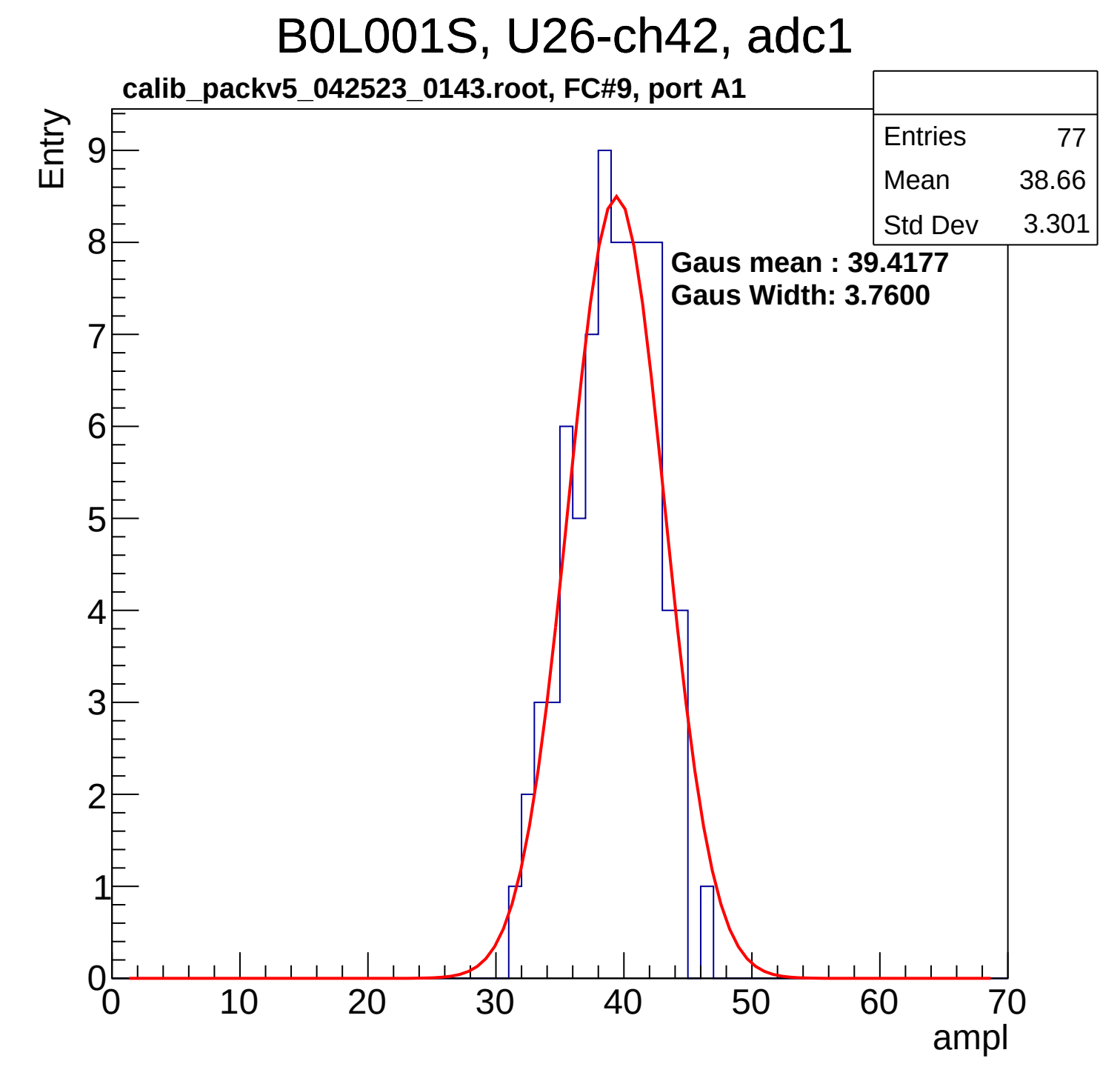
B0L001S, U26-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.66
Std Dev 3.301
Gaus mean : 39.4177
Gaus Width: 3.7600
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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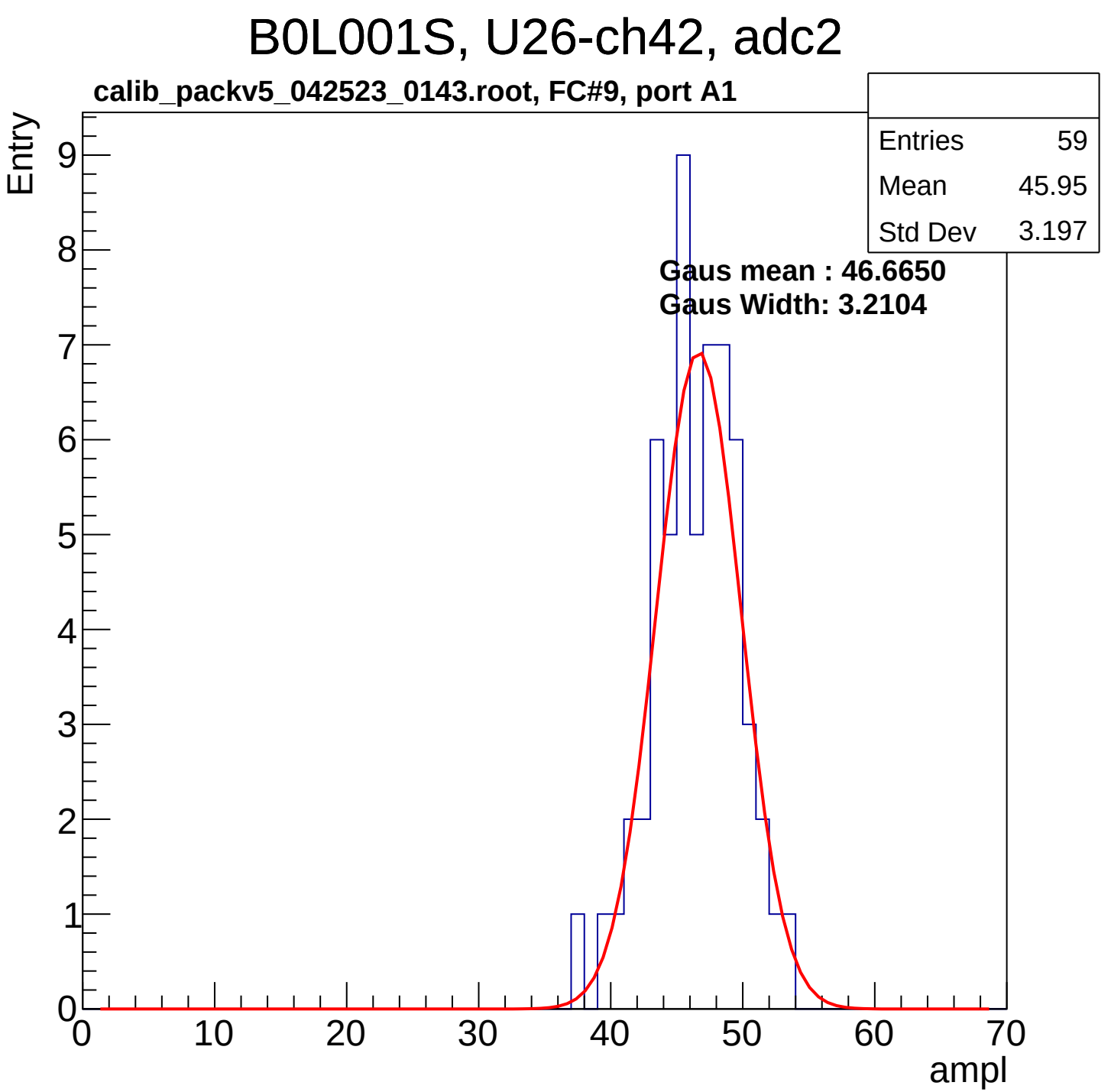
B0L001S, U26-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.95
Std Dev 3.197
Gaus mean : 46.6650
Gaus Width: 3.2104
Entry
ampl
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3
4
5
6
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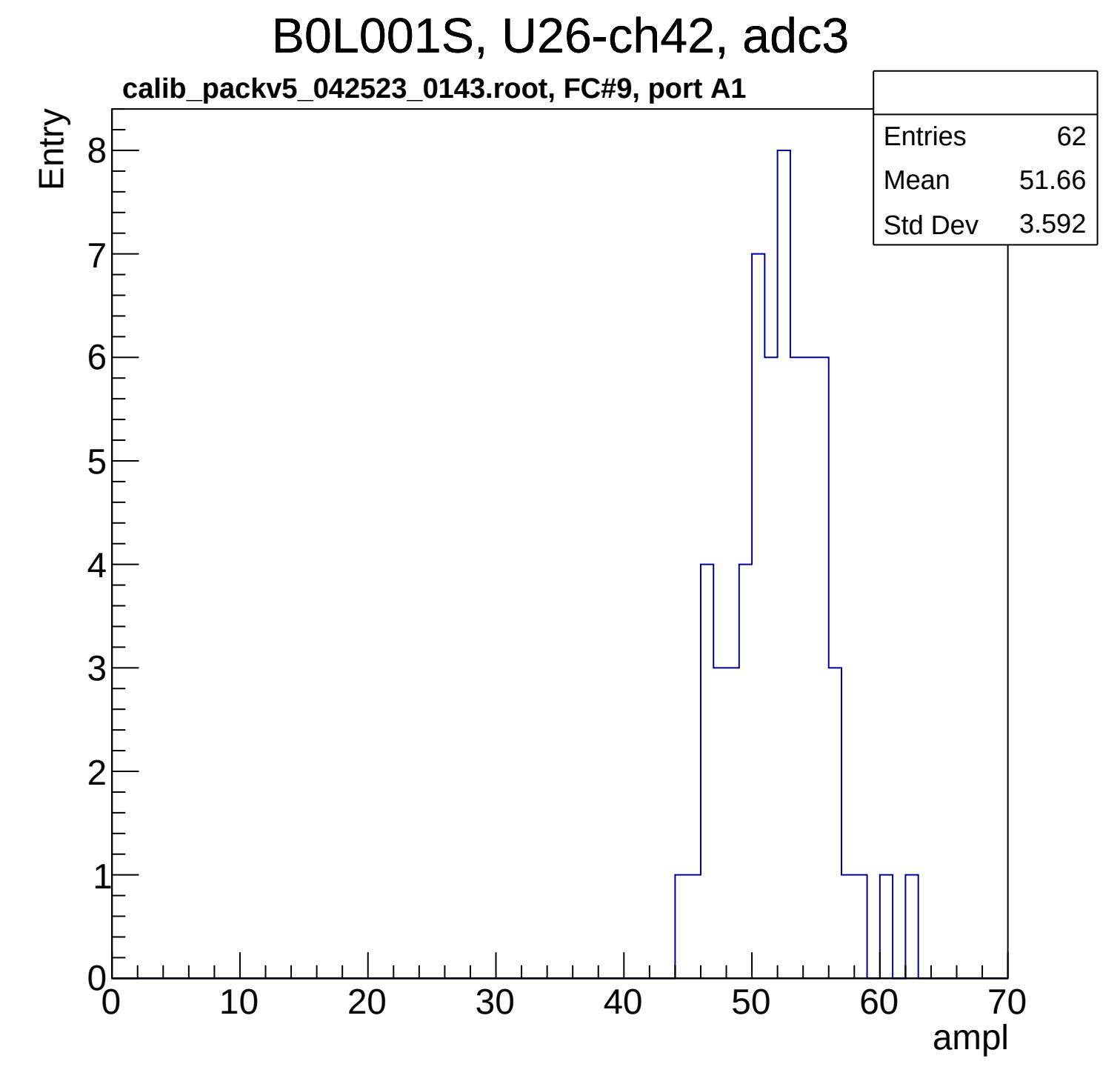
B0L001S, U26-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.66
Std Dev 3.592
Entry
ampl
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0
10
20
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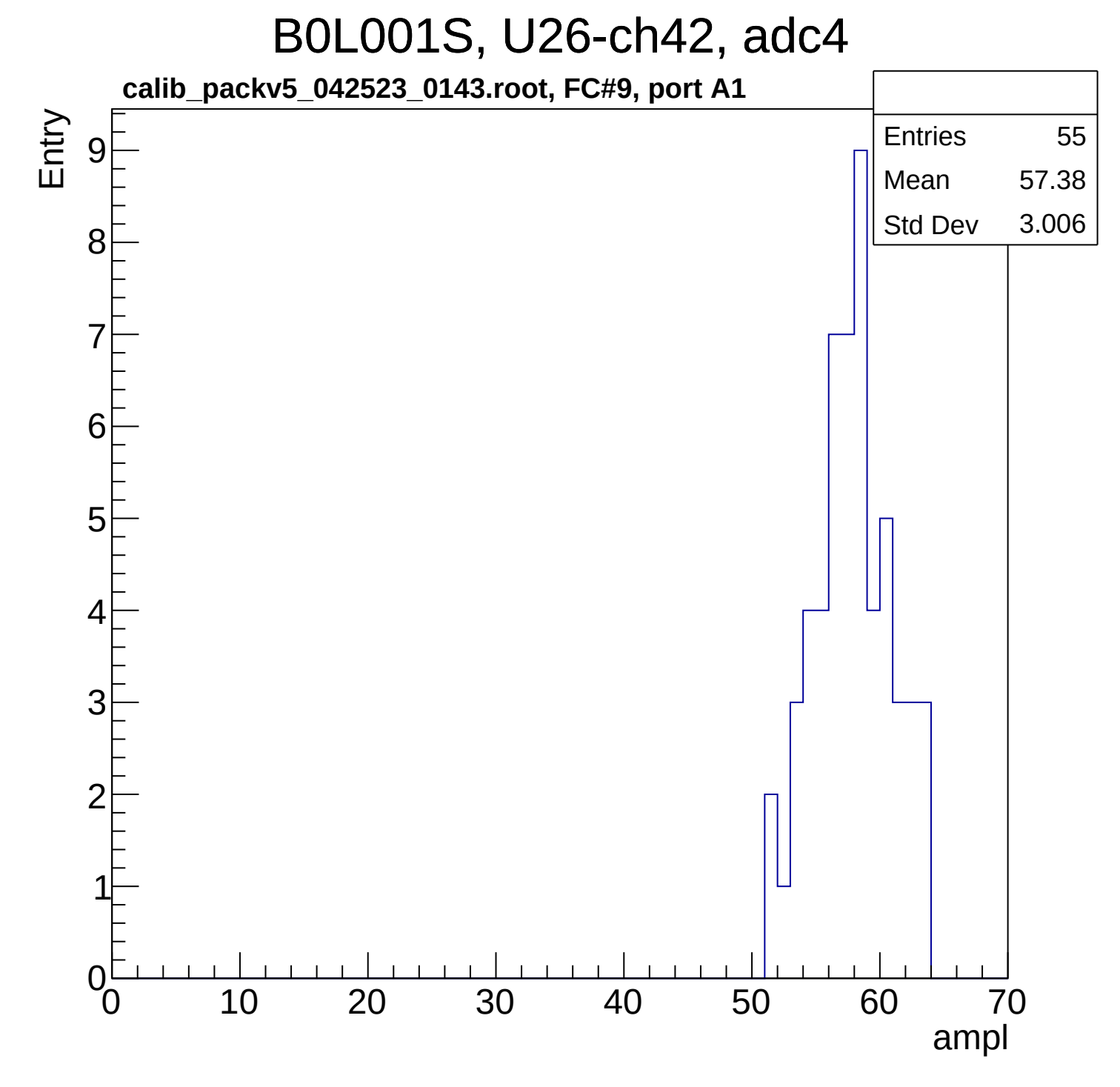
B0L001S, U26-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.38
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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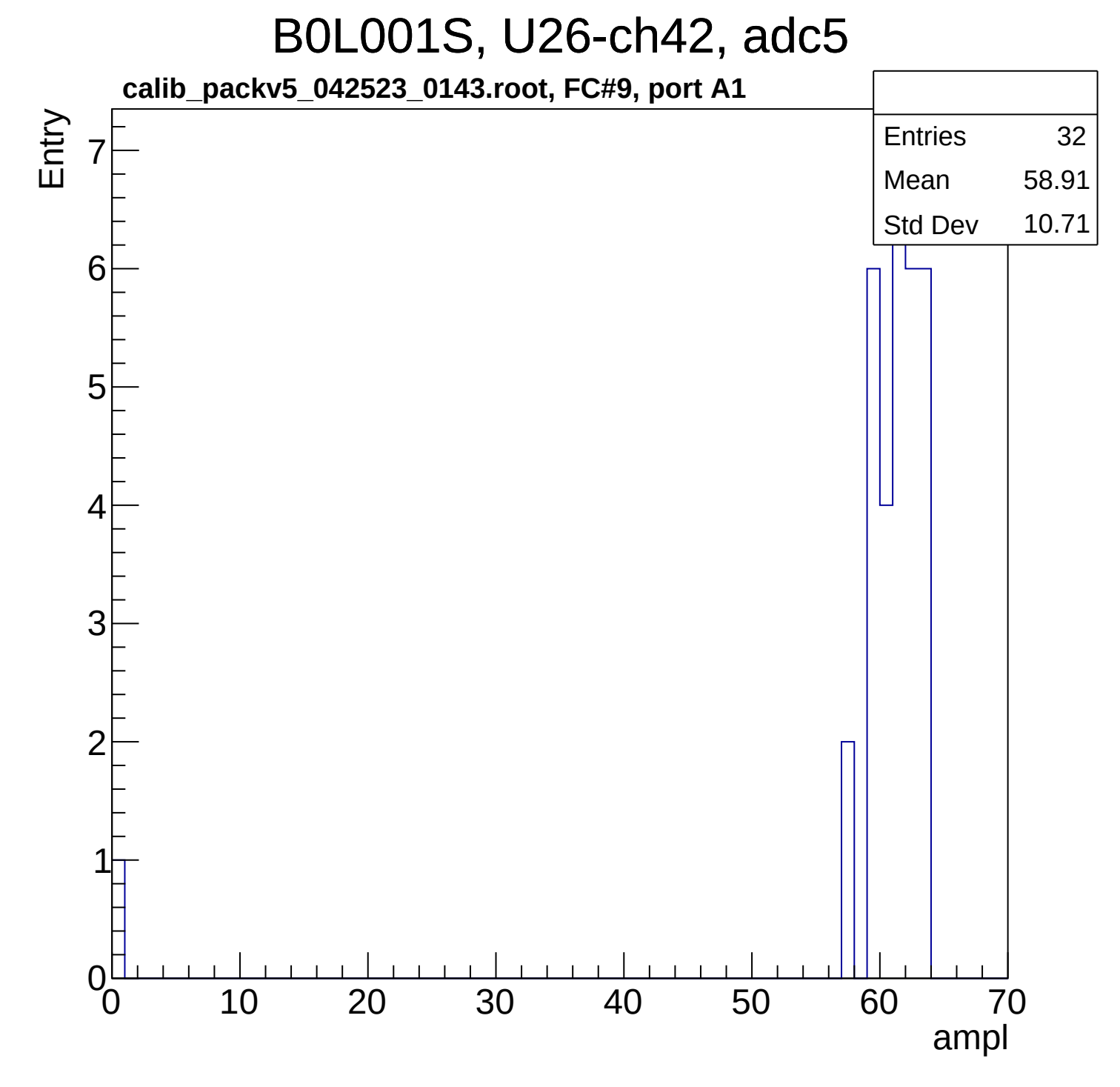
B0L001S, U26-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.91
Std Dev 10.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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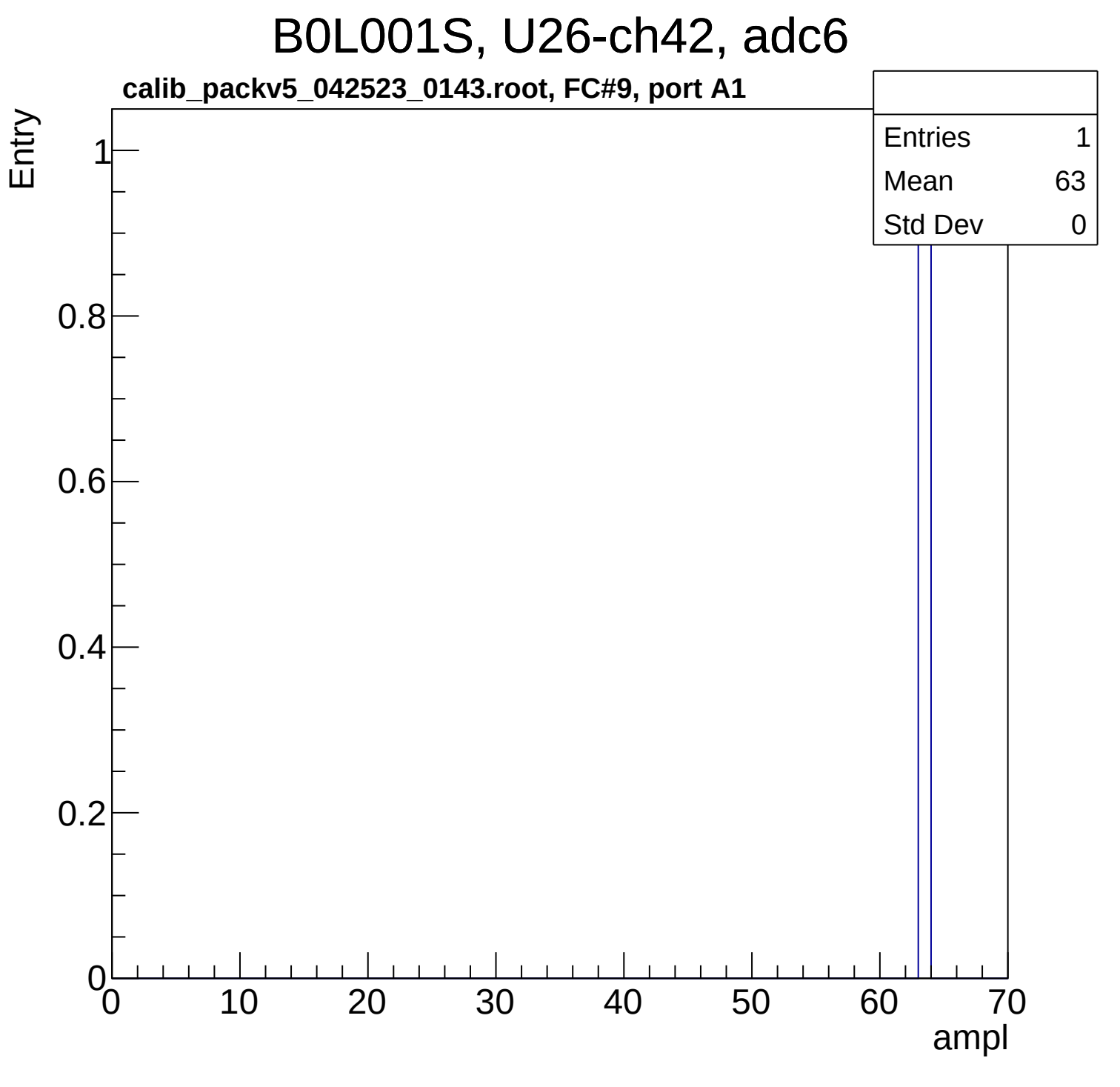
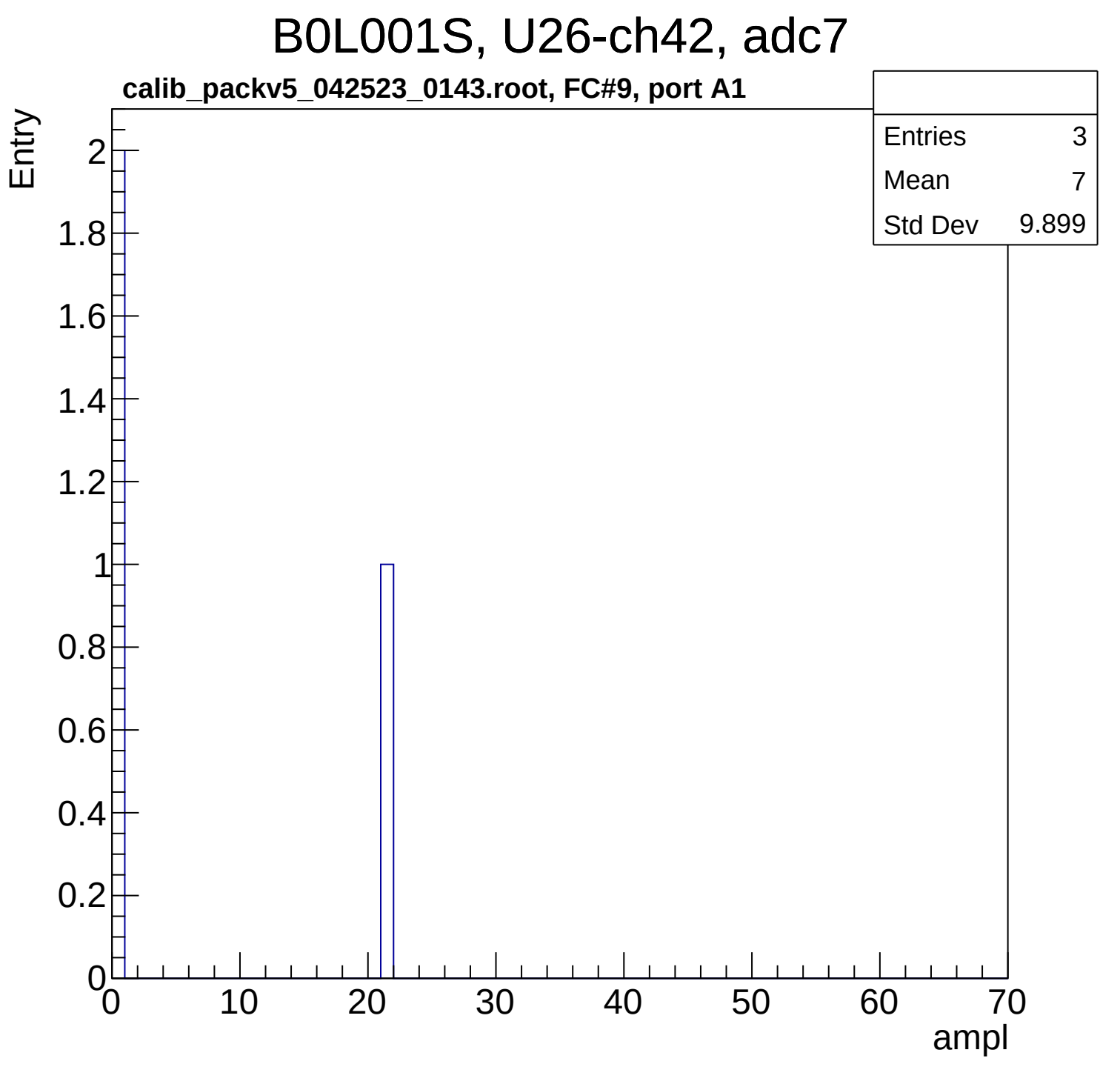

B0L001S, U26-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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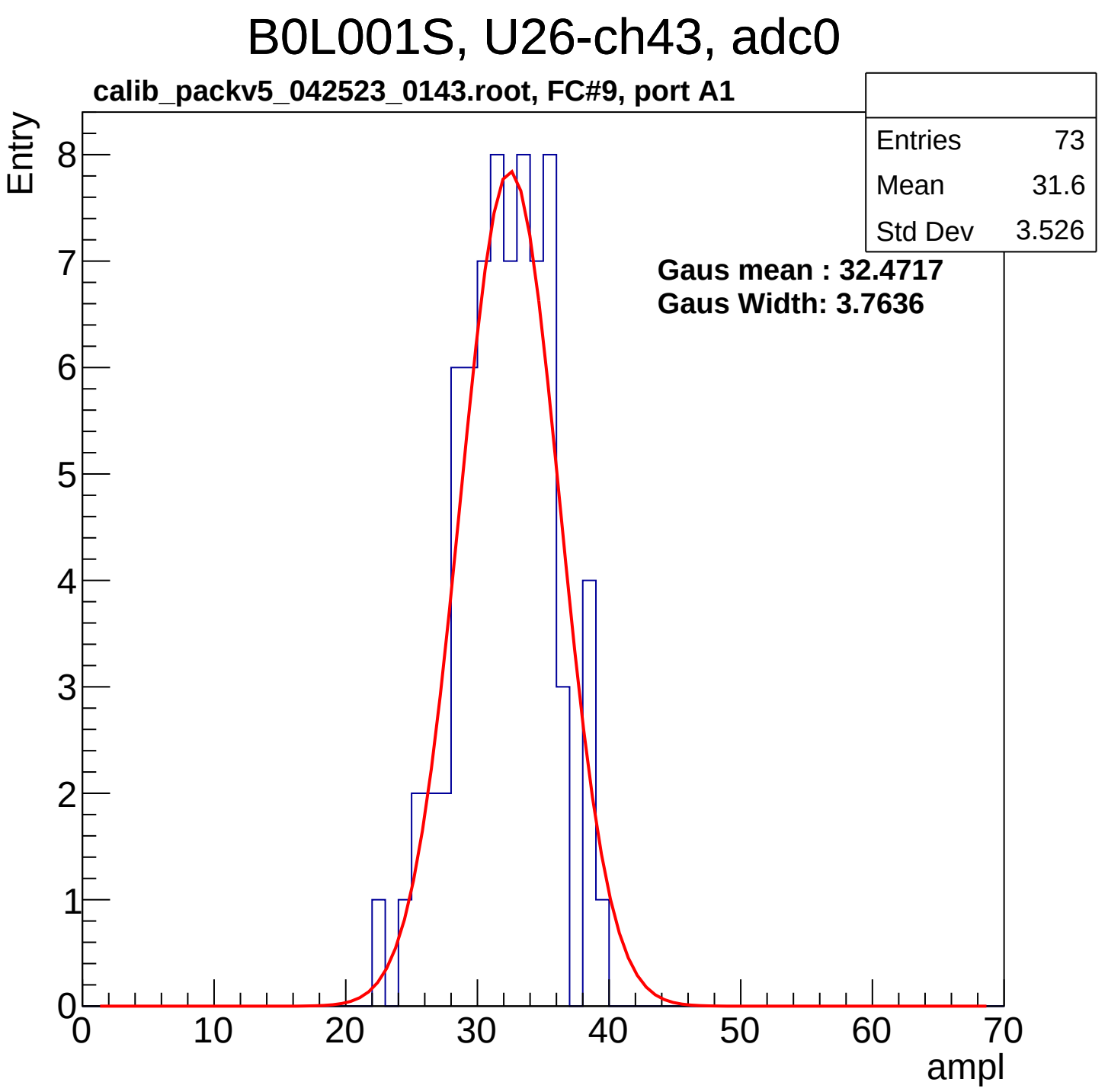
B0L001S, U26-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 31.6
Std Dev 3.526
Gaus mean : 32.4717
Gaus Width: 3.7636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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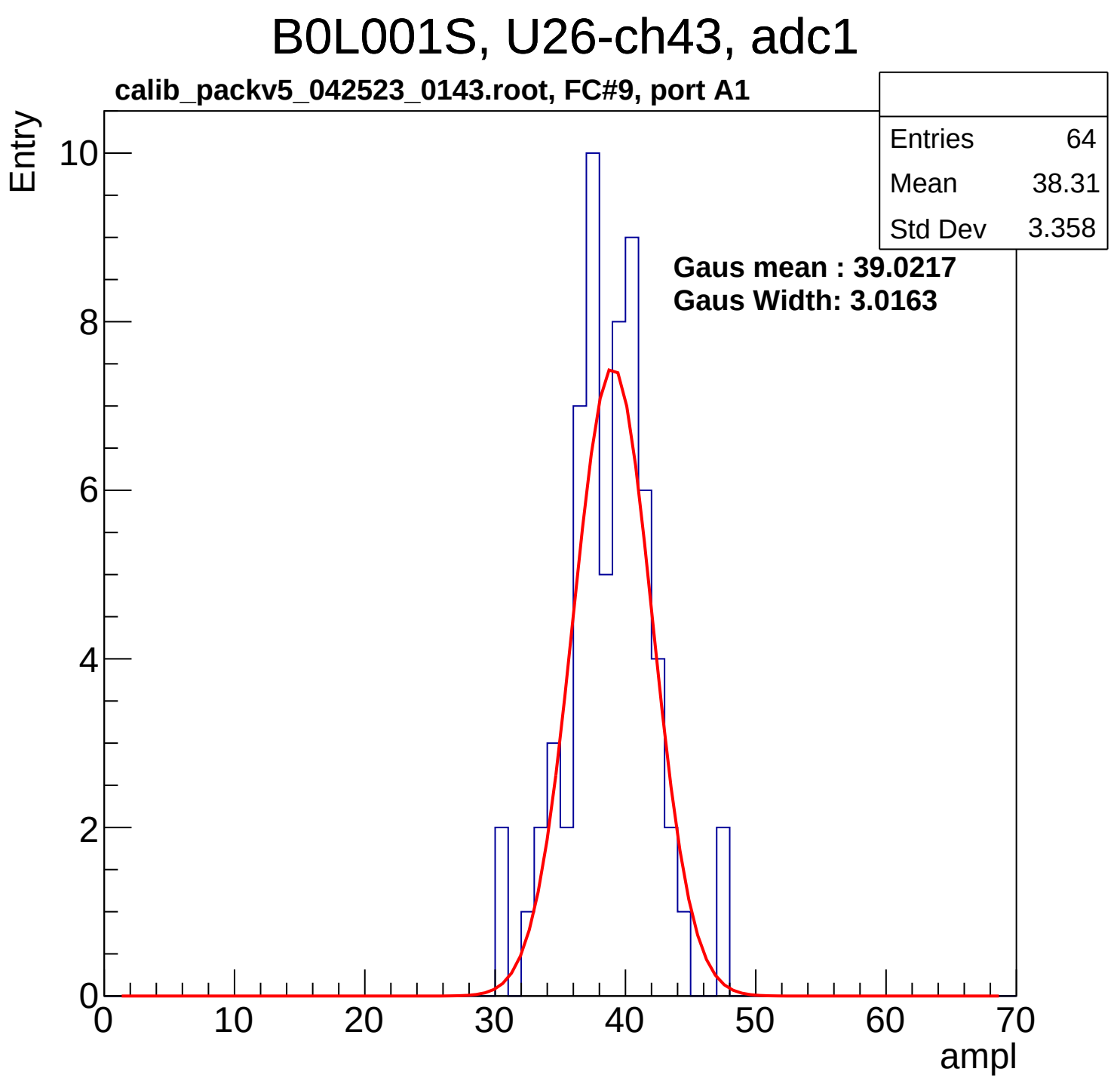
B0L001S, U26-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.31
Std Dev 3.358
Gaus mean : 39.0217
Gaus Width: 3.0163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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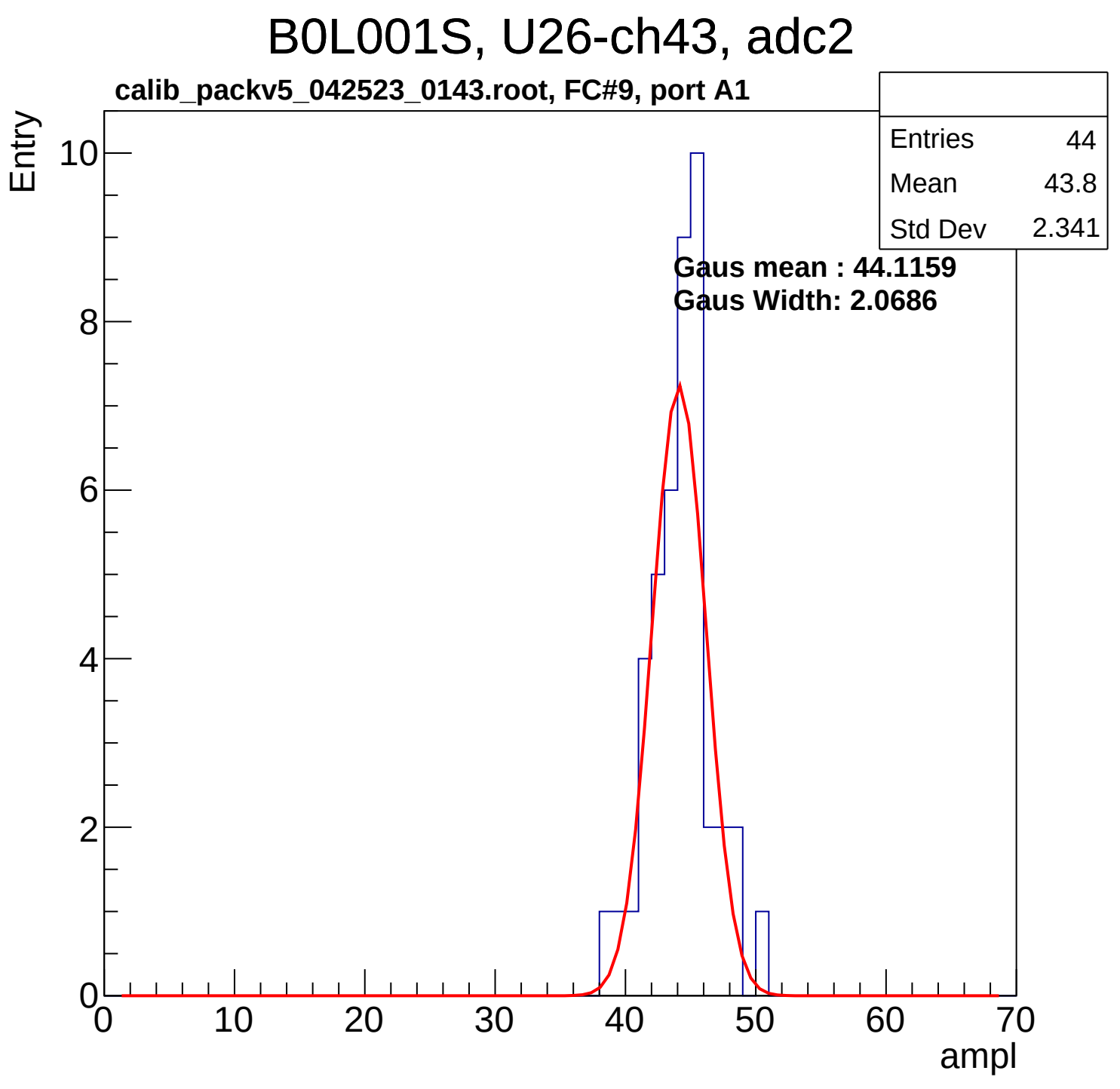
B0L001S, U26-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 43.8
Std Dev 2.341
Gaus mean : 44.1159
Gaus Width: 2.0686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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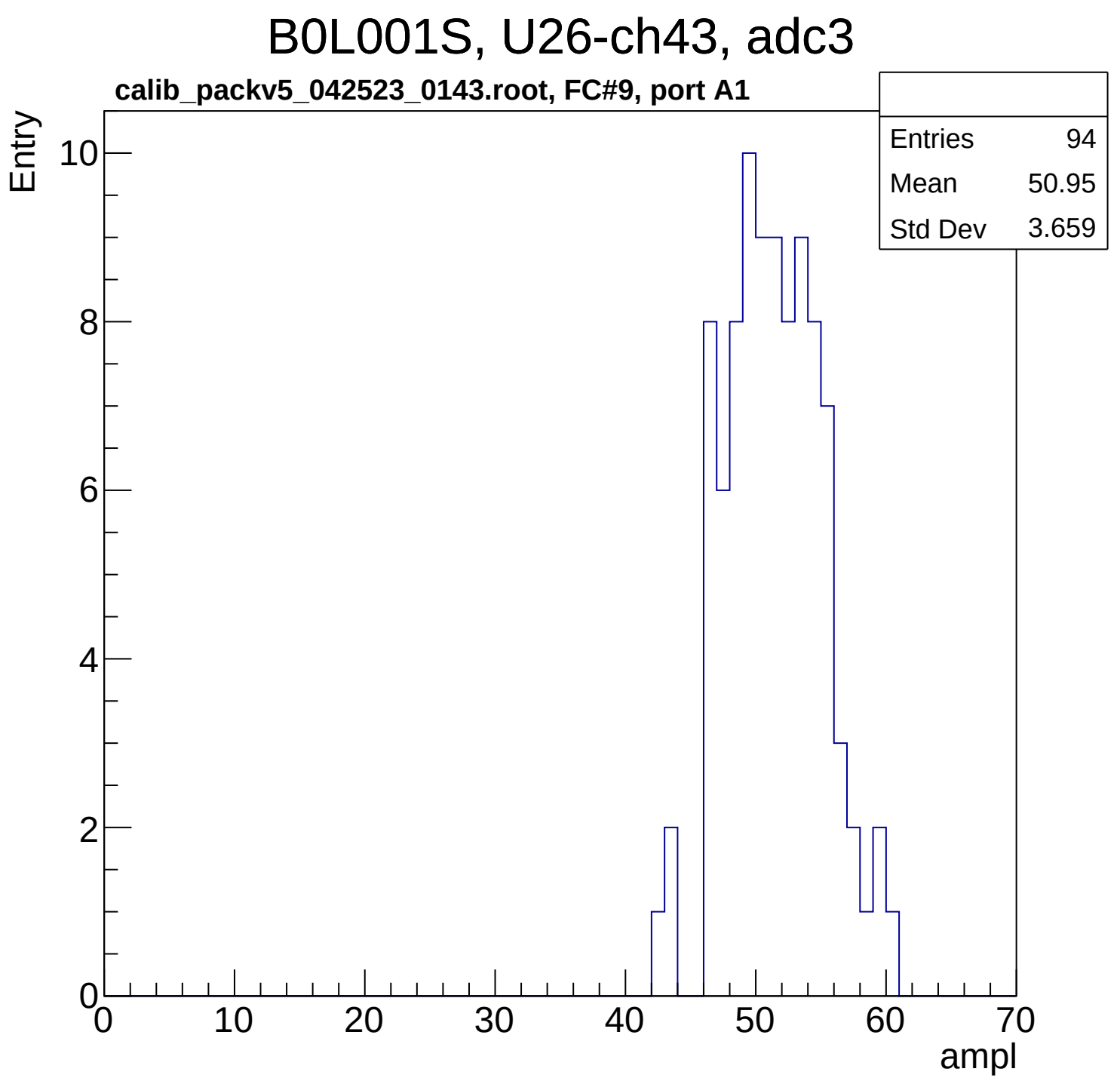
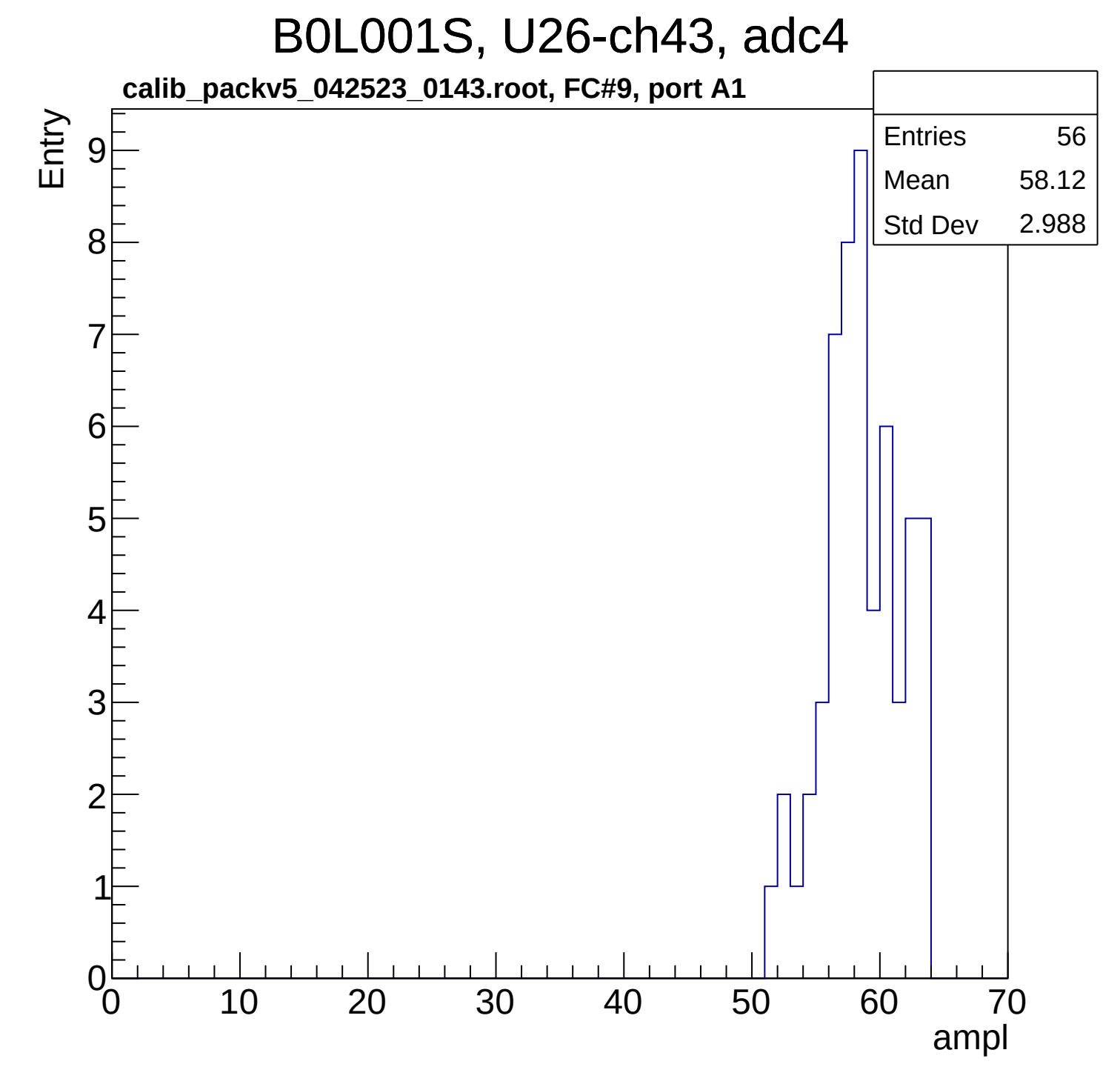
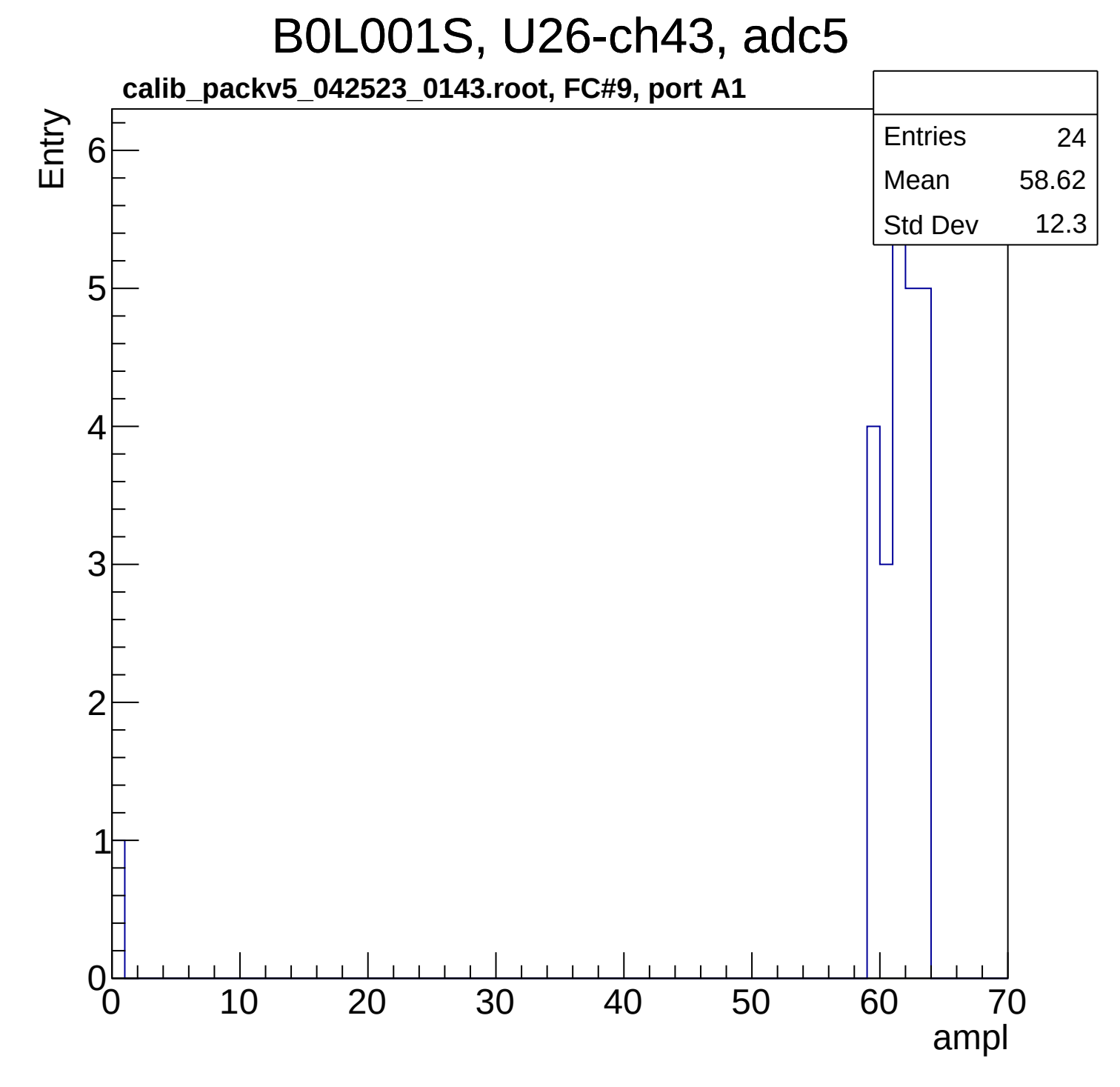
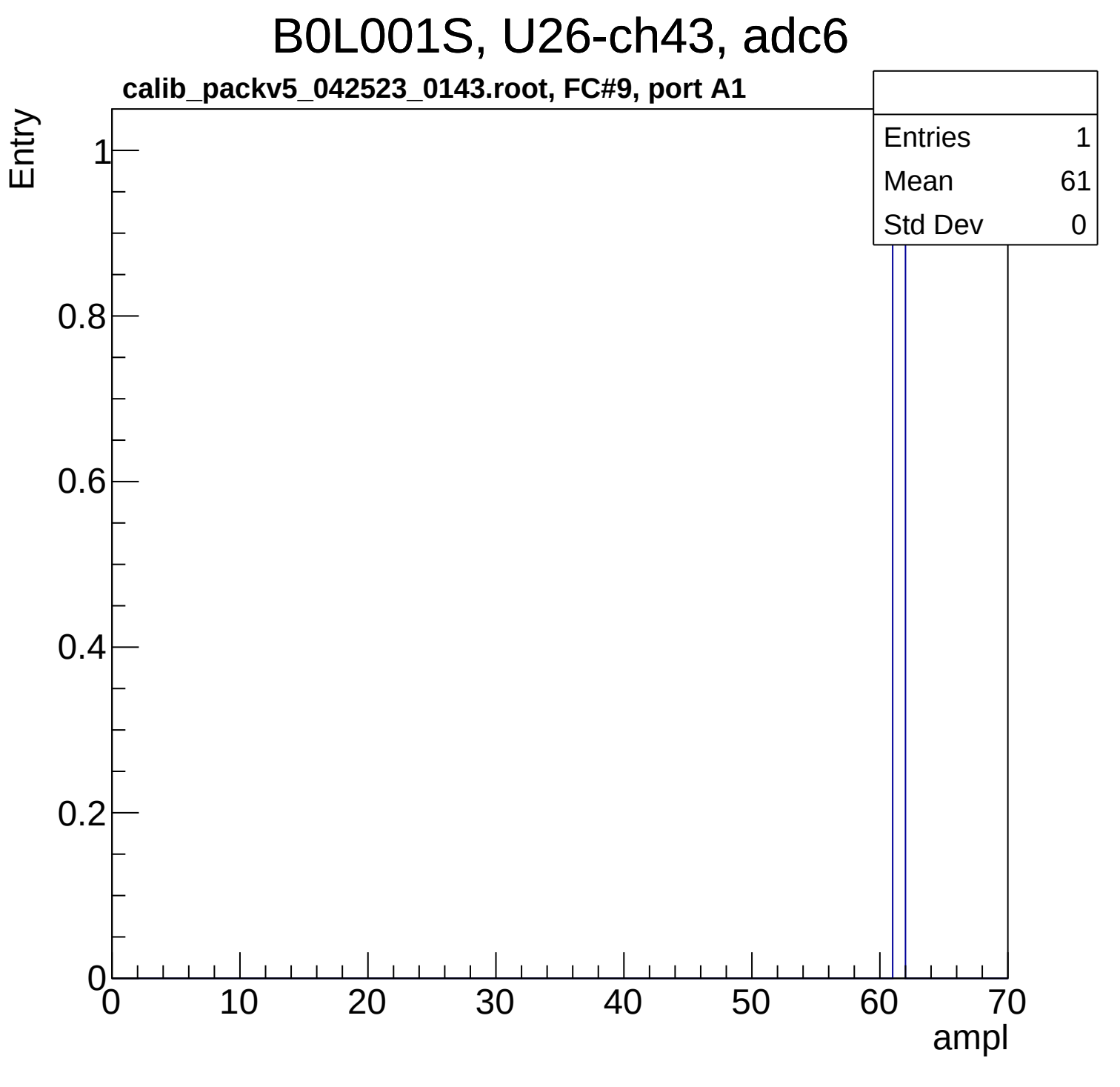
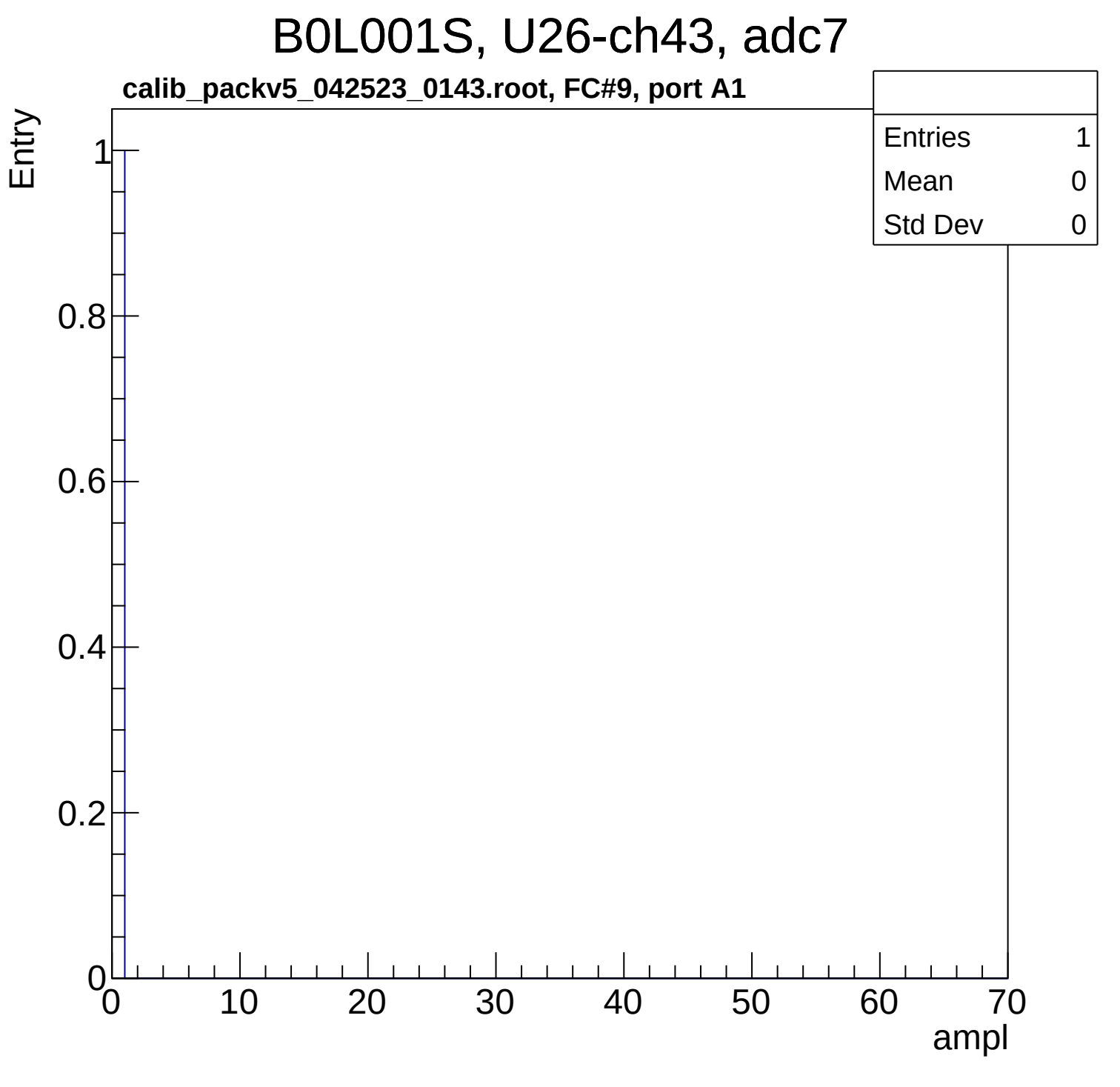
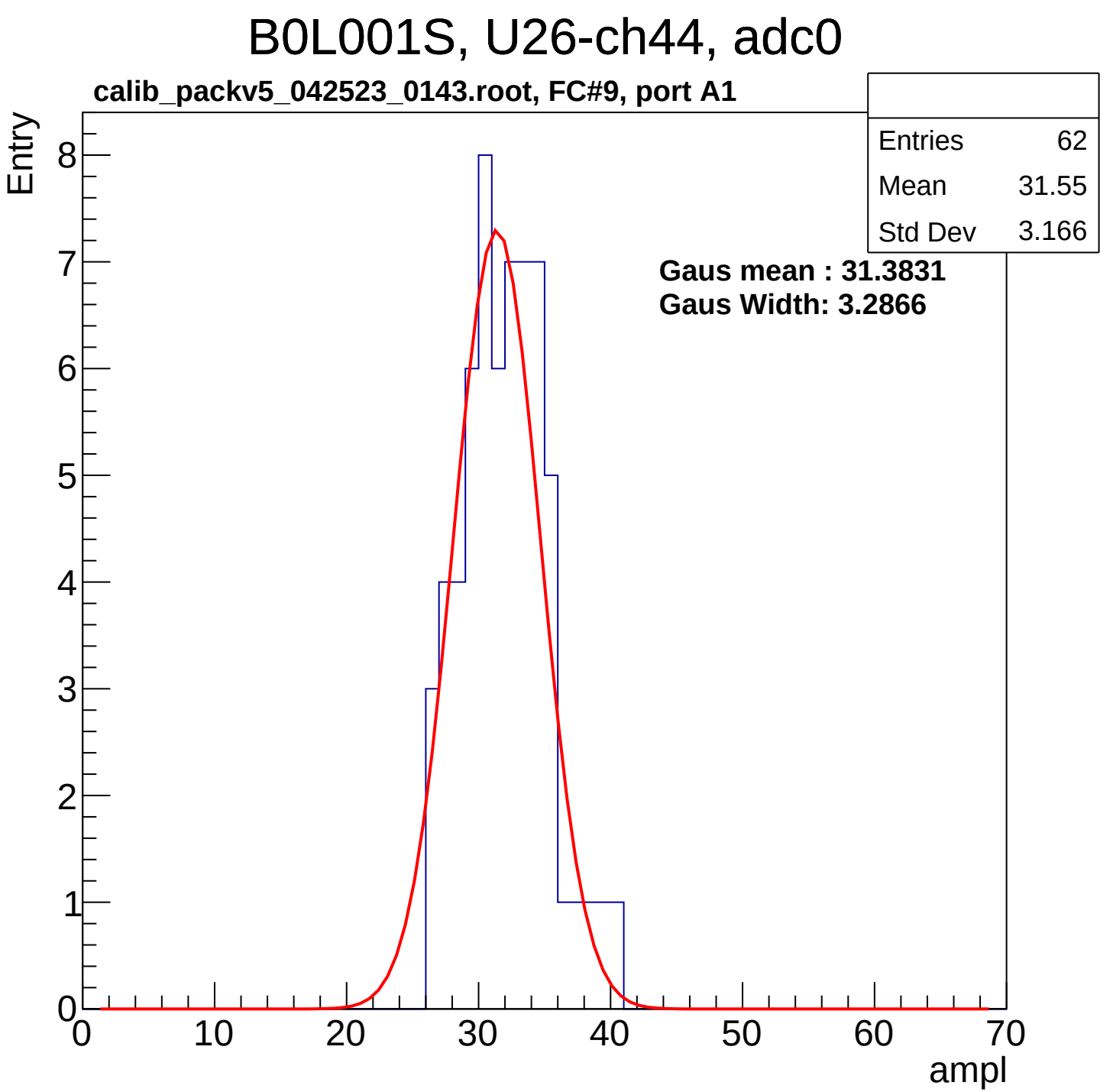

B0L001S, U26-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 94
Mean 50.95
Std Dev 3.659
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

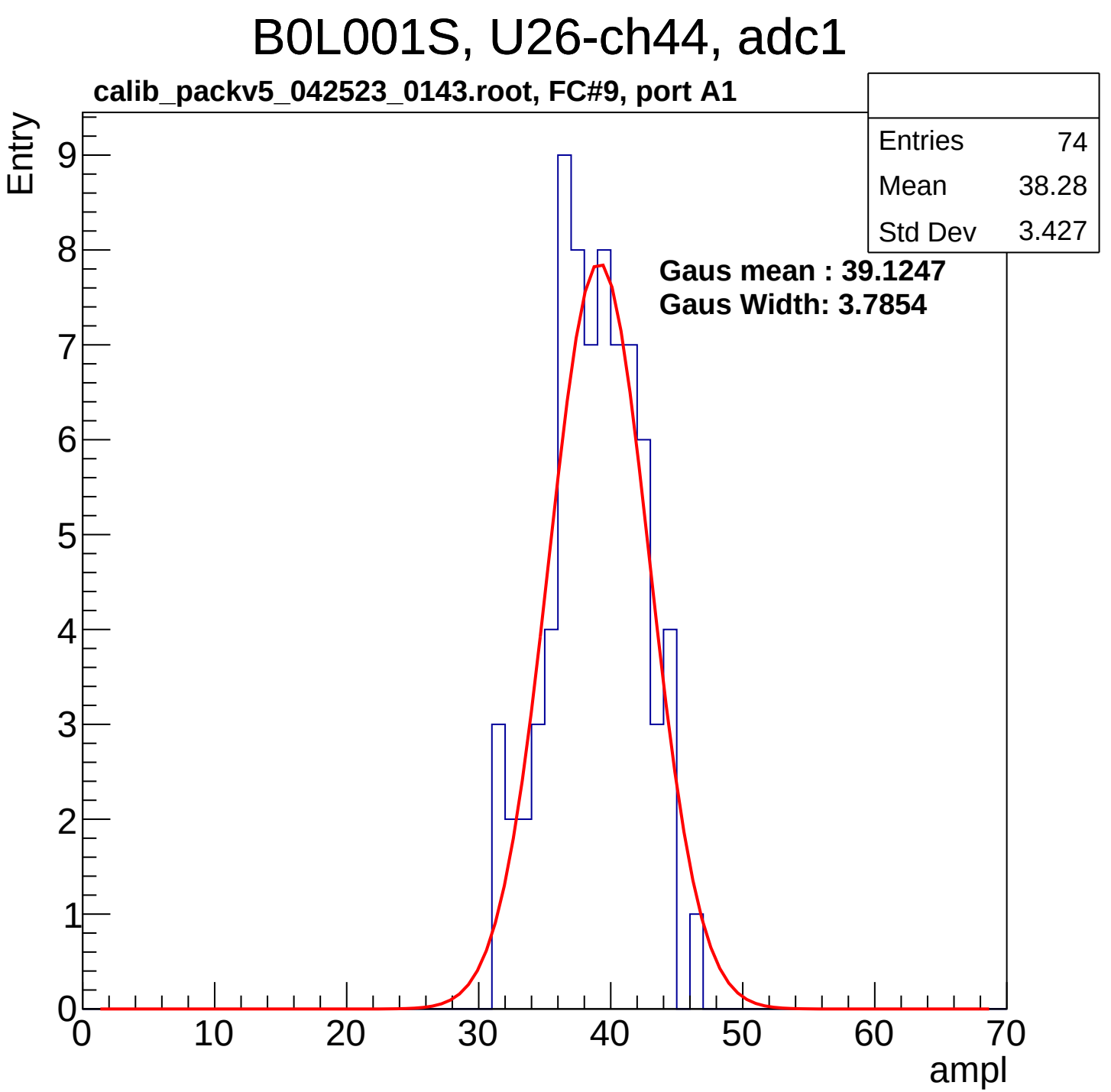

B0L001S, U26-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.12
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

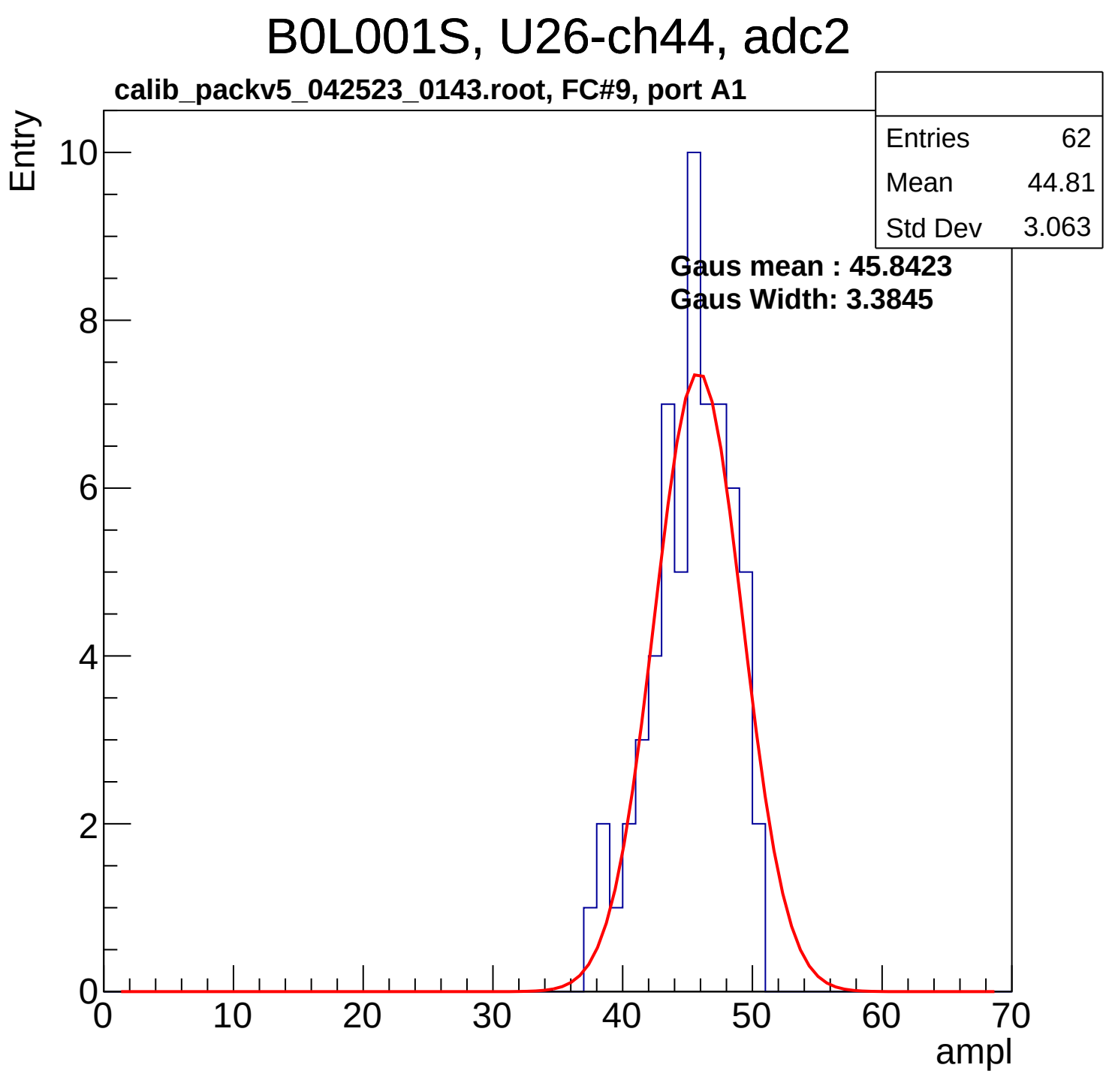

B0L001S, U26-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.62
Std Dev 12.3
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

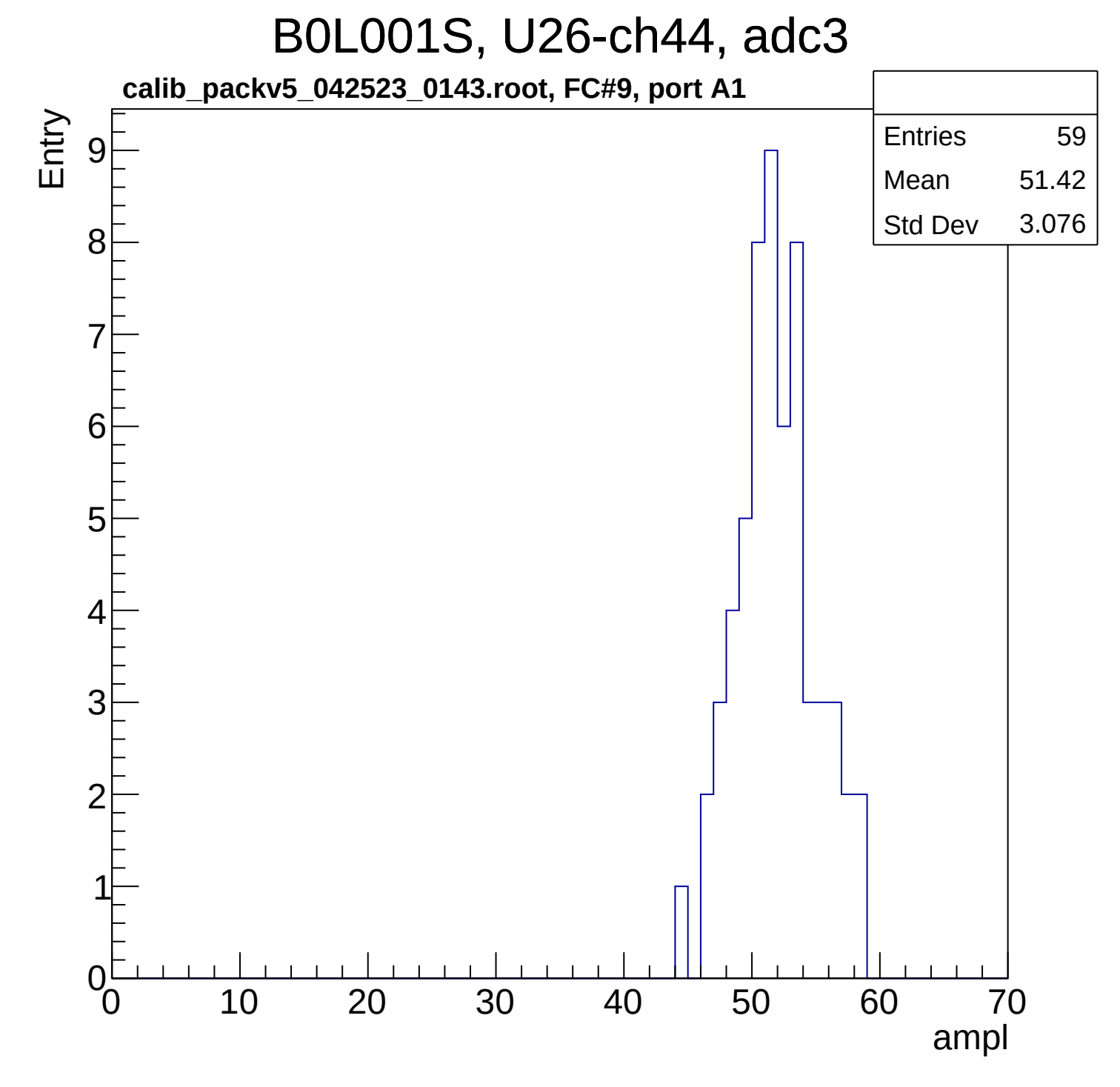

B0L001S, U26-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

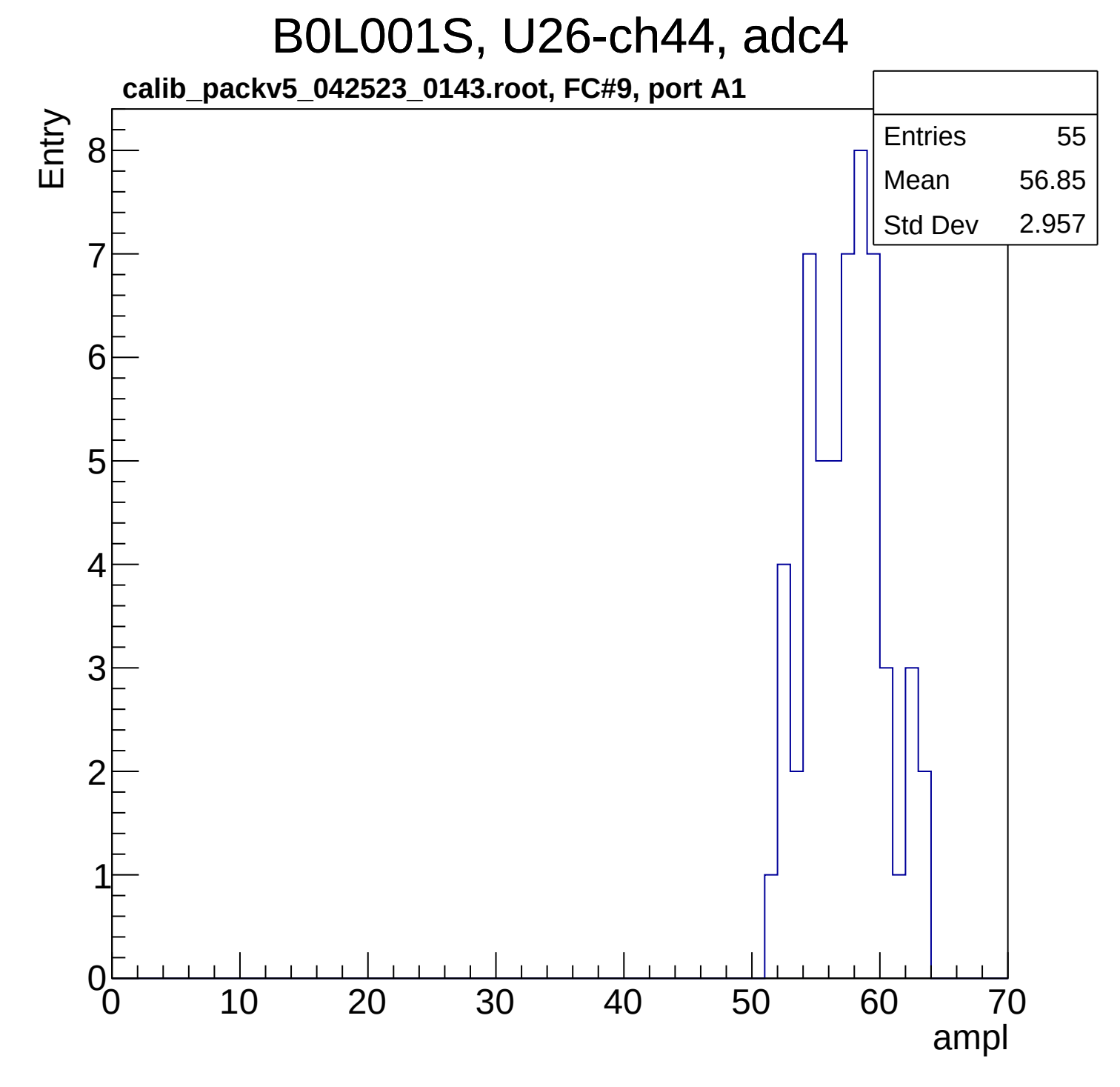

B0L001S, U26-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.55
Std Dev 3.166
Gaus mean : 31.3831
Gaus Width: 3.2866
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 38.28
Std Dev 3.427
Gaus mean : 39.1247
Gaus Width: 3.7854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.81
Std Dev 3.063
Gaus mean : 45.8423
Gaus Width: 3.3845
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.42
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.85
Std Dev 2.957
Entry
ampl
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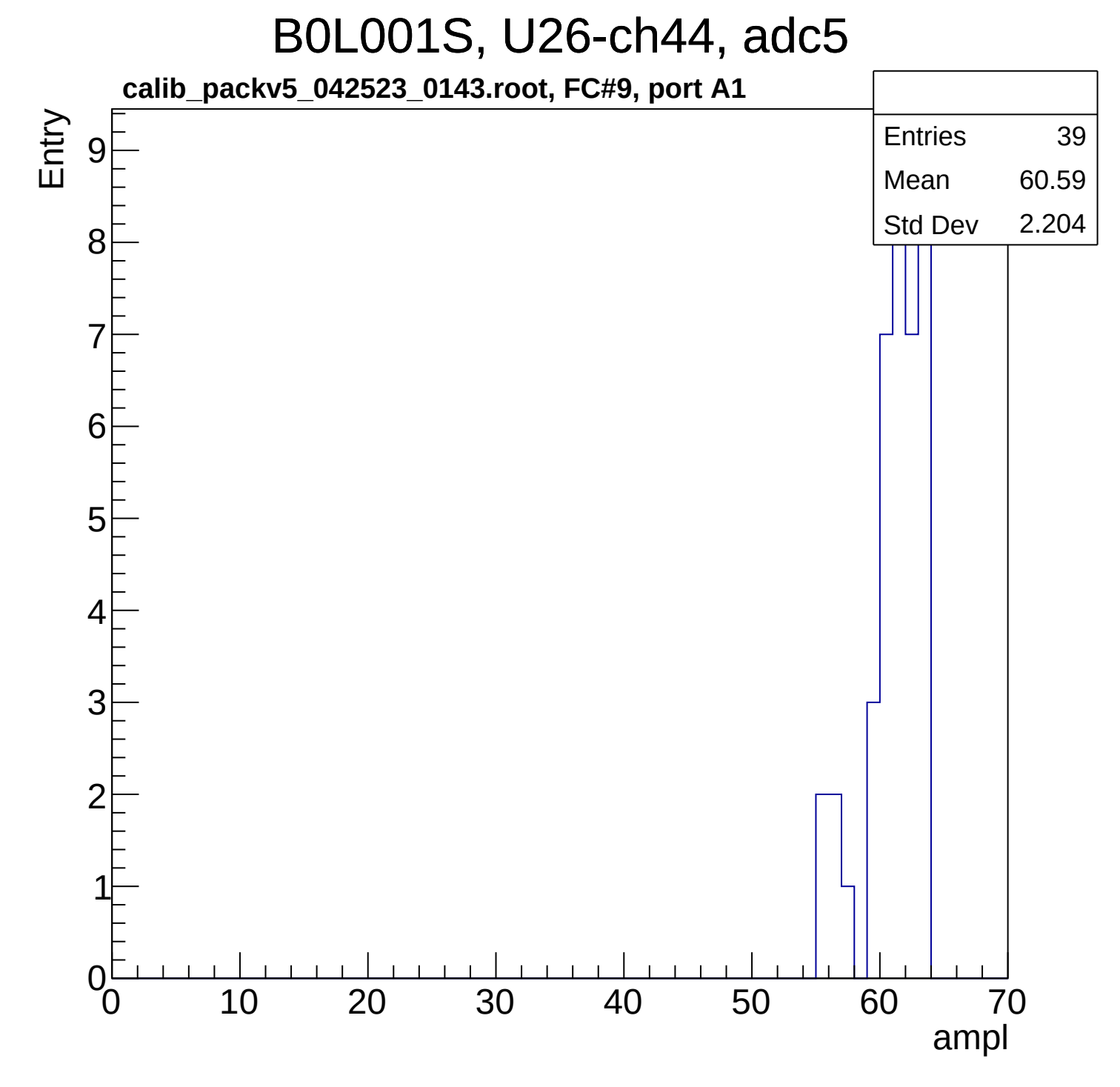
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 60.59
Std Dev 2.204
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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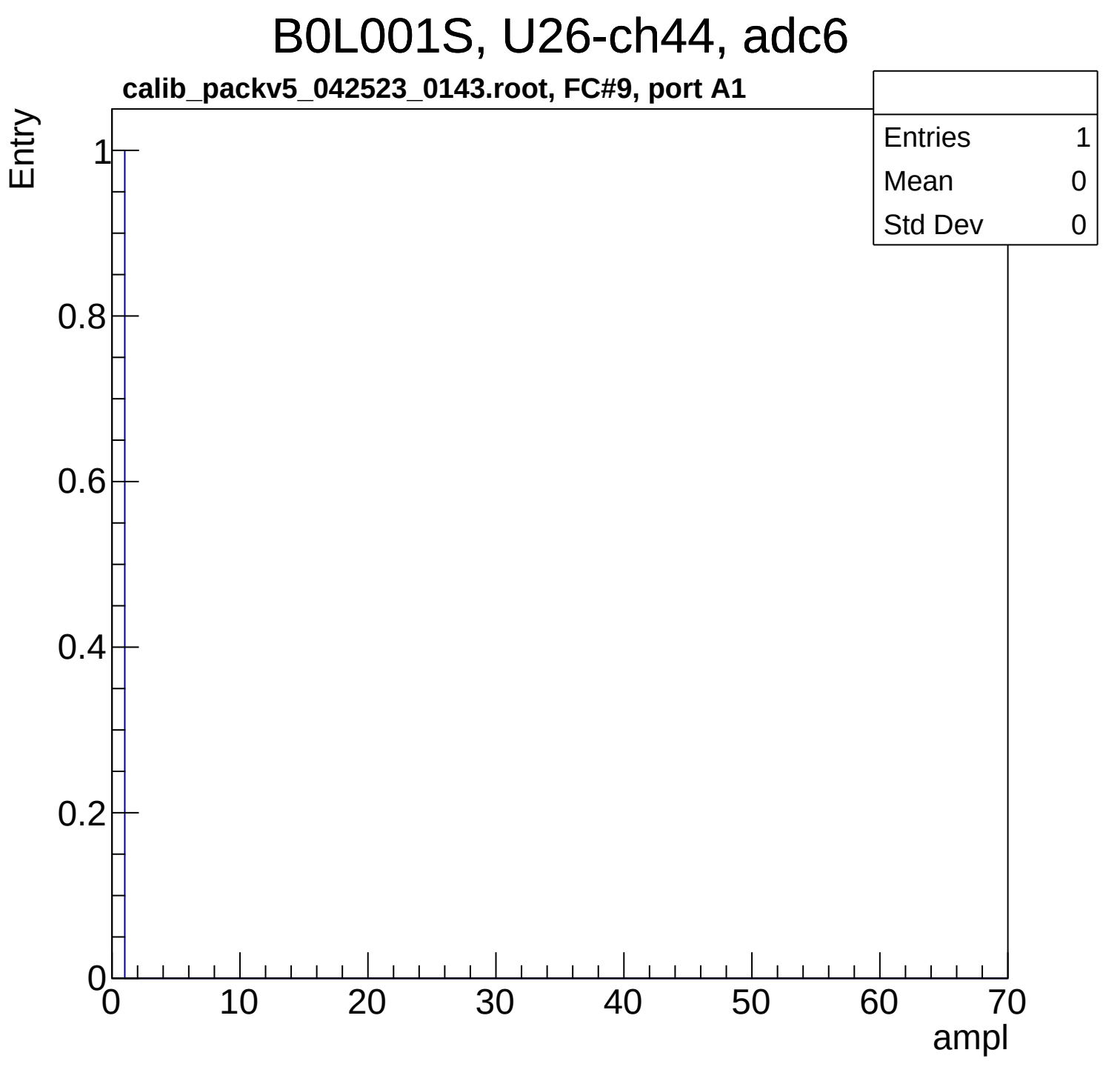
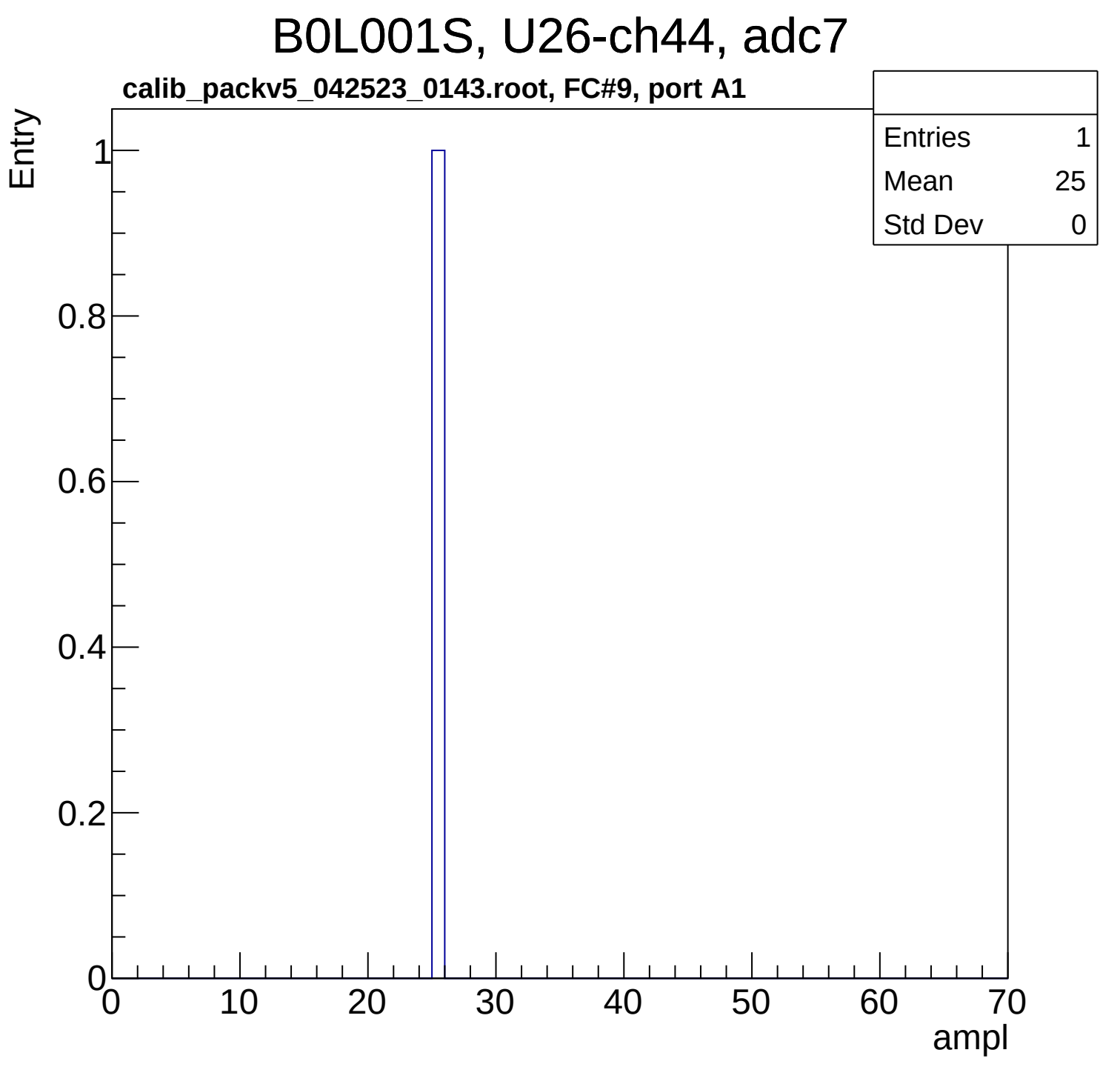

B0L001S, U26-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 25
Std Dev 0
Entry
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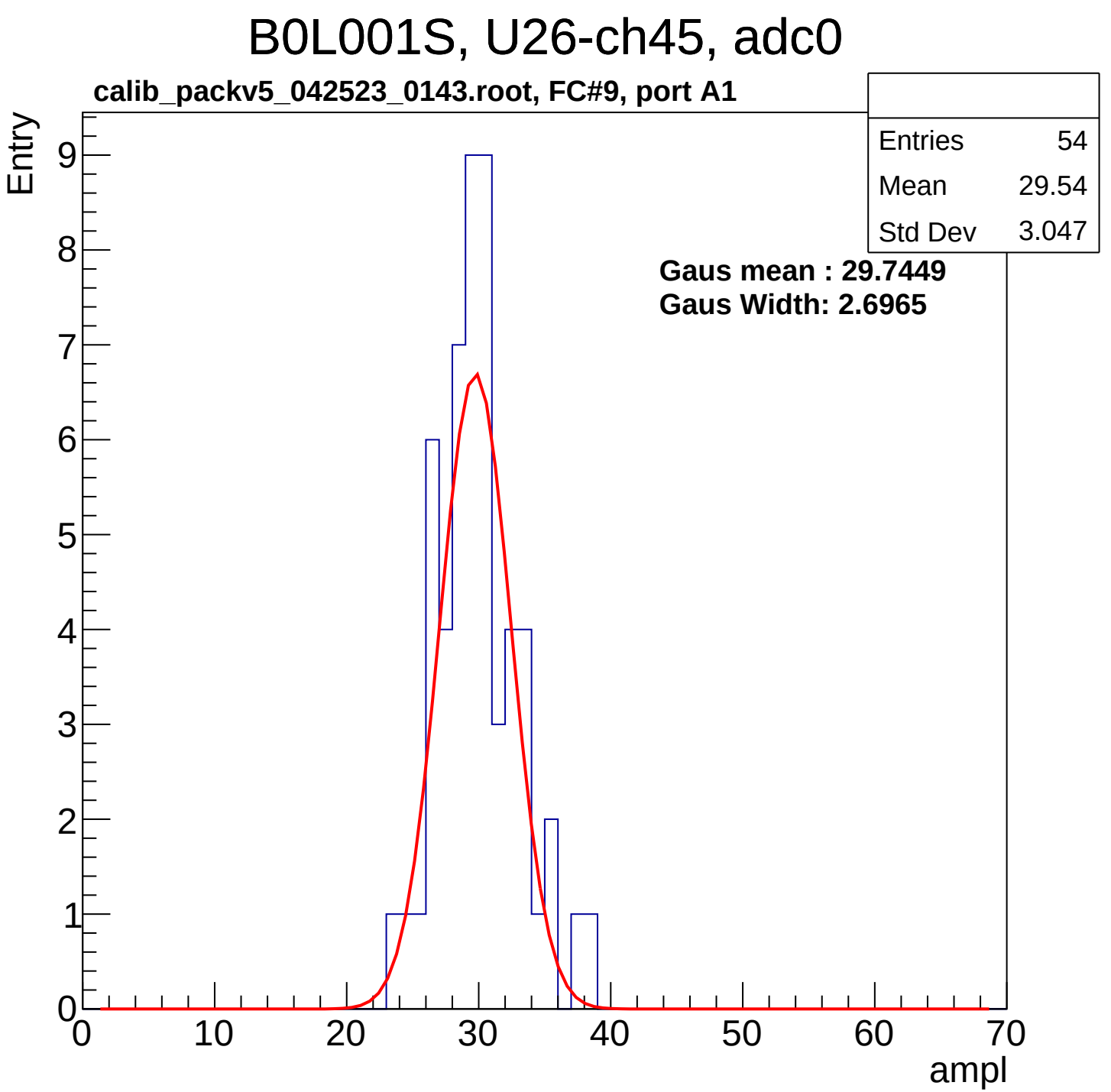
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 29.54
Std Dev 3.047
Gaus mean : 29.7449
Gaus Width: 2.6965
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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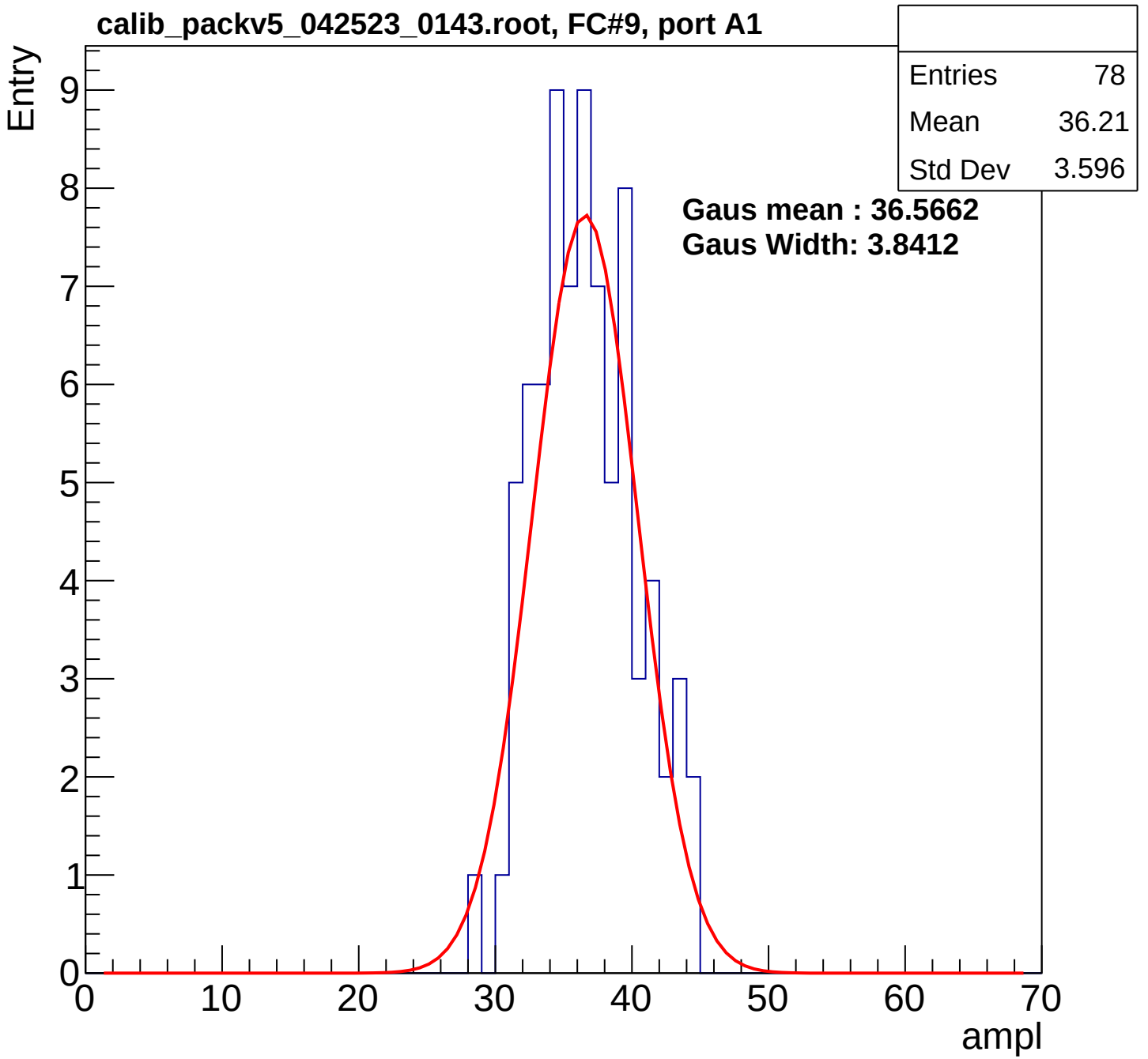
40
50
60
70


B0L001S, U26-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 36.21
Std Dev 3.596
Gaus mean : 36.5662
Gaus Width: 3.8412
Entry
ampl
0
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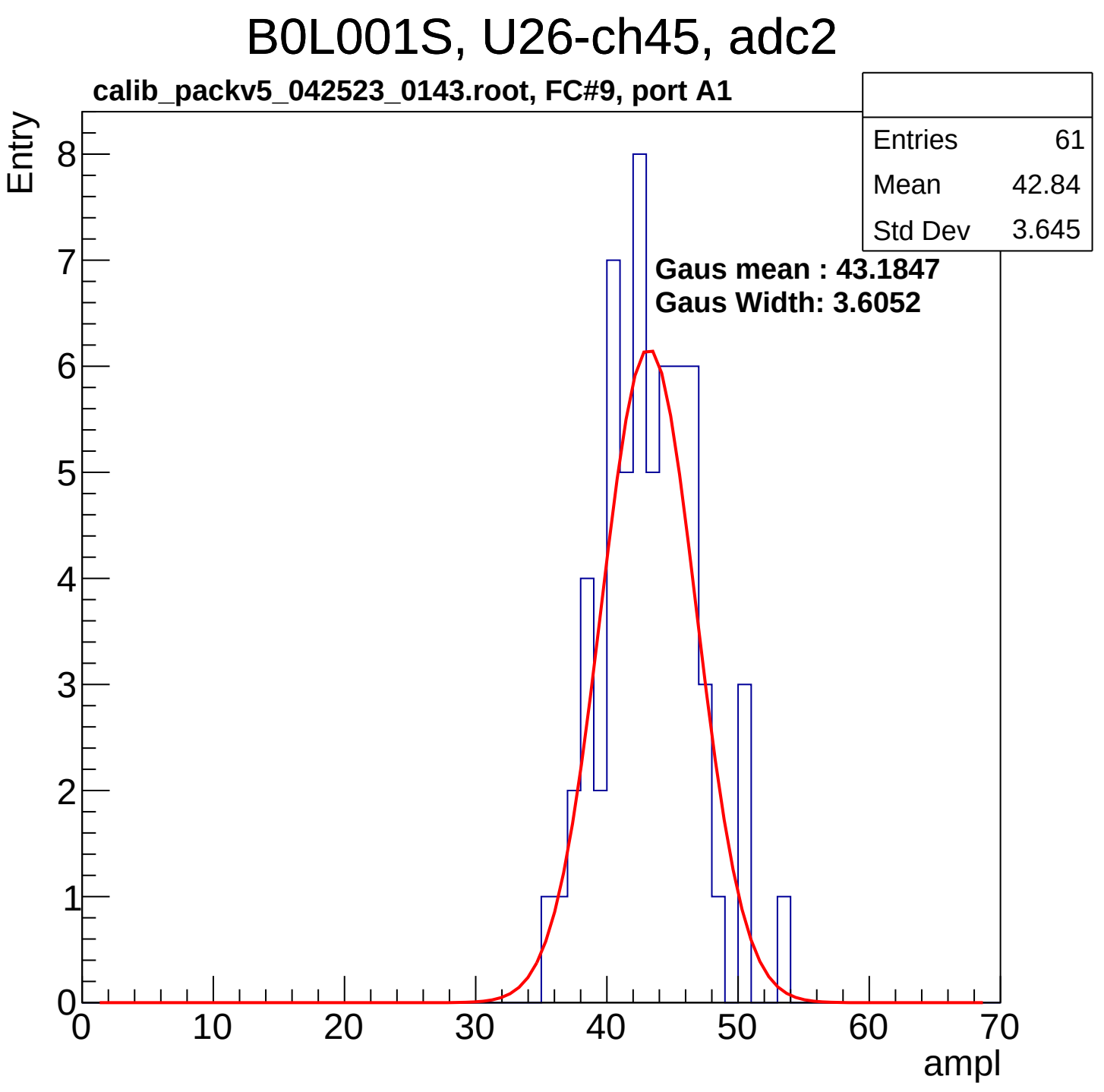
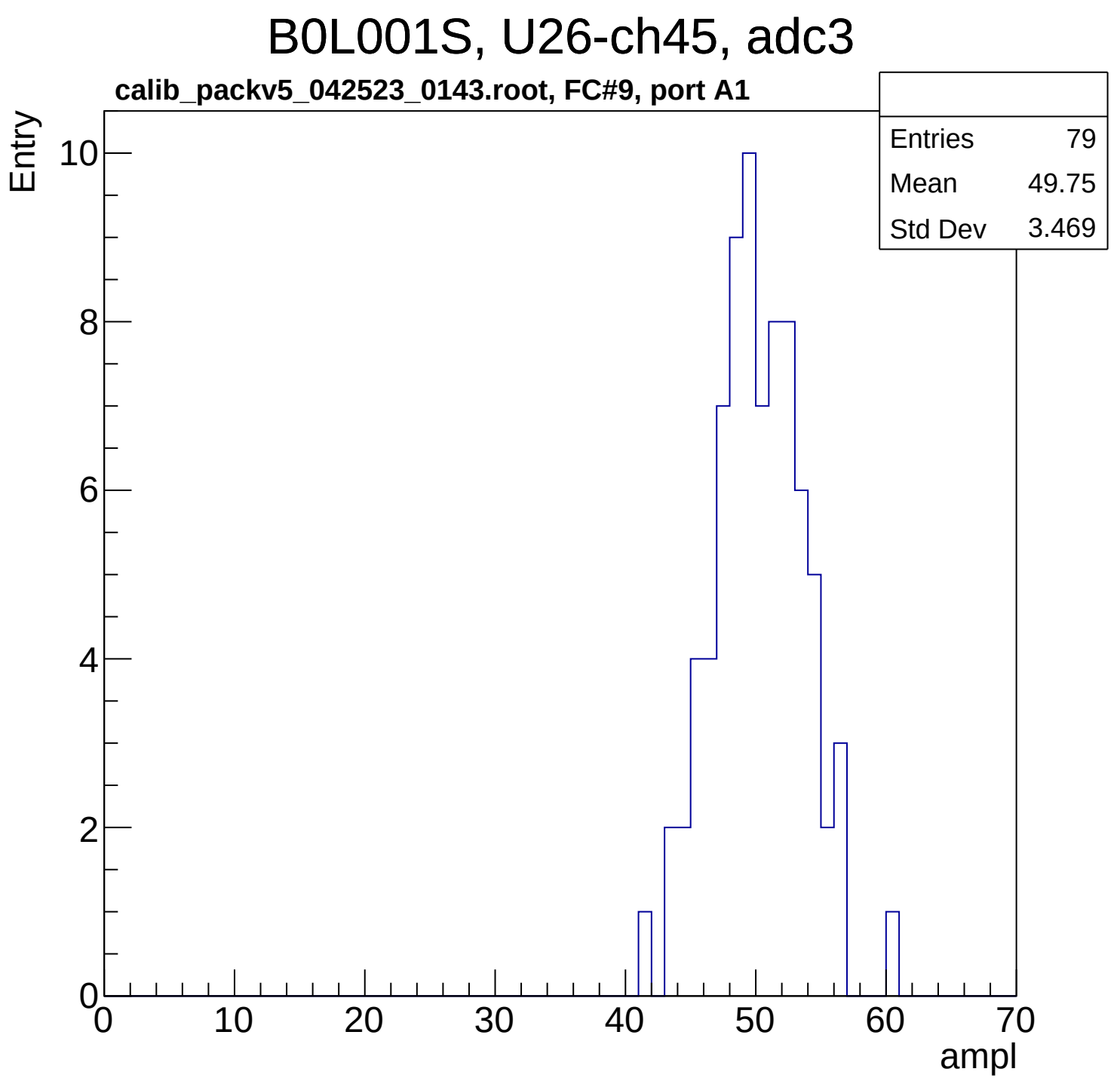
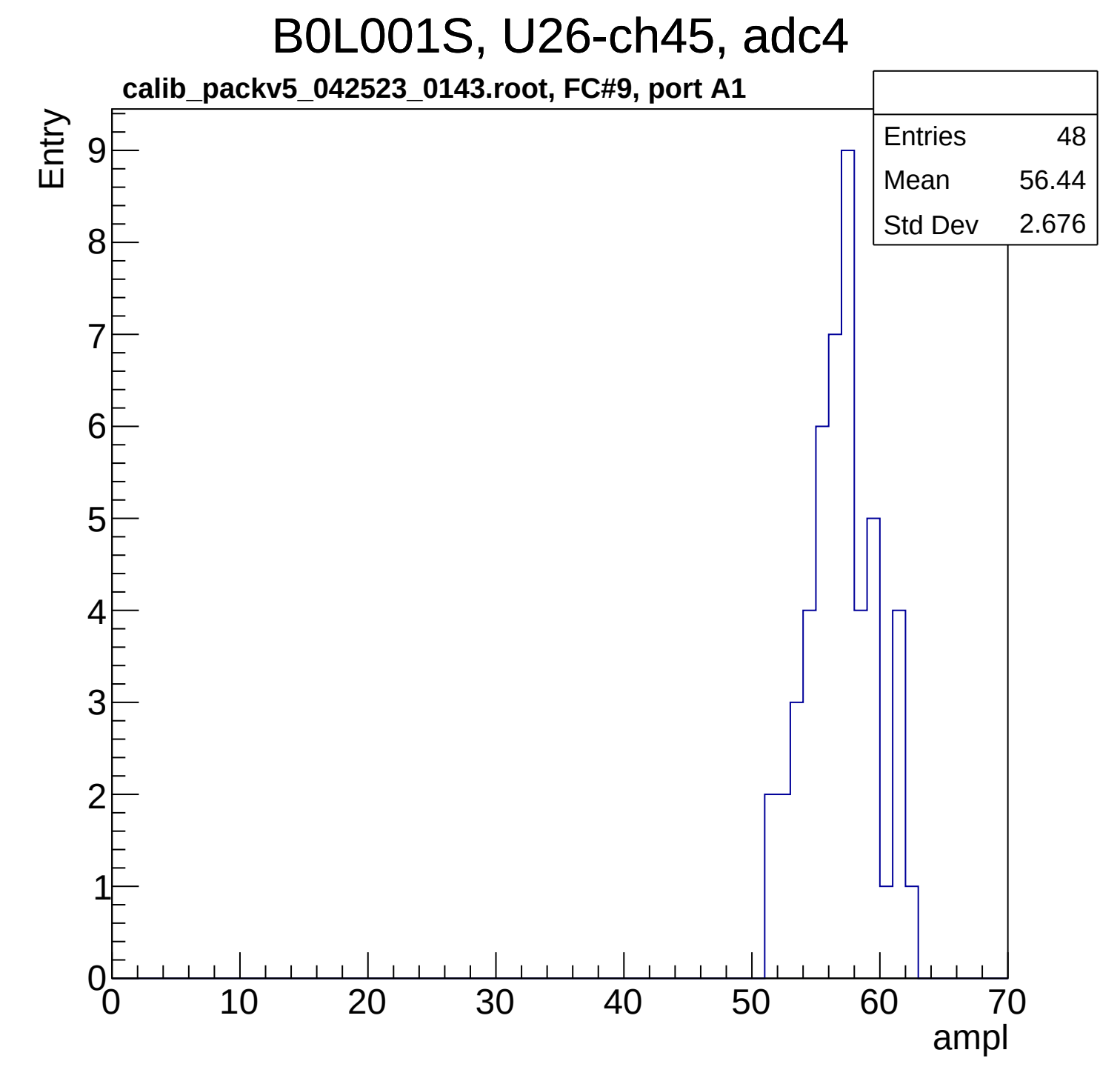
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 42.84
Std Dev 3.645
Gaus mean : 43.1847
Gaus Width: 3.6052
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 49.75
Std Dev 3.469
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.44
Std Dev 2.676
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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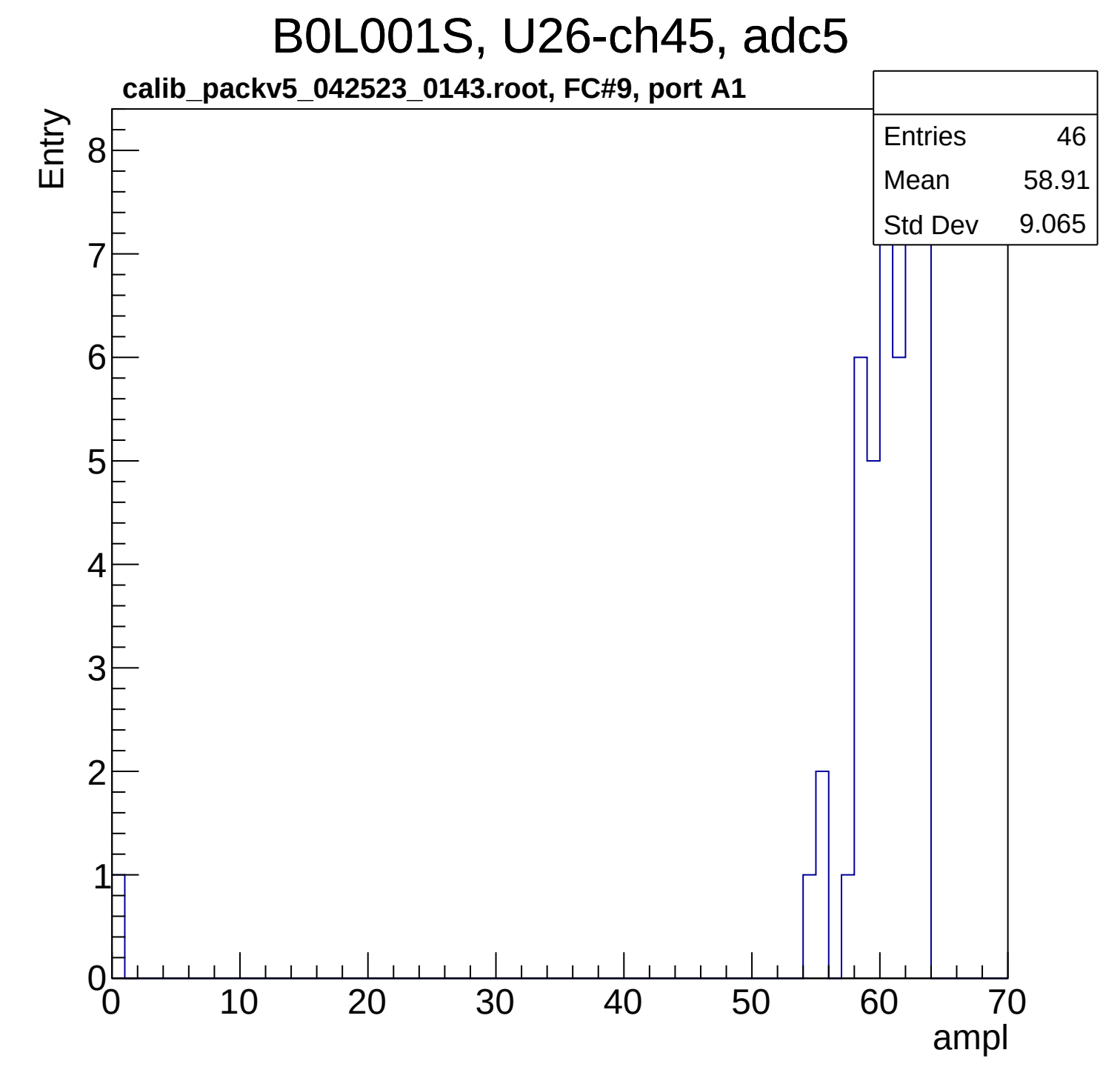
B0L001S, U26-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.91
Std Dev 9.065
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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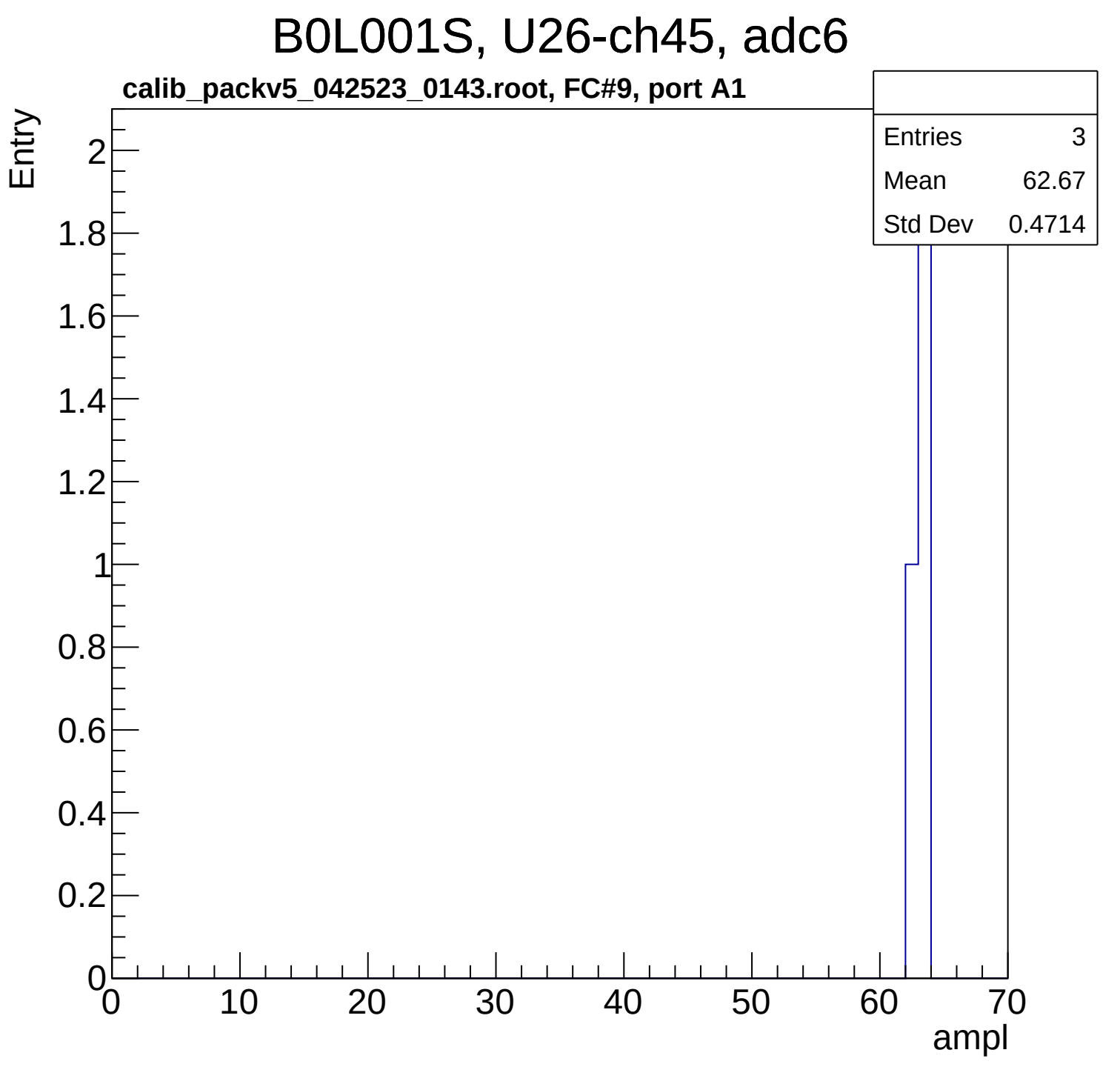
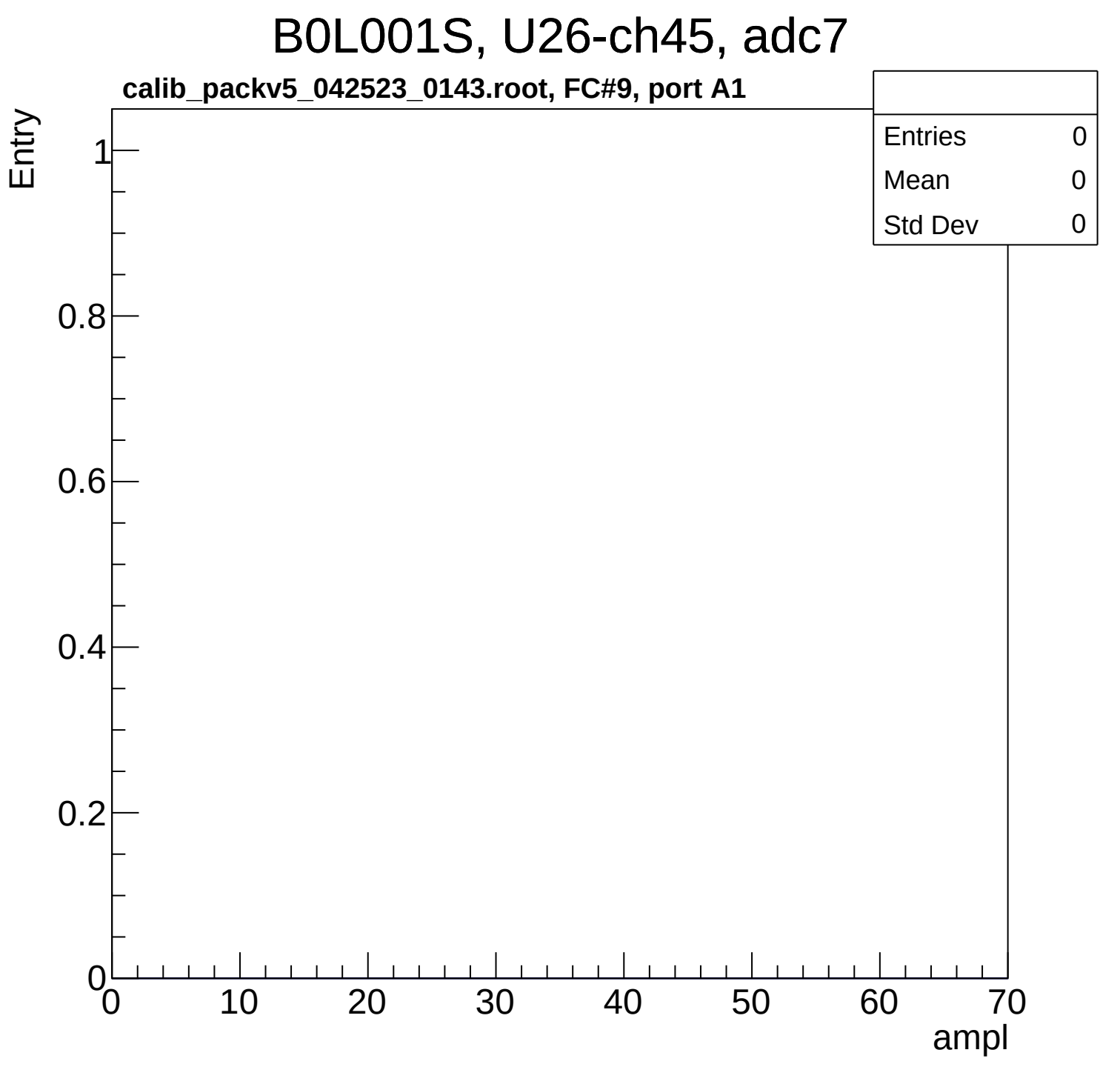
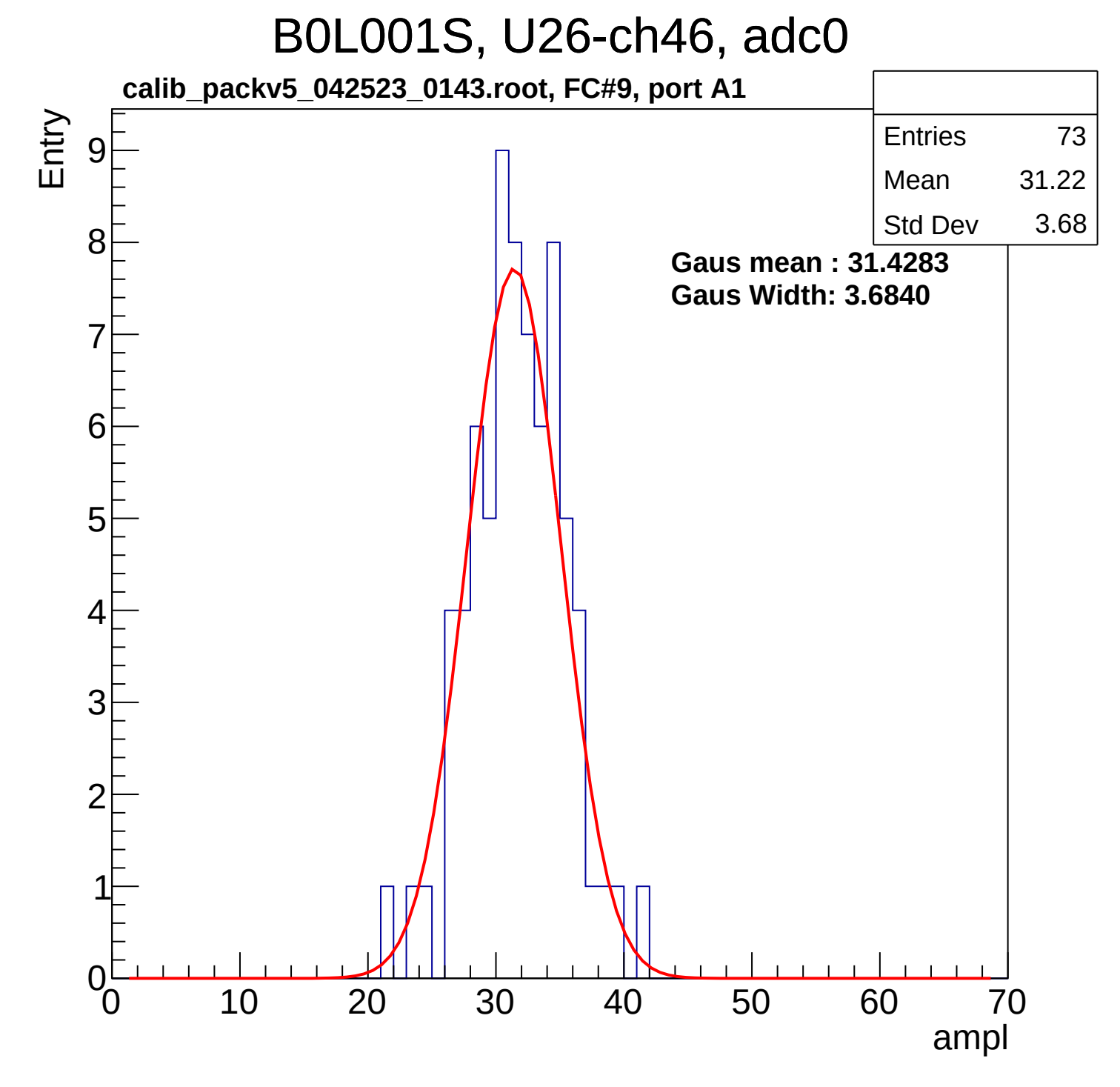

B0L001S, U26-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 31.22
Std Dev 3.68
Gaus mean : 31.4283
Gaus Width: 3.6840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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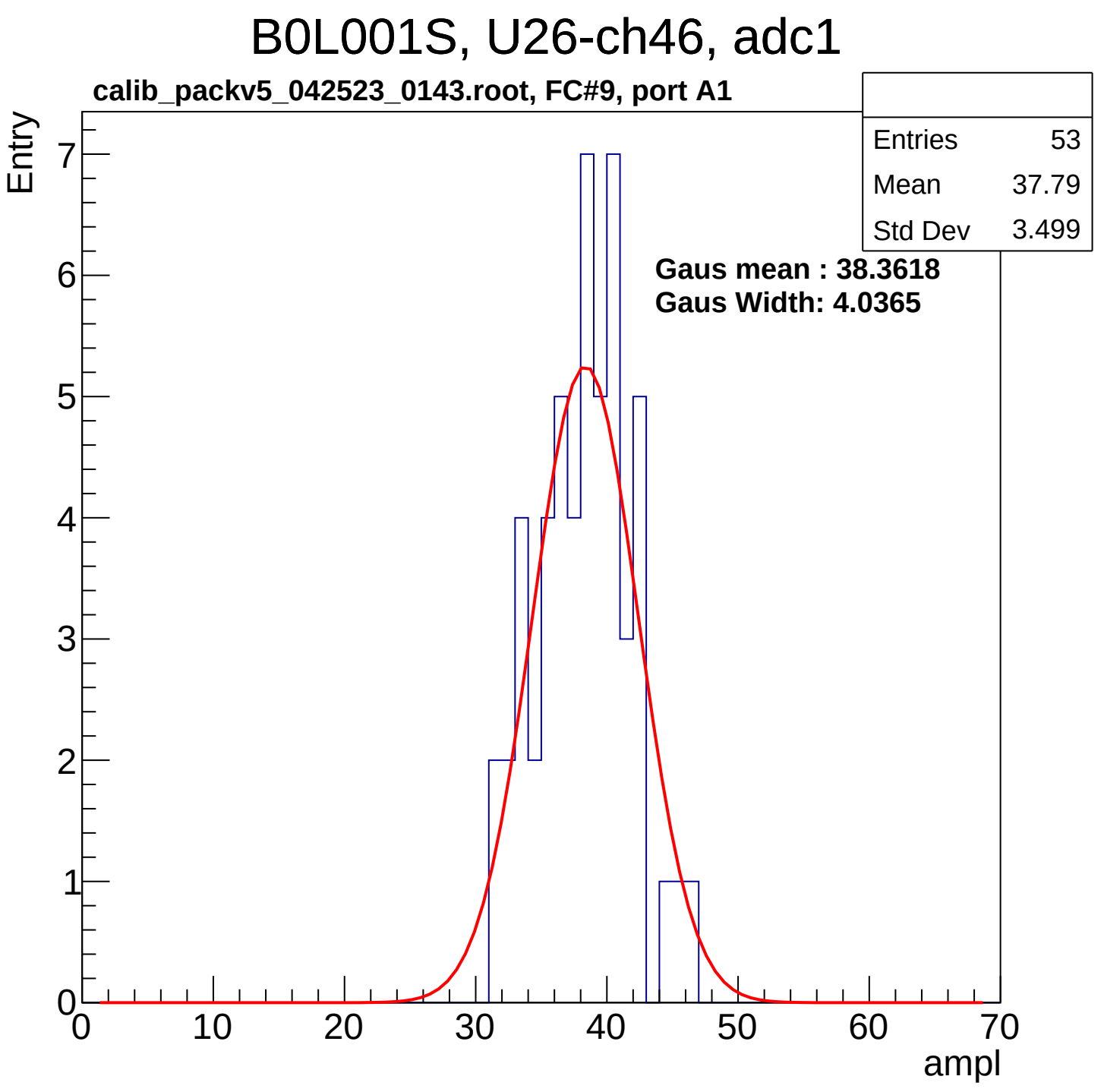
B0L001S, U26-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 37.79
Std Dev 3.499
Gaus mean : 38.3618
Gaus Width: 4.0365
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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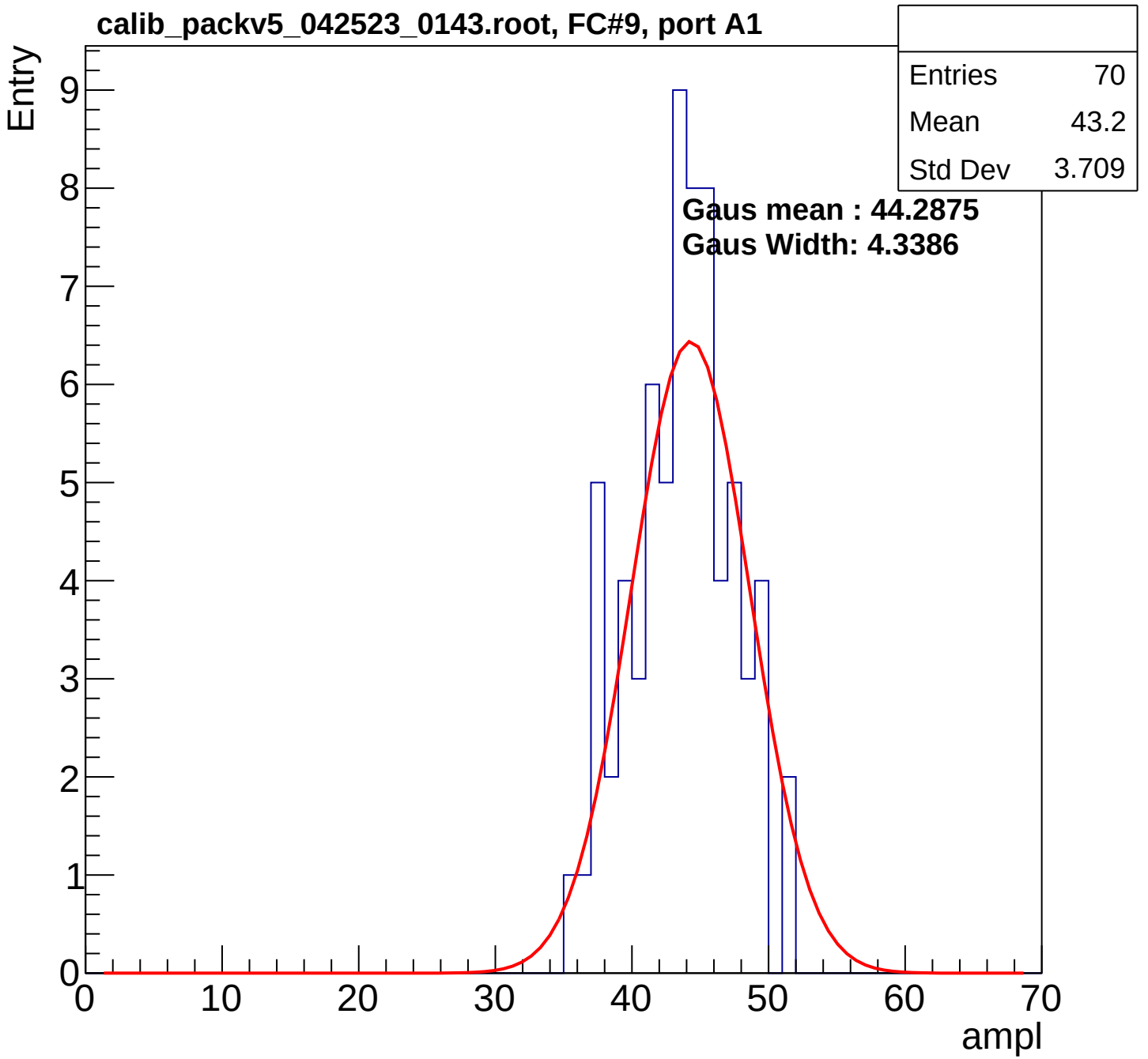
B0L001S, U26-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.2
Std Dev 3.709
Gaus mean : 44.2875
Gaus Width: 4.3386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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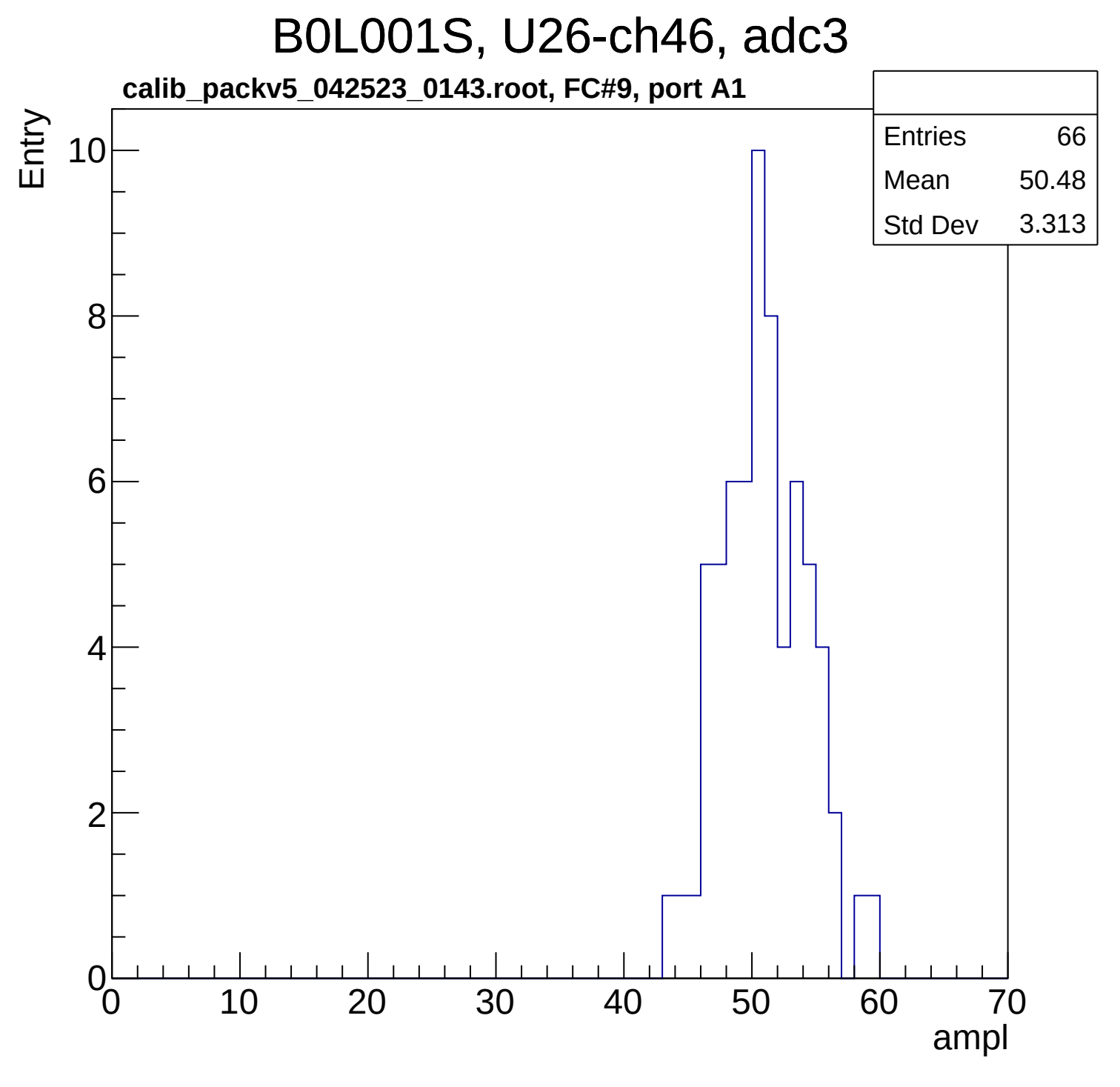
B0L001S, U26-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.48
Std Dev 3.313
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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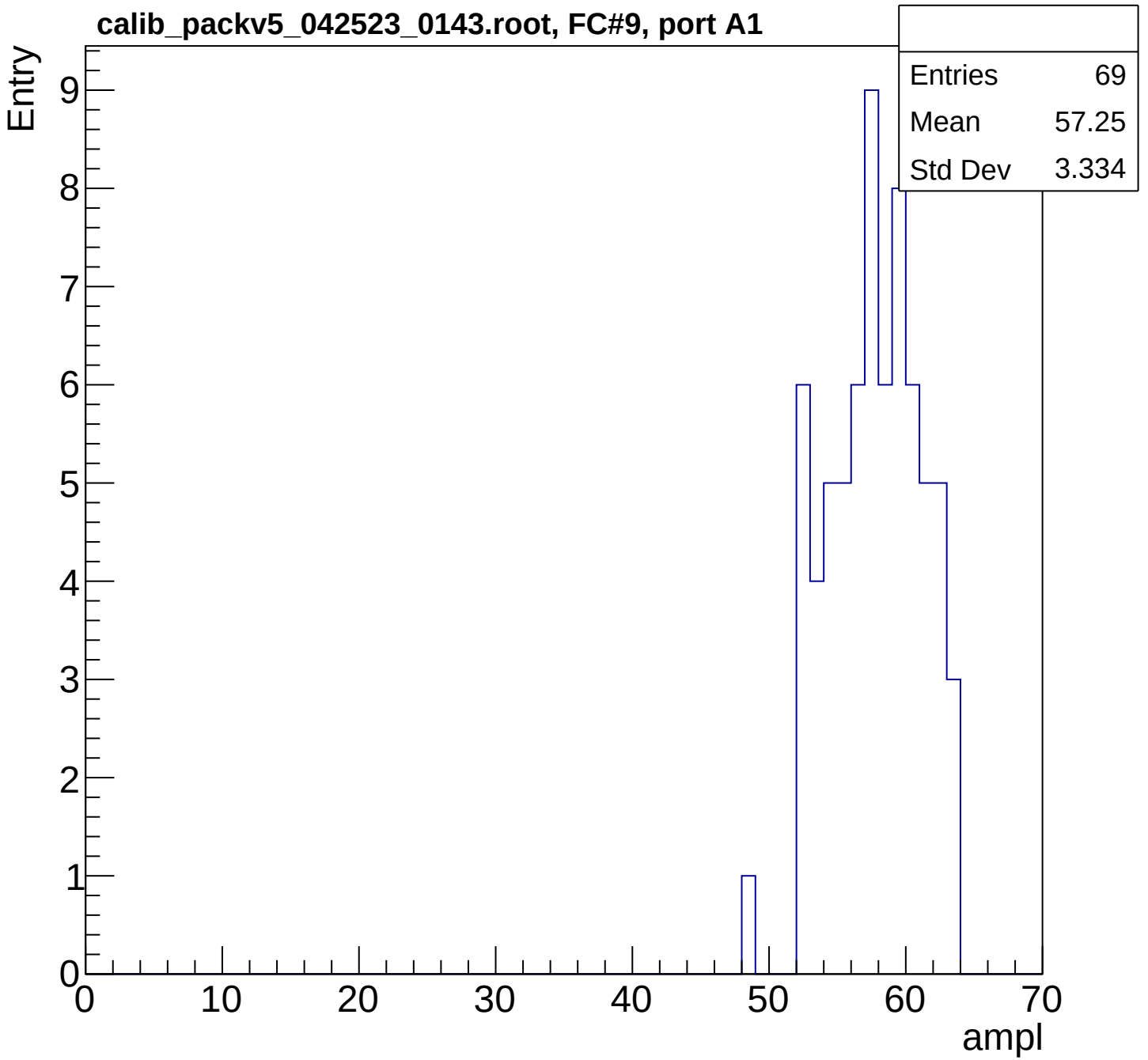
B0L001S, U26-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 57.25
Std Dev 3.334
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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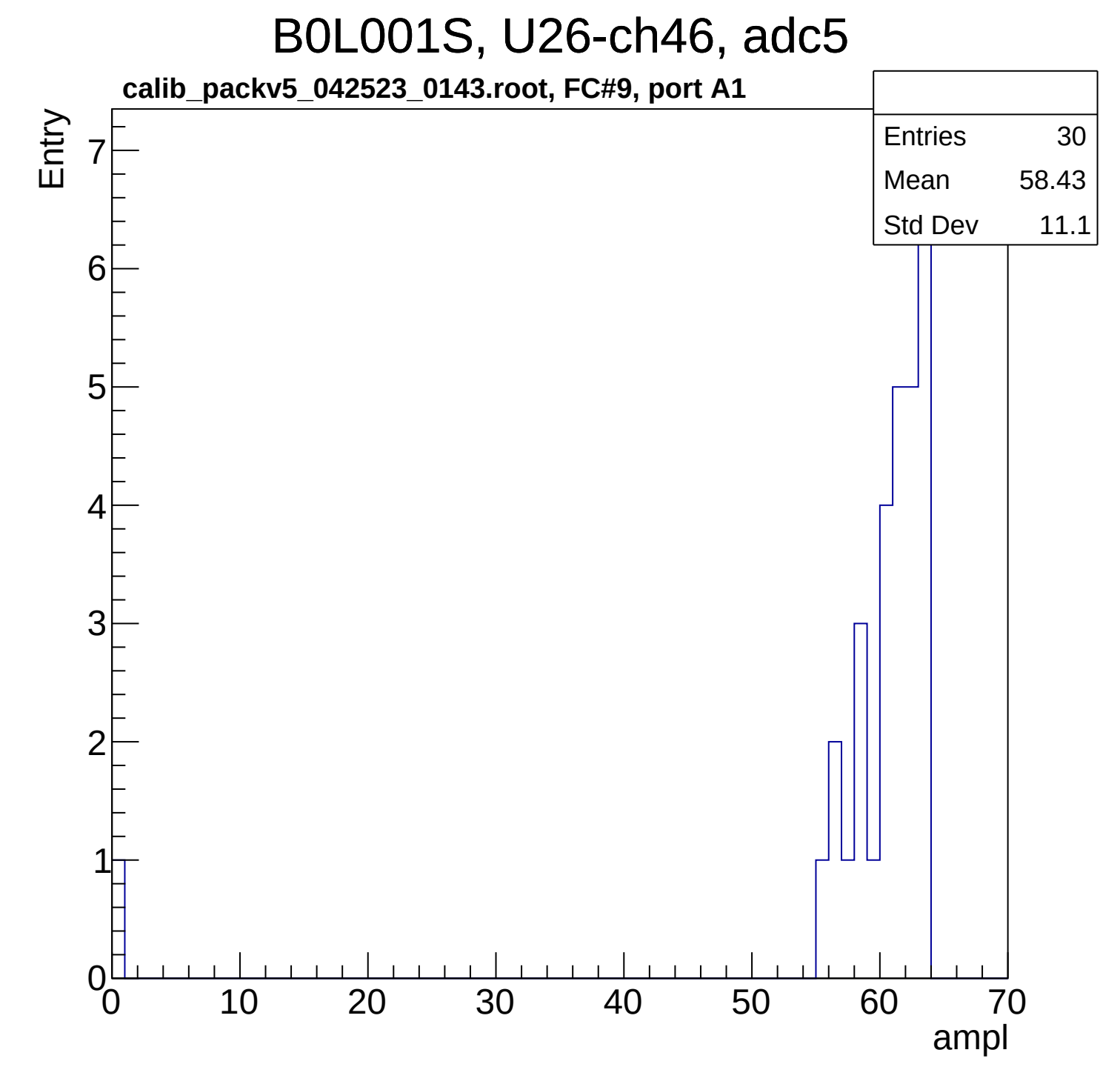
B0L001S, U26-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.43
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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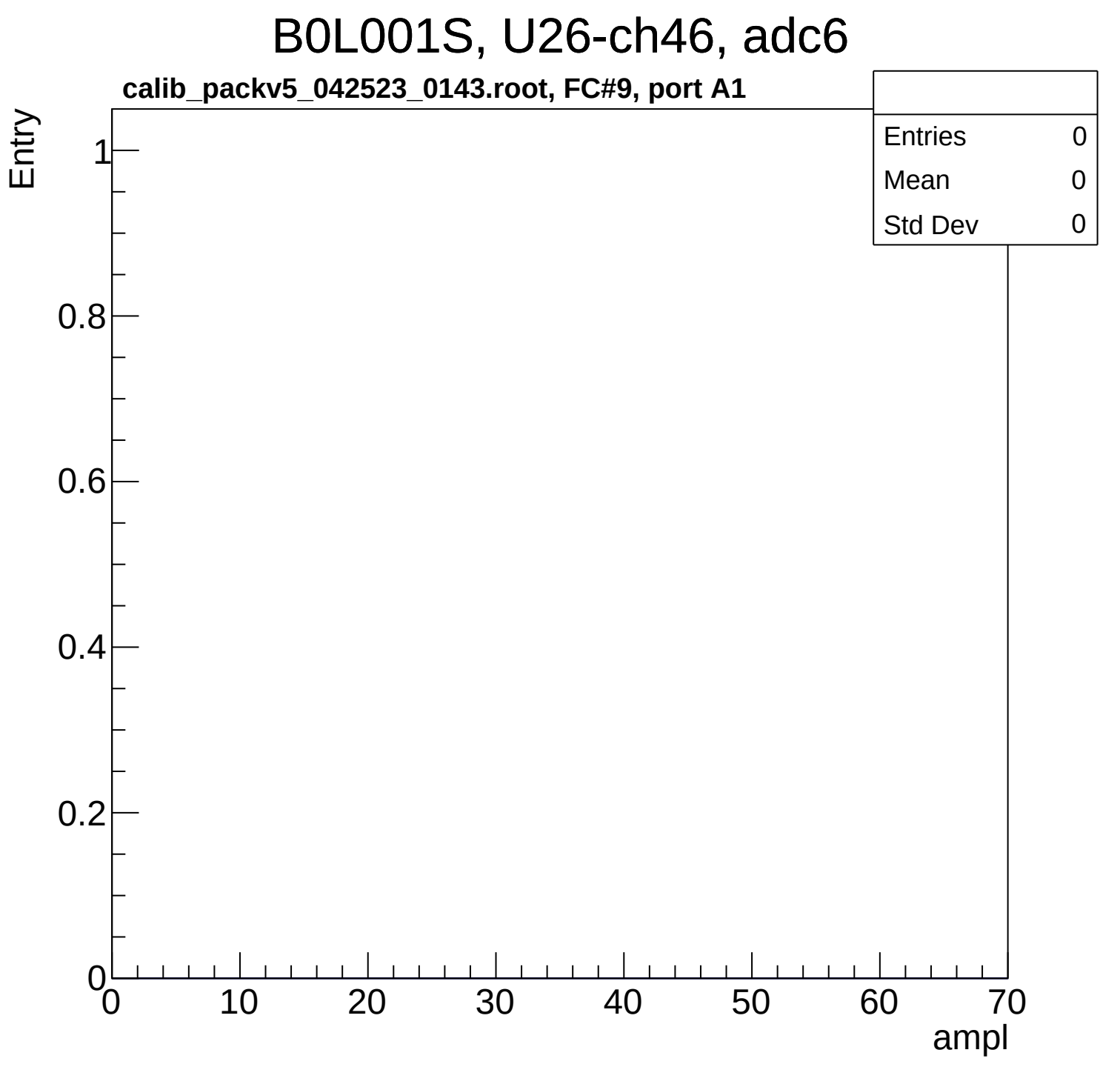
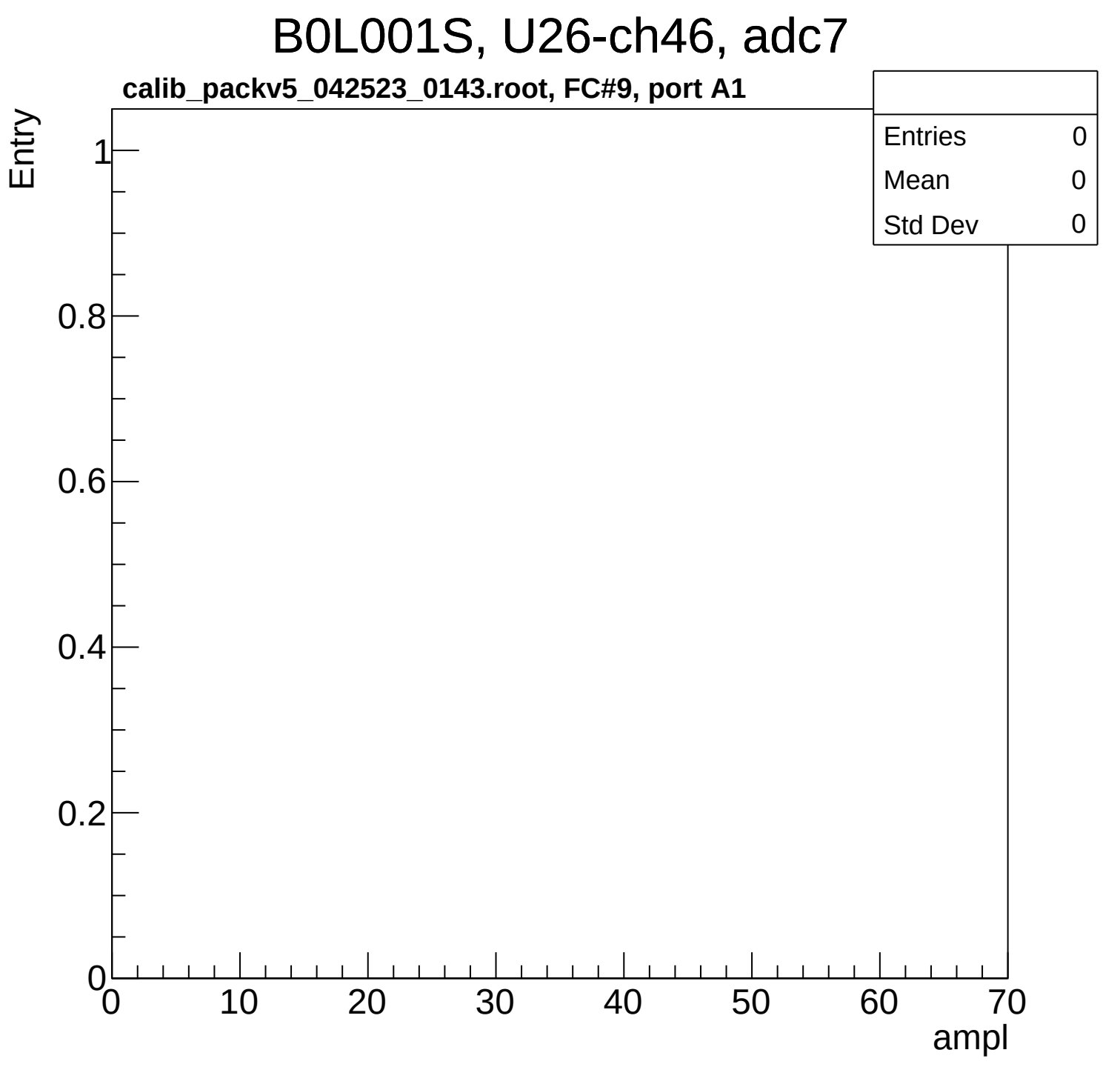
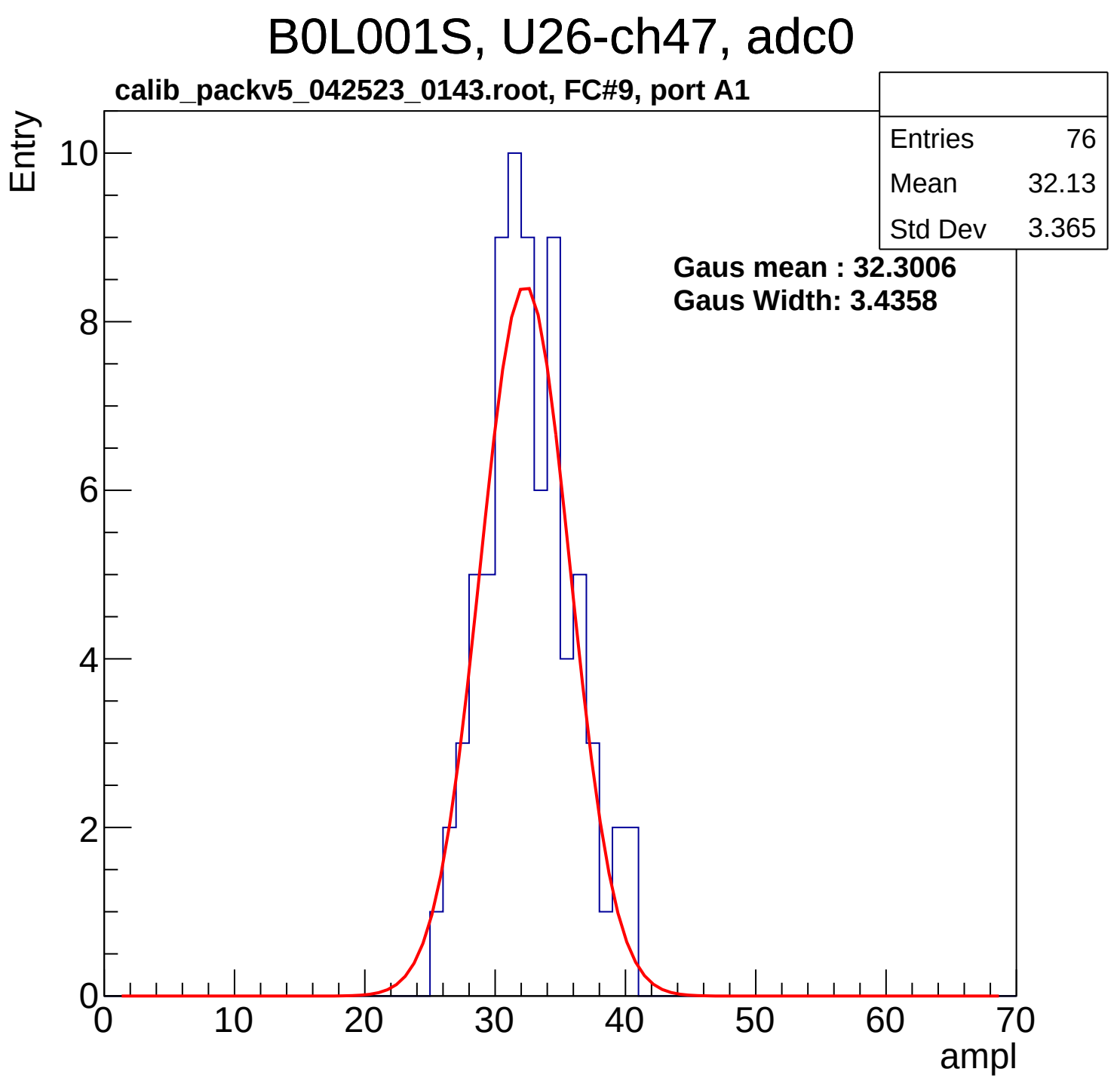
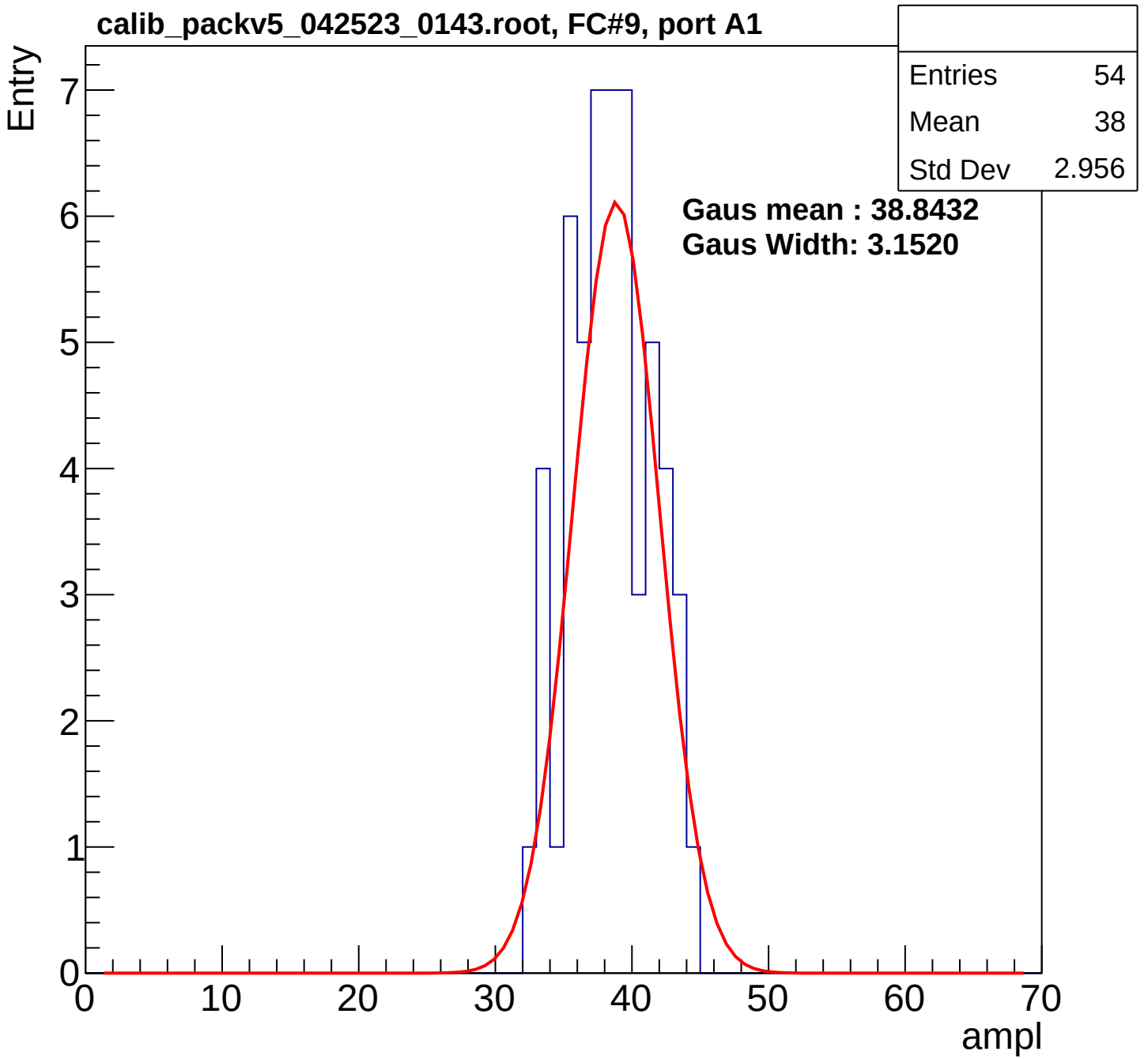

B0L001S, U26-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 32.13
Std Dev 3.365
Gaus mean : 32.3006
Gaus Width: 3.4358
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38
Std Dev 2.956
Gaus mean : 38.8432
Gaus Width: 3.1520
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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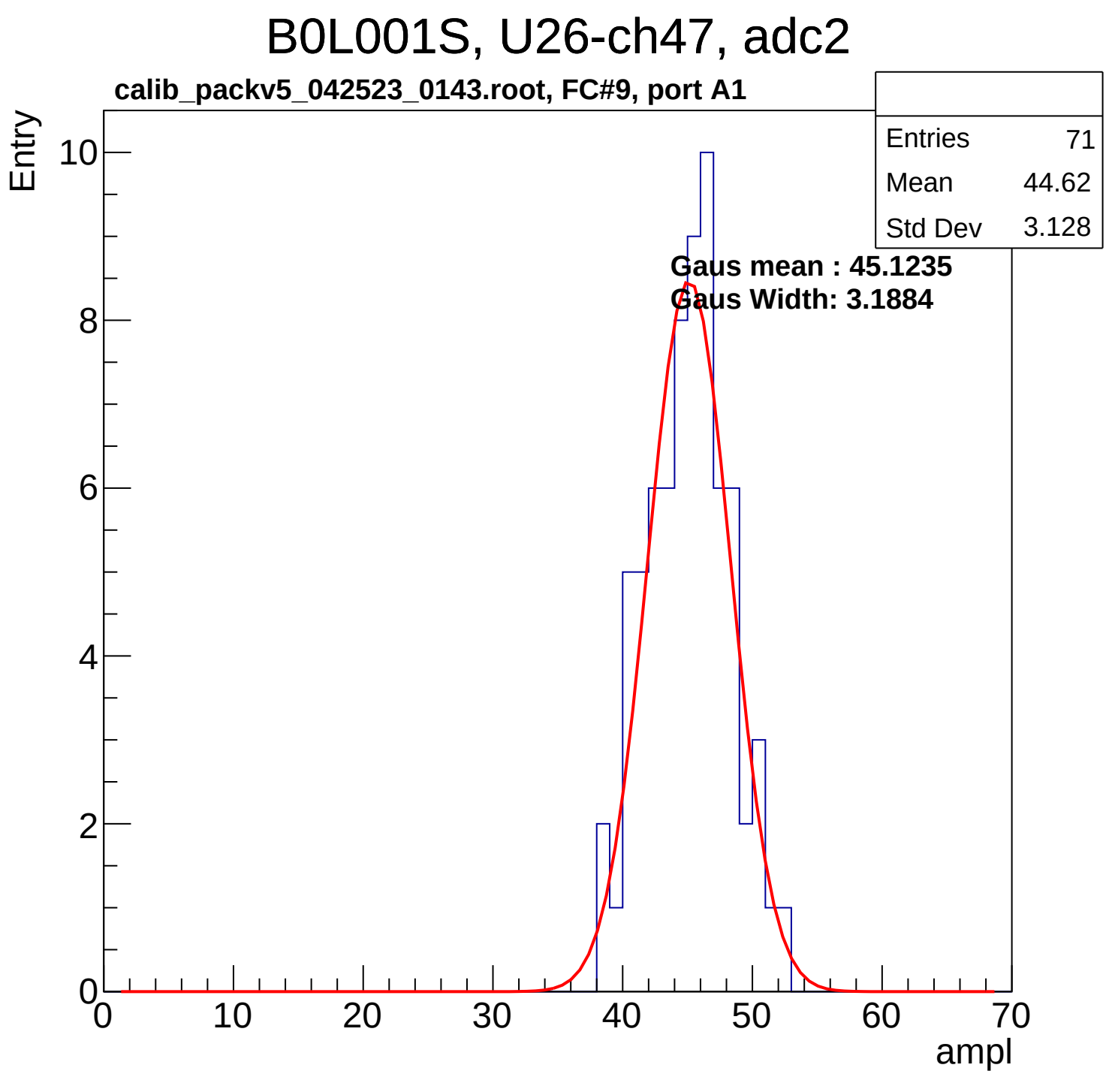
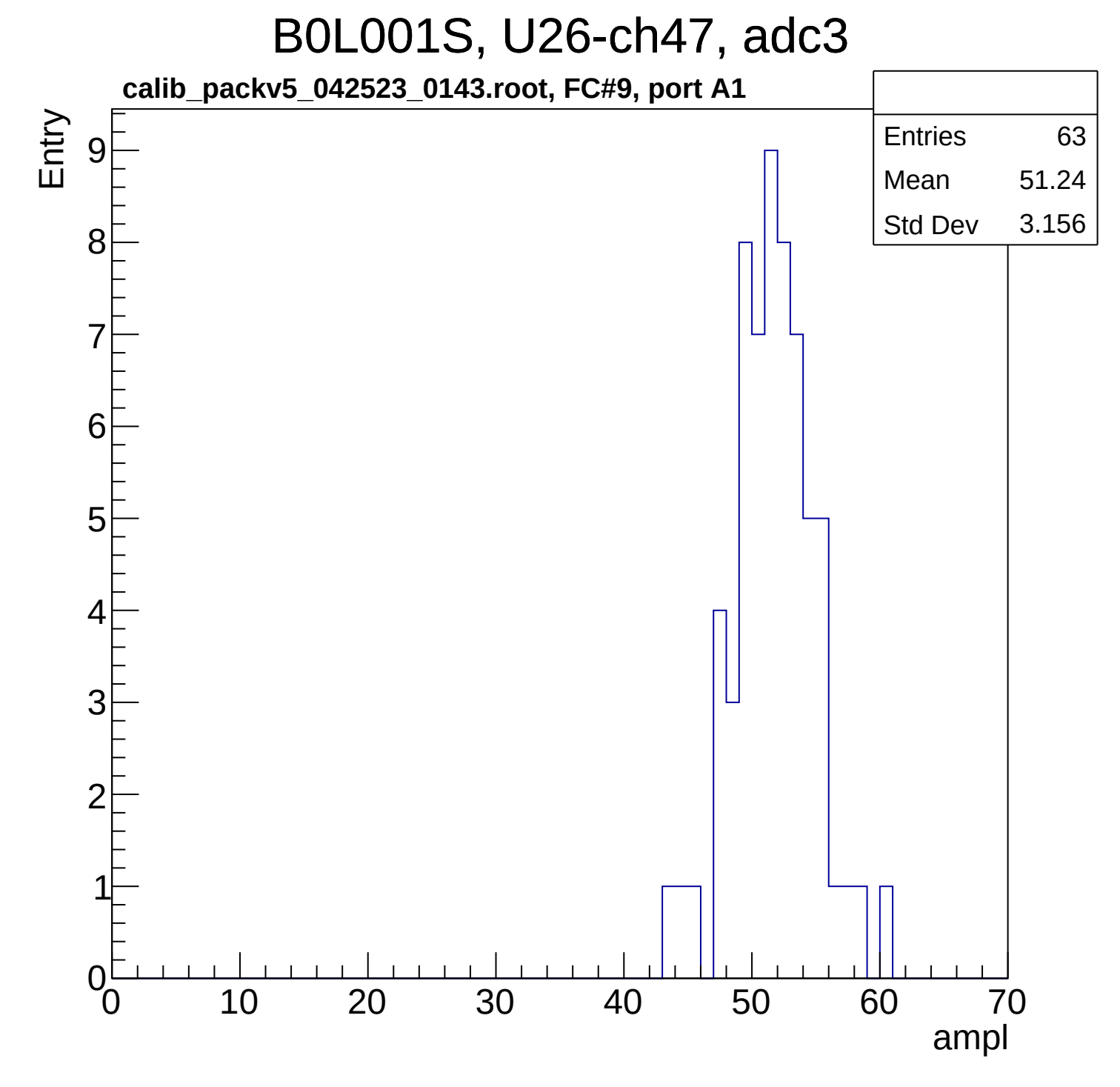
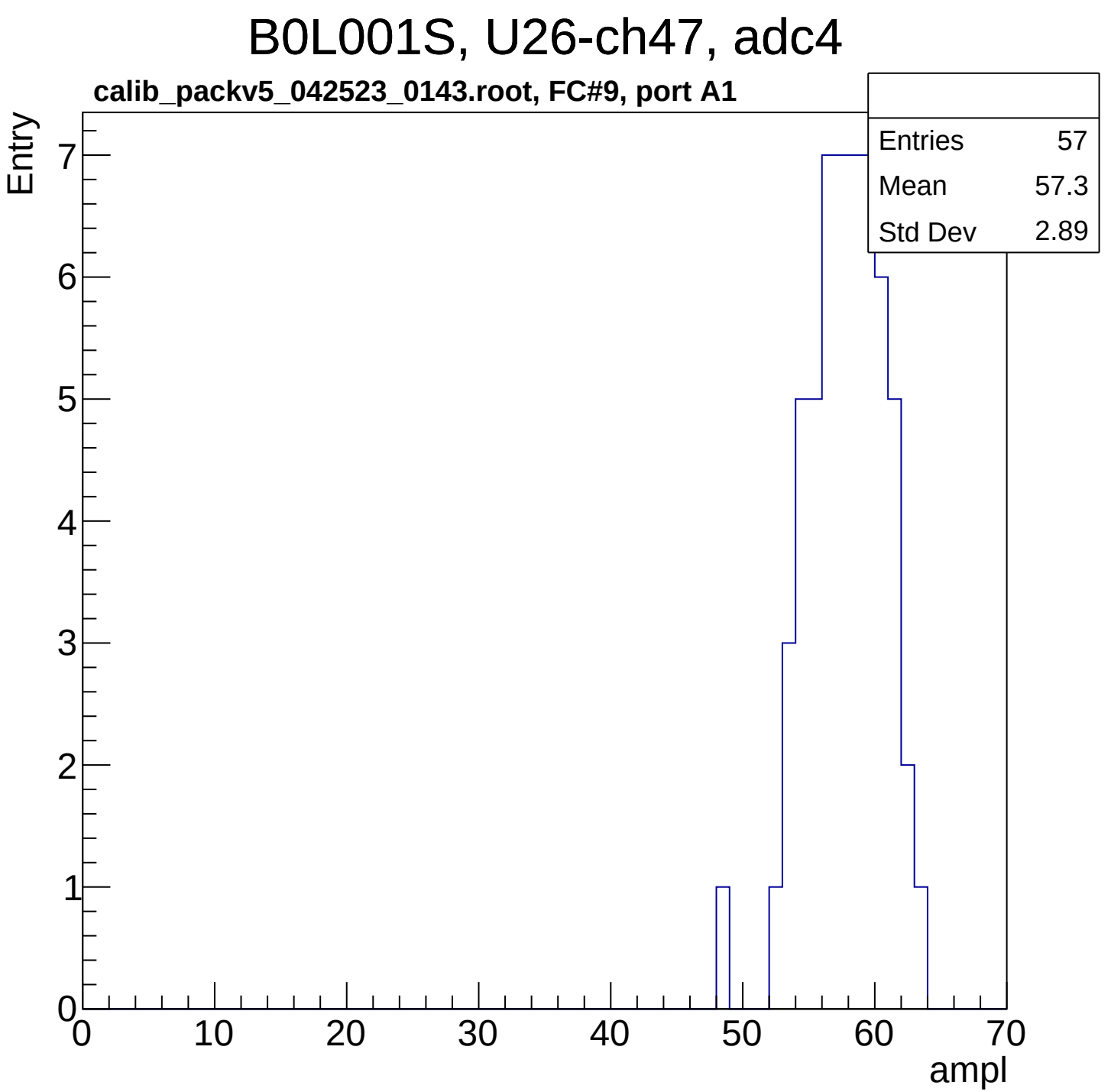
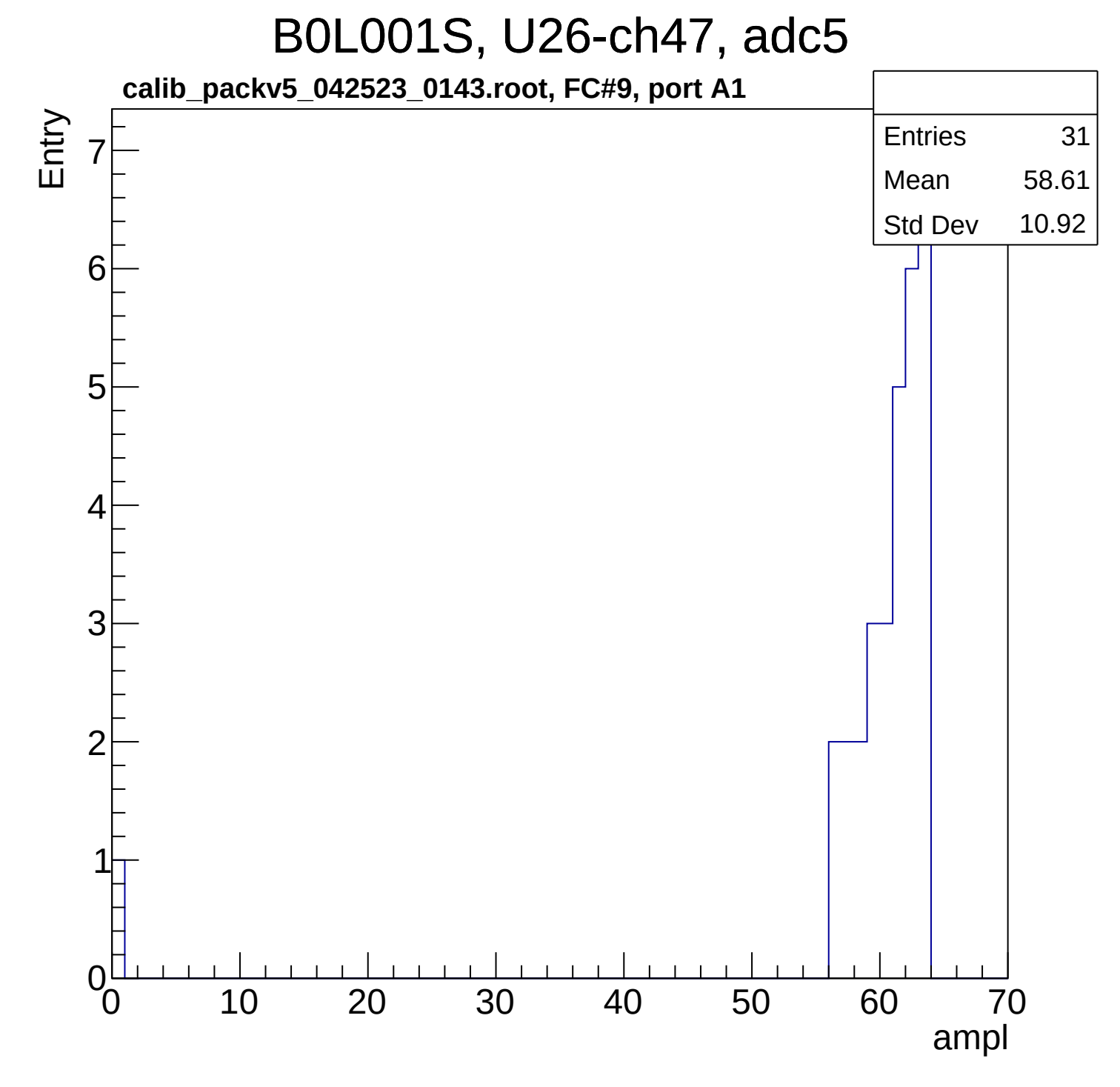
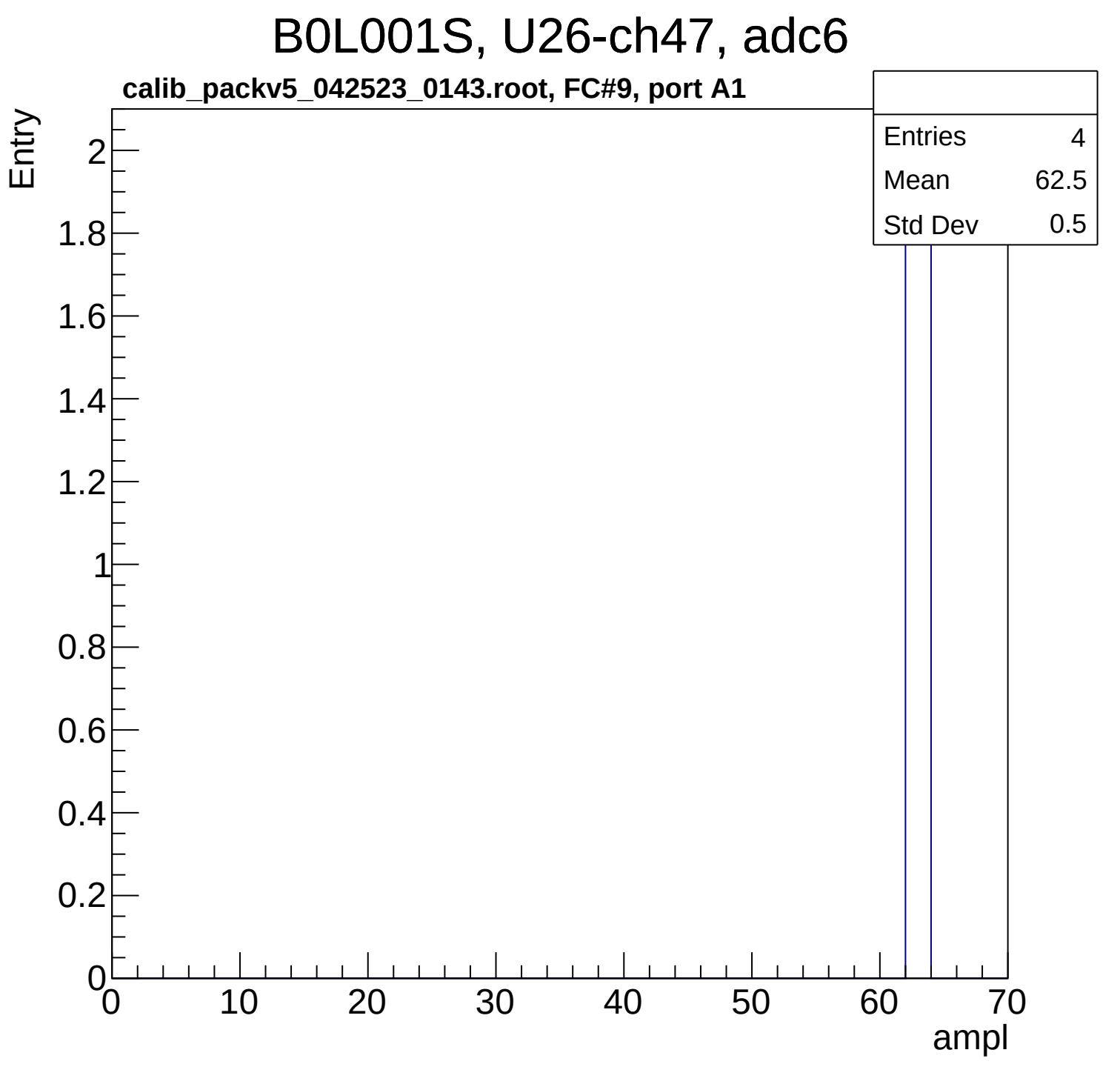

B0L001S, U26-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.62
Std Dev 3.128
Gaus mean : 45.1235
Gaus Width: 3.1884
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.24
Std Dev 3.156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.3
Std Dev 2.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.61
Std Dev 10.92
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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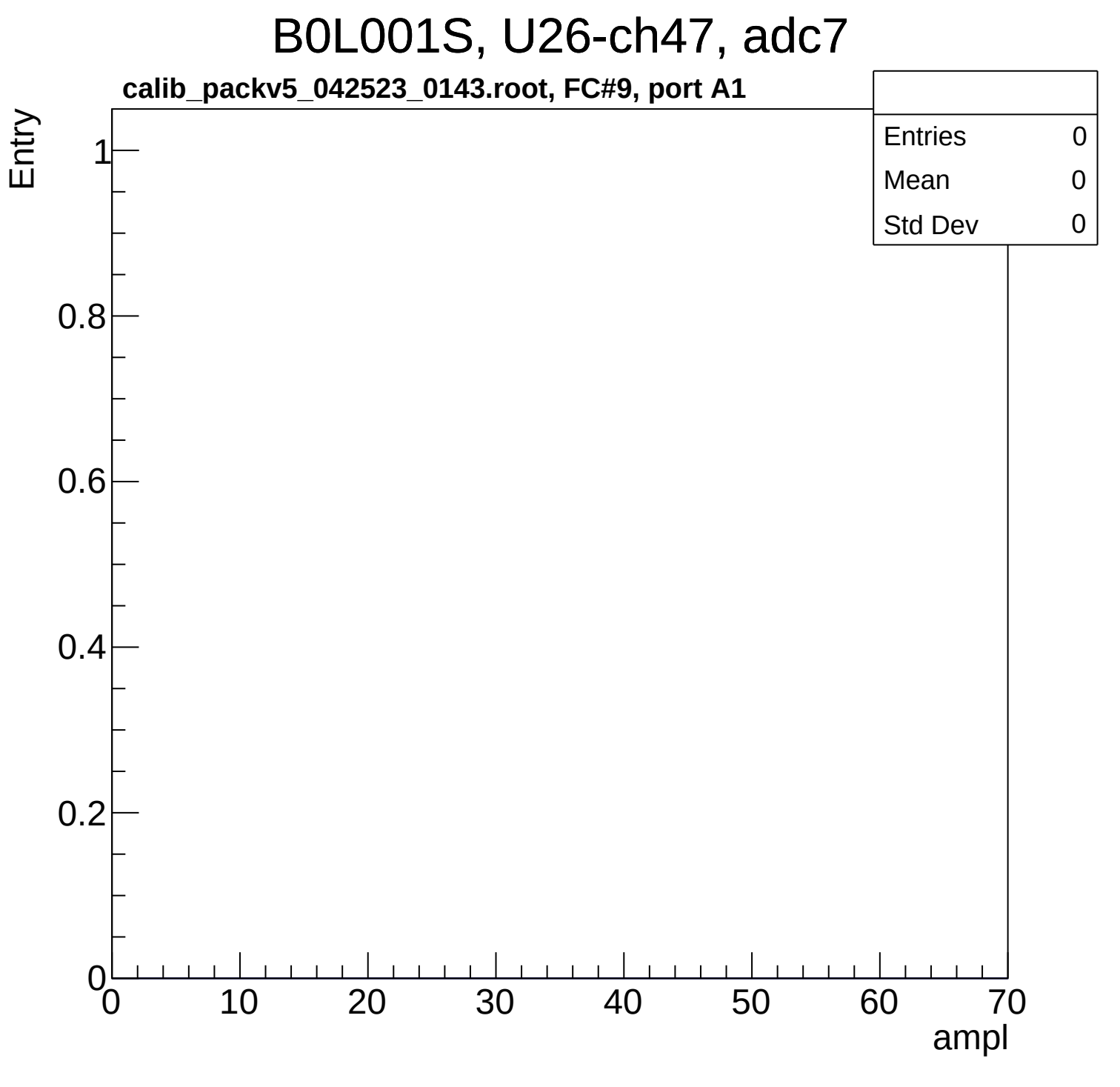
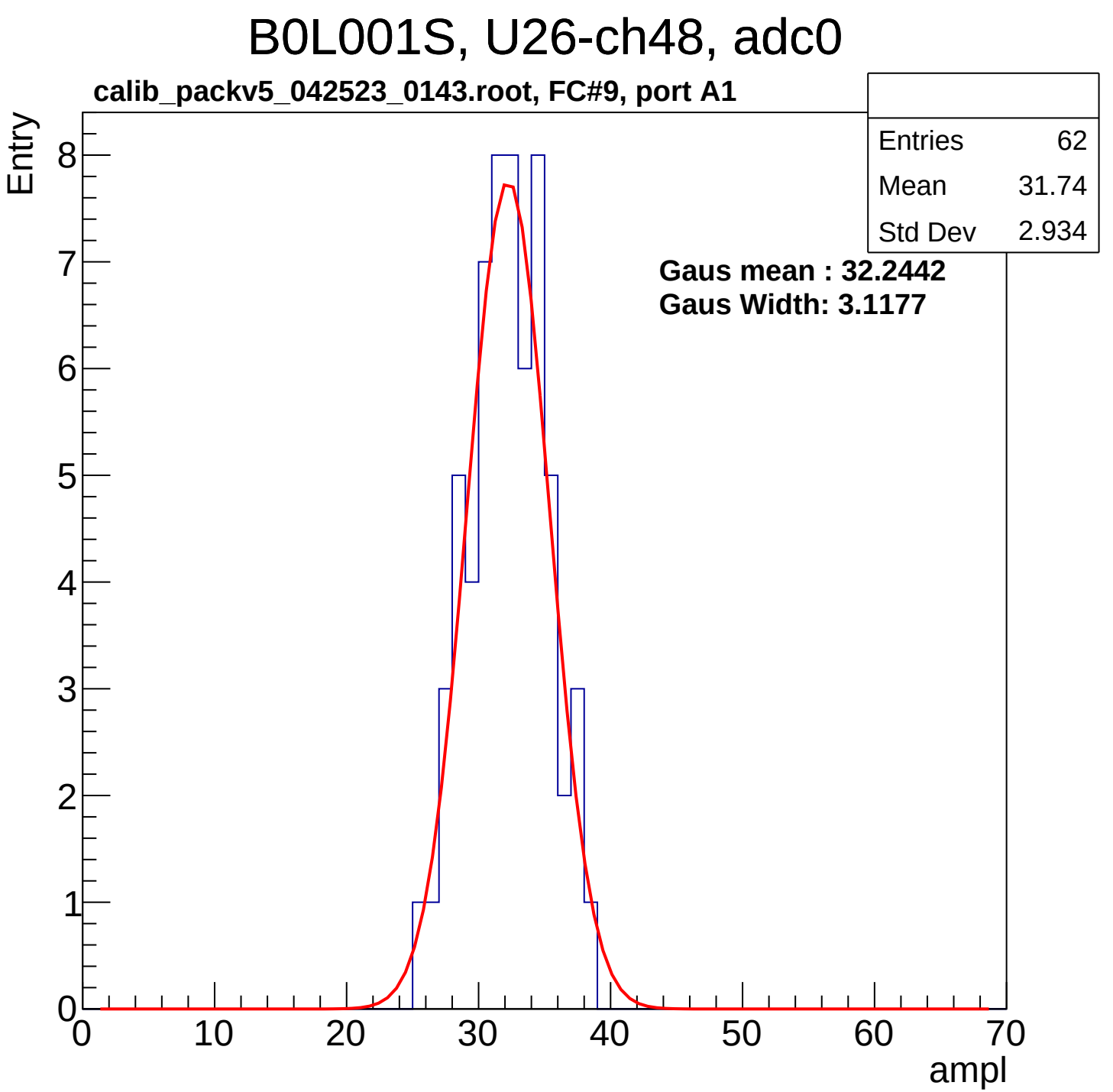
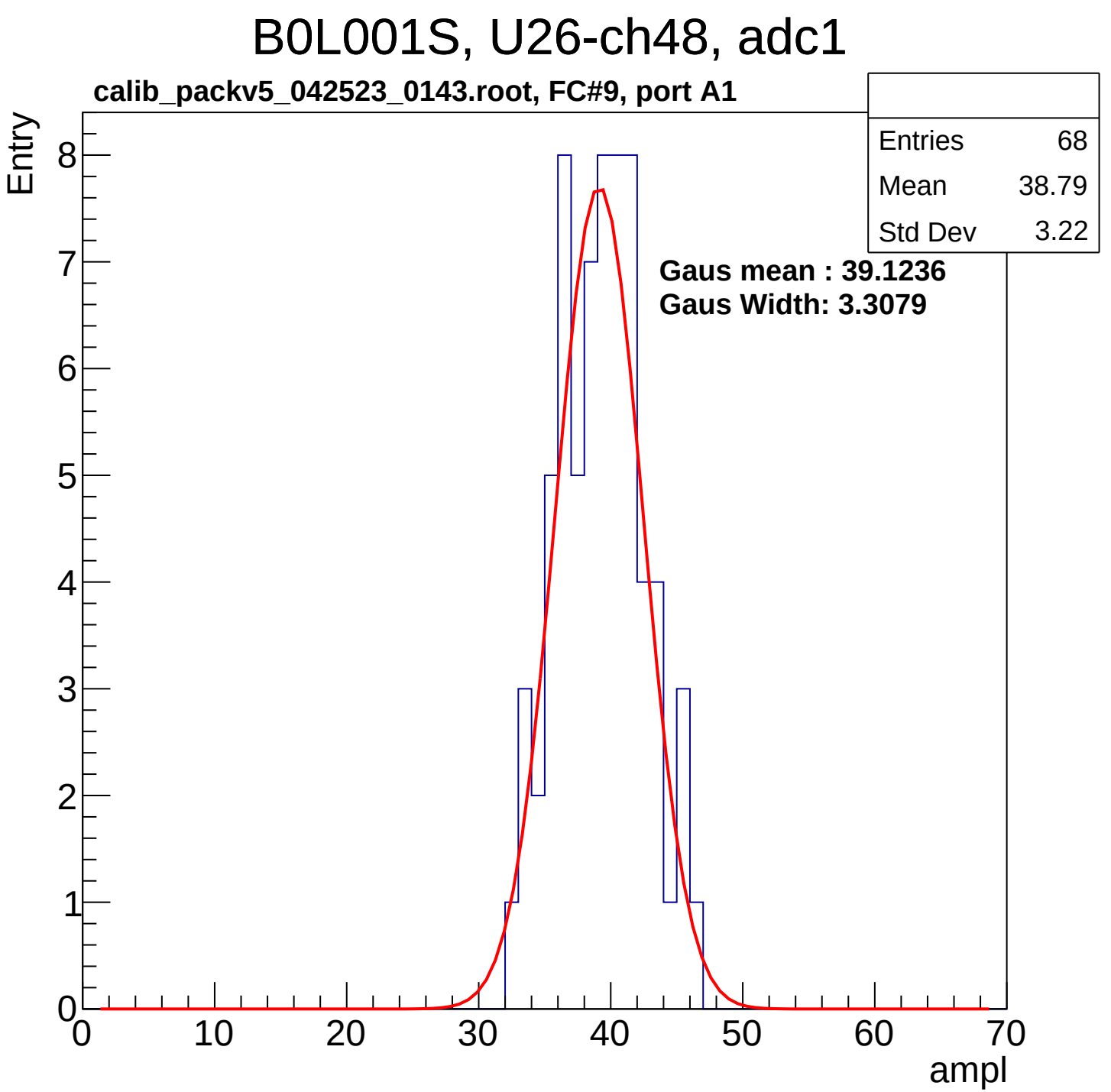
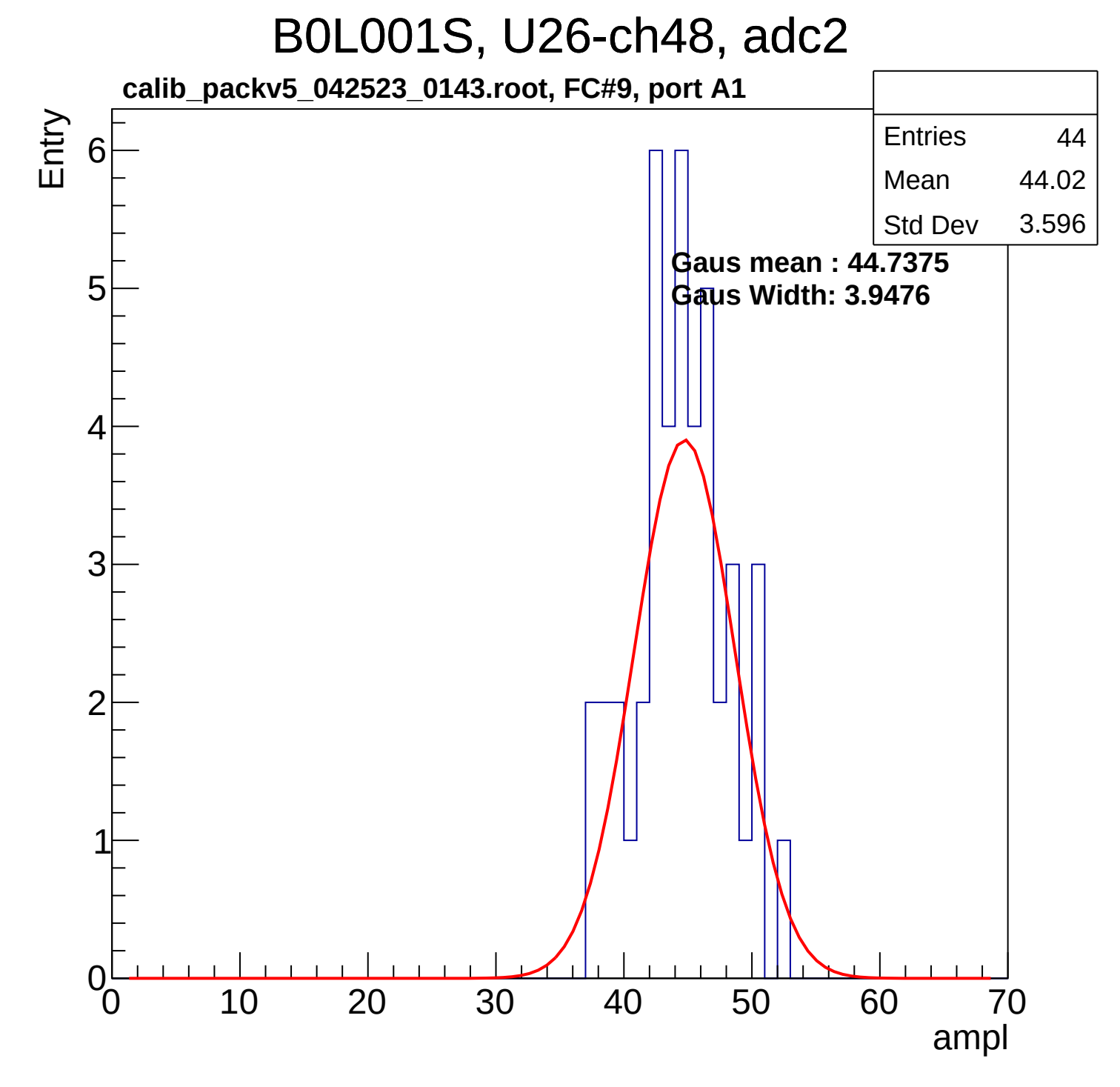
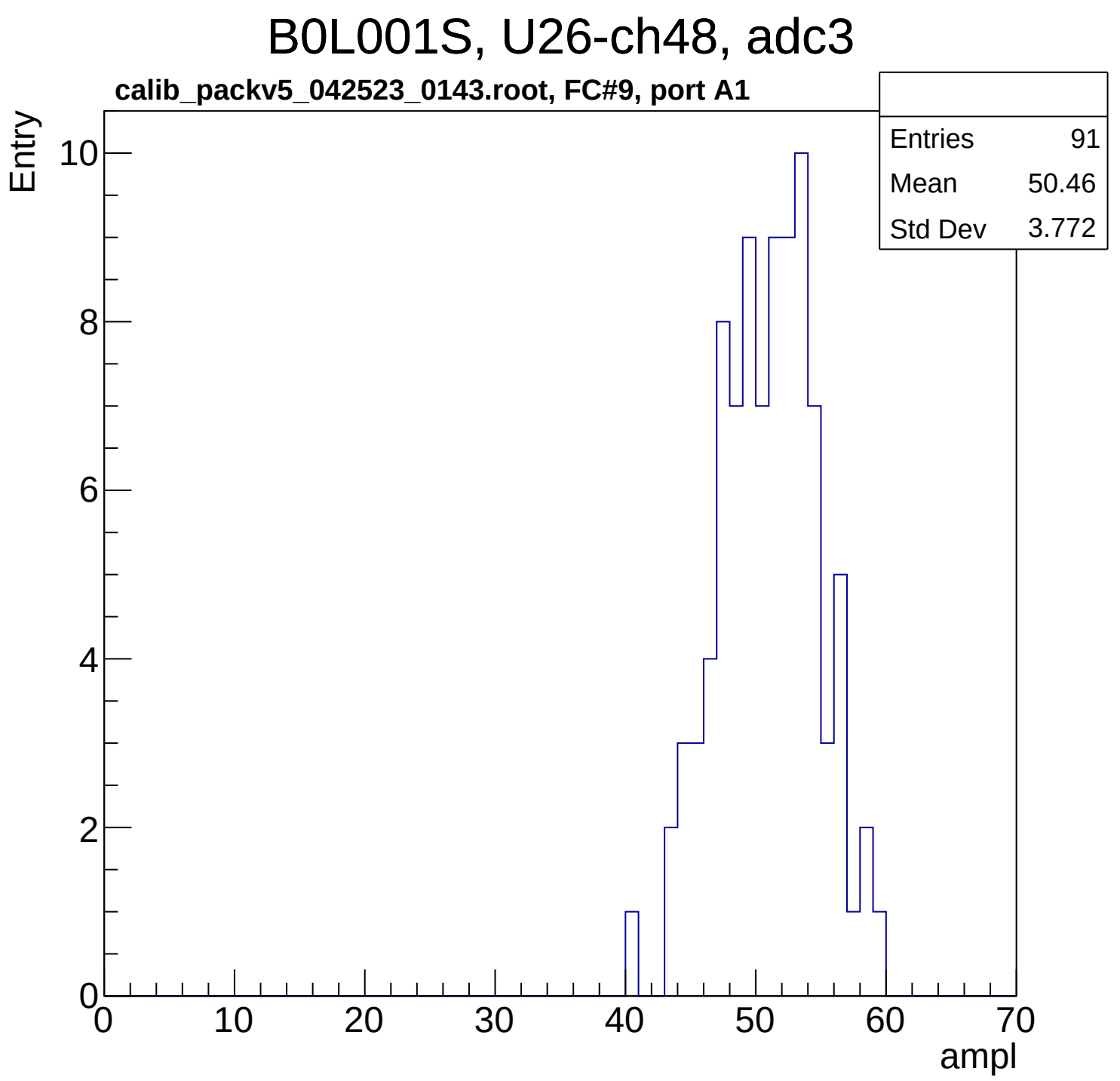
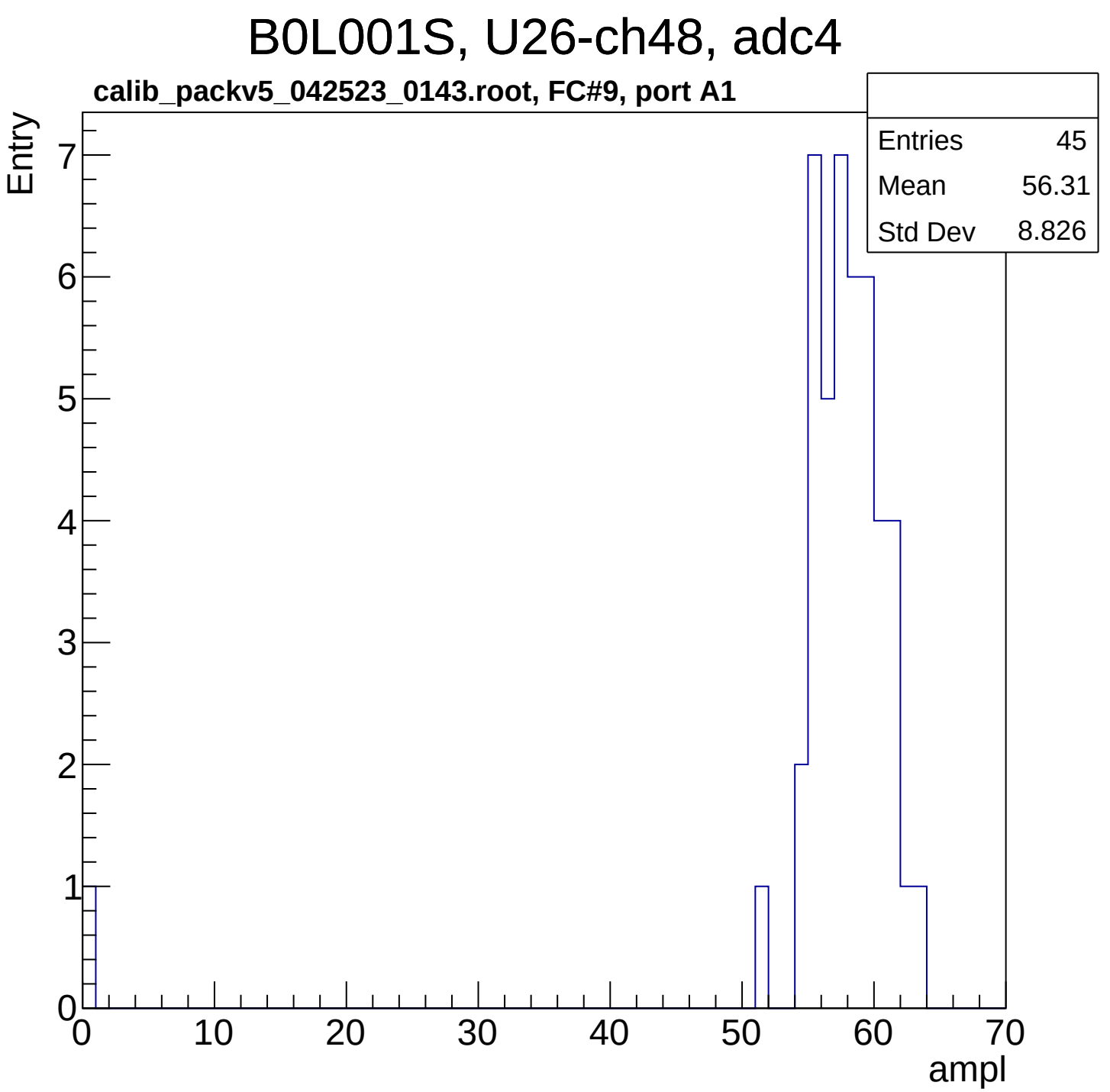

B0L001S, U26-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.74
Std Dev 2.934
Gaus mean : 32.2442
Gaus Width: 3.1177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 38.79
Std Dev 3.22
Gaus mean : 39.1236
Gaus Width: 3.3079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 44.02
Std Dev 3.596
Gaus mean : 44.7375
Gaus Width: 3.9476
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U26-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 50.46
Std Dev 3.772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.31
Std Dev 8.826
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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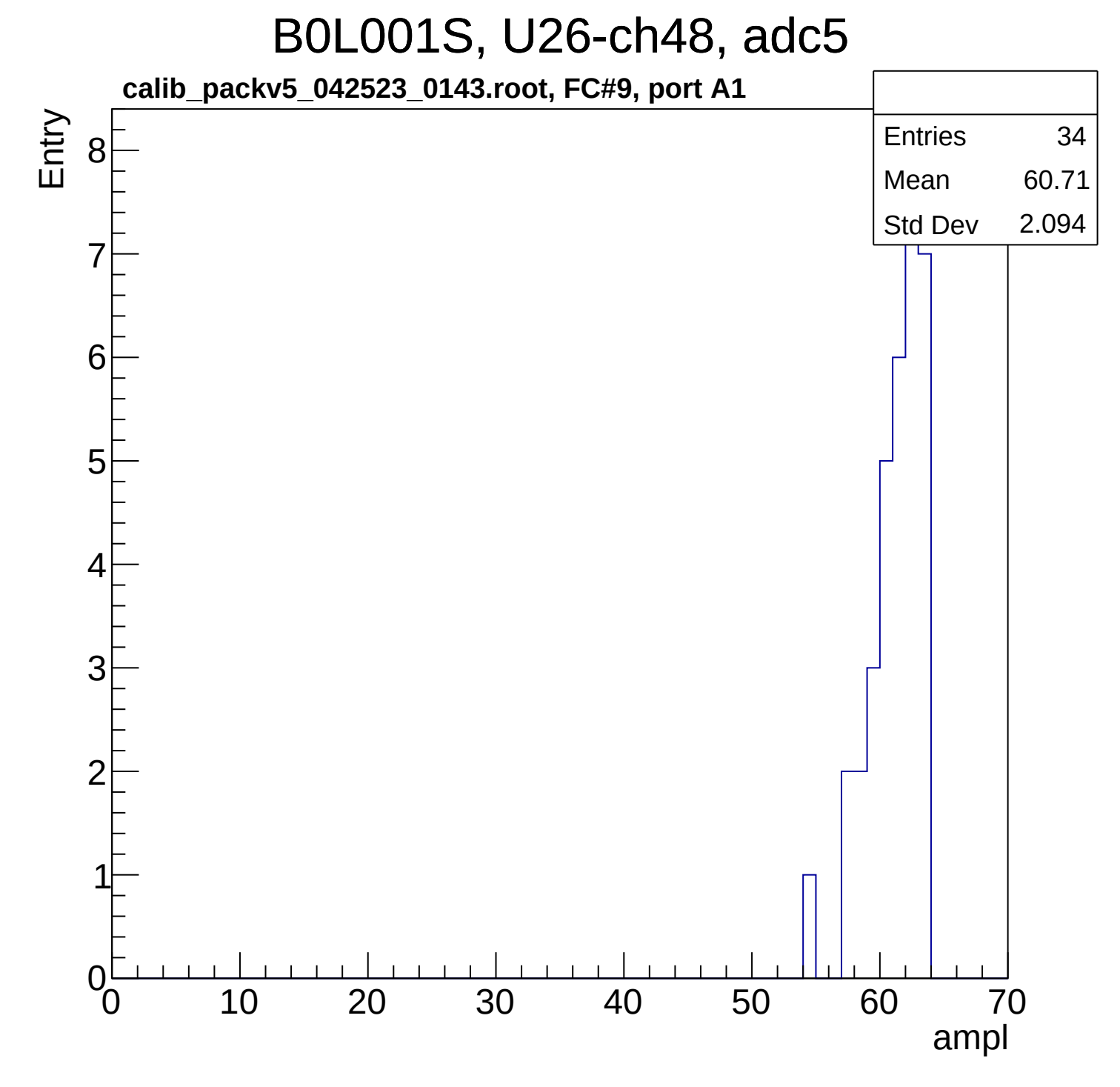
B0L001S, U26-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.71
Std Dev 2.094
Entry
ampl
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1
2
3
4
5
6
7
8
0
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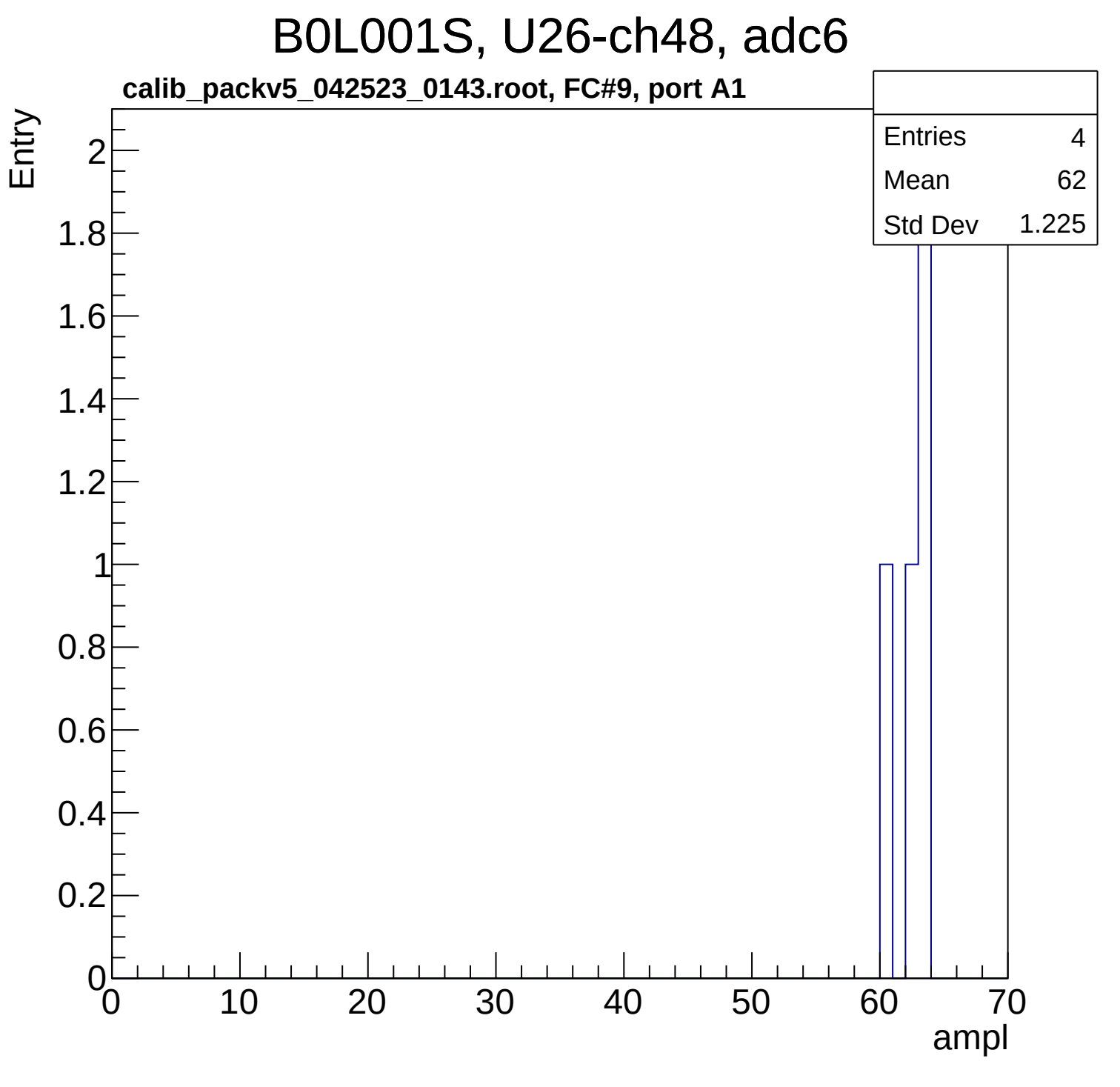
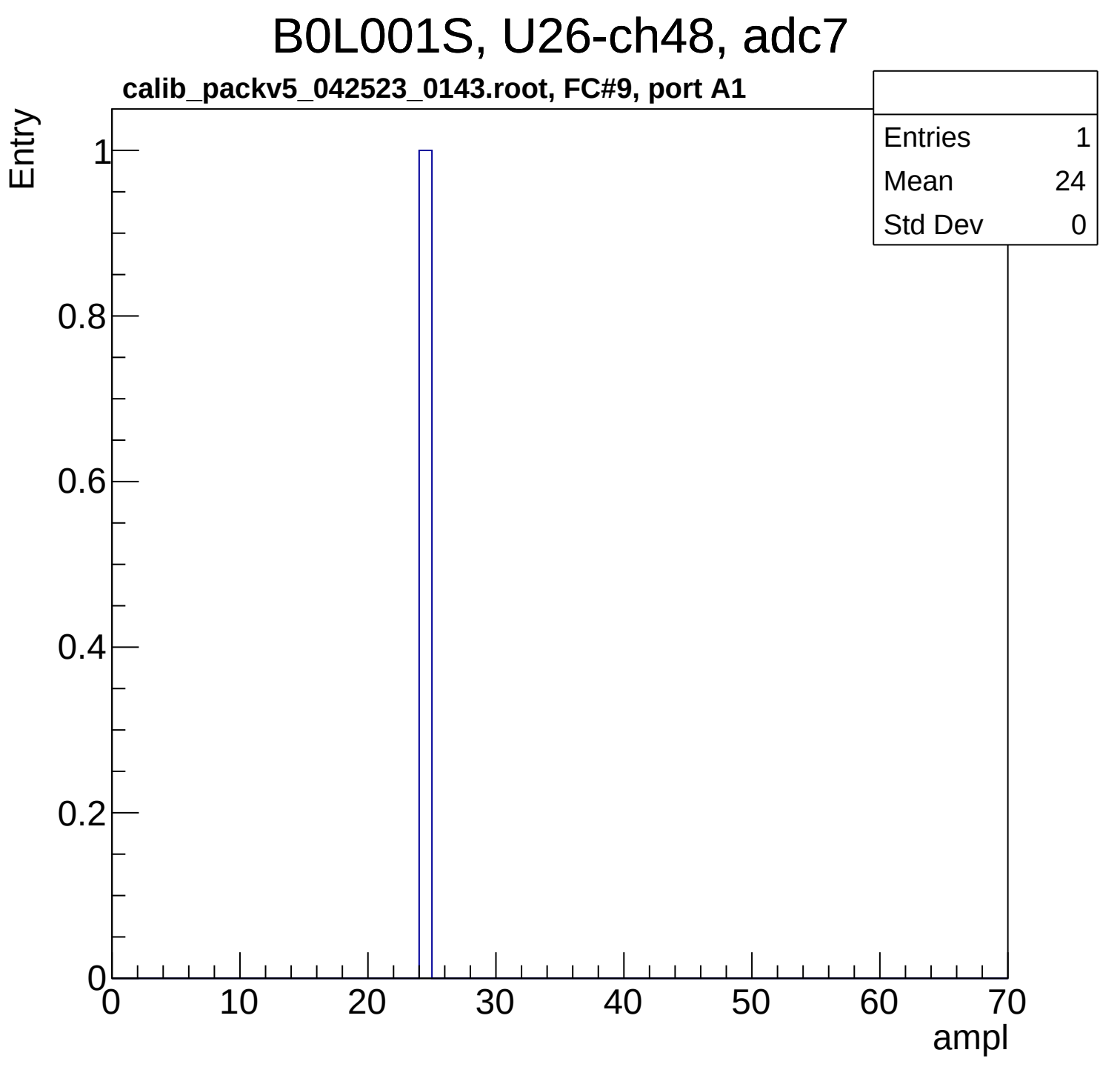
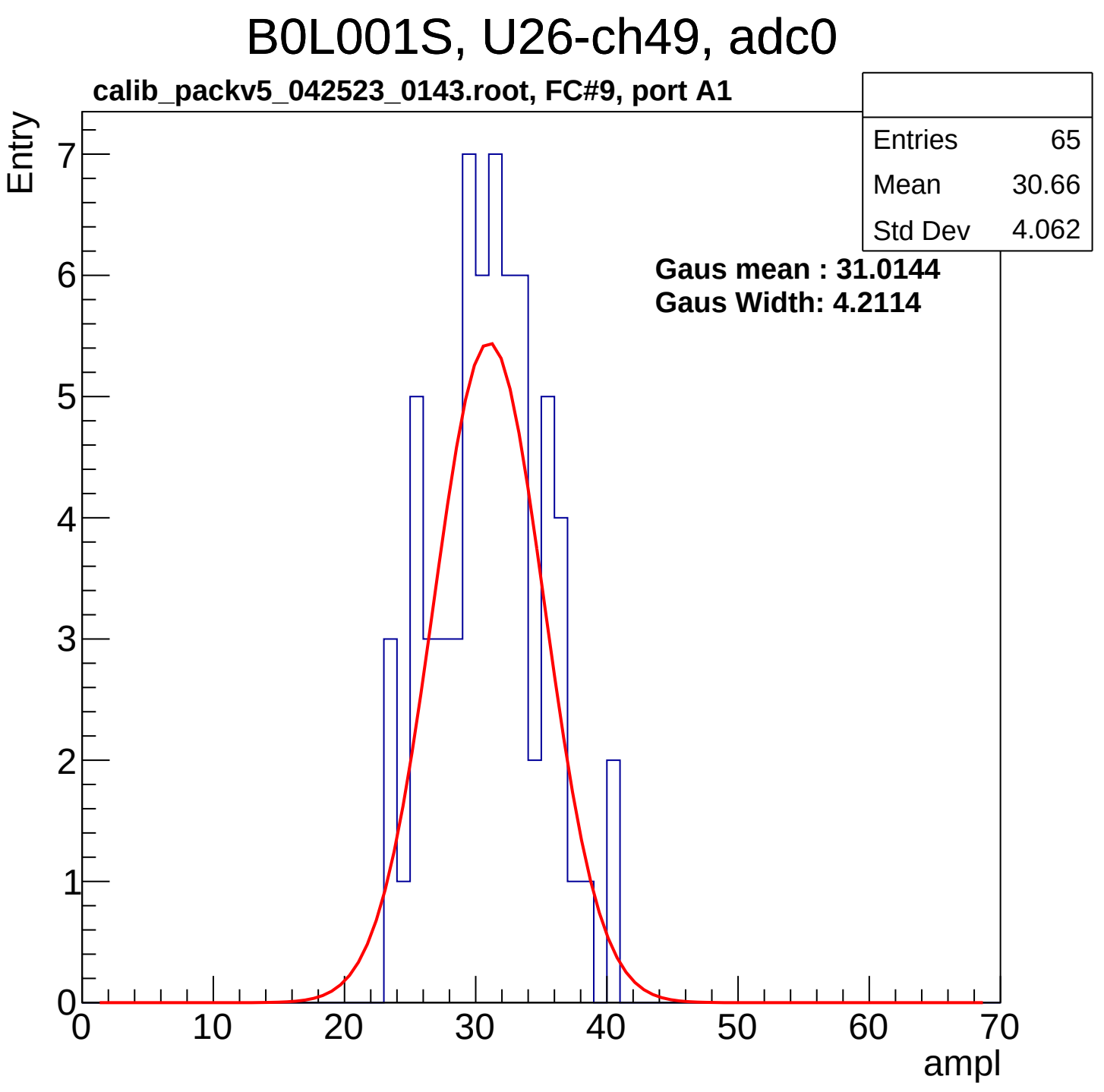
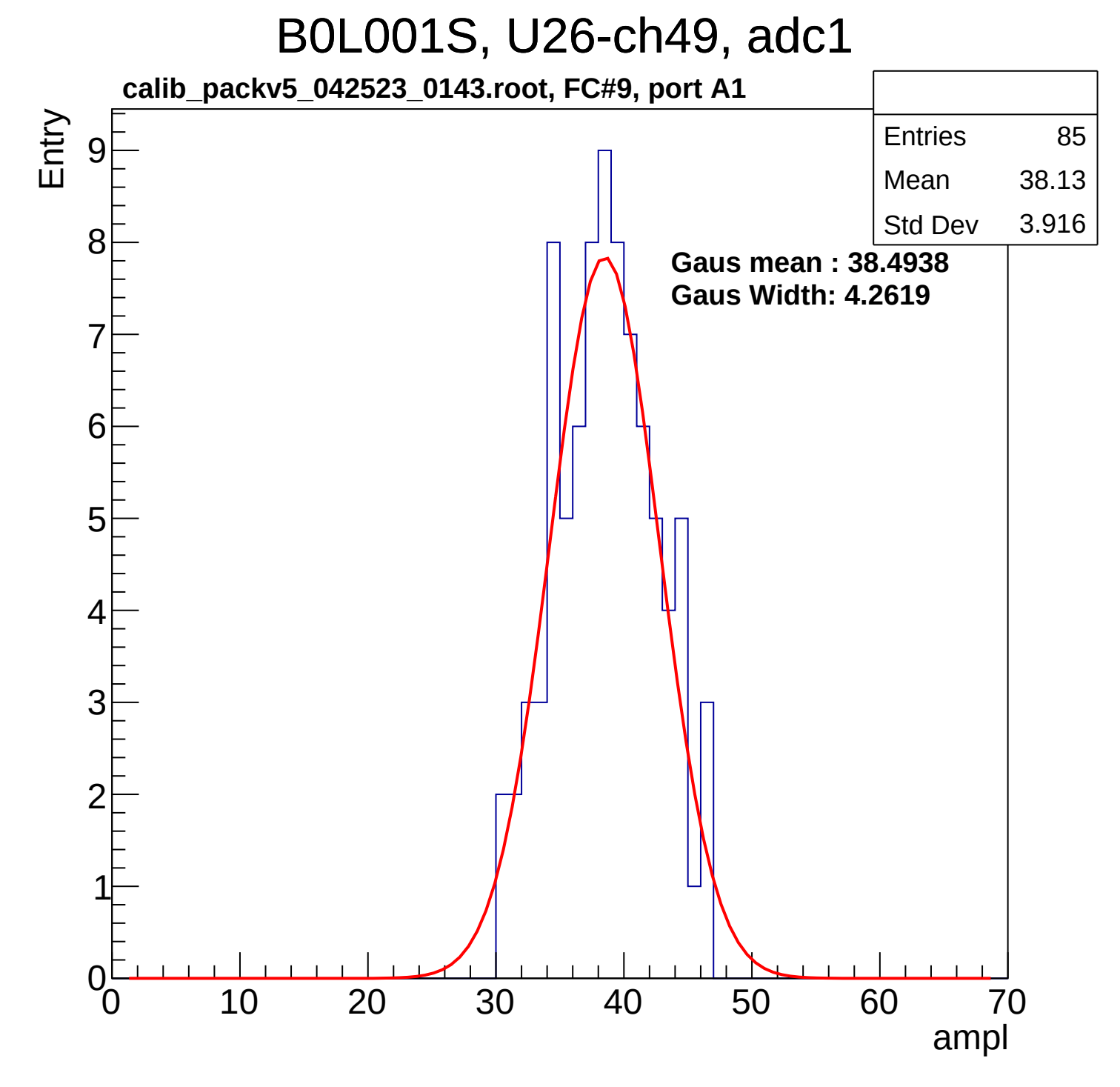
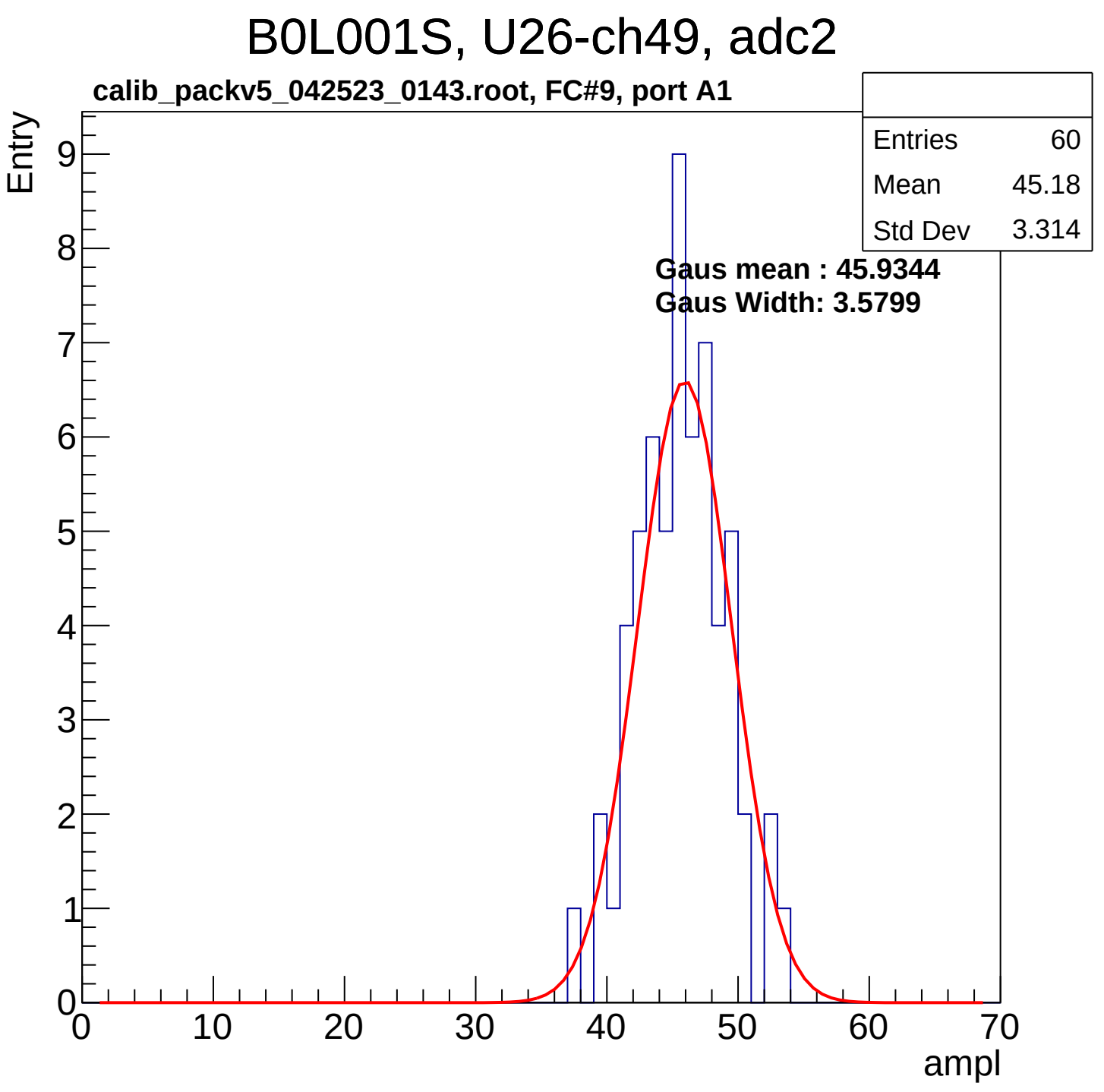
20
30
40
50
60
70


B0L001S, U26-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.66
Std Dev 4.062
Gaus mean : 31.0144
Gaus Width: 4.2114
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 38.13
Std Dev 3.916
Gaus mean : 38.4938
Gaus Width: 4.2619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.18
Std Dev 3.314
Gaus mean : 45.9344
Gaus Width: 3.5799
Entry
ampl
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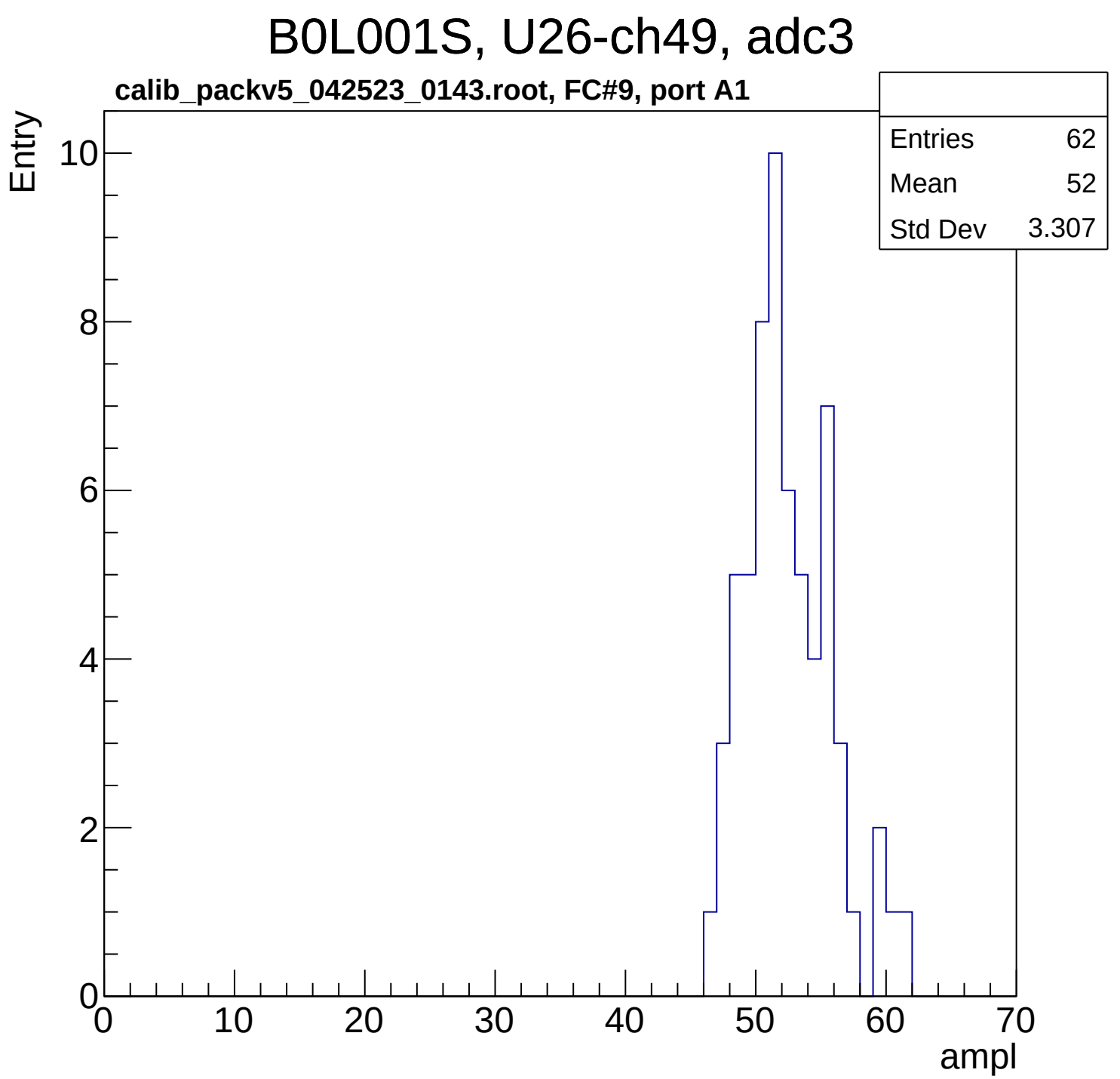
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 52
Std Dev 3.307
Entry
ampl
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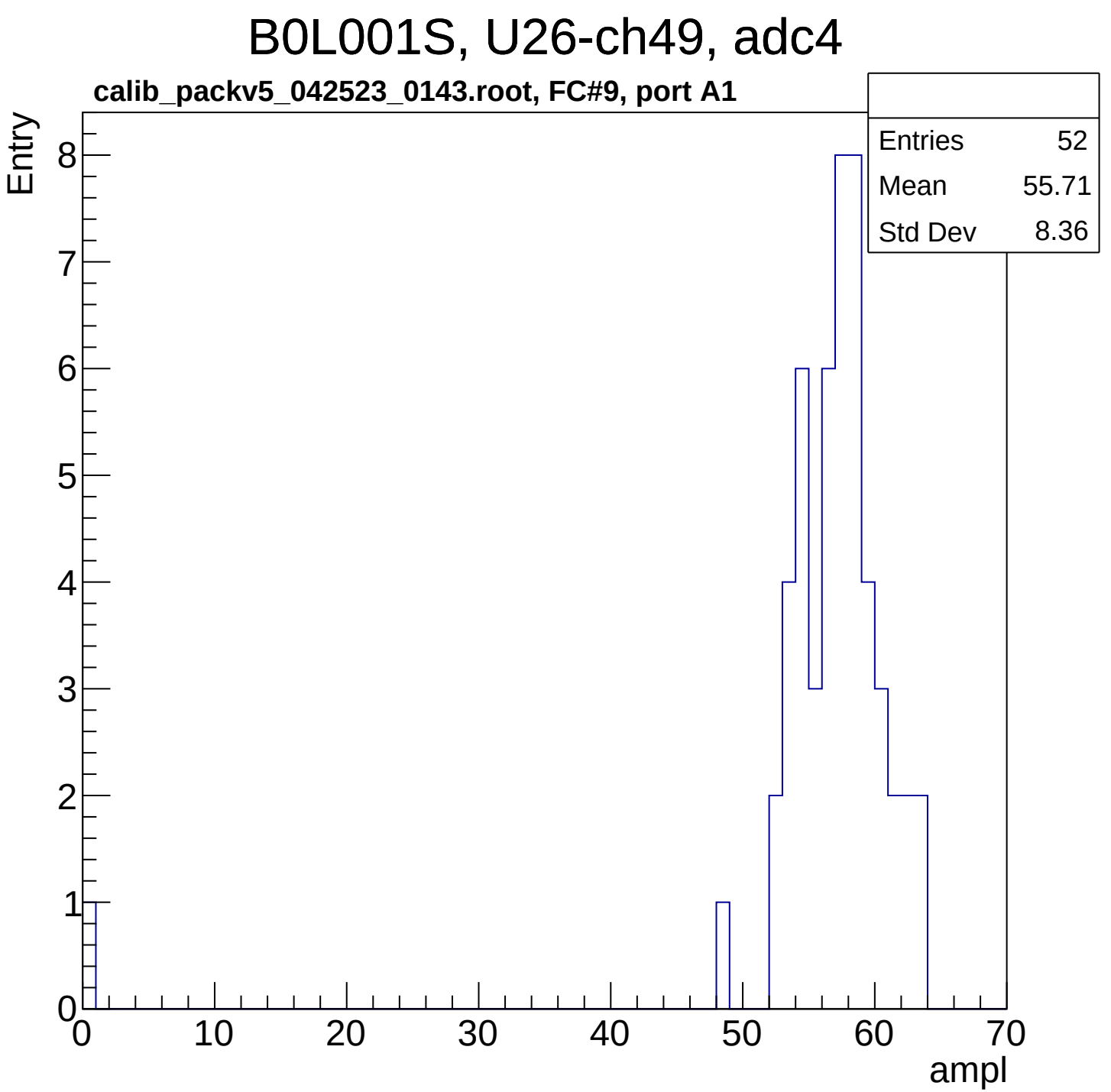
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.71
Std Dev 8.36
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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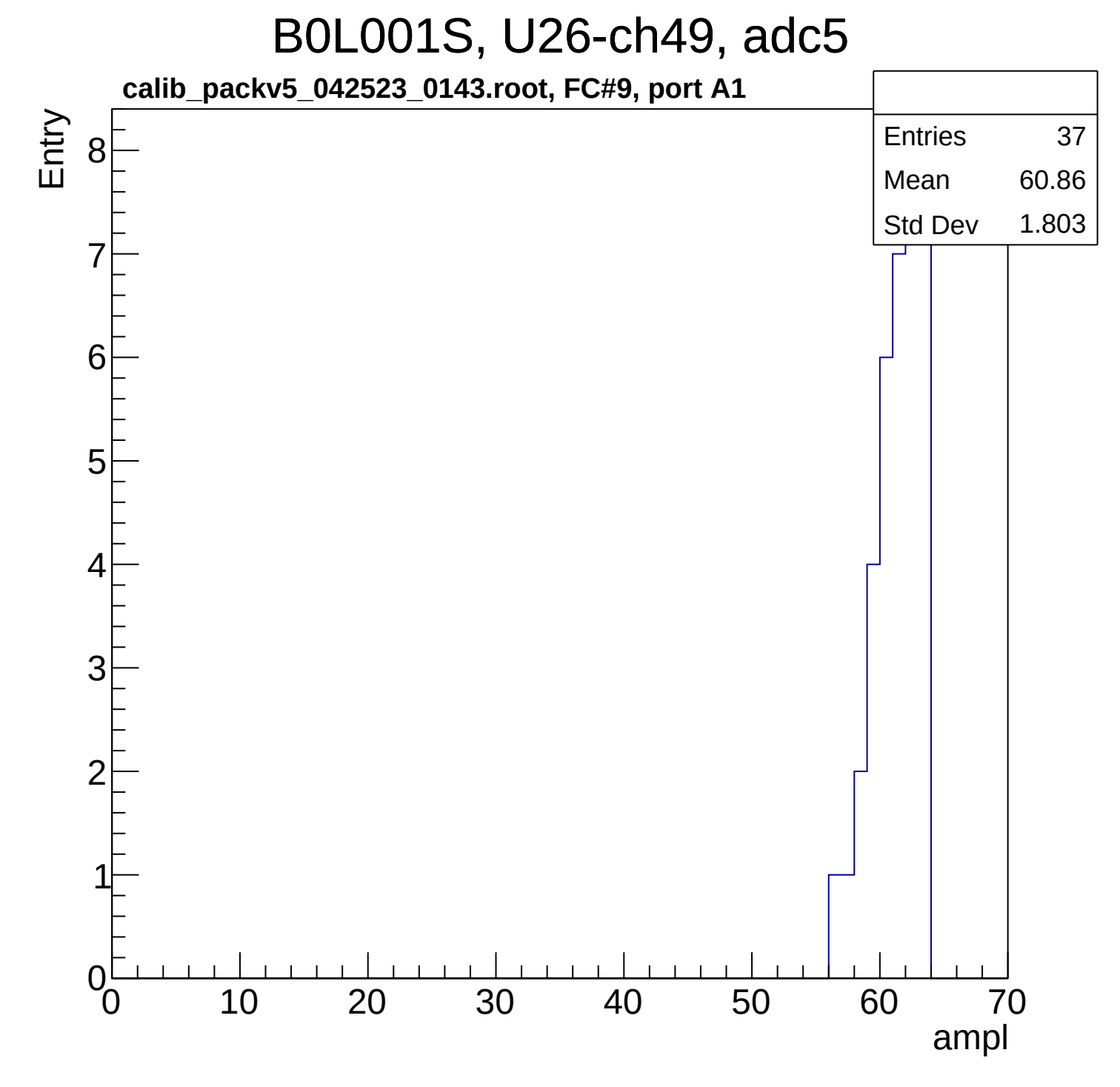
B0L001S, U26-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.86
Std Dev 1.803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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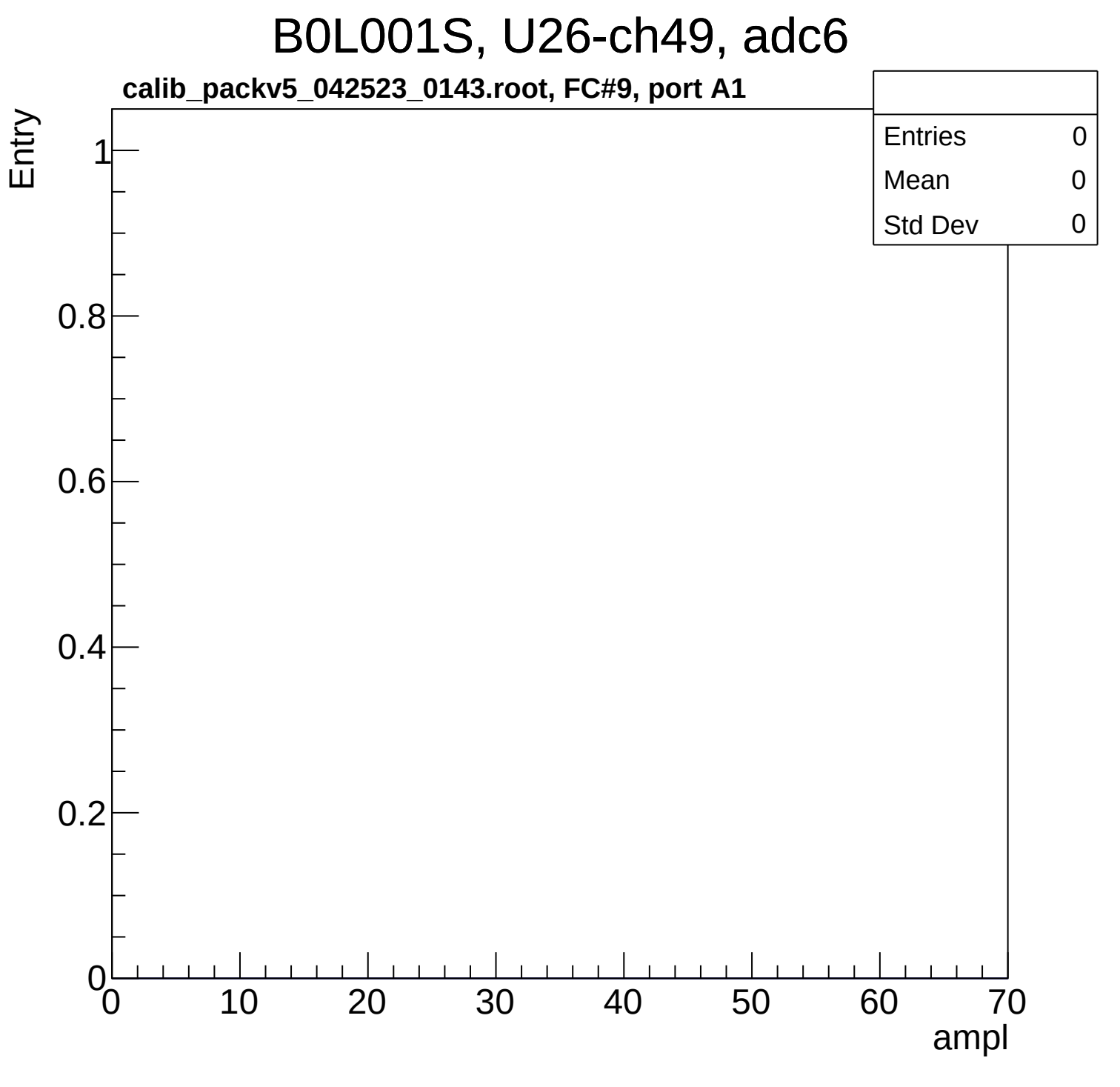
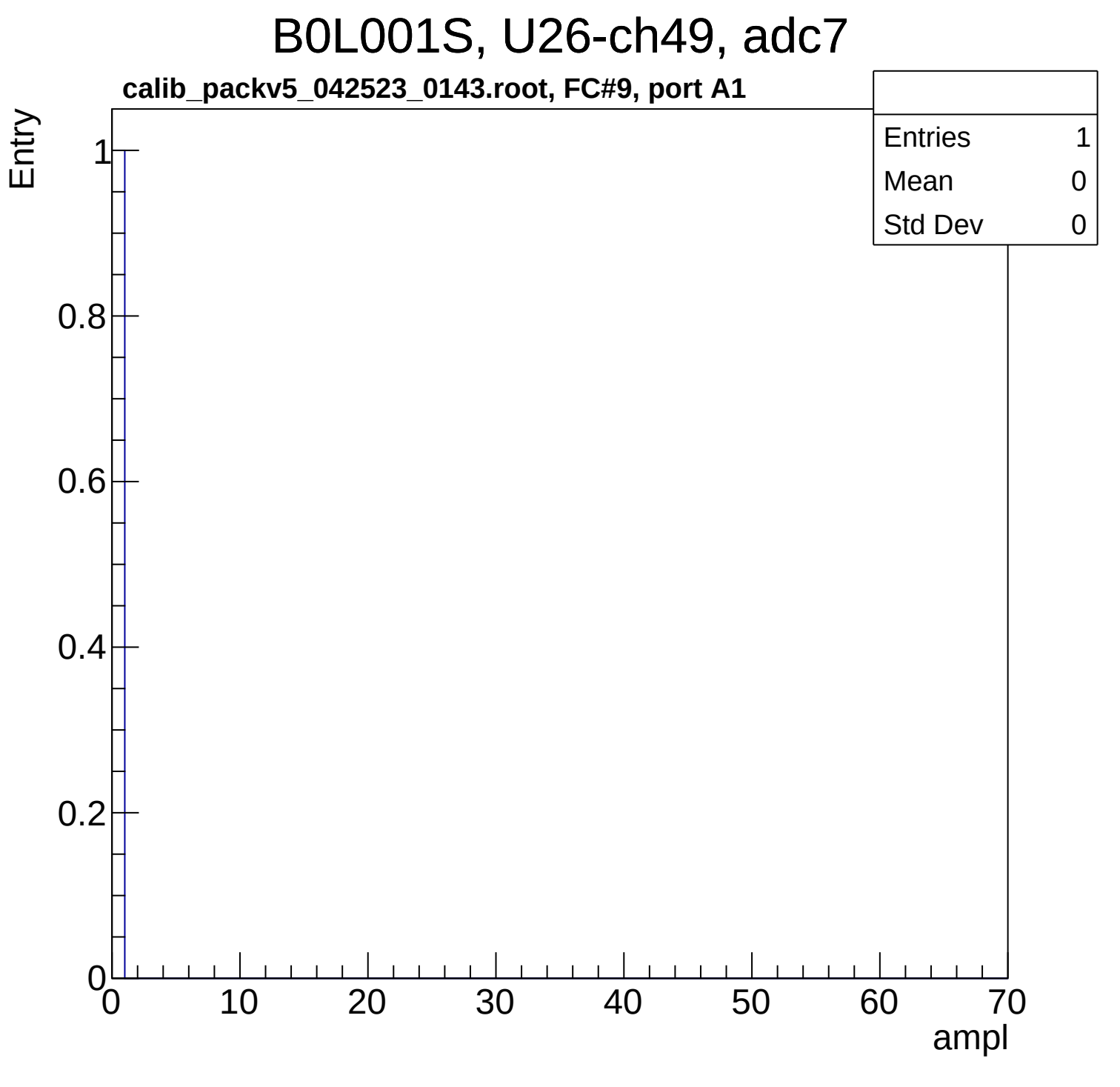
40
50
60
70


B0L001S, U26-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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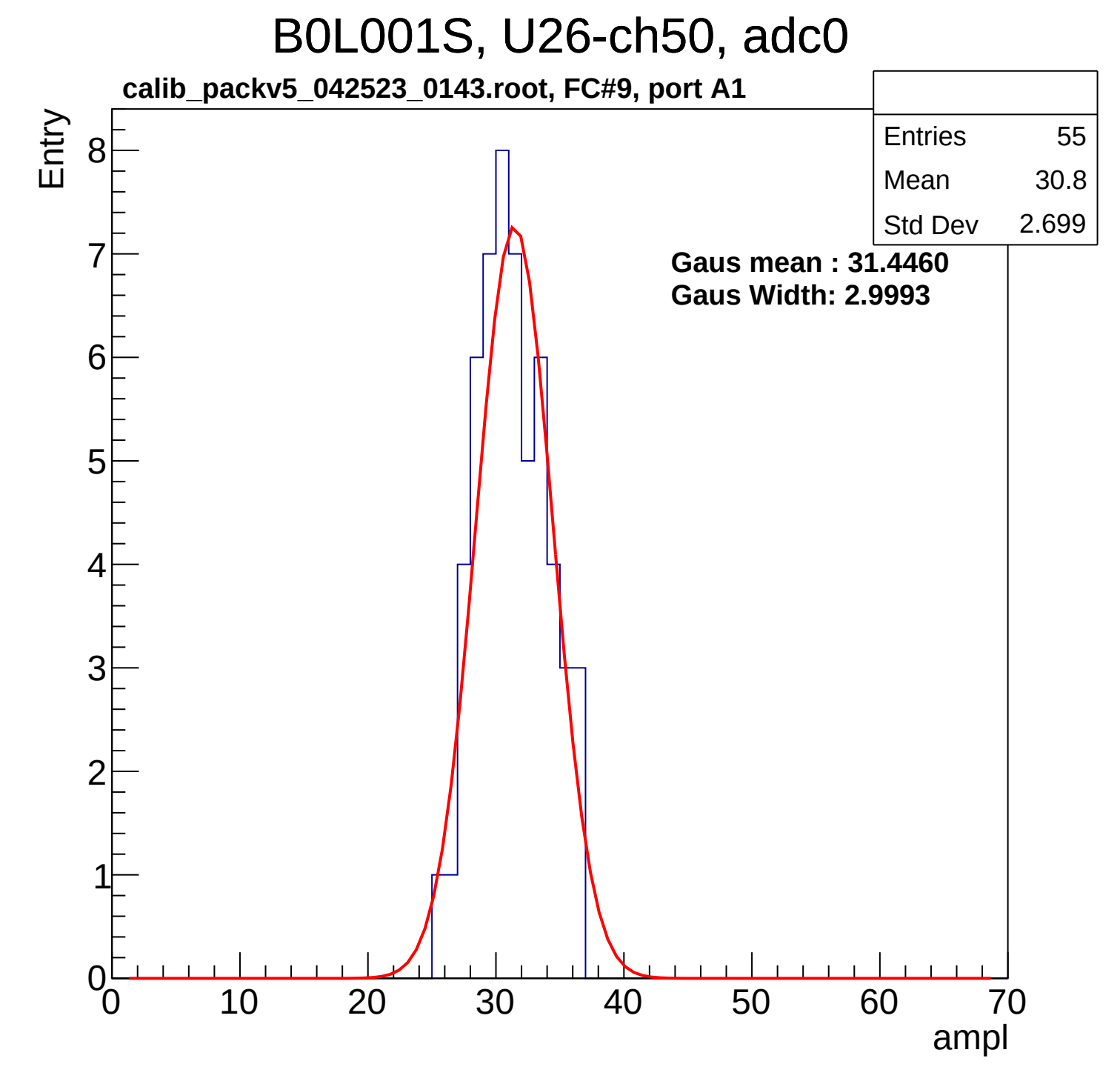
50
60
70
ampl


B0L001S, U26-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.8
Std Dev 2.699
Gaus mean : 31.4460
Gaus Width: 2.9993
Entry
ampl
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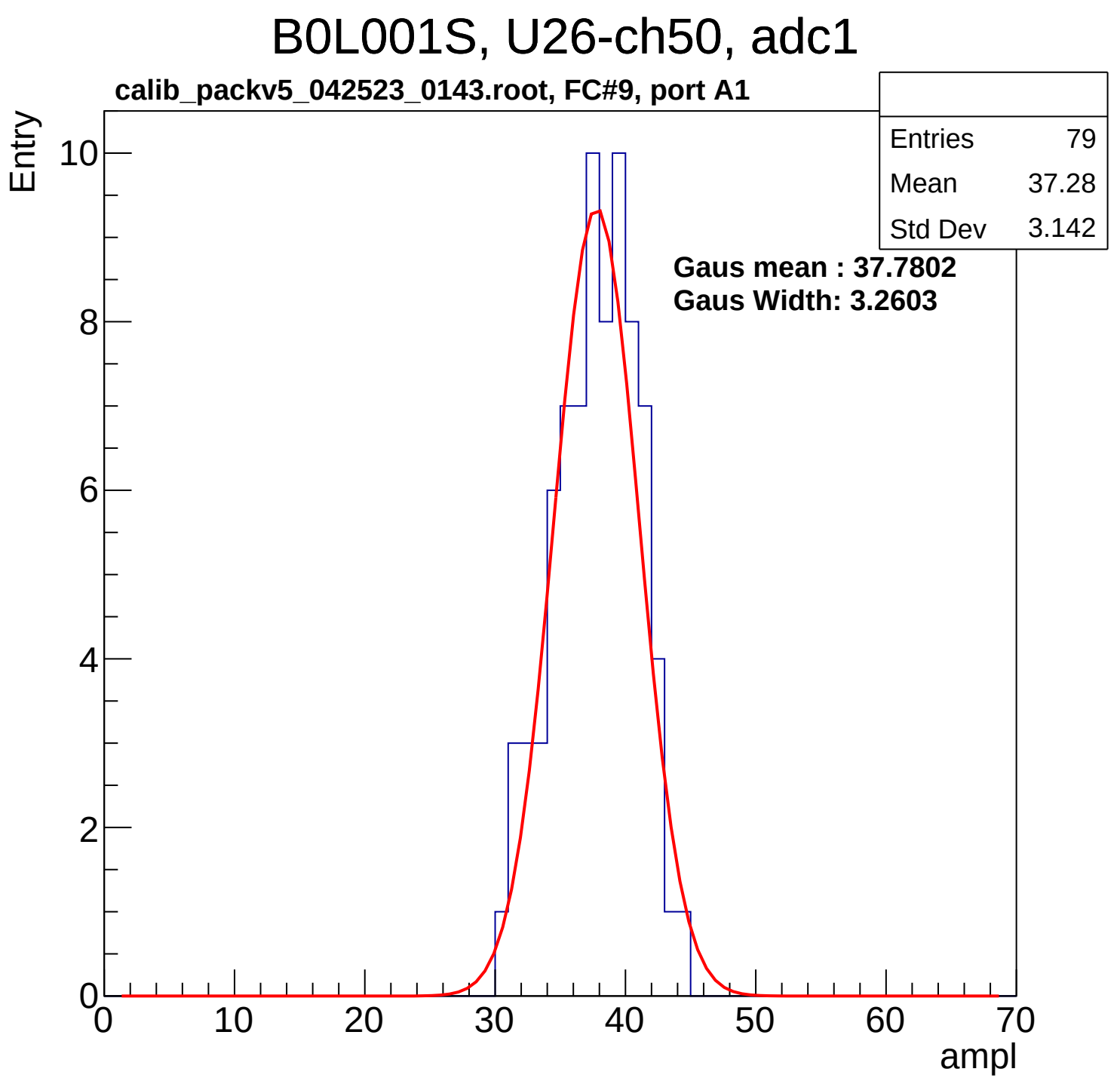
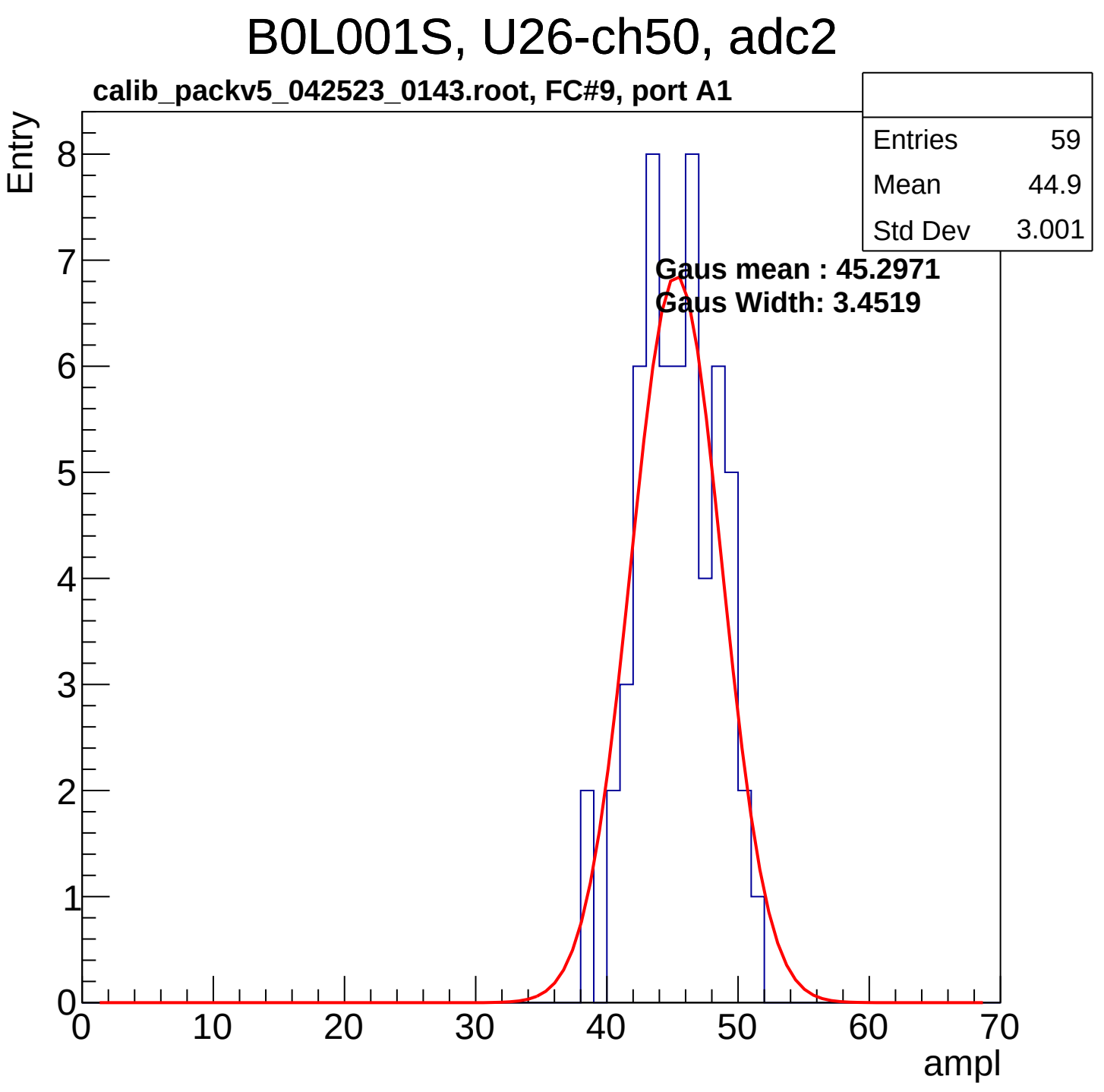
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.28
Std Dev 3.142
Gaus mean : 37.7802
Gaus Width: 3.2603
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.9
Std Dev 3.001
Gaus mean : 45.2971
Gaus Width: 3.4519
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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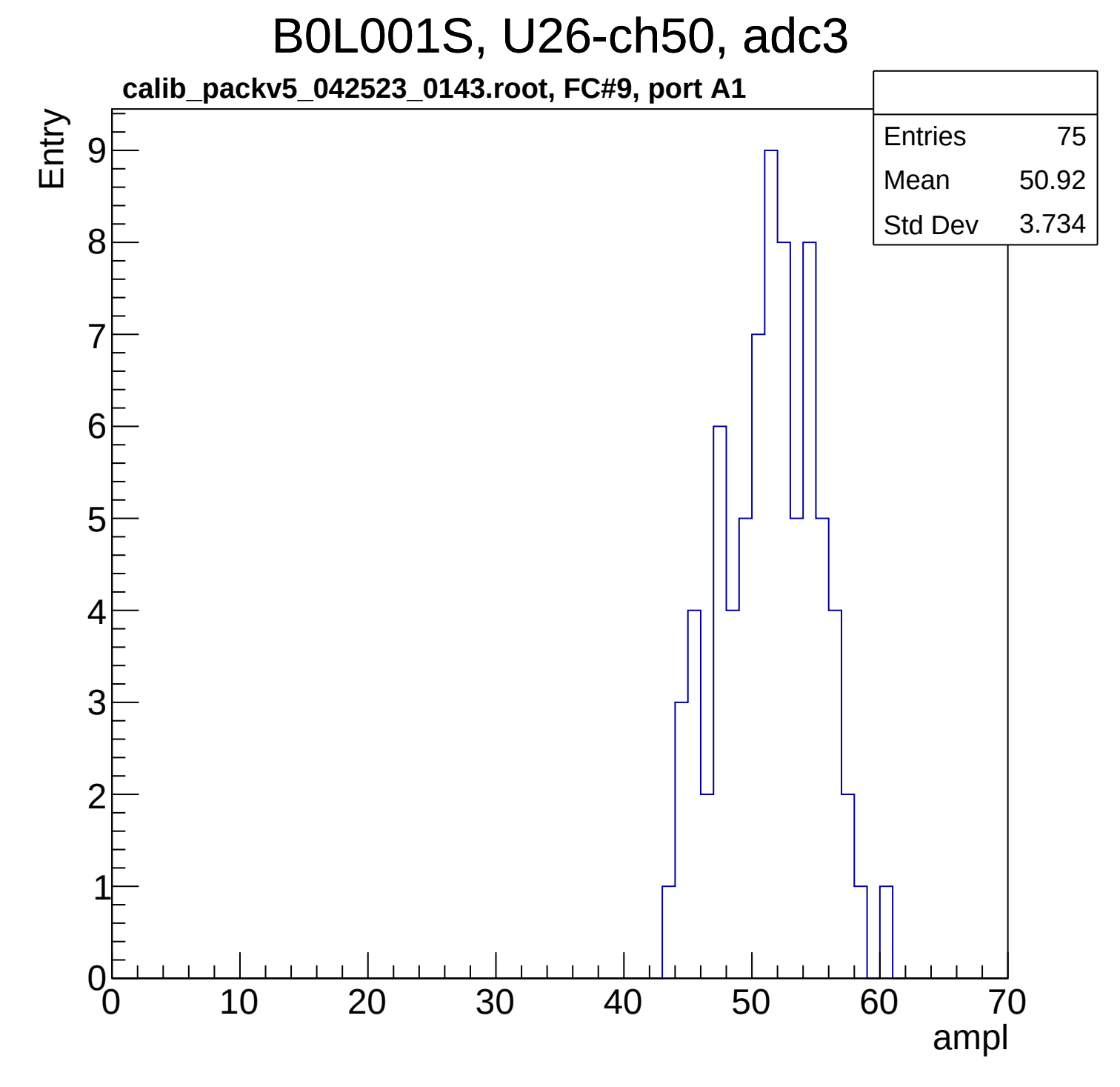
B0L001S, U26-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.92
Std Dev 3.734
Entry
ampl
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2
3
4
5
6
7
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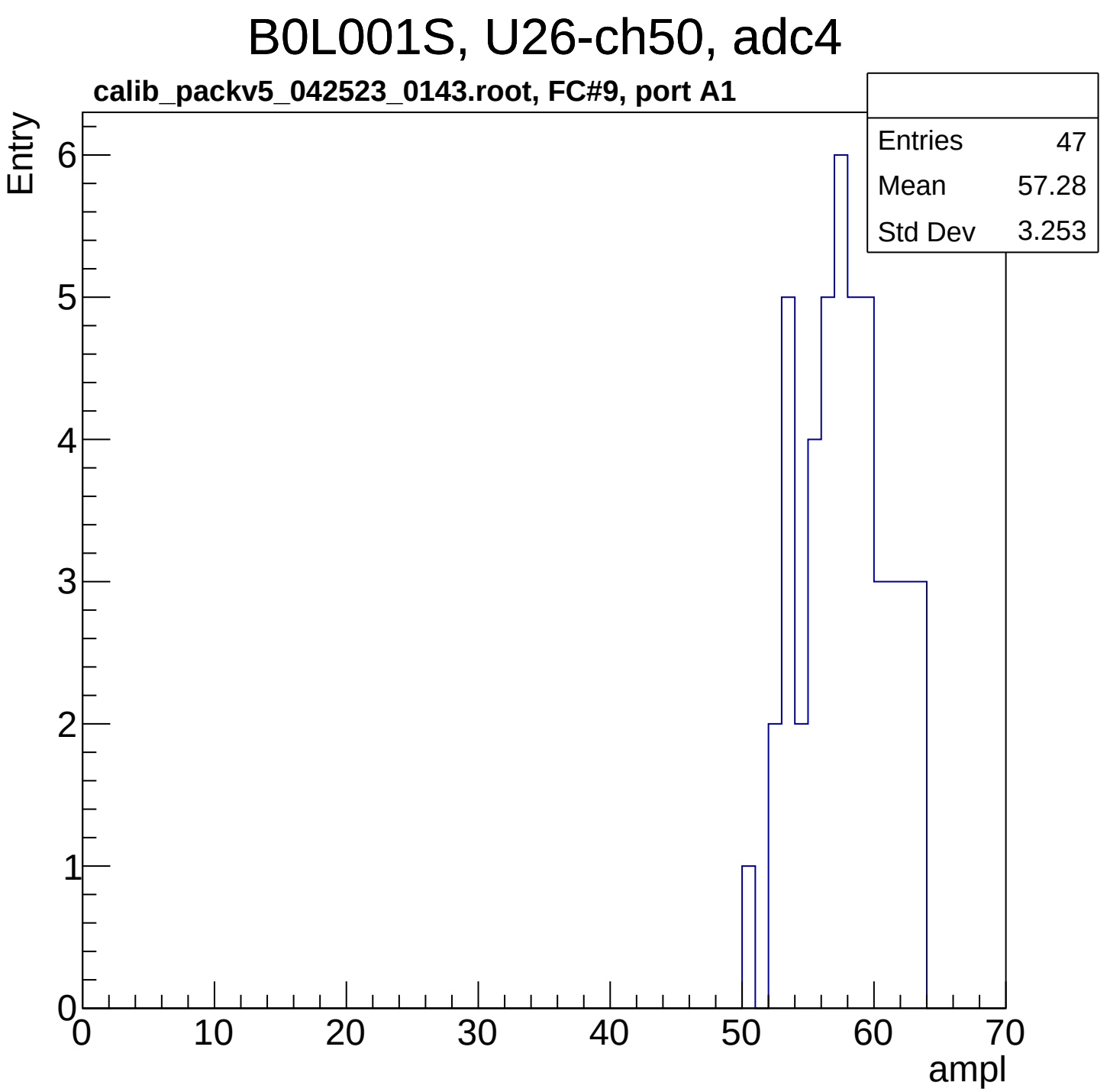
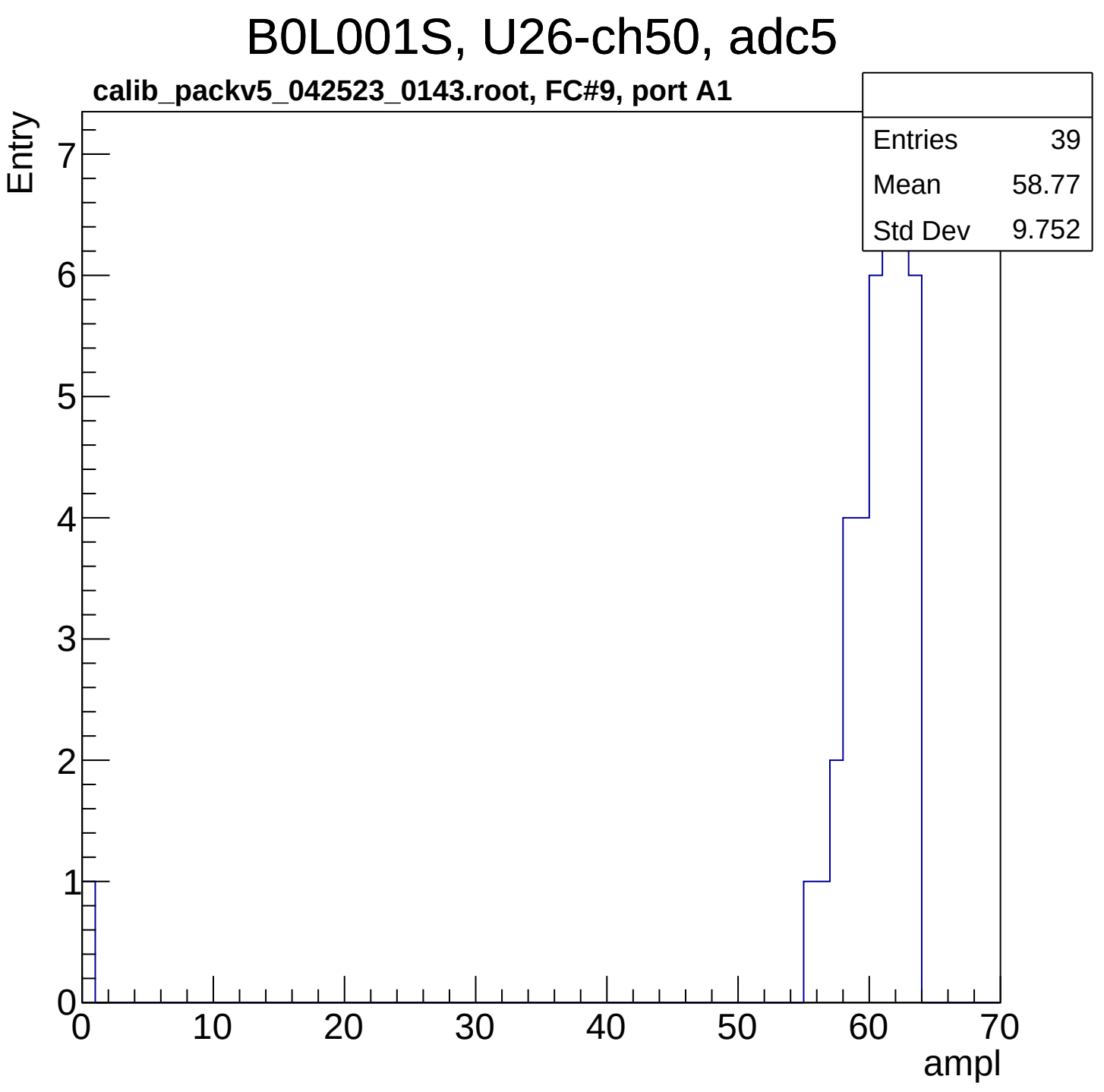

B0L001S, U26-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 57.28
Std Dev 3.253
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.77
Std Dev 9.752
Entry
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1
2
3
4
5
6
7
0
10
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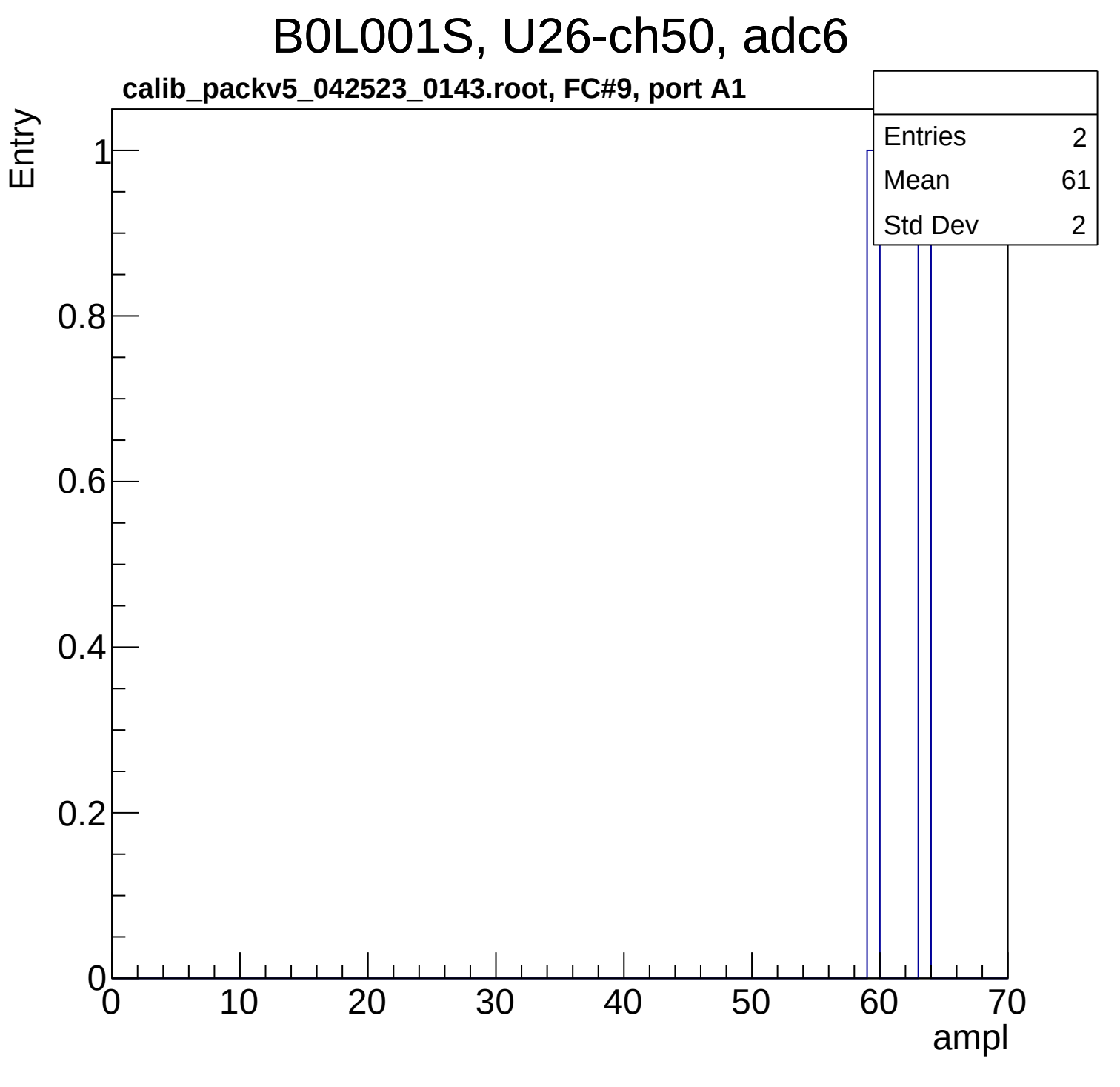
50
60
70
ampl


B0L001S, U26-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 2
Entry
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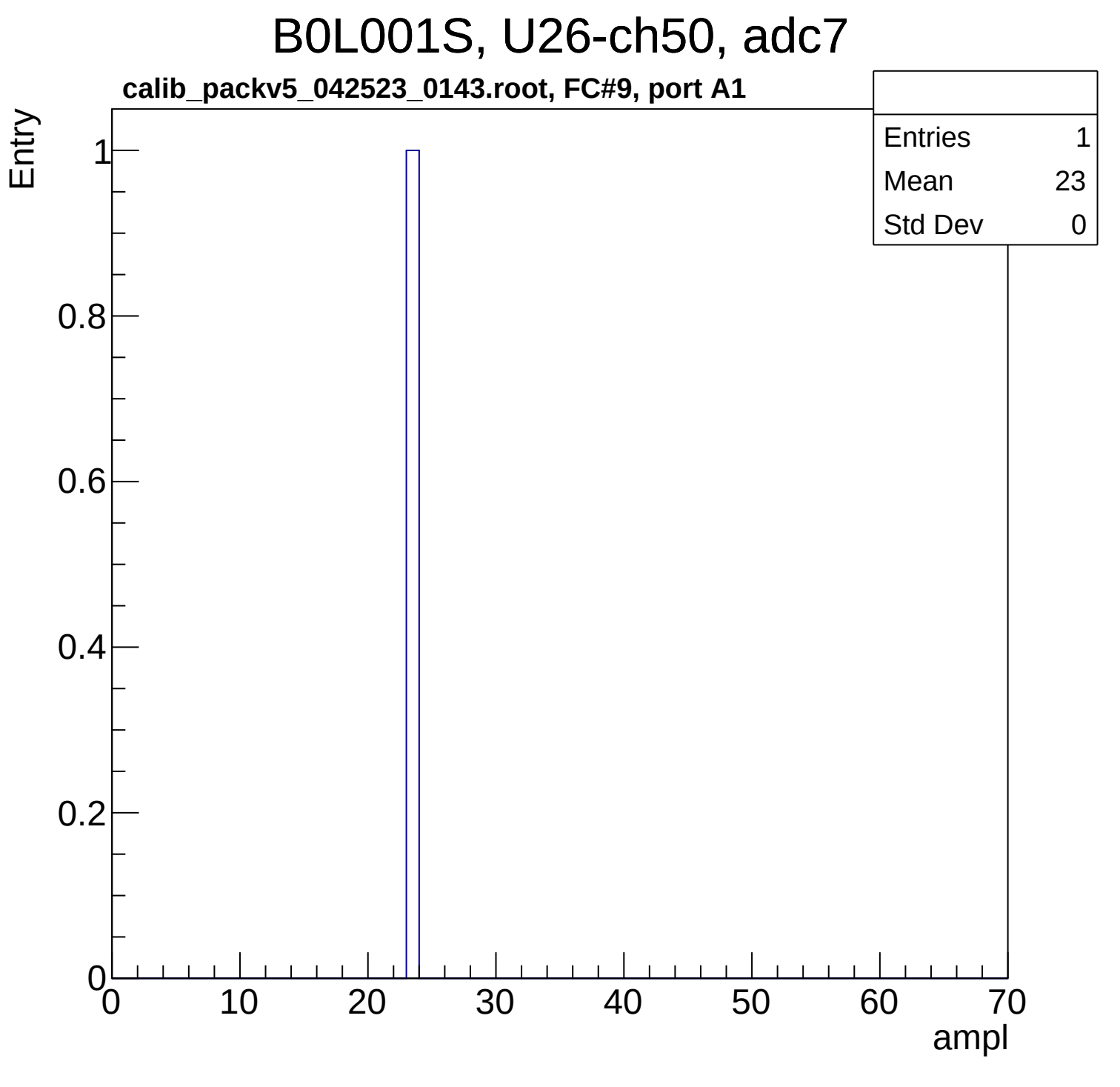
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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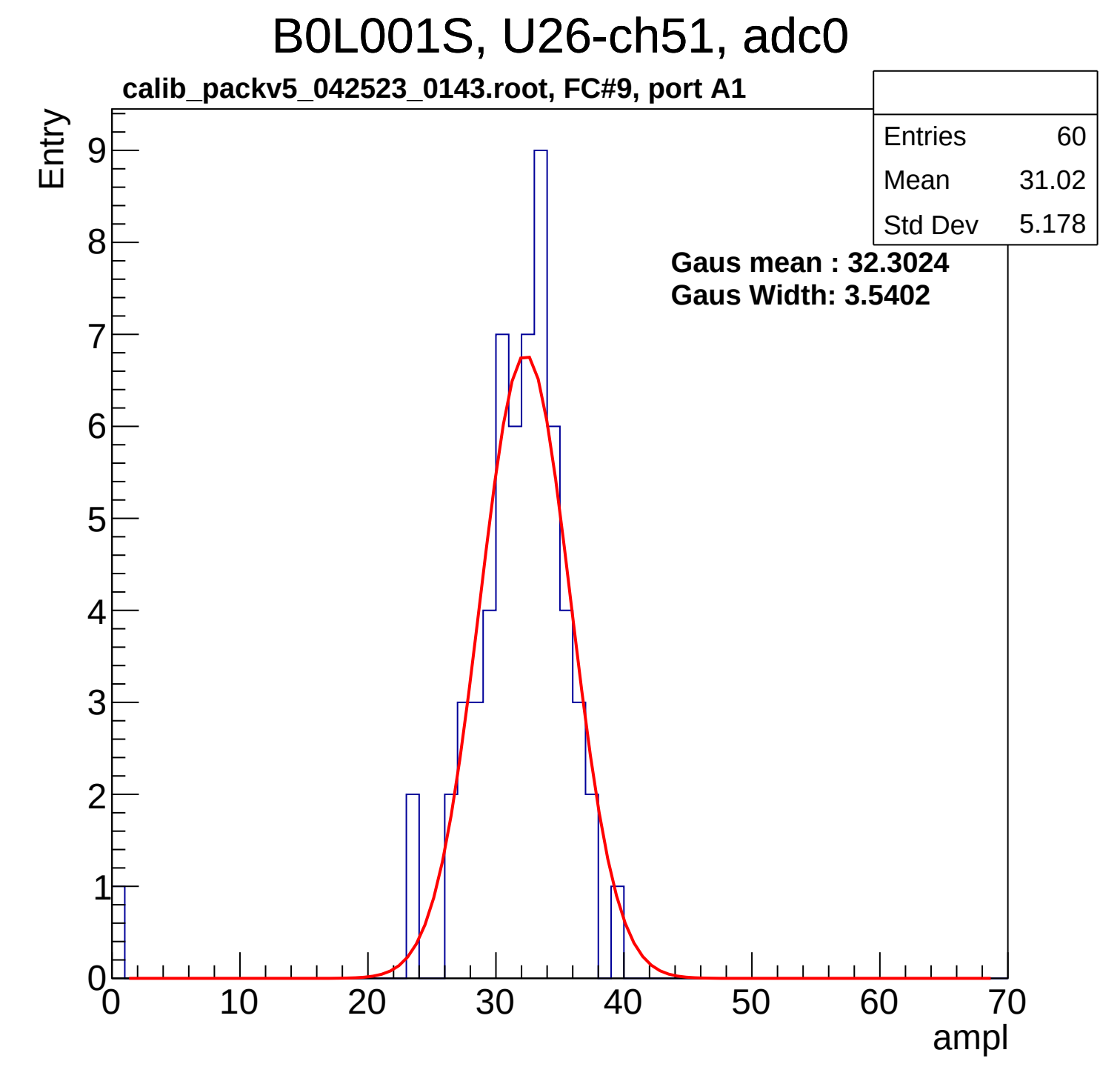
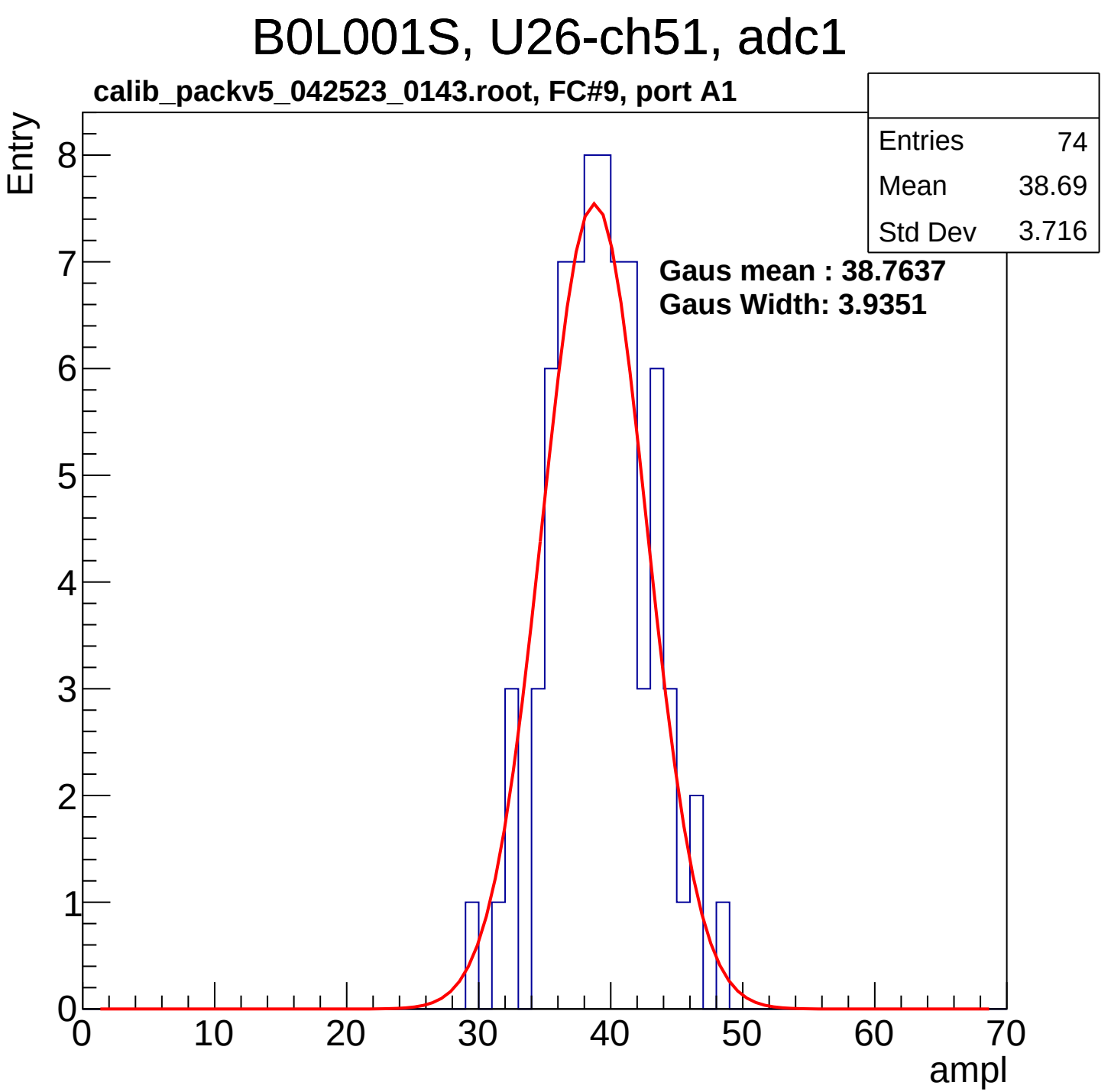
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.02
Std Dev 5.178
Gaus mean : 32.3024
Gaus Width: 3.5402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 38.69
Std Dev 3.716
Gaus mean : 38.7637
Gaus Width: 3.9351
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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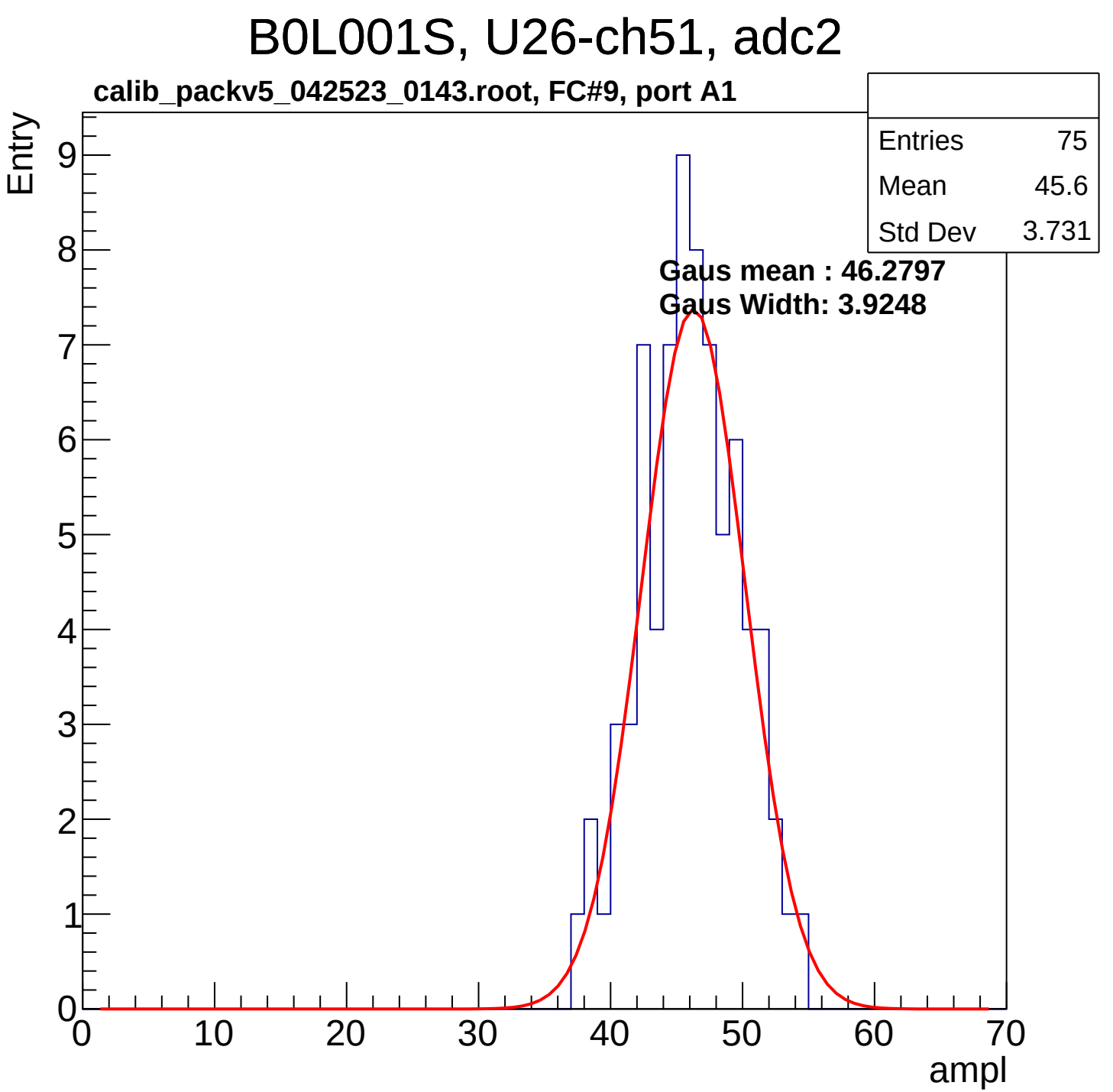
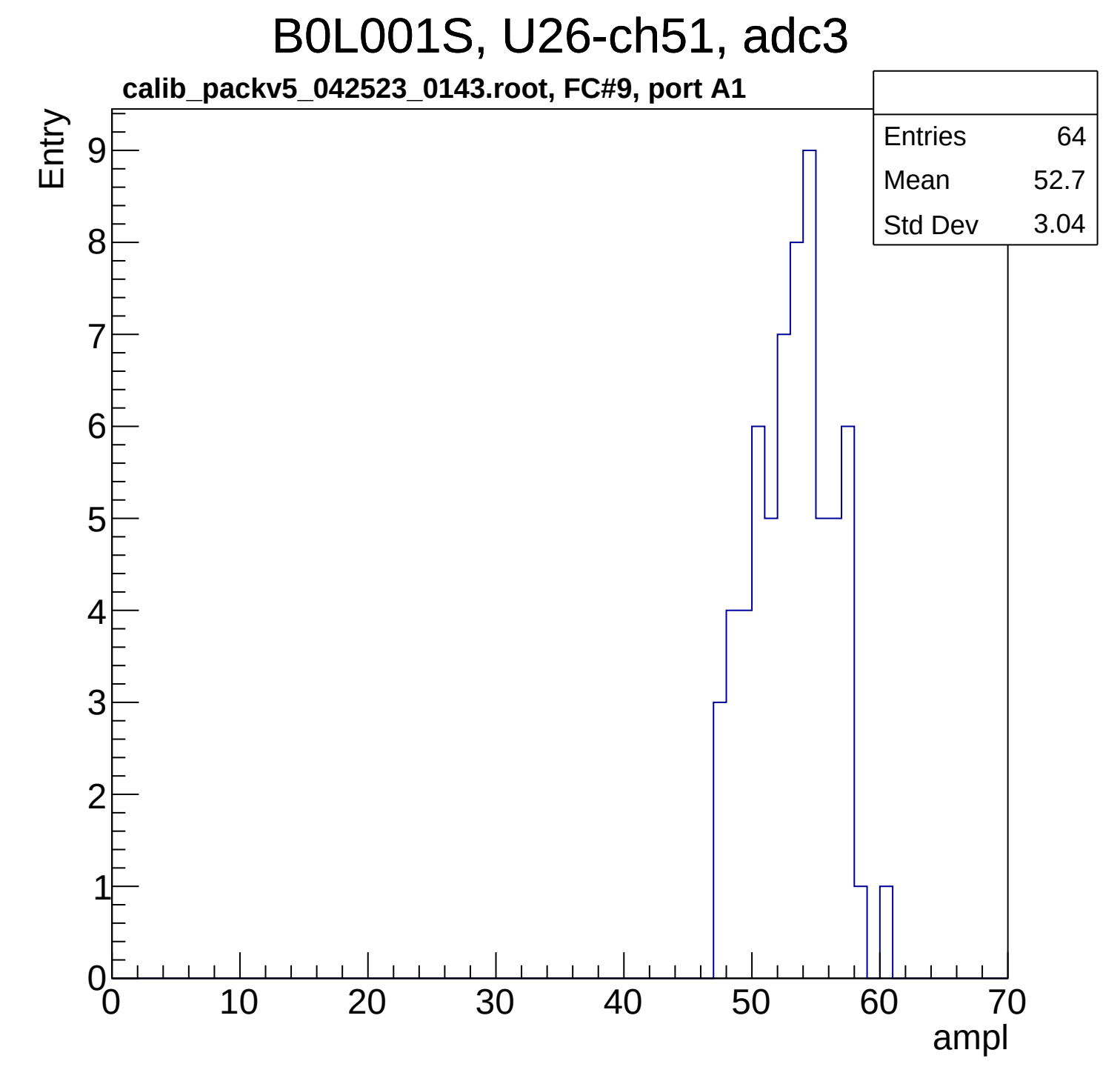
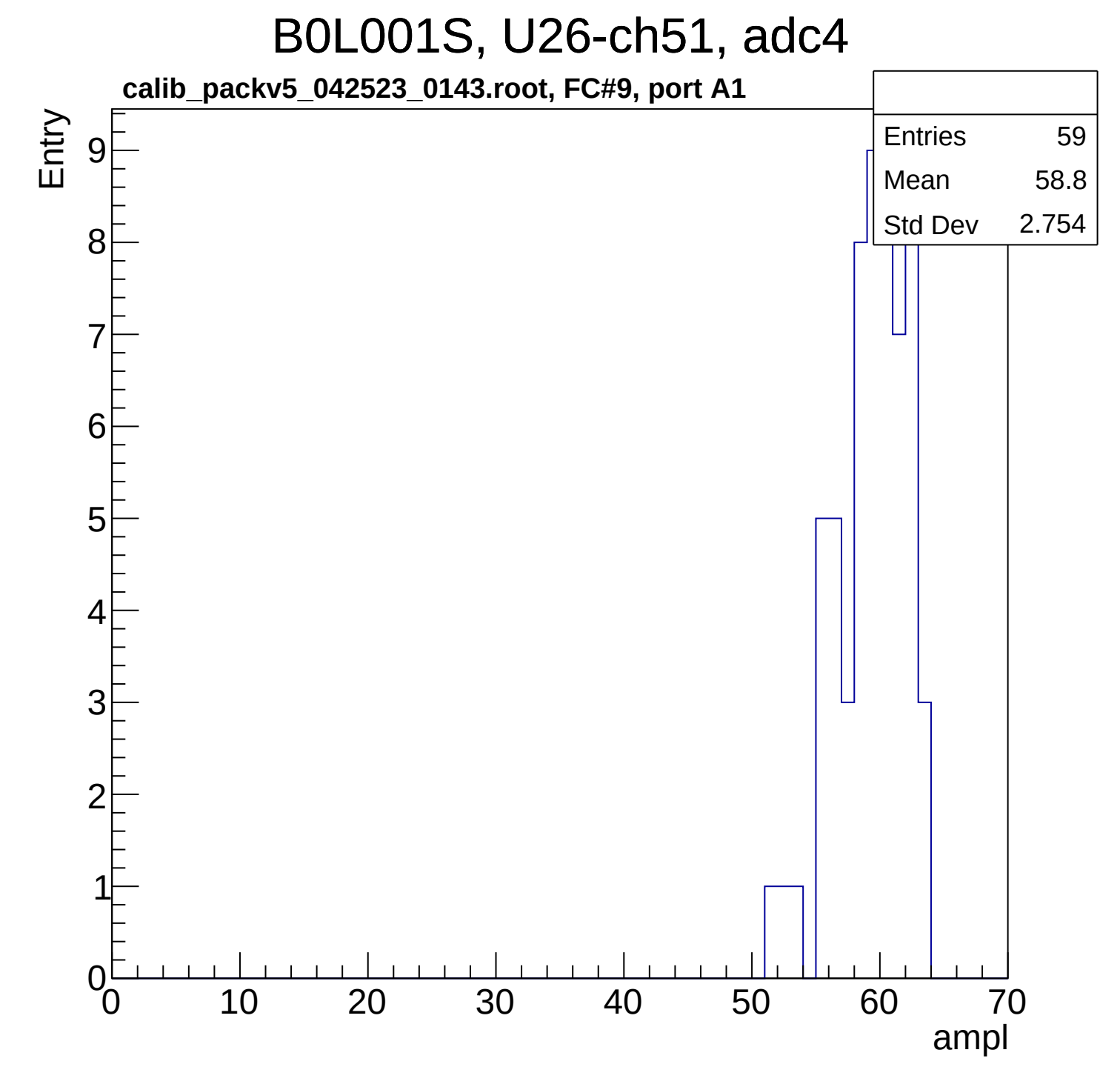
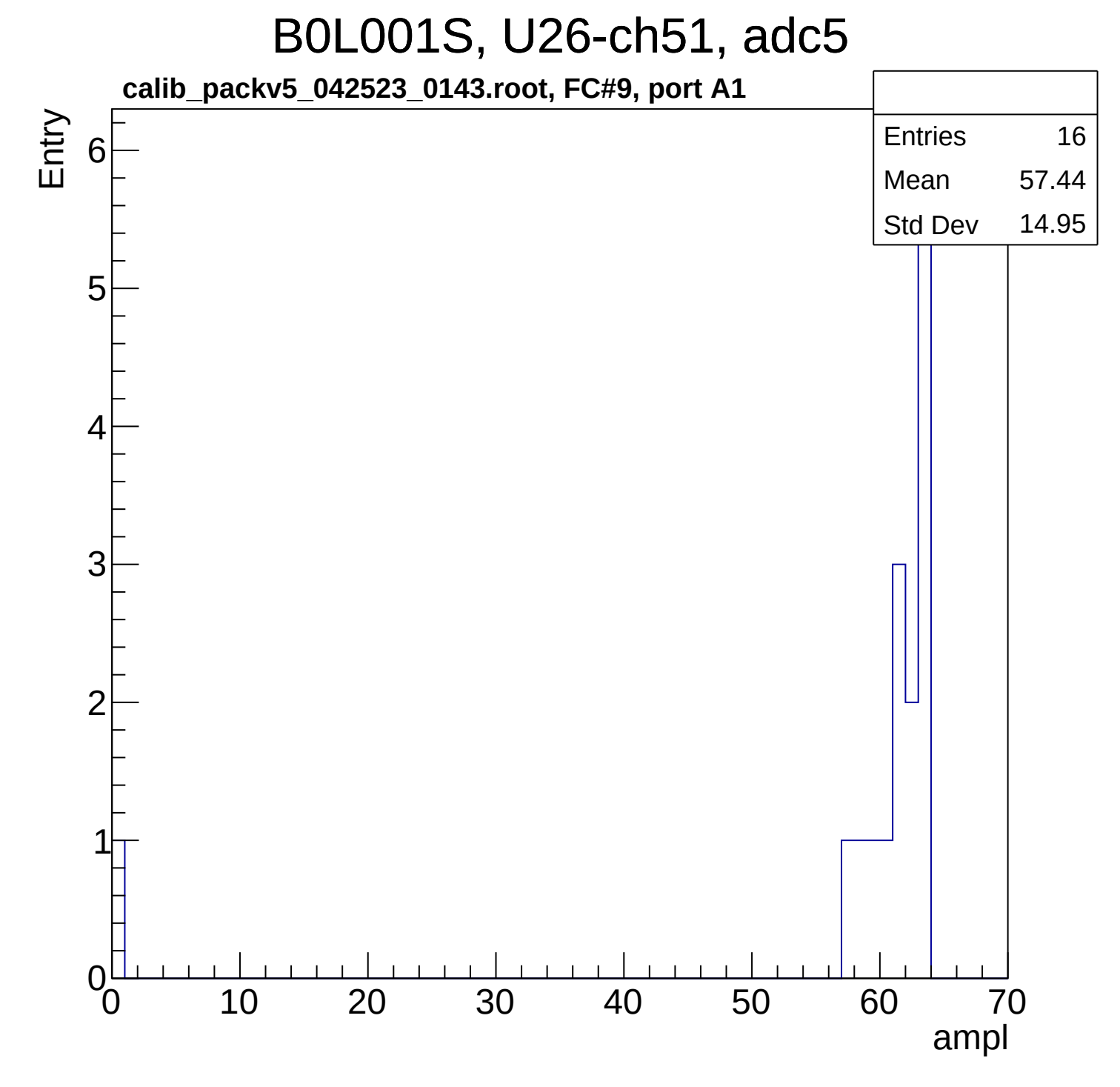
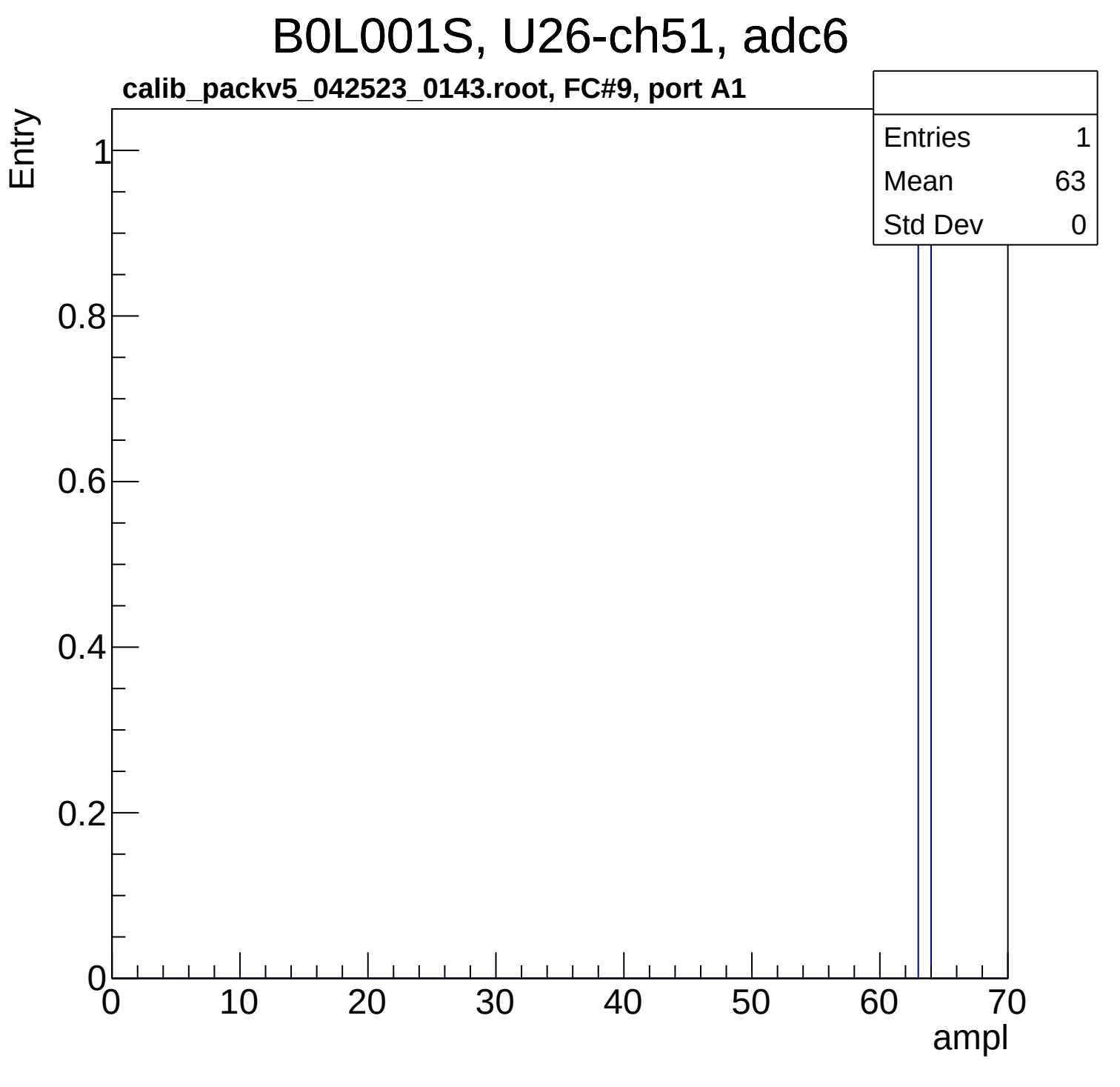
40
50
60
70

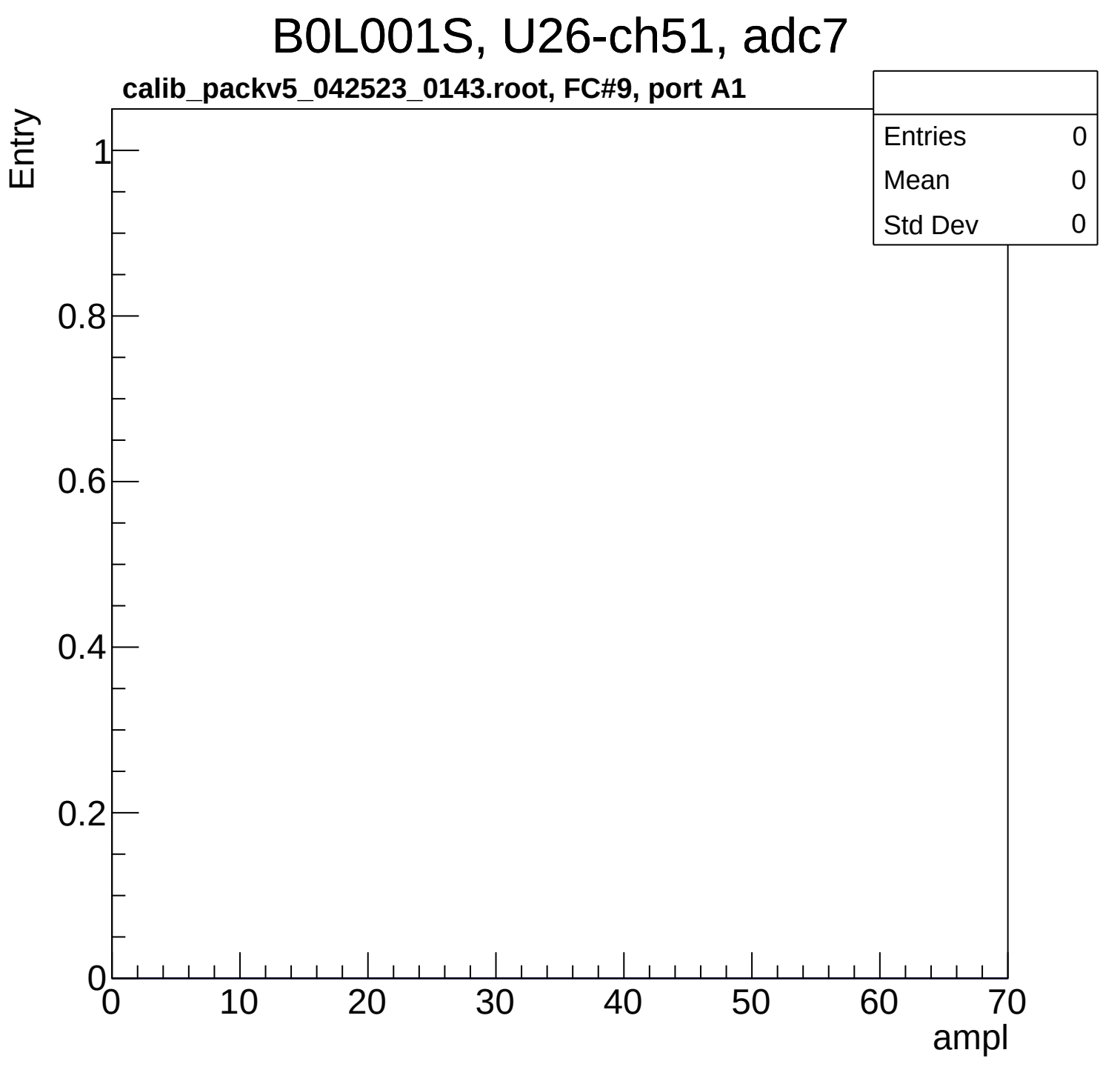

B0L001S, U26-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 45.6
Std Dev 3.731
Gaus mean : 46.2797
Gaus Width: 3.9248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

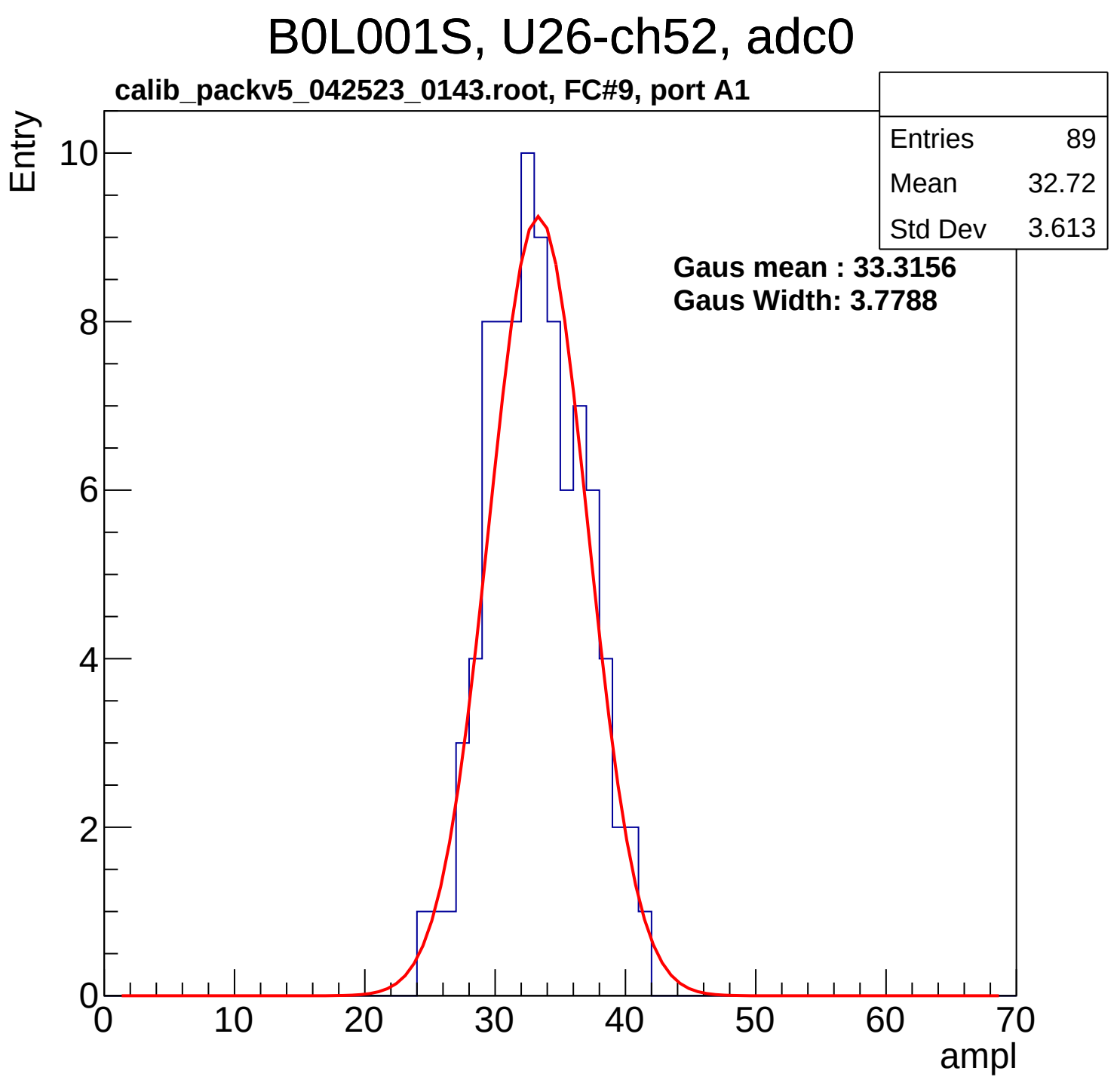

B0L001S, U26-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.7
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

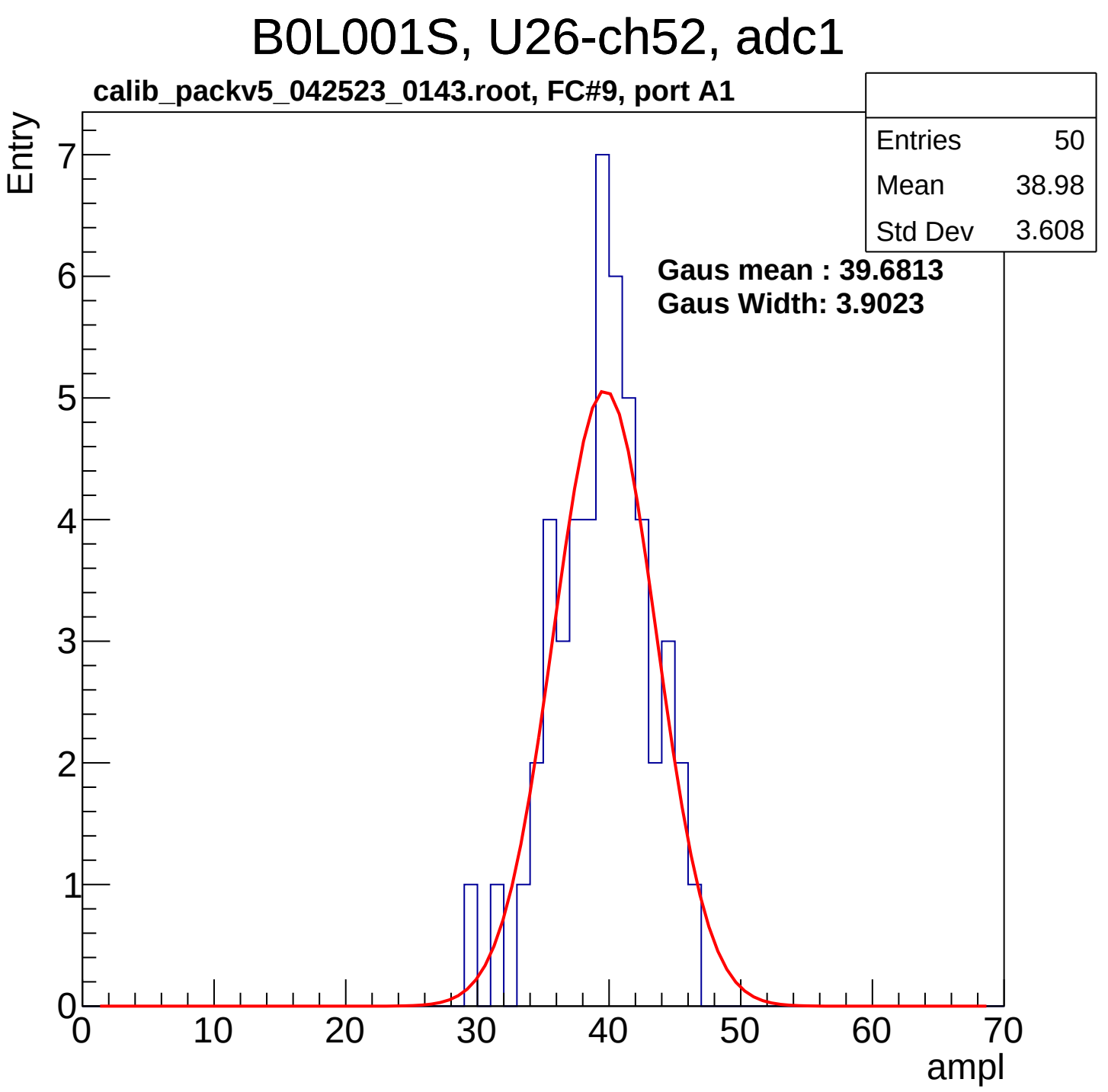

B0L001S, U26-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.8
Std Dev 2.754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 57.44
Std Dev 14.95
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 32.72
Std Dev 3.613
Gaus mean : 33.3156
Gaus Width: 3.7788
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 38.98
Std Dev 3.608
Gaus mean : 39.6813
Gaus Width: 3.9023
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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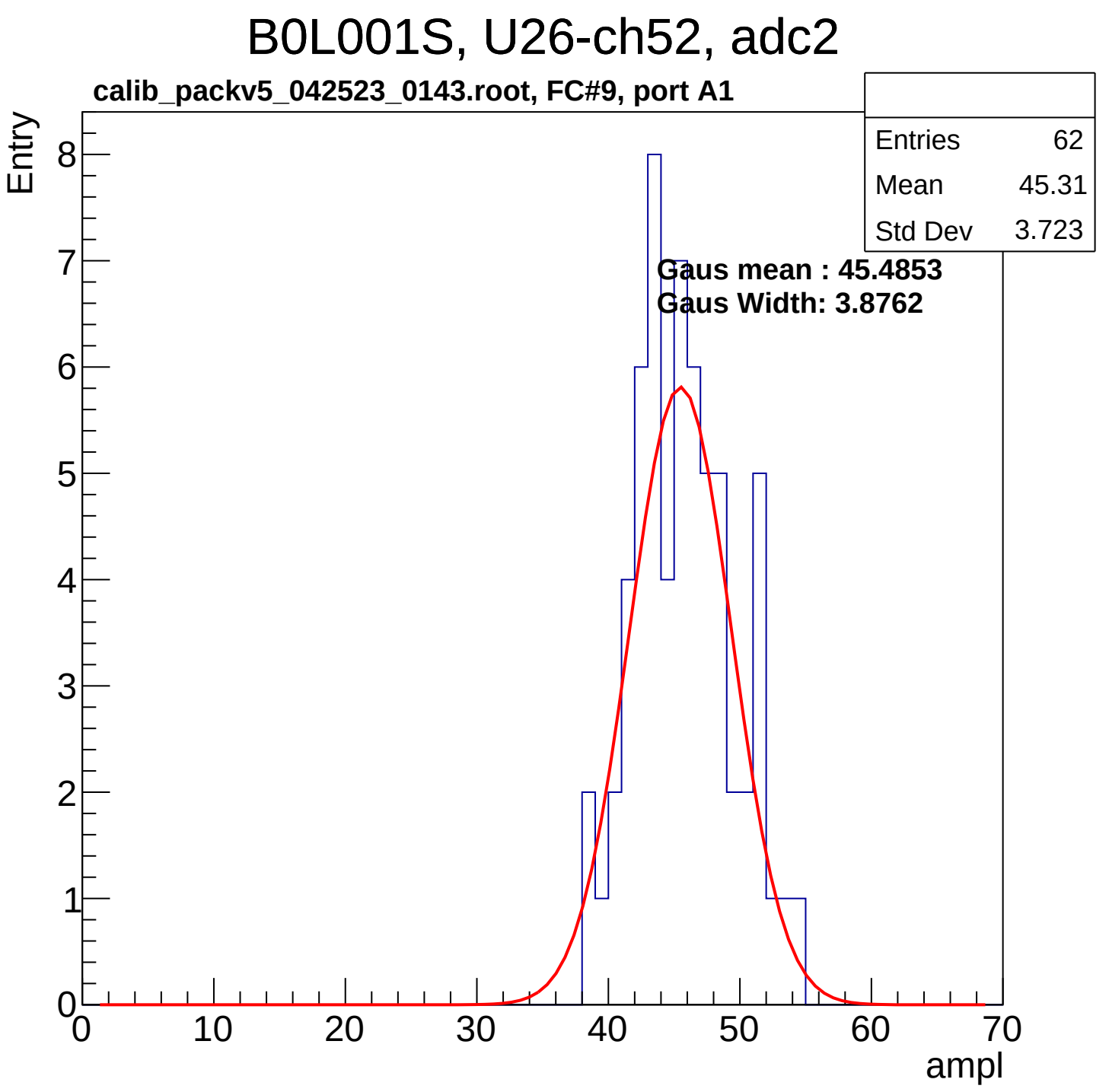
B0L001S, U26-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 45.31
Std Dev 3.723
Gaus mean : 45.4853
Gaus Width: 3.8762
Entry
ampl
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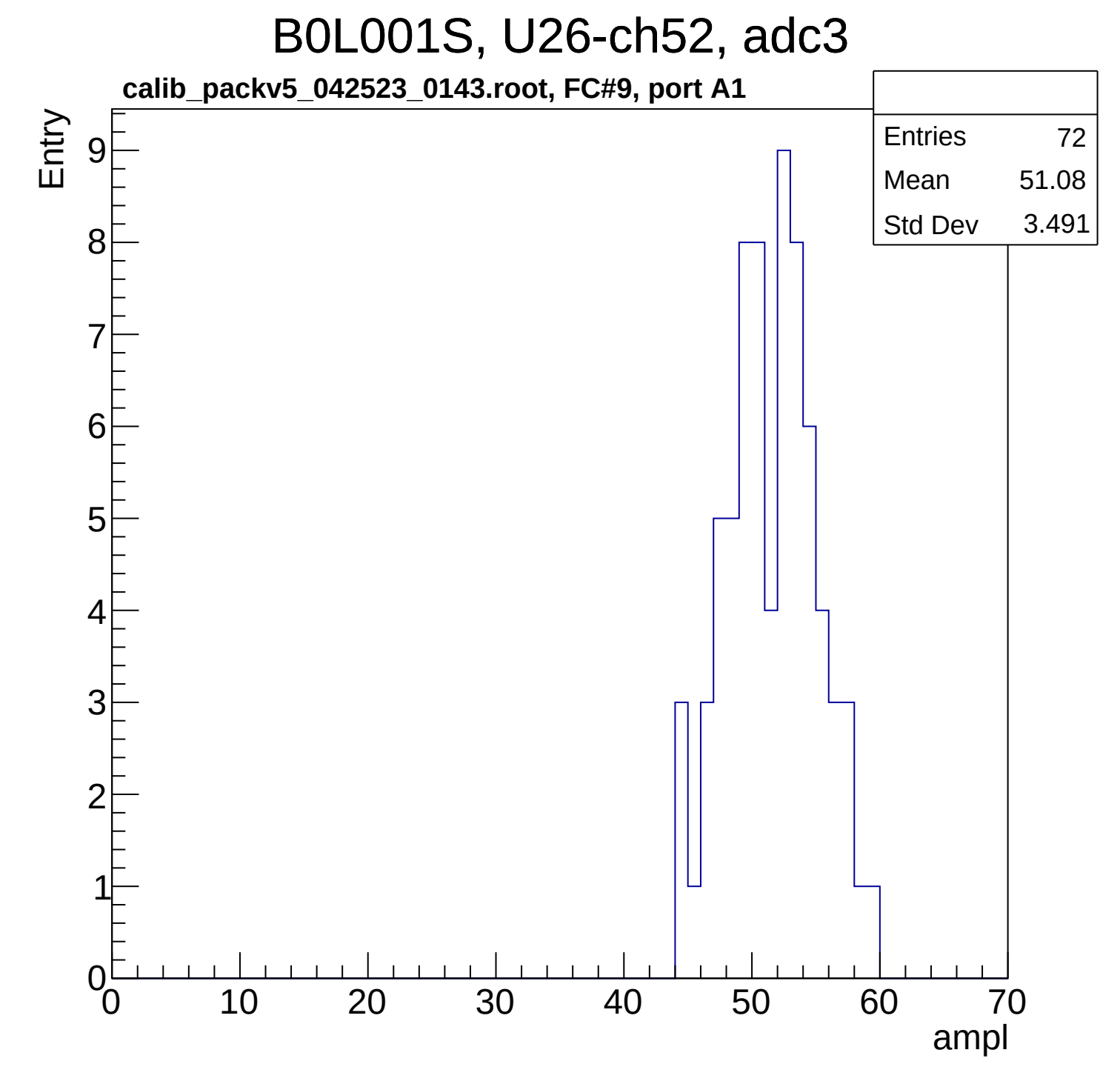
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.08
Std Dev 3.491
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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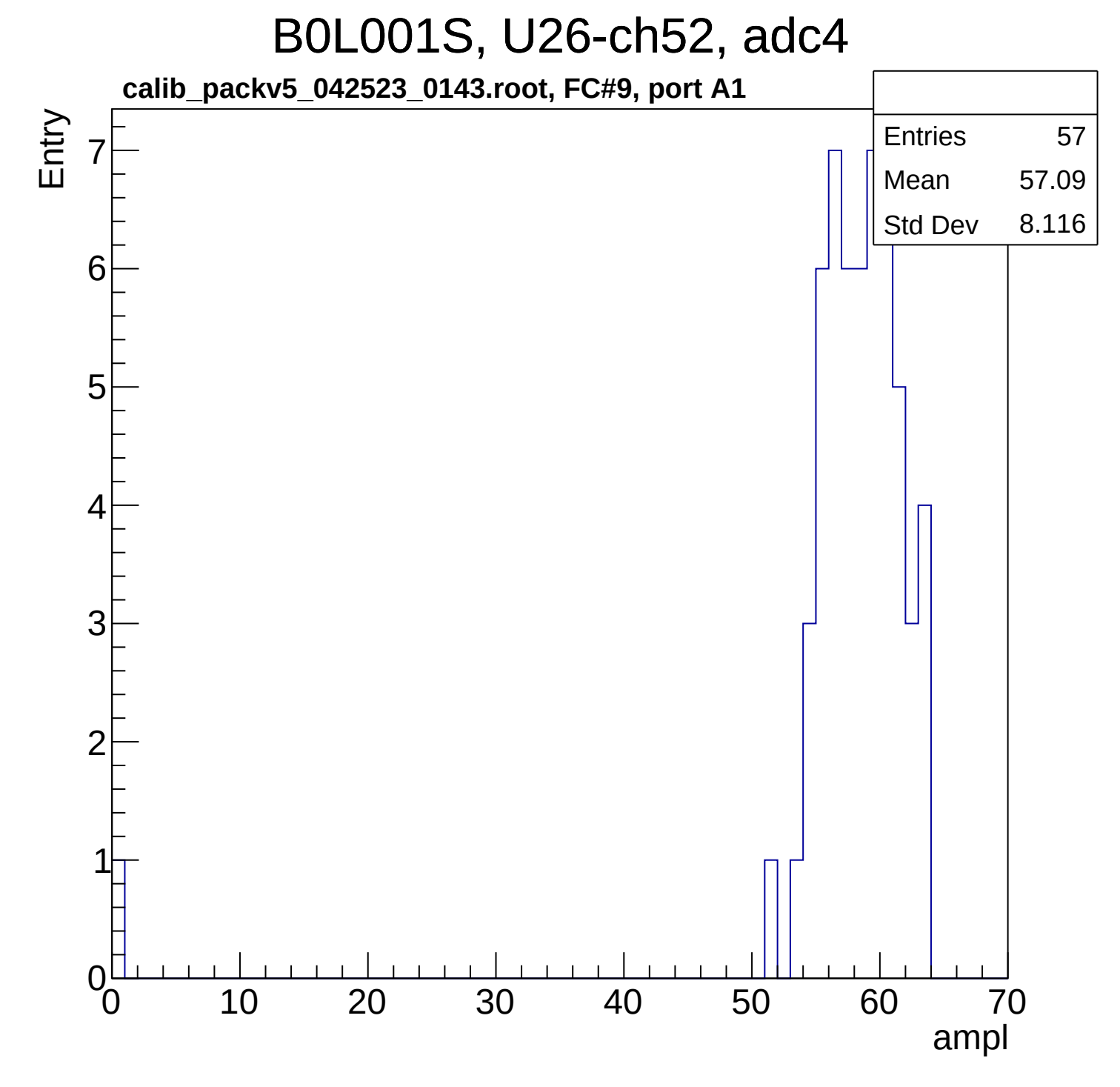
B0L001S, U26-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.09
Std Dev 8.116
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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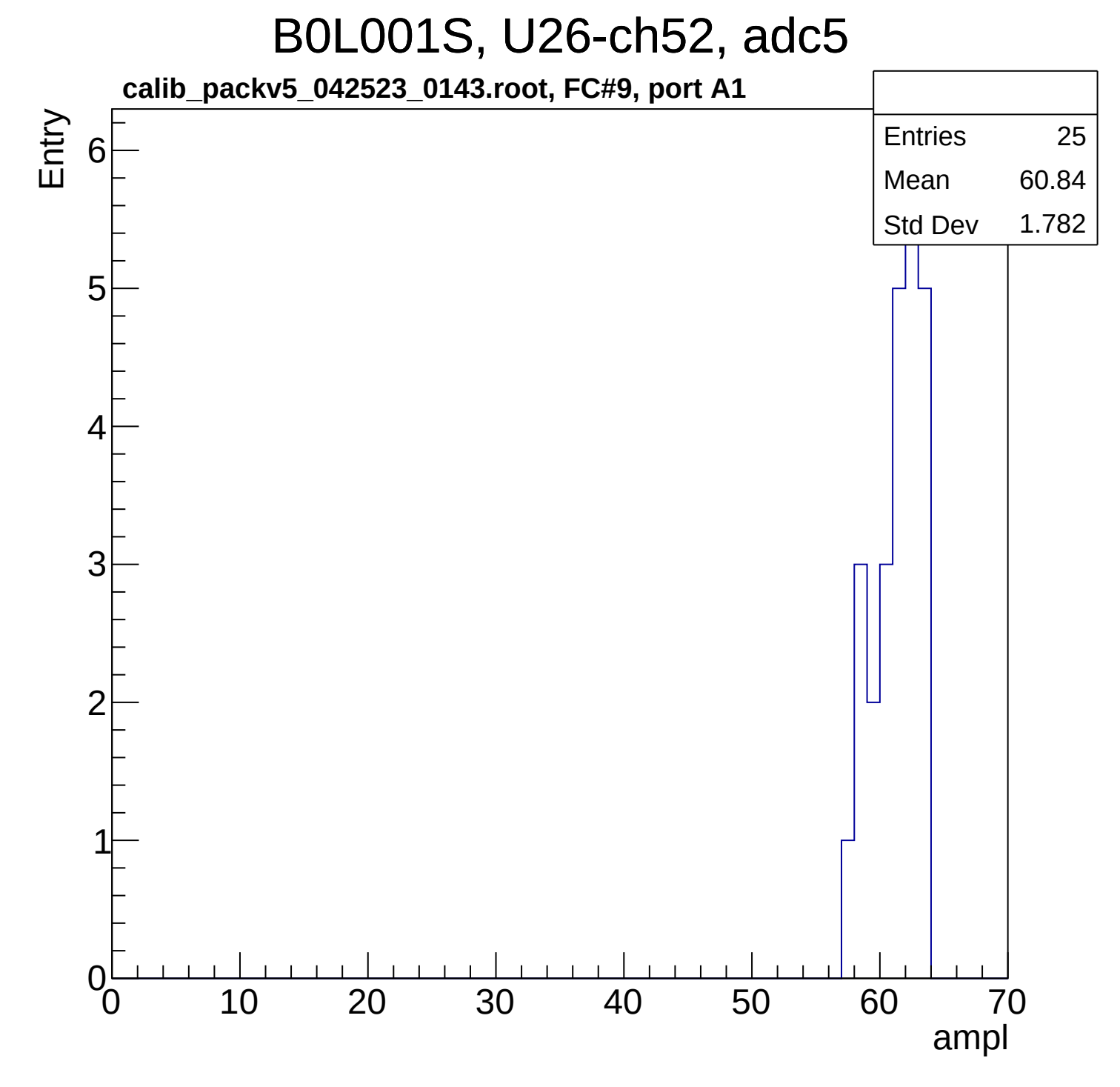
40
50
60
70


B0L001S, U26-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 60.84
Std Dev 1.782
Entry
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1
2
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4
5
6
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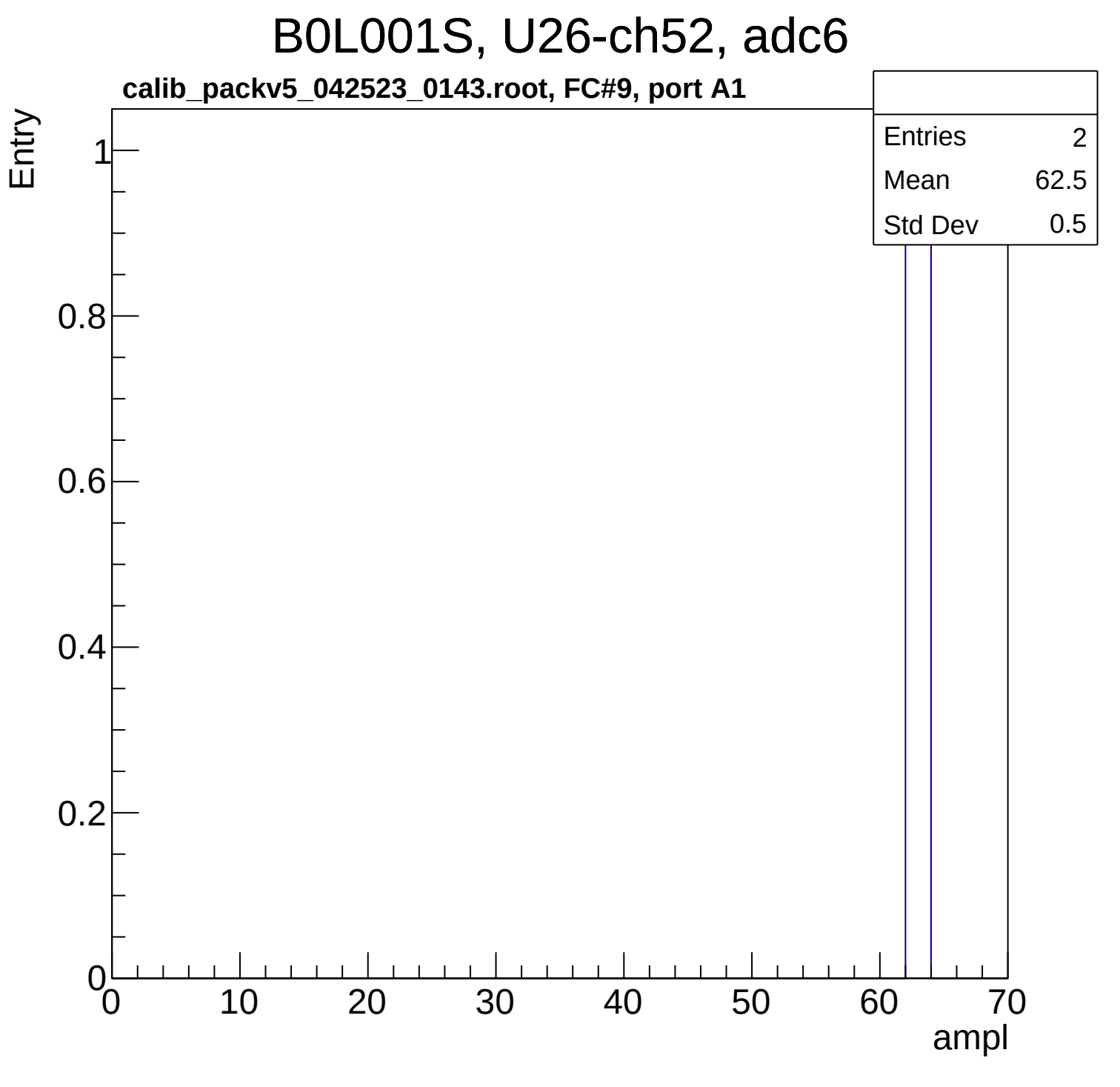
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.6
0.4
0.2
0
0
10
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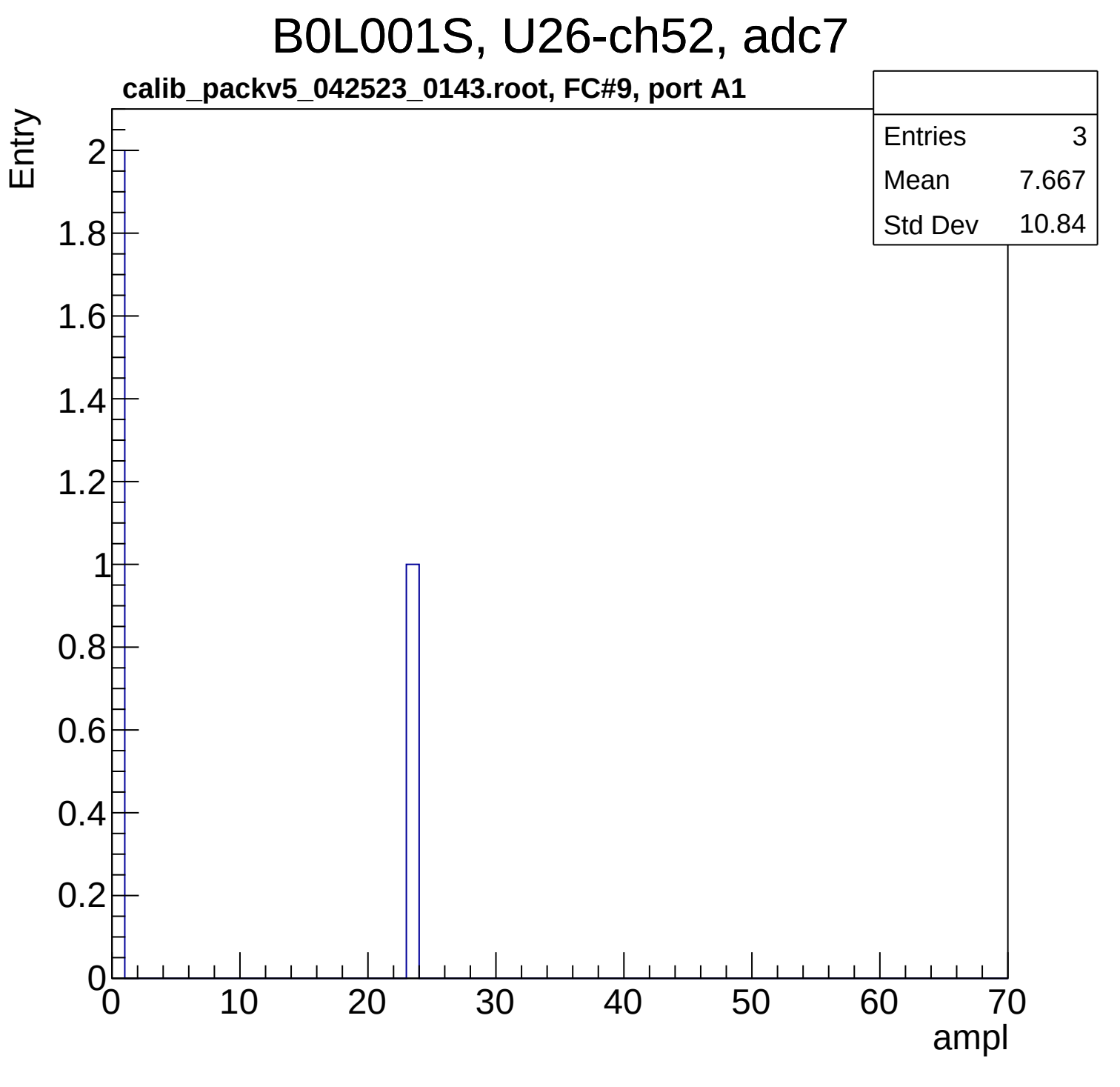
30
40
50
60
70
ampl


B0L001S, U26-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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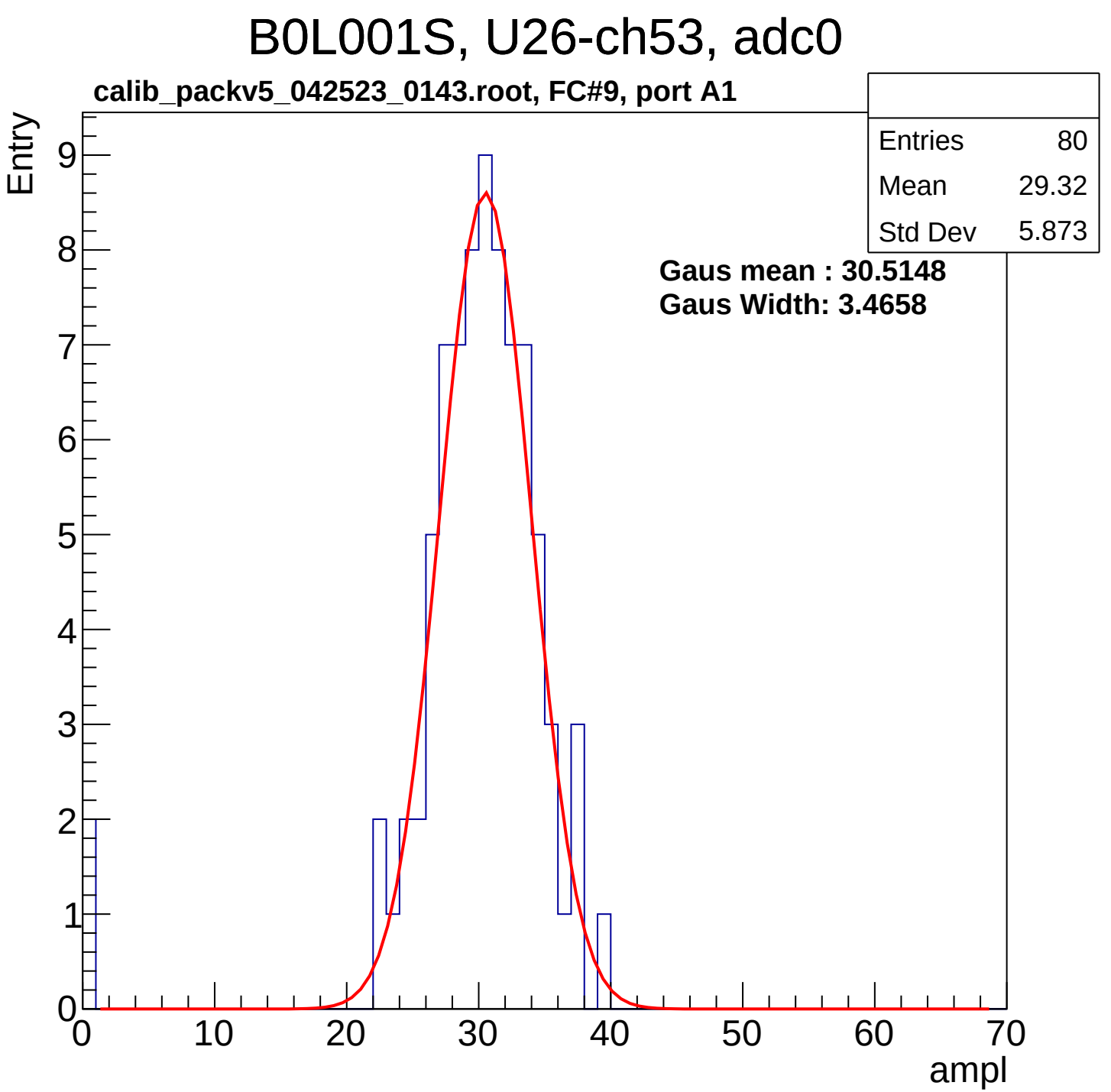
40
50
60
70


B0L001S, U26-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.32
Std Dev 5.873
Gaus mean : 30.5148
Gaus Width: 3.4658
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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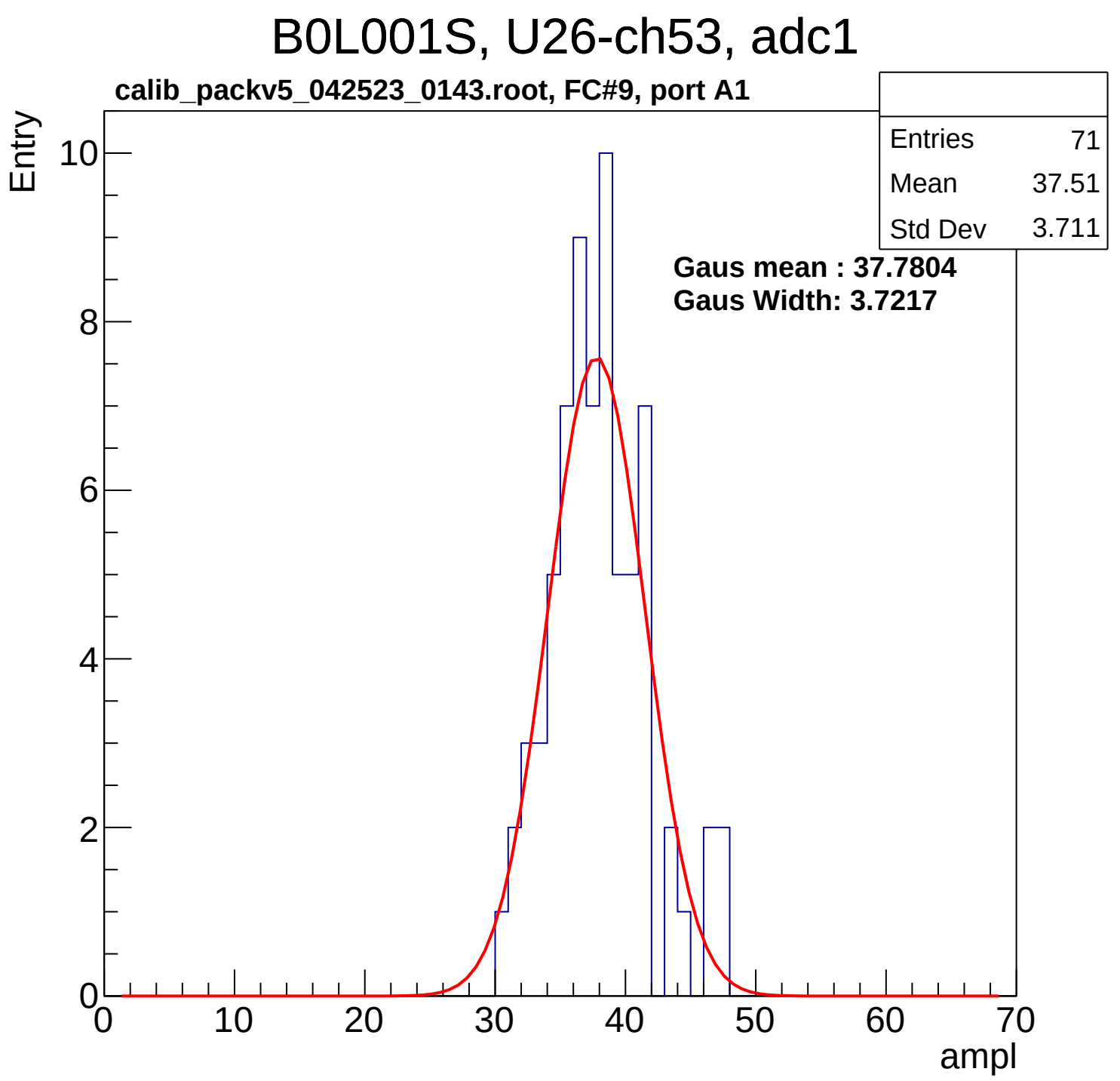
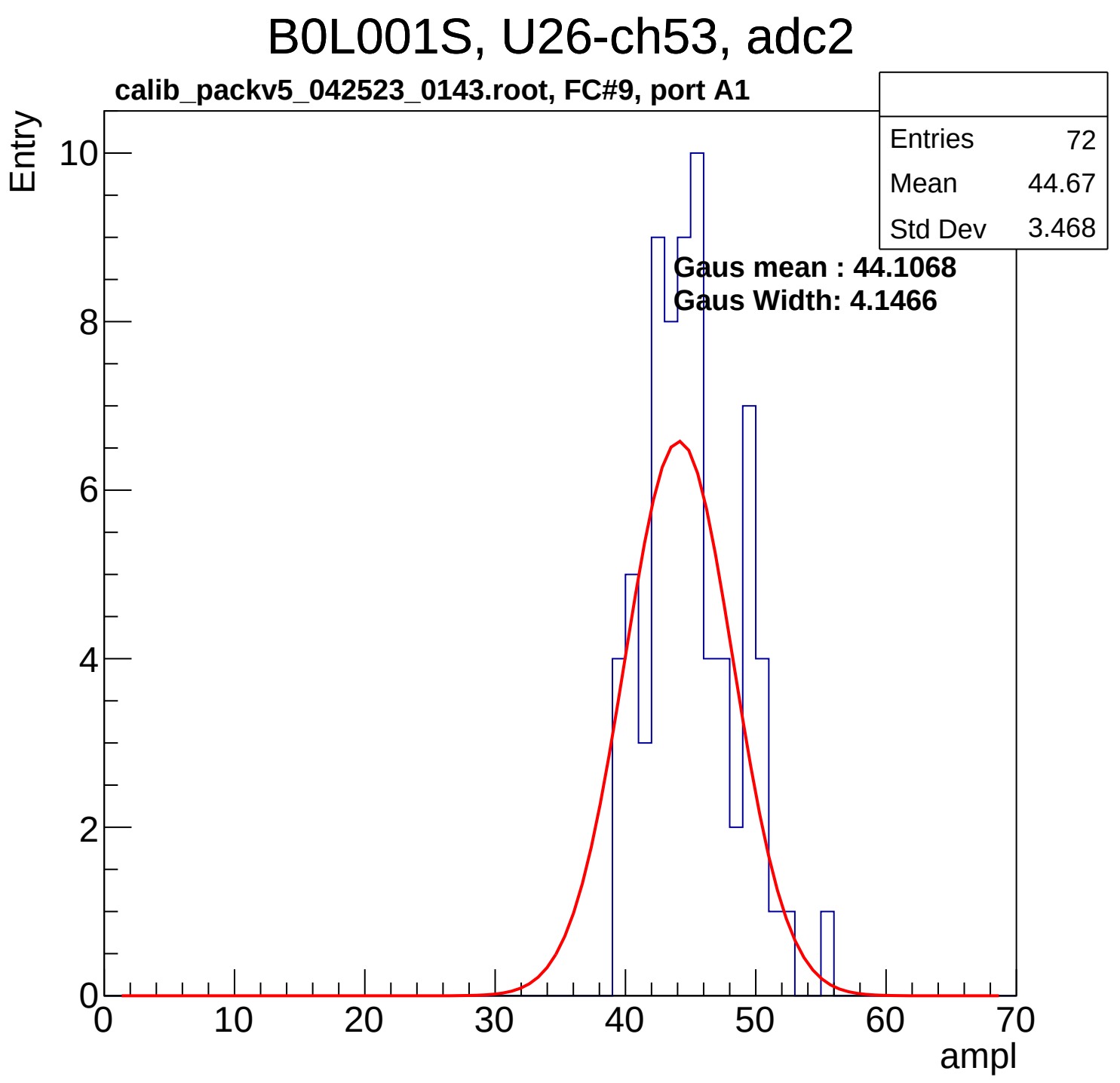

B0L001S, U26-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.51
Std Dev 3.711
Gaus mean : 37.7804
Gaus Width: 3.7217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.67
Std Dev 3.468
Gaus mean : 44.1068
Gaus Width: 4.1466
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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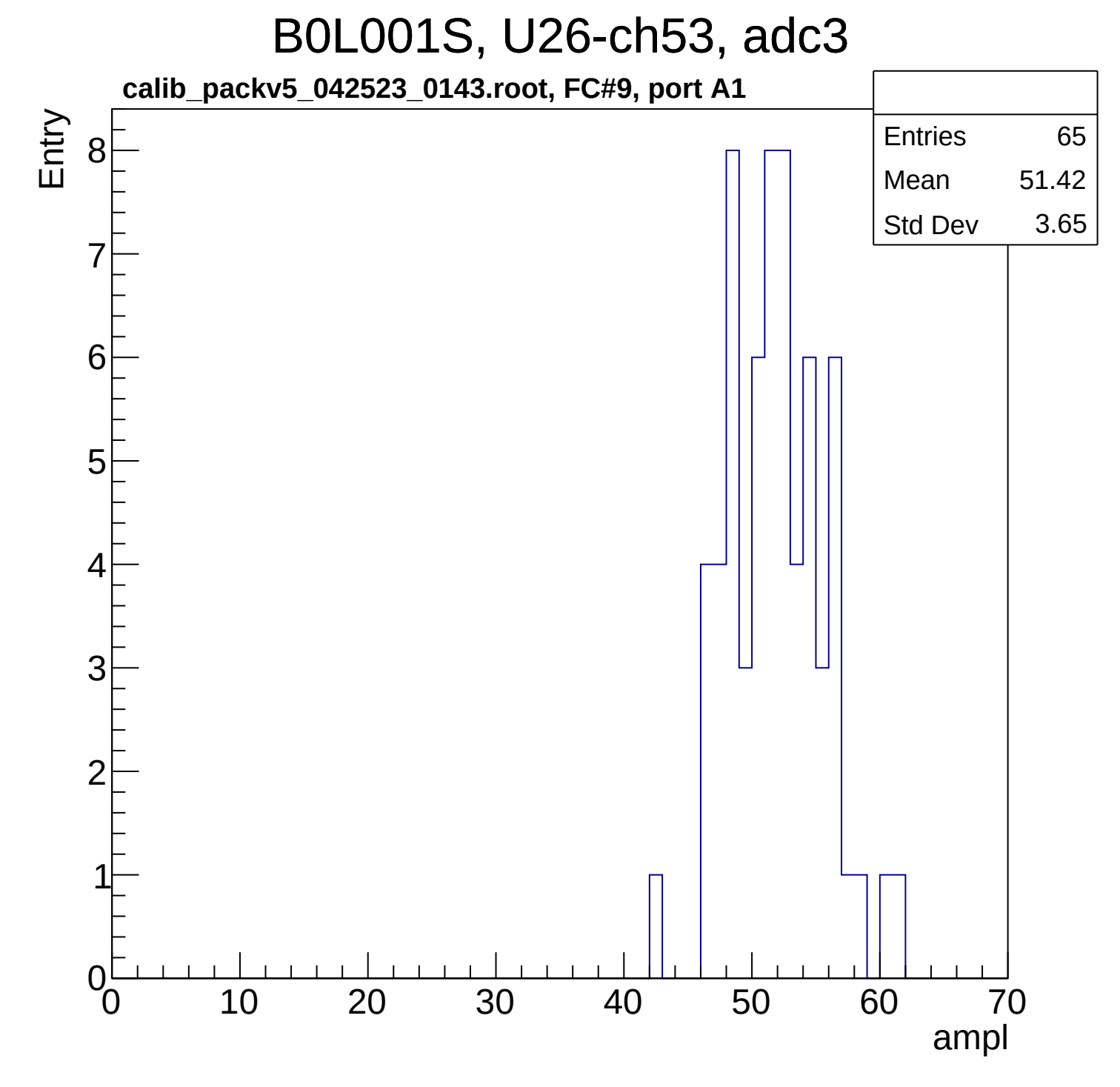
B0L001S, U26-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.42
Std Dev 3.65
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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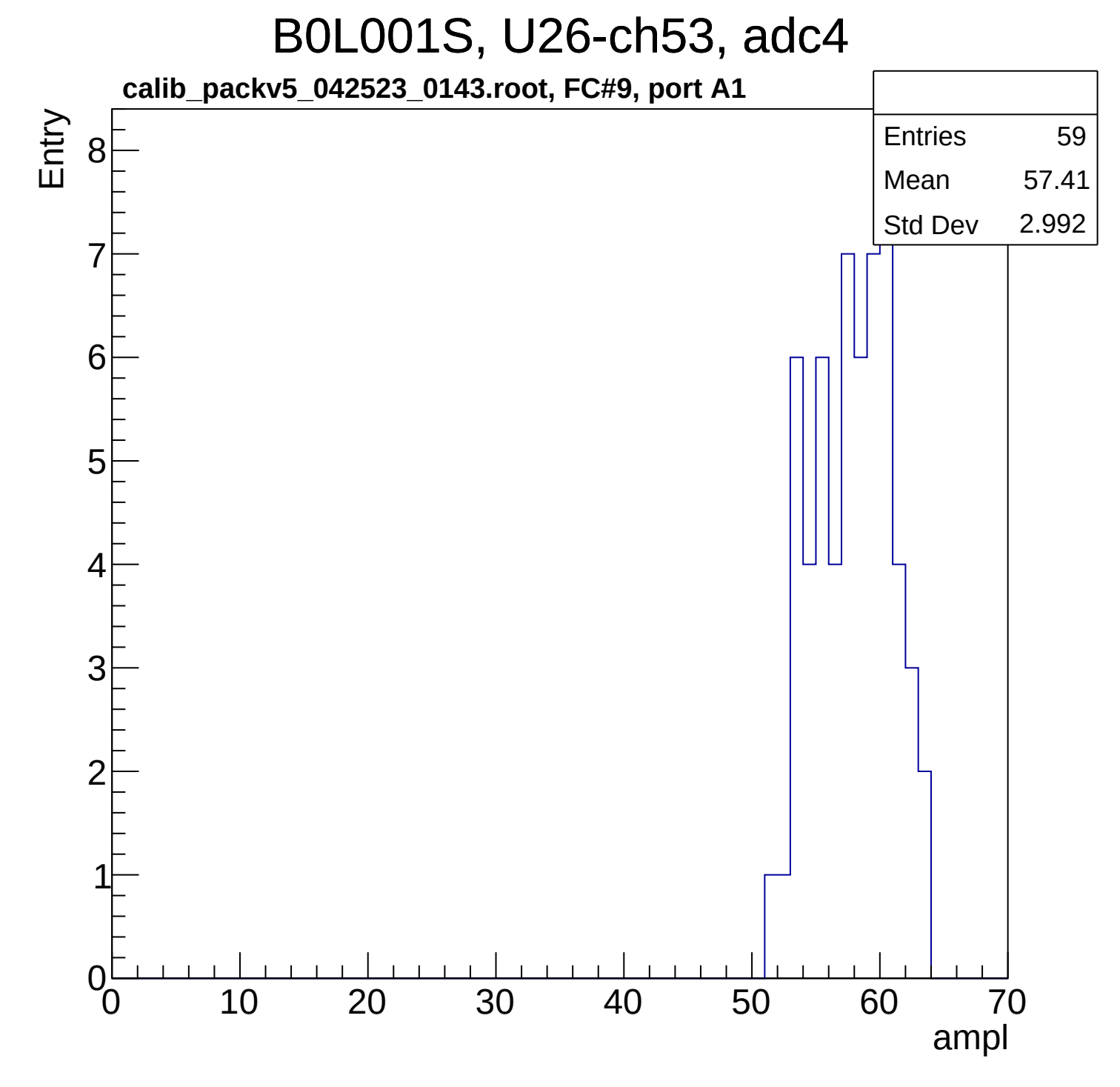
B0L001S, U26-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.41
Std Dev 2.992
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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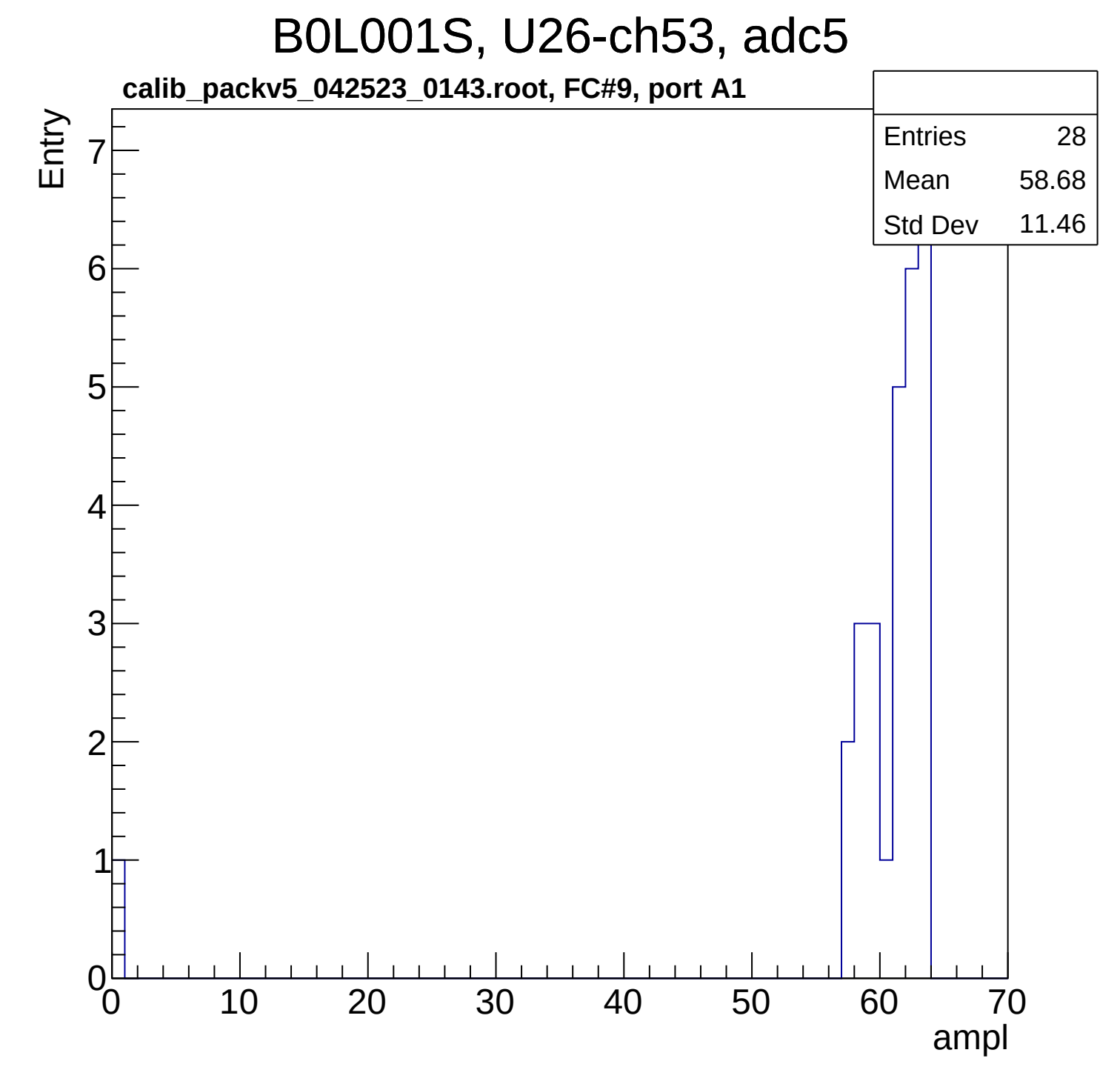
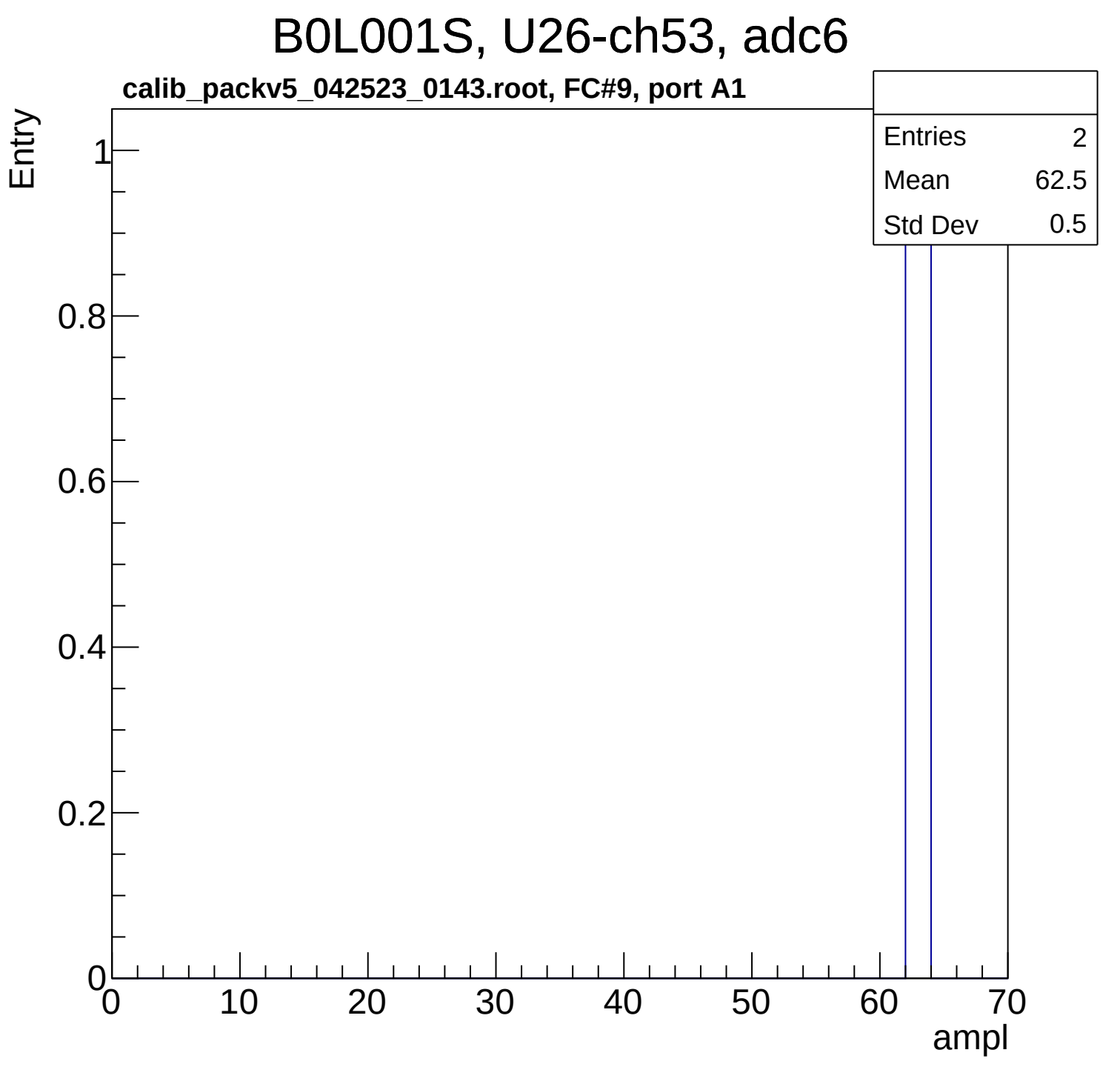

B0L001S, U26-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.68
Std Dev 11.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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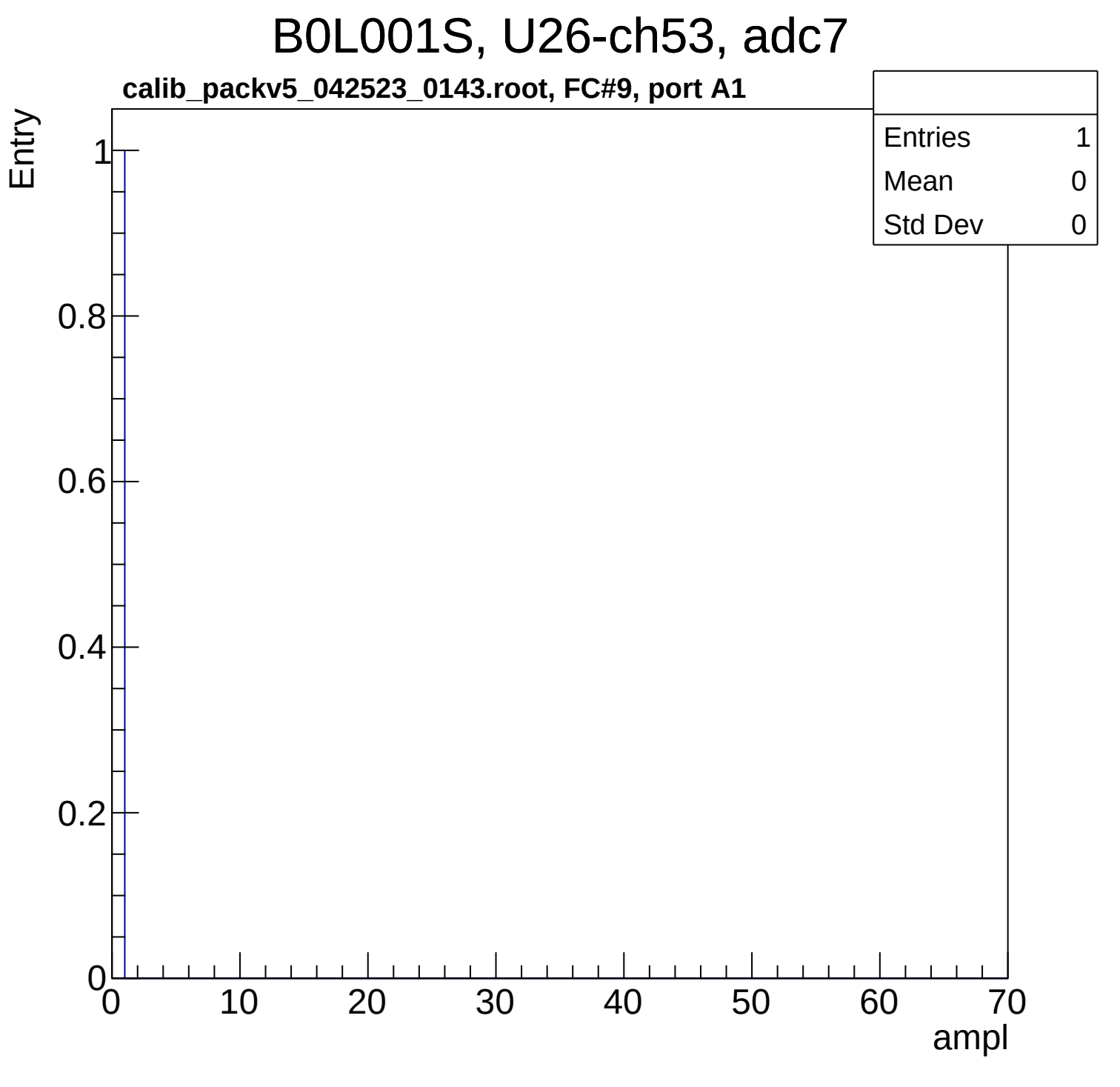
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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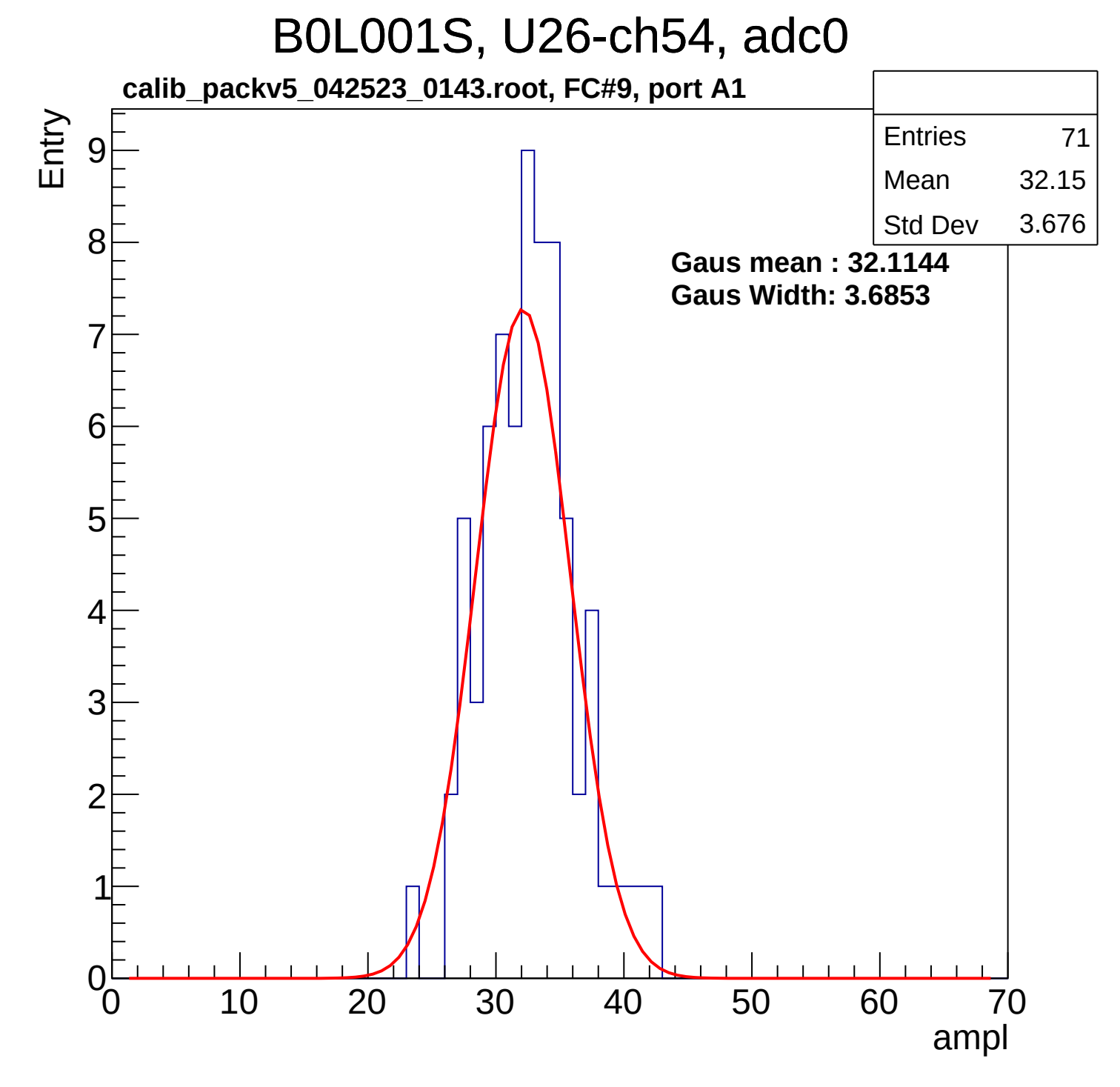
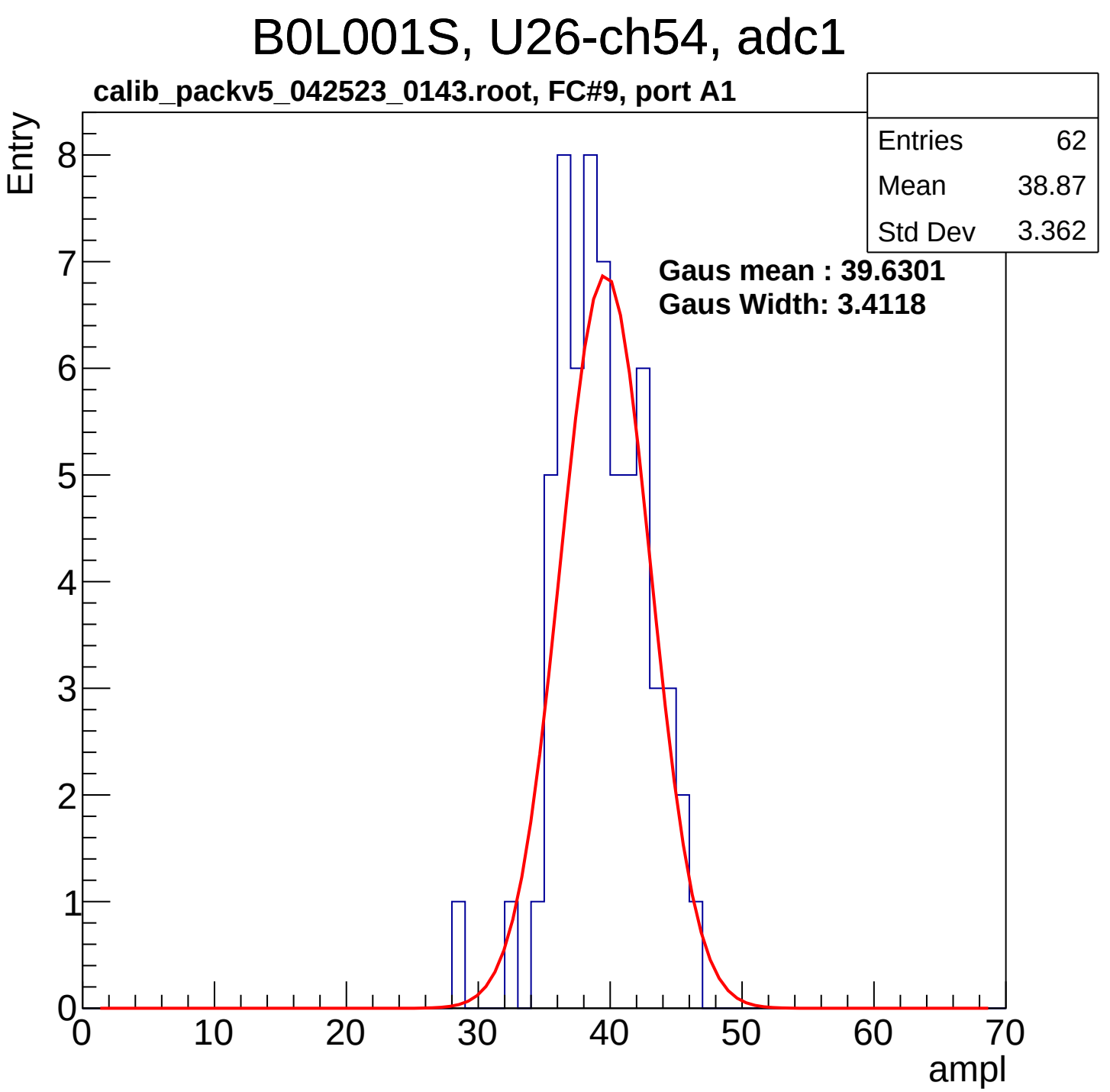
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.15
Std Dev 3.676
Gaus mean : 32.1144
Gaus Width: 3.6853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.87
Std Dev 3.362
Gaus mean : 39.6301
Gaus Width: 3.4118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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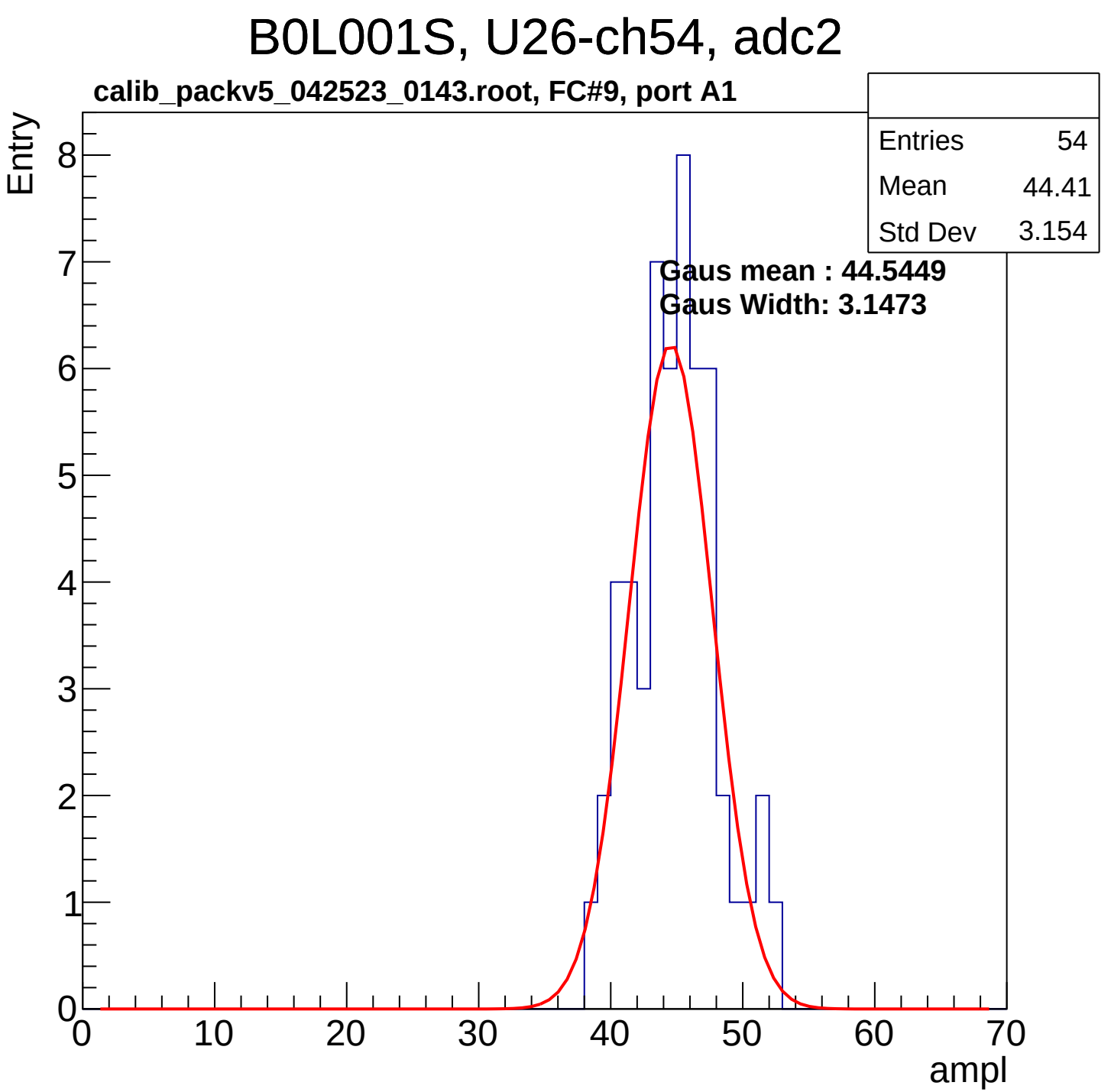
B0L001S, U26-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.41
Std Dev 3.154
Gaus mean : 44.5449
Gaus Width: 3.1473
Entry
ampl
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1
2
3
4
5
6
7
8
0
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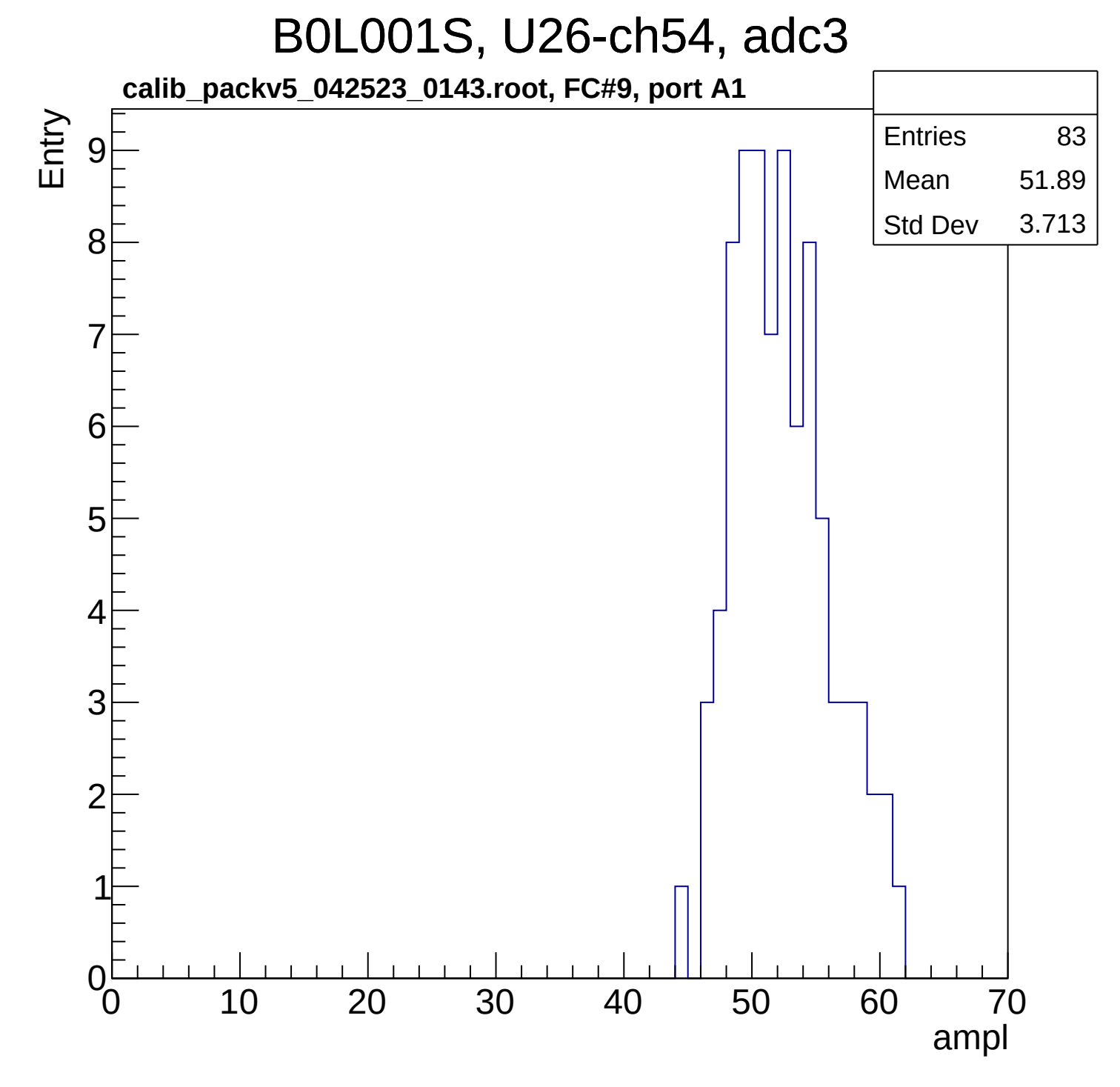
B0L001S, U26-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 51.89
Std Dev 3.713
Entry
ampl
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6
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0
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60
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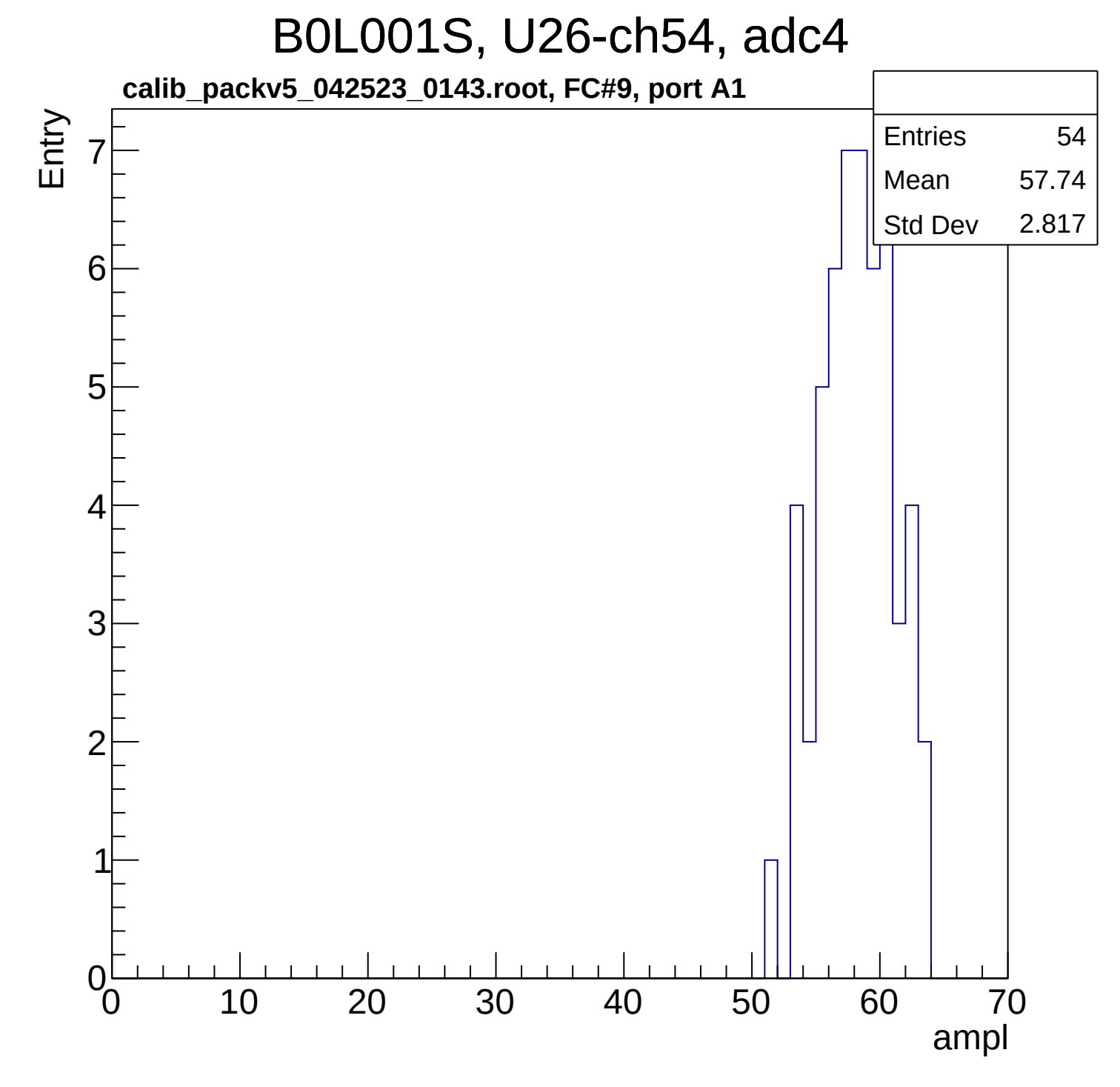
B0L001S, U26-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.74
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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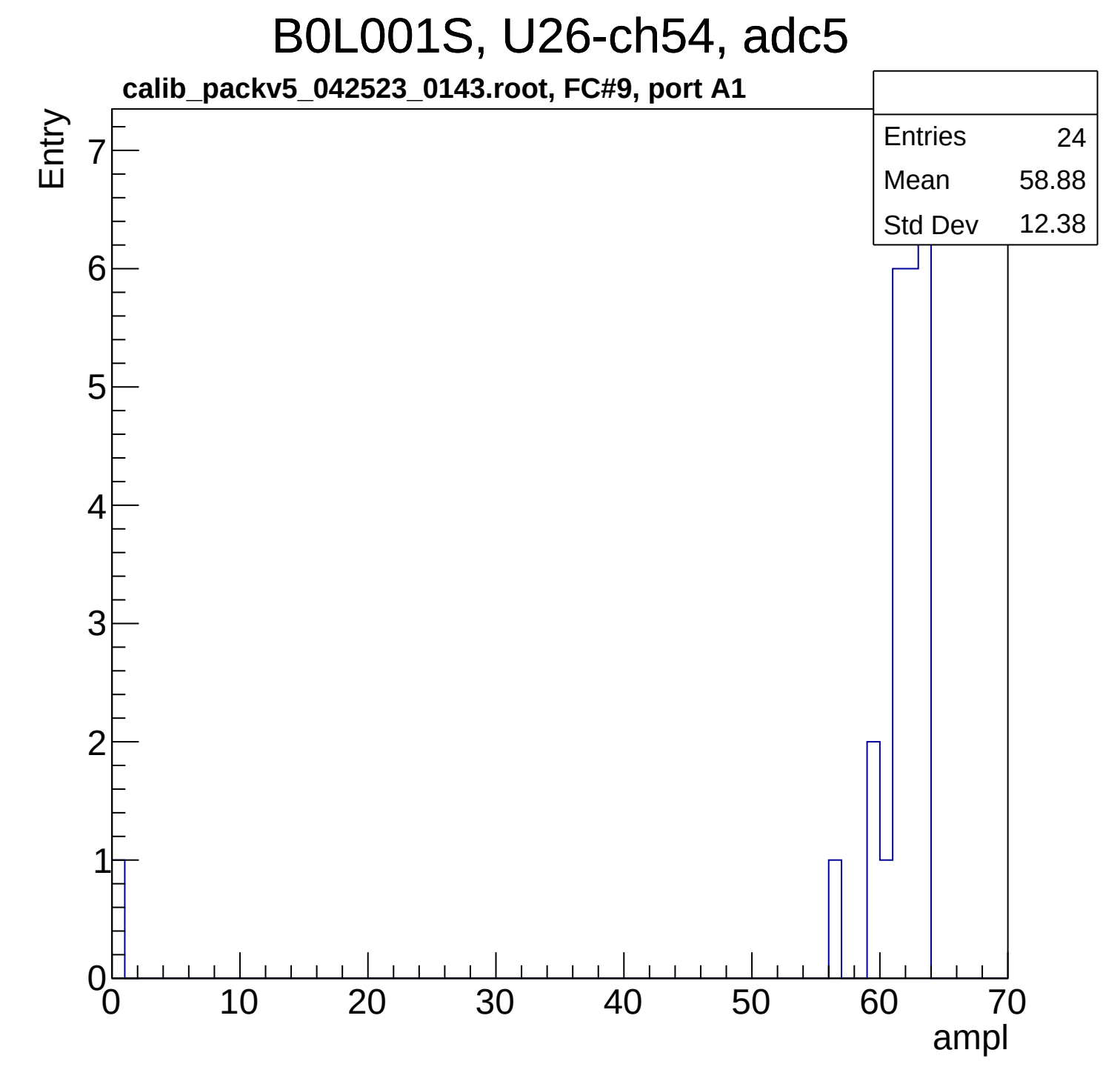
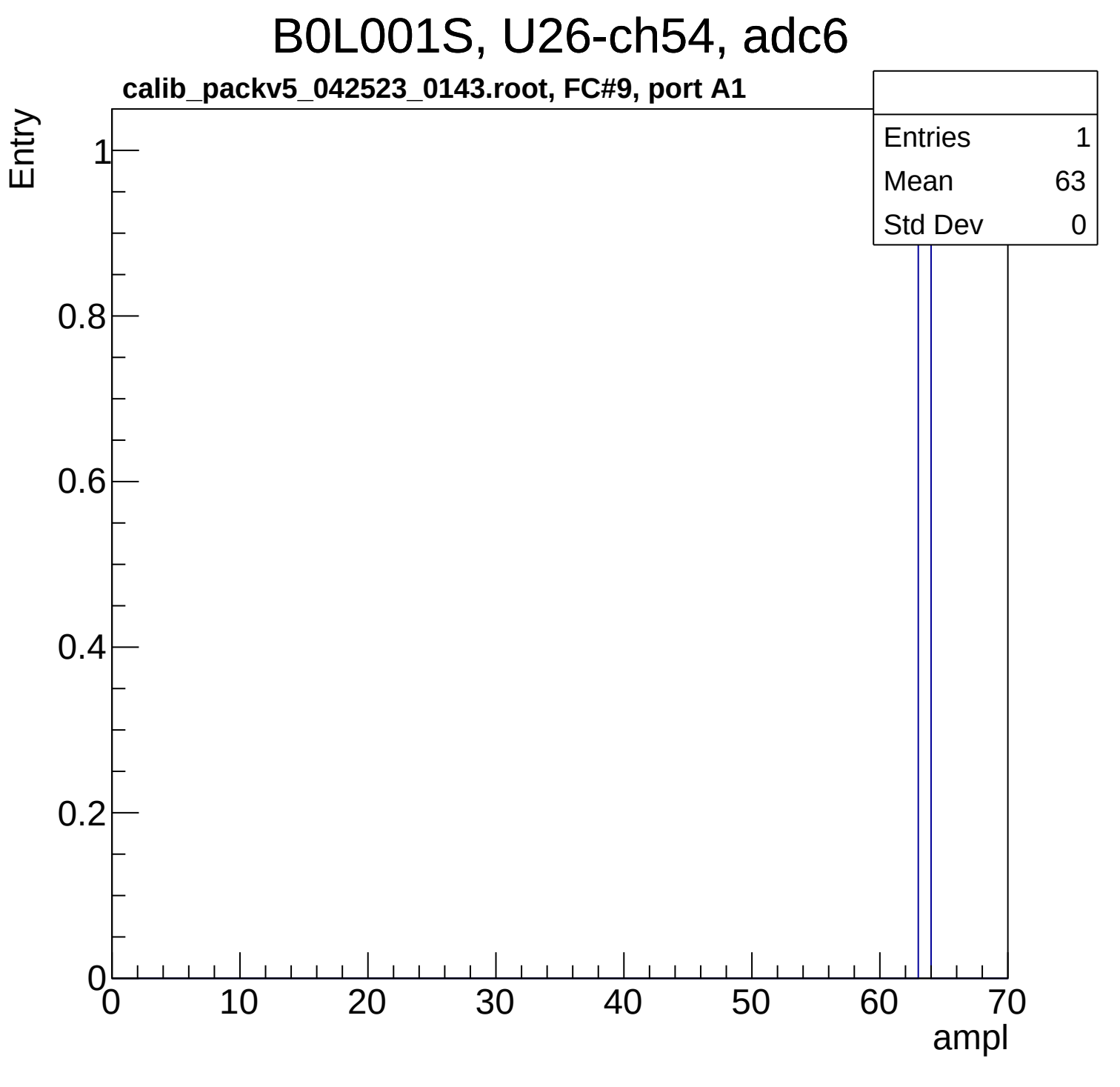
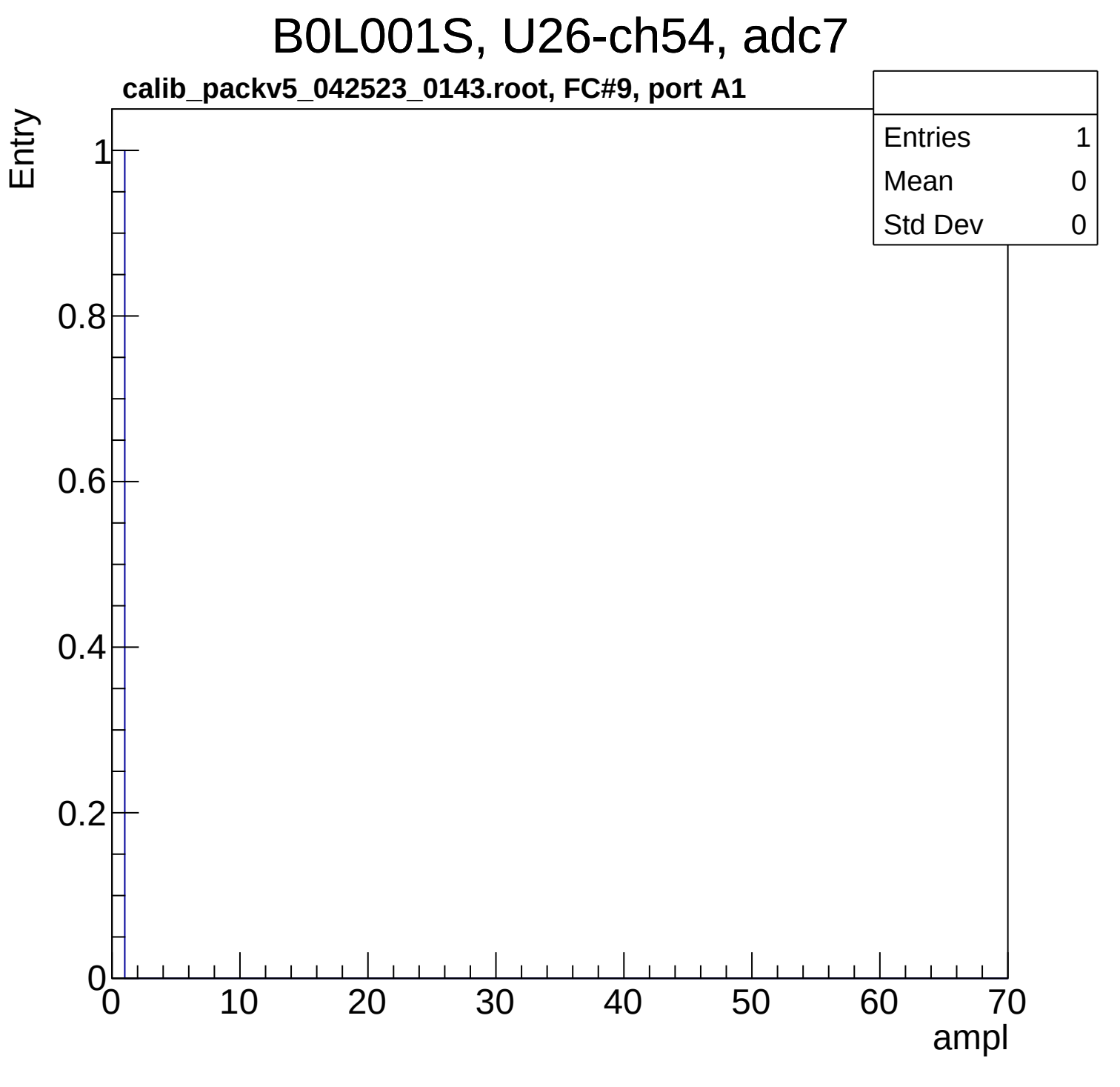
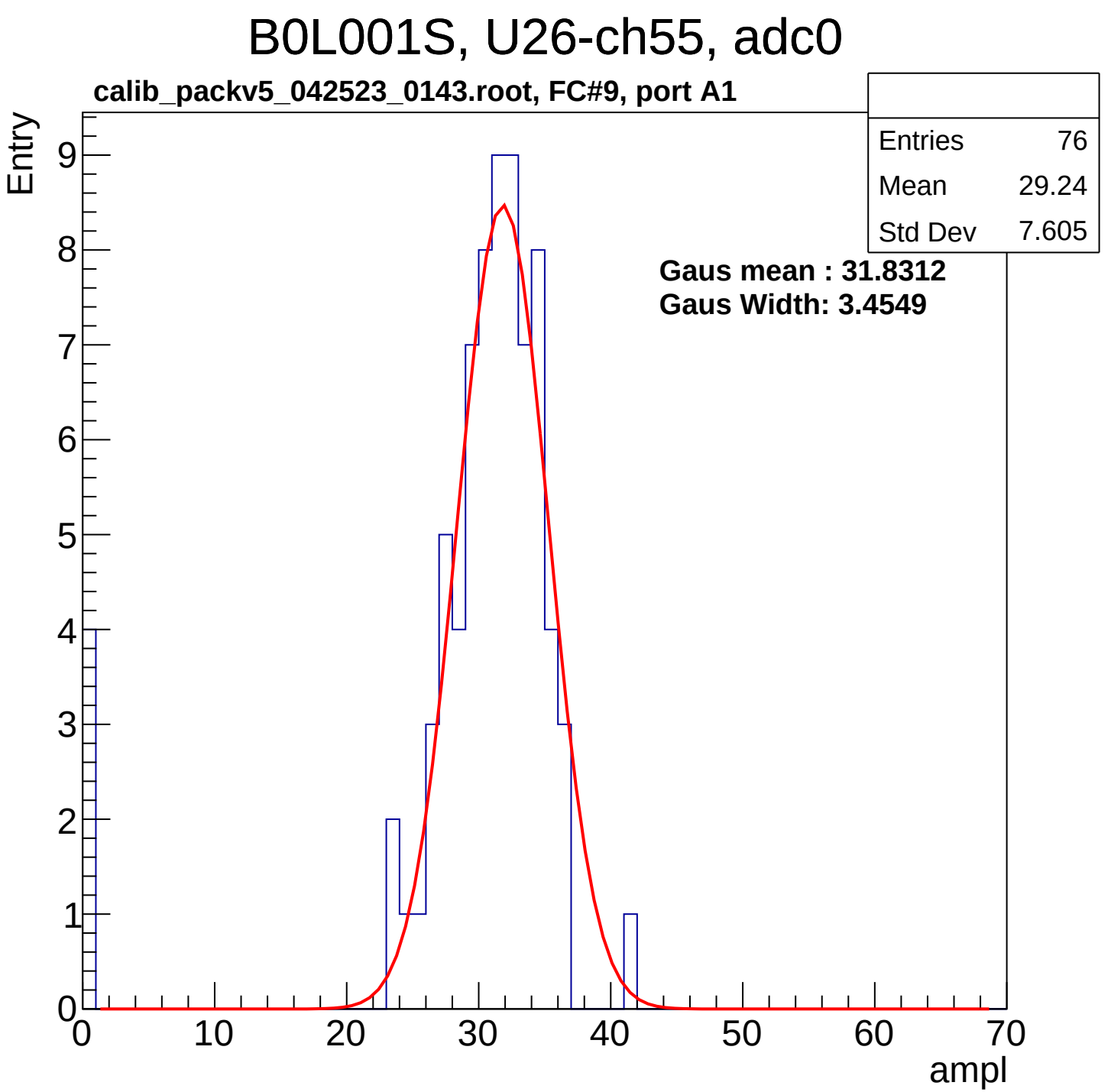

B0L001S, U26-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.88
Std Dev 12.38
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.24
Std Dev 7.605
Gaus mean : 31.8312
Gaus Width: 3.4549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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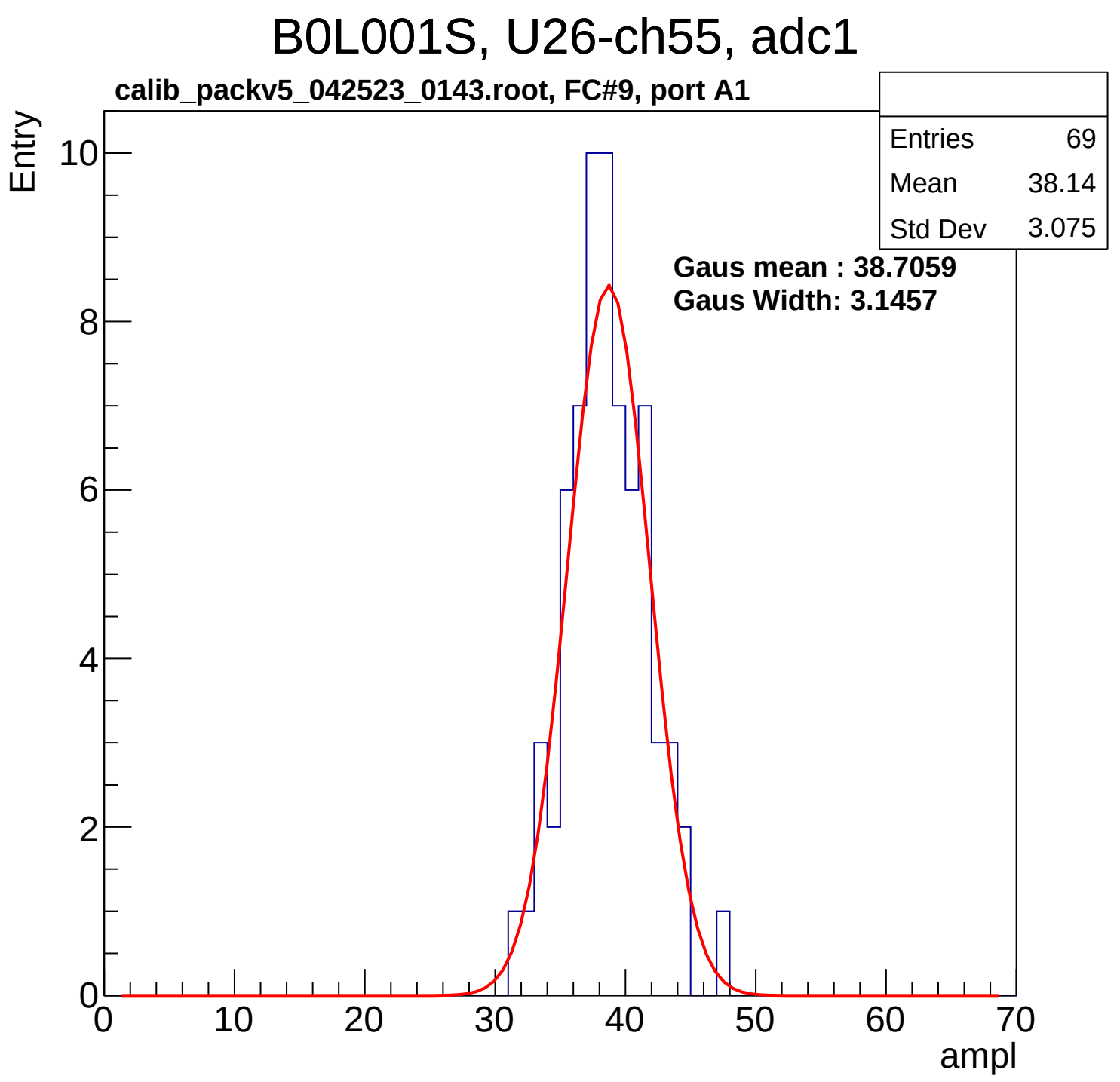
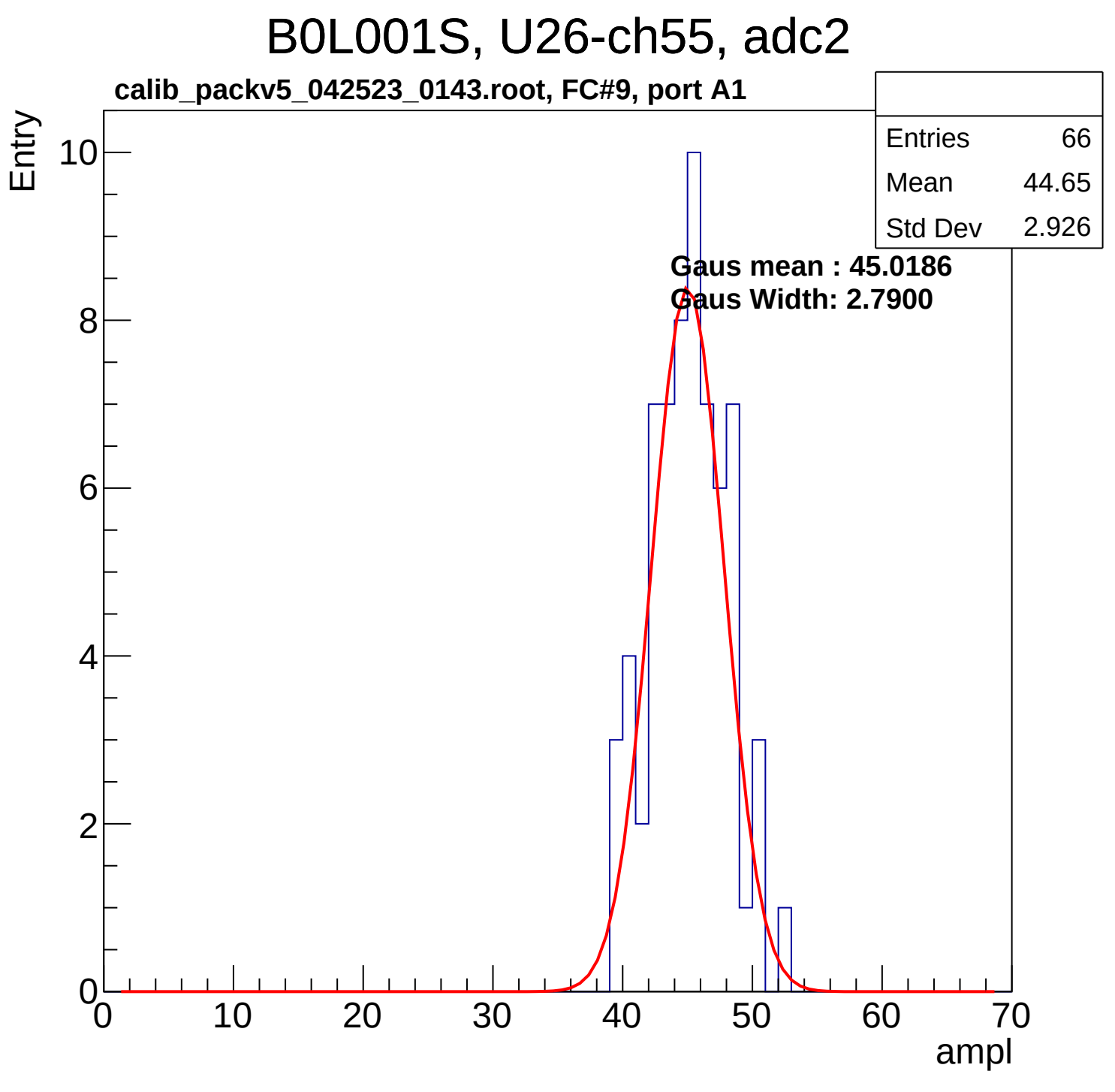
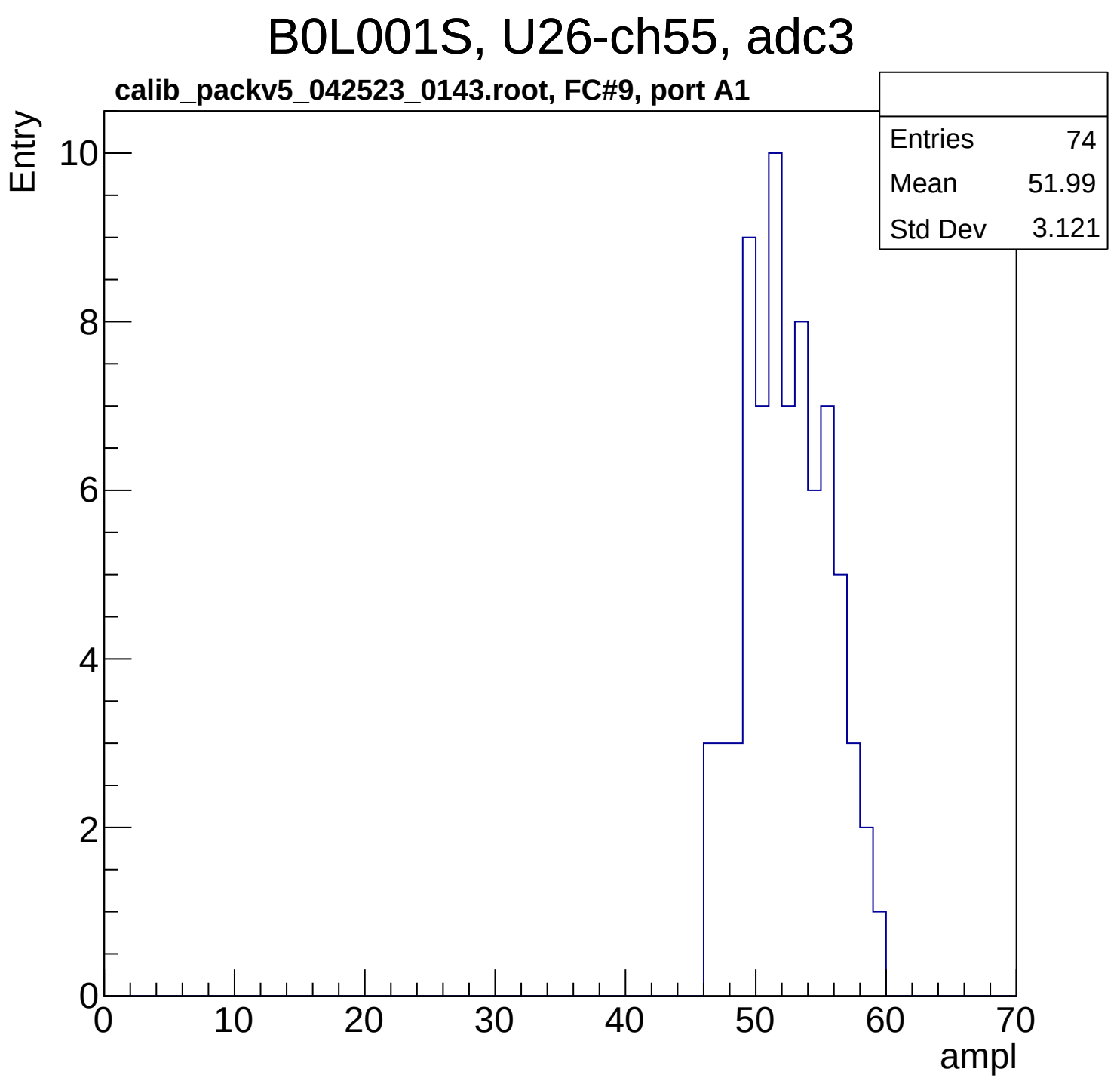
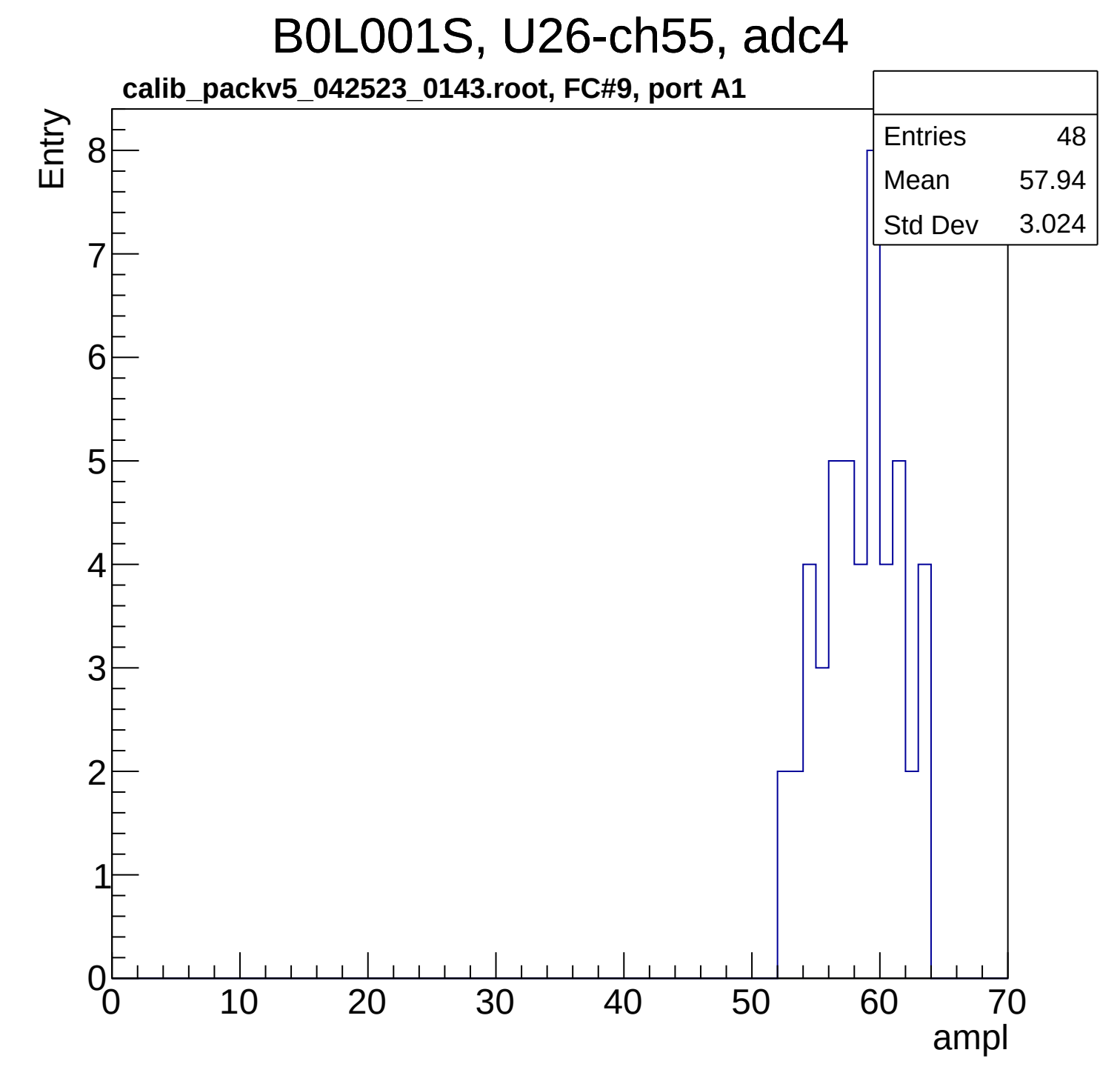
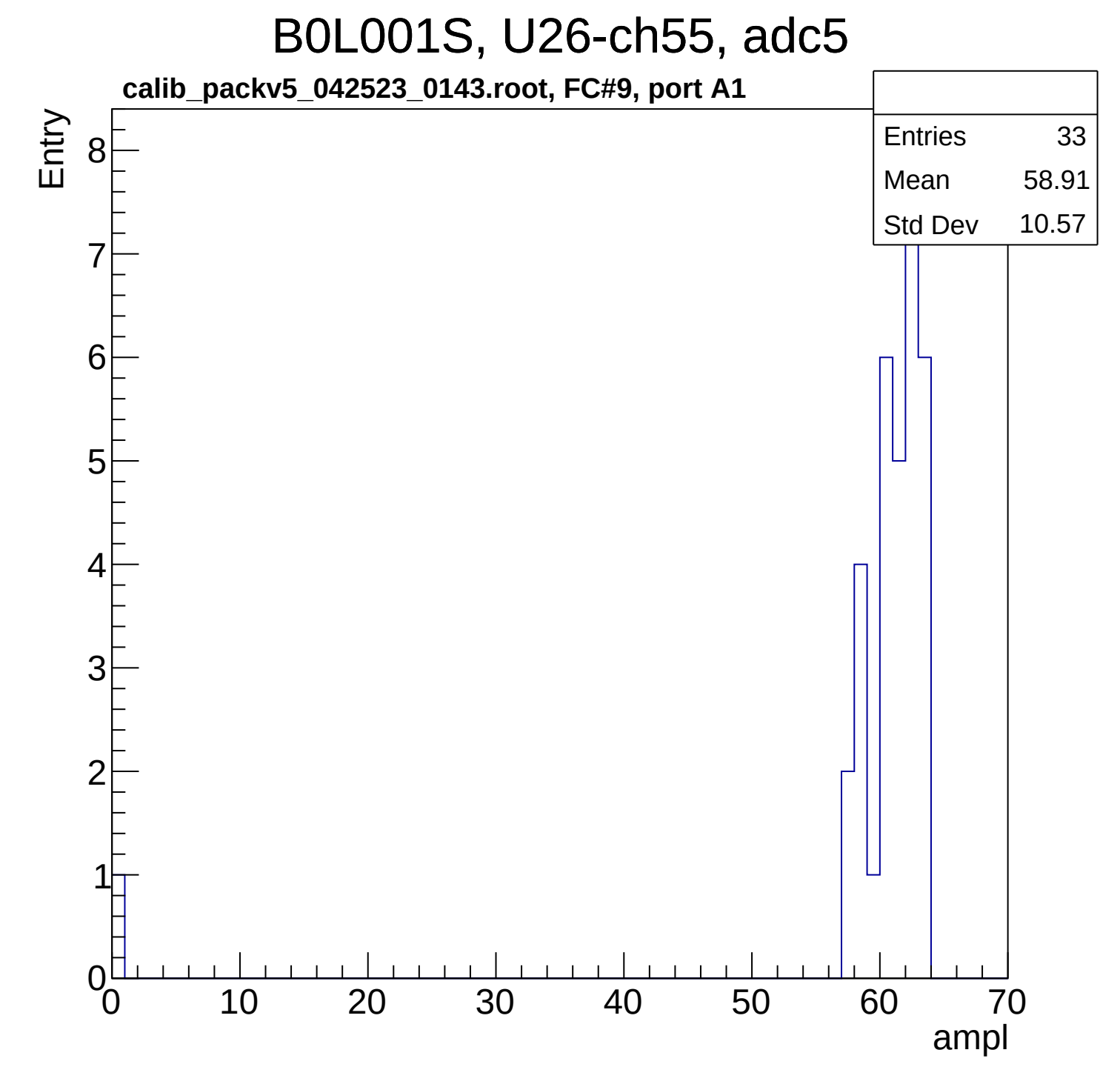

B0L001S, U26-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.14
Std Dev 3.075
Gaus mean : 38.7059
Gaus Width: 3.1457
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.65
Std Dev 2.926
Gaus mean : 45.0186
Gaus Width: 2.7900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.99
Std Dev 3.121
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.94
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.91
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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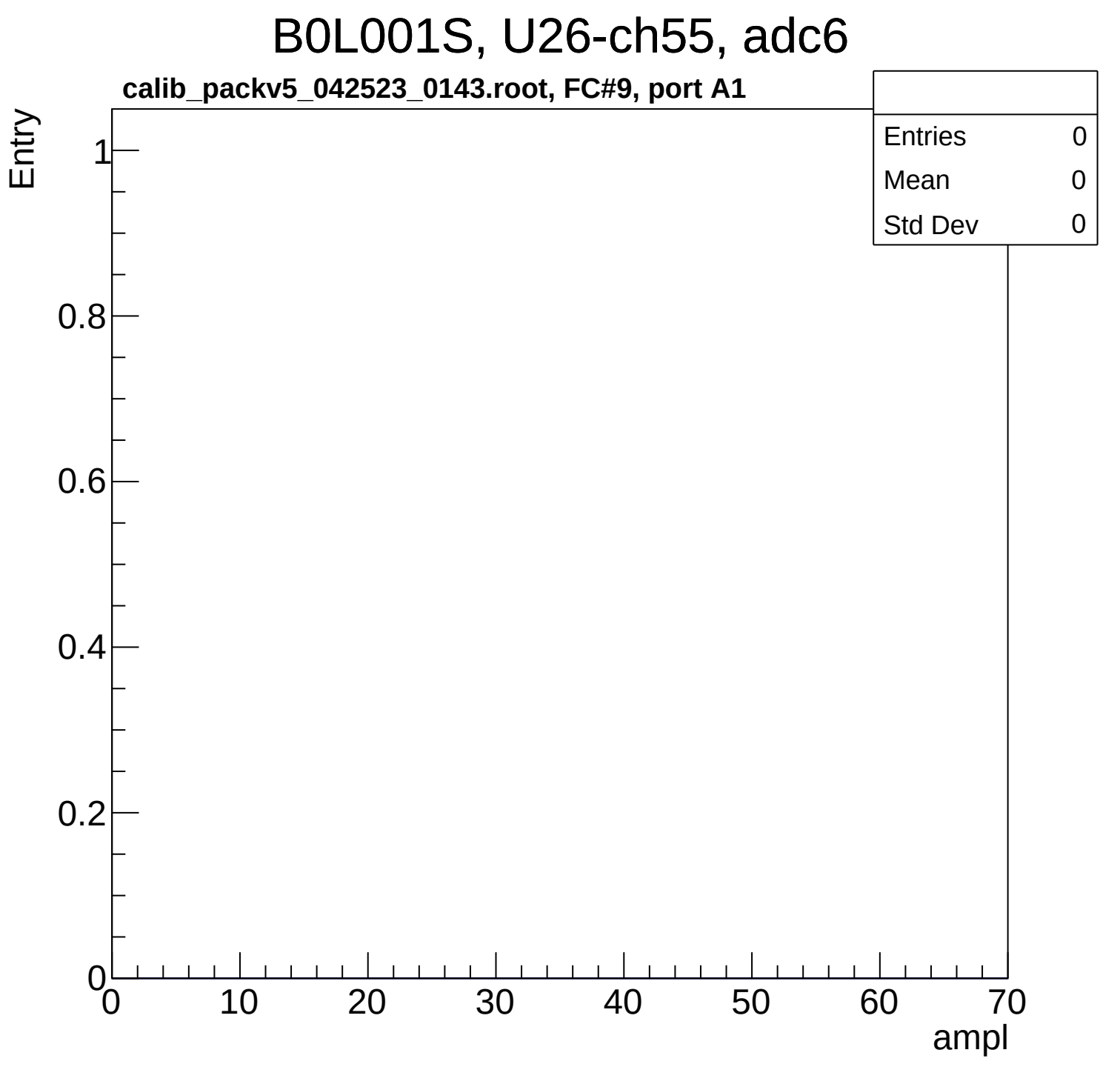
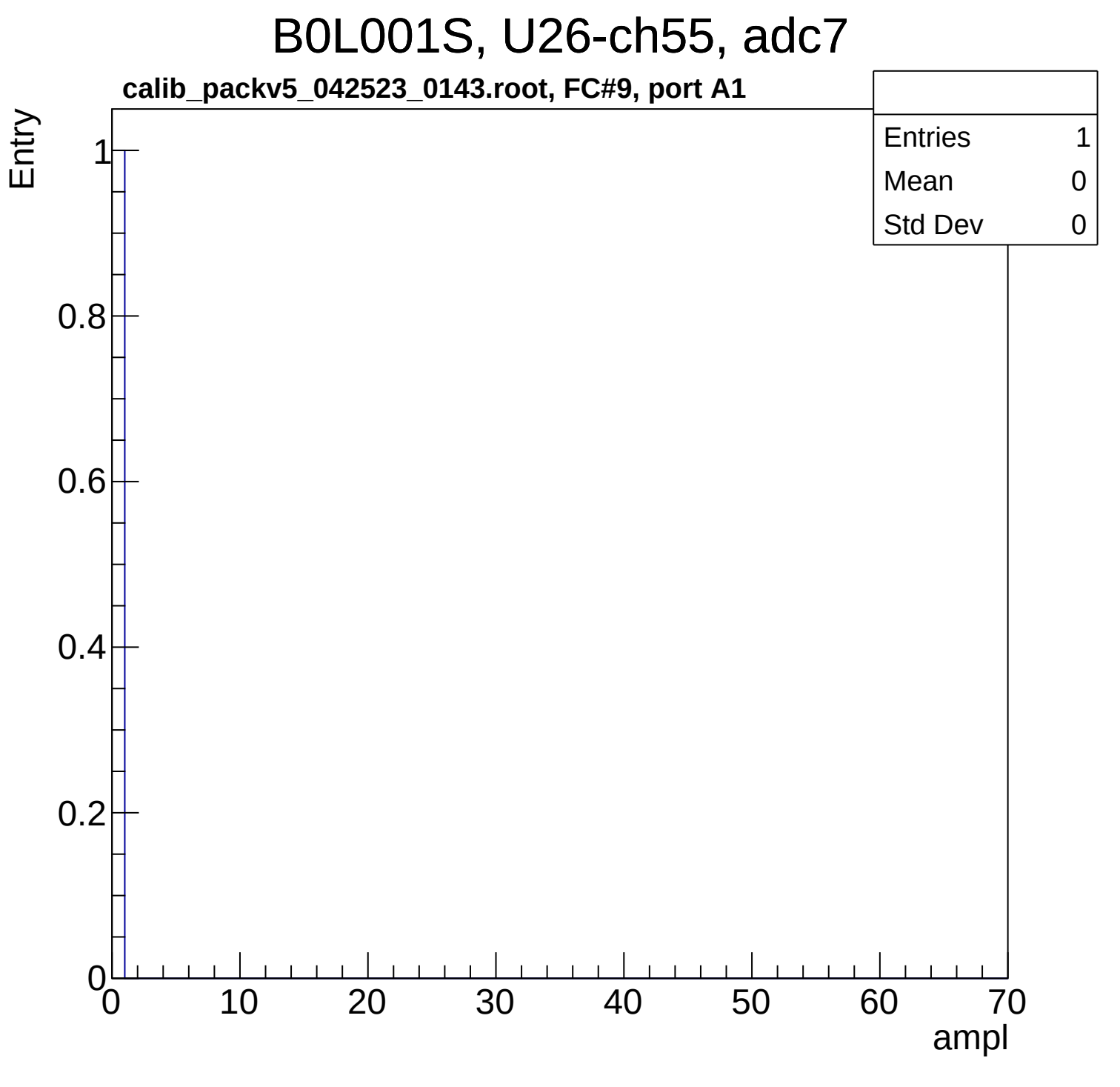
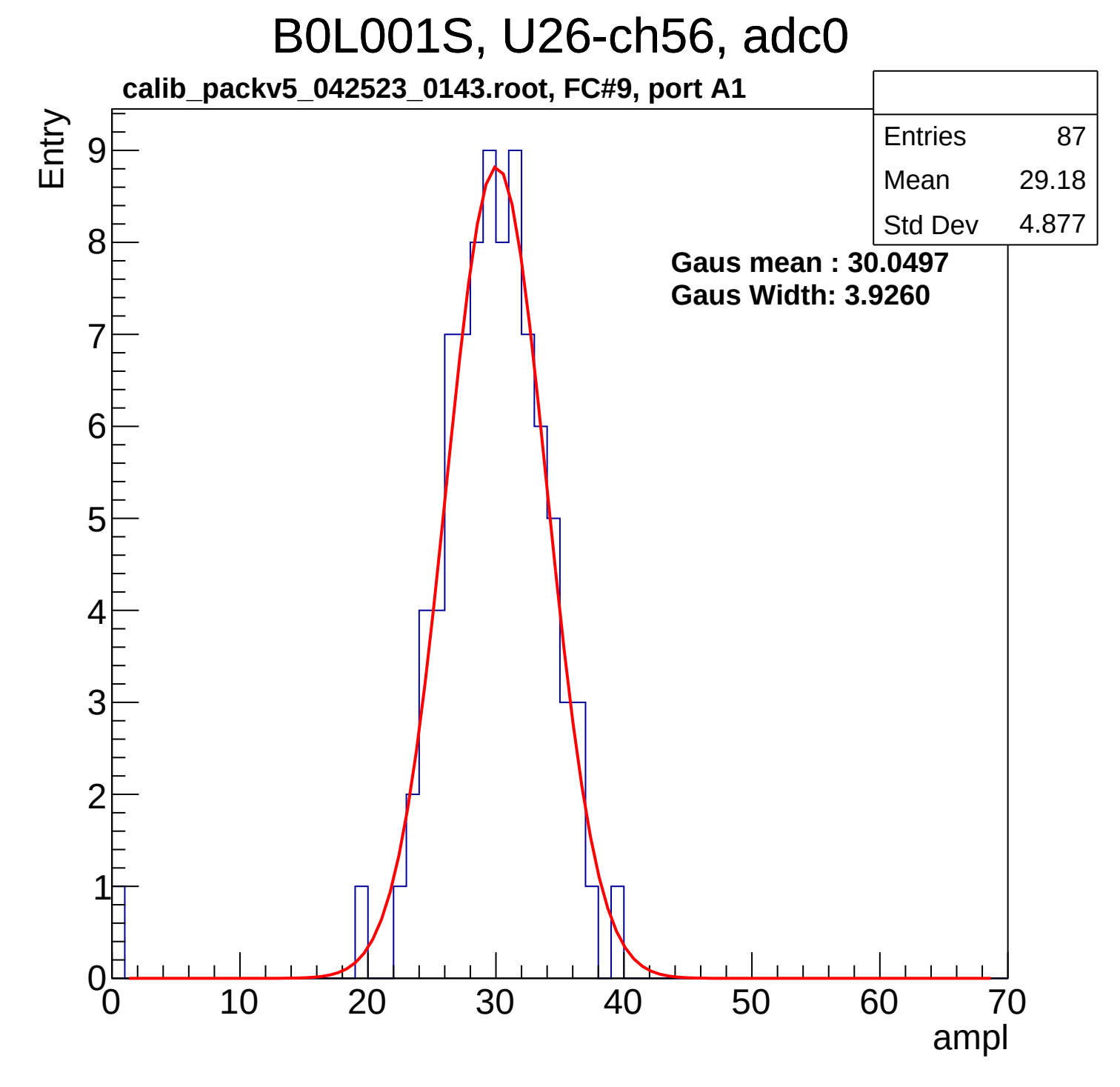
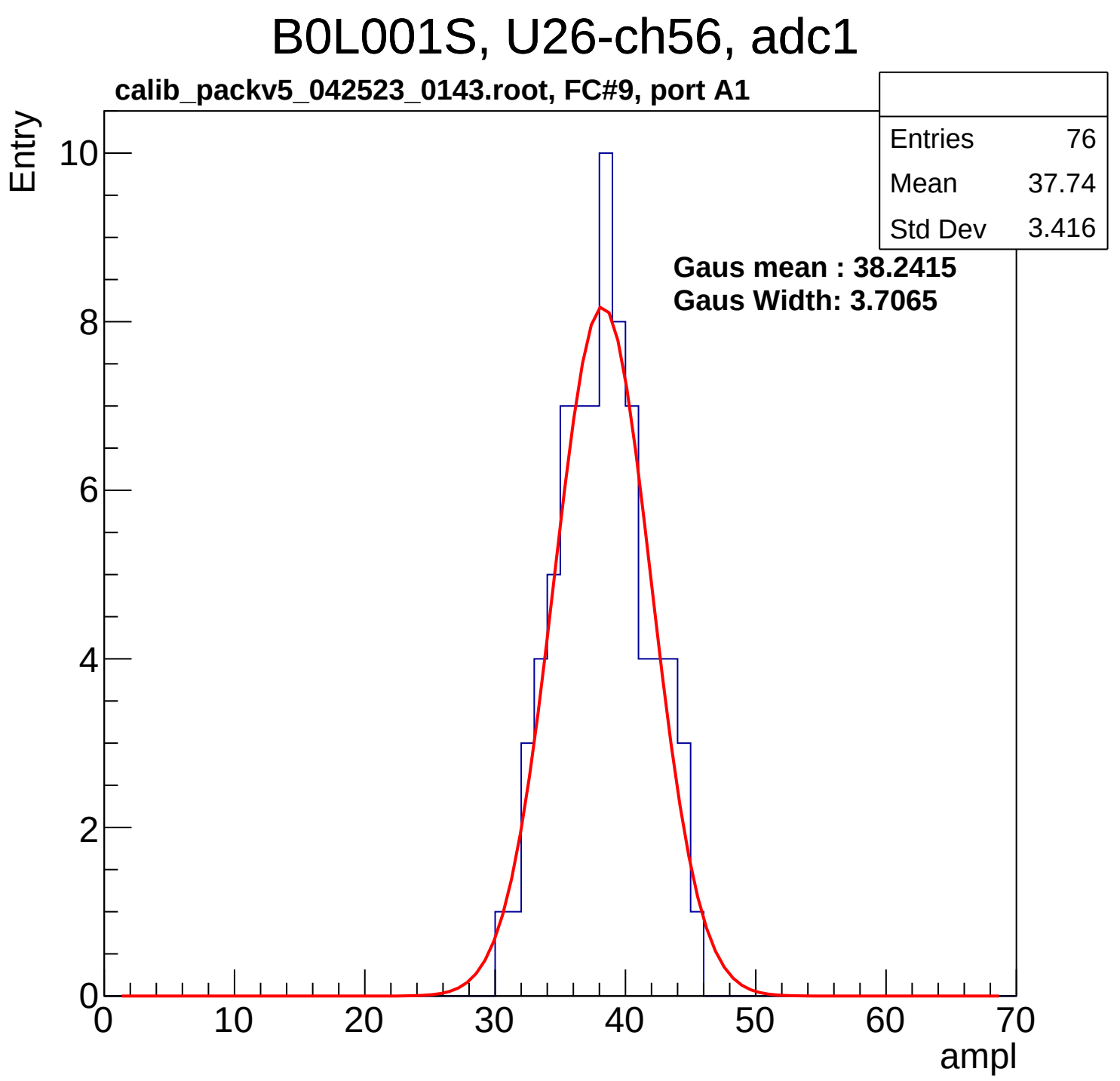
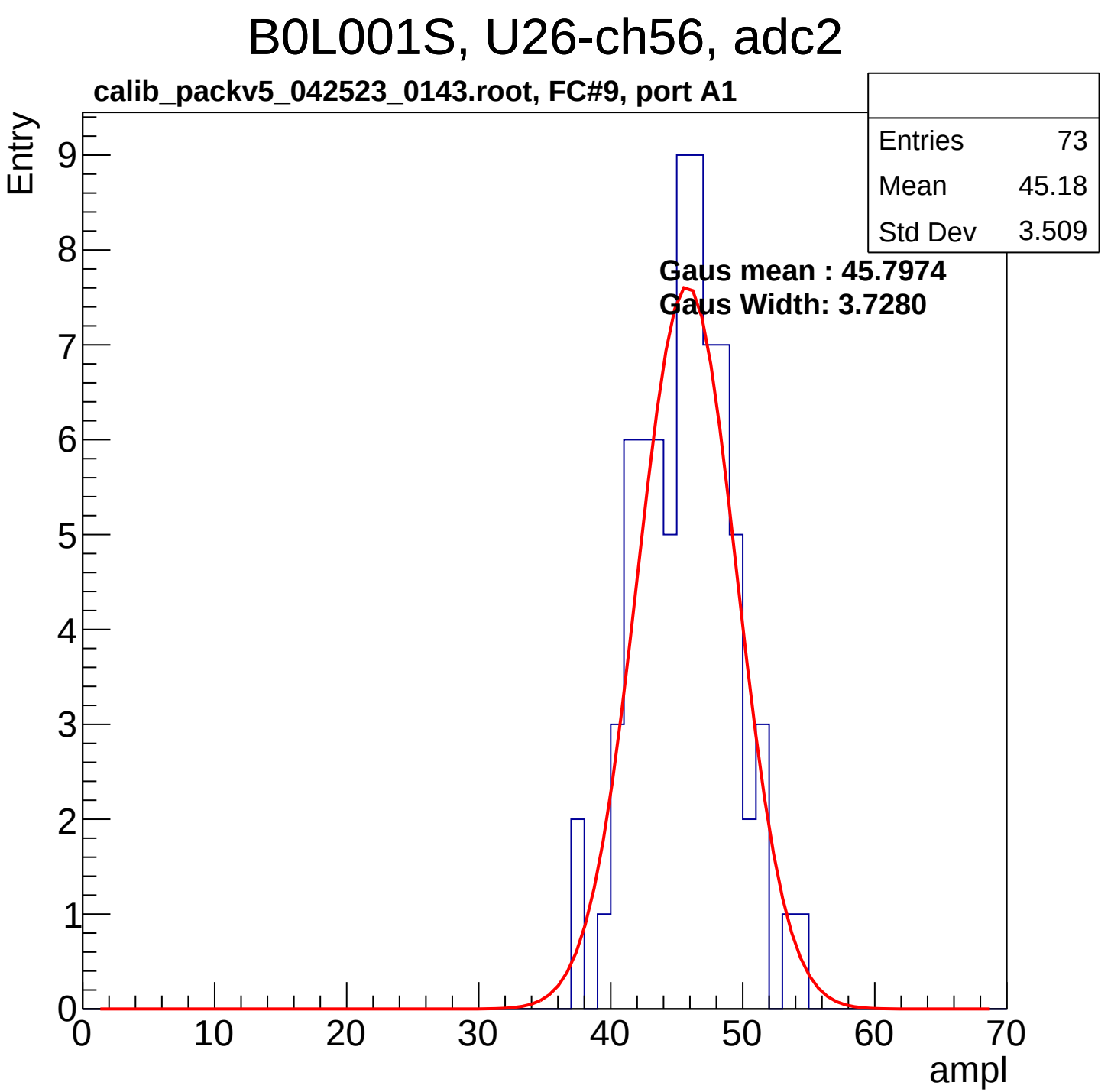
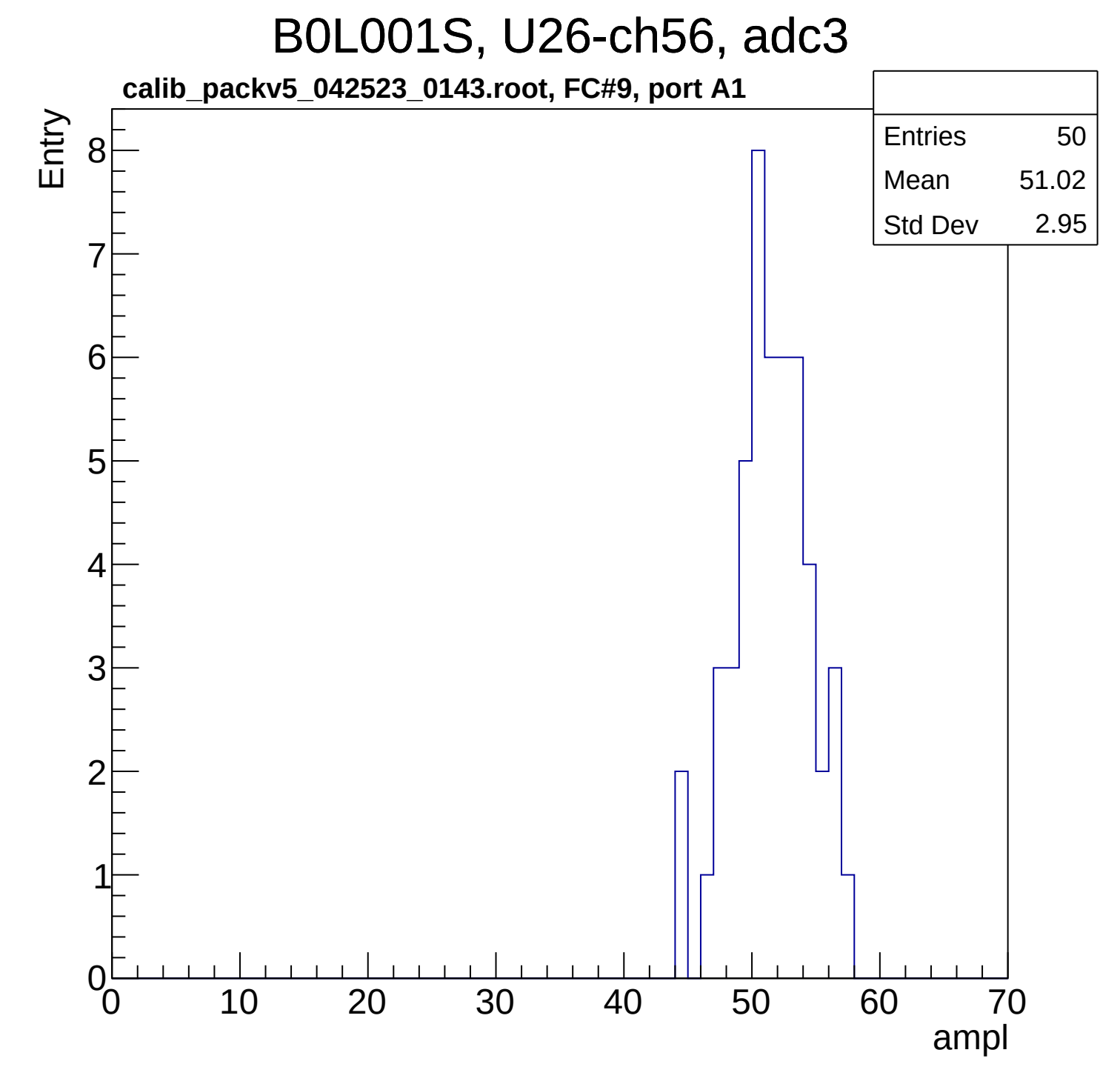

B0L001S, U26-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

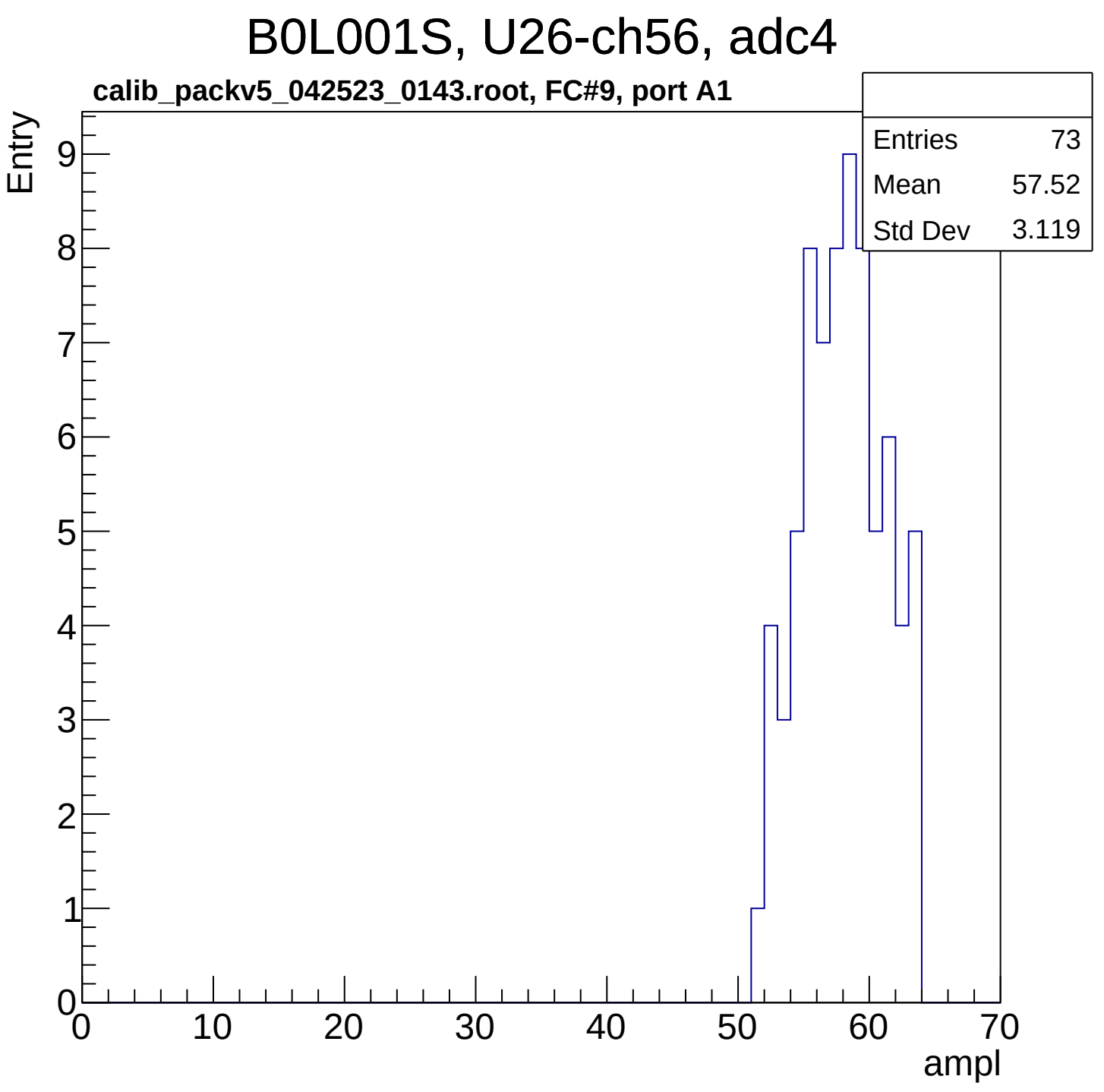

B0L001S, U26-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 29.18
Std Dev 4.877
Gaus mean : 30.0497
Gaus Width: 3.9260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.74
Std Dev 3.416
Gaus mean : 38.2415
Gaus Width: 3.7065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.18
Std Dev 3.509
Gaus mean : 45.7974
Gaus Width: 3.7280
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 51.02
Std Dev 2.95
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 57.52
Std Dev 3.119
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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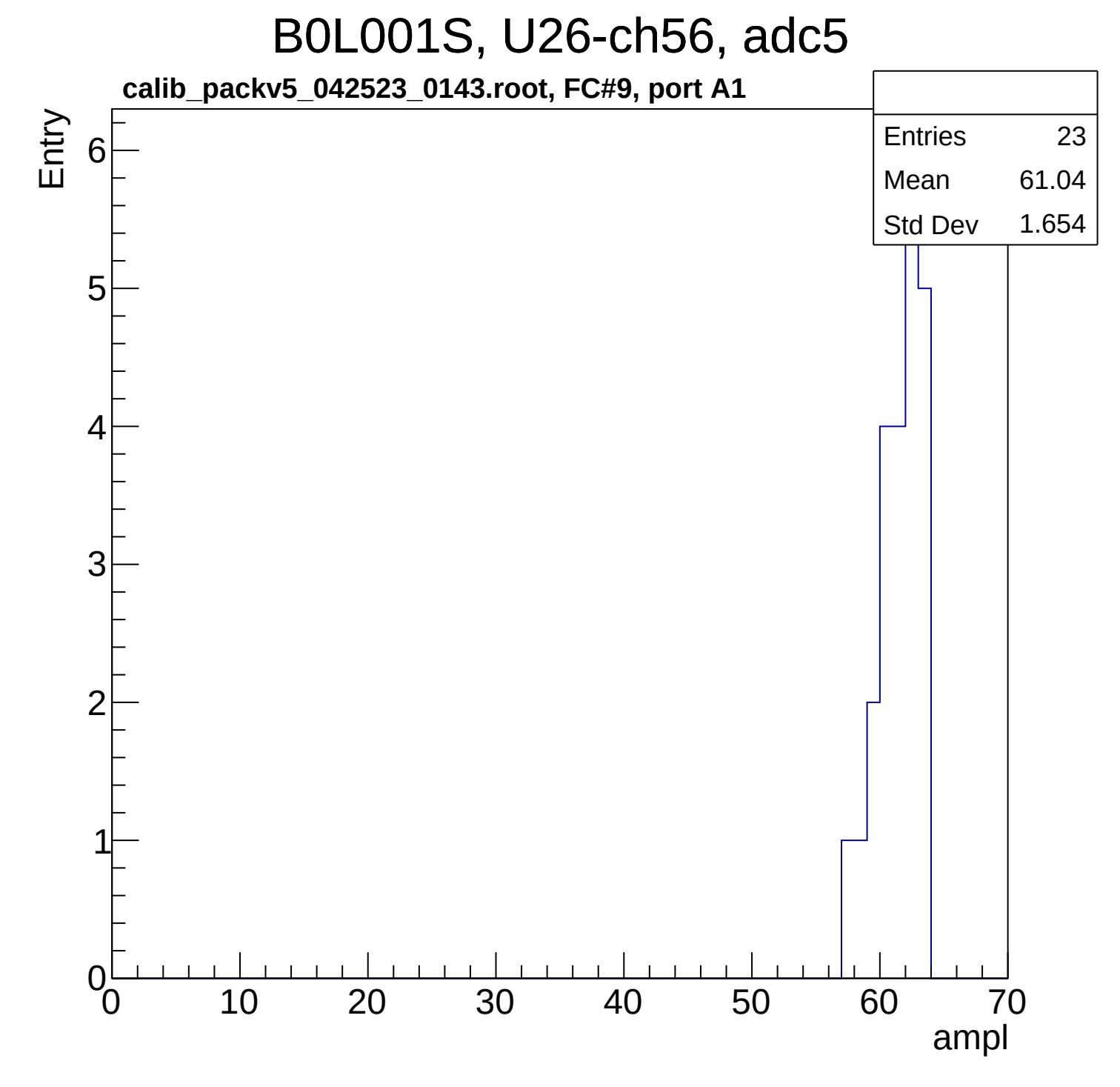
B0L001S, U26-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.04
Std Dev 1.654
Entry
ampl
0
1
2
3
4
5
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0
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20
30
40
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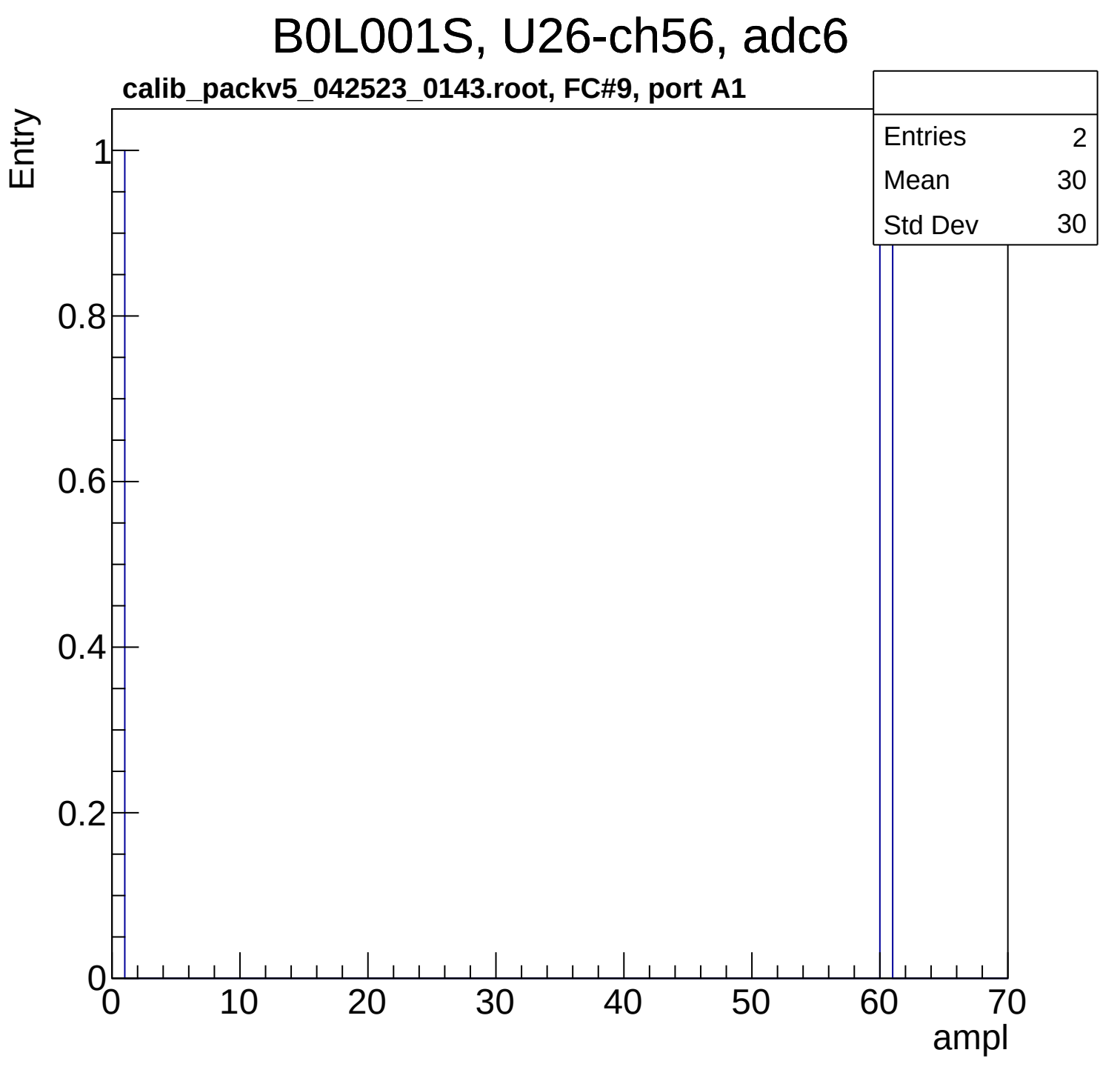
60
70


B0L001S, U26-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 30
Std Dev 30
Entry
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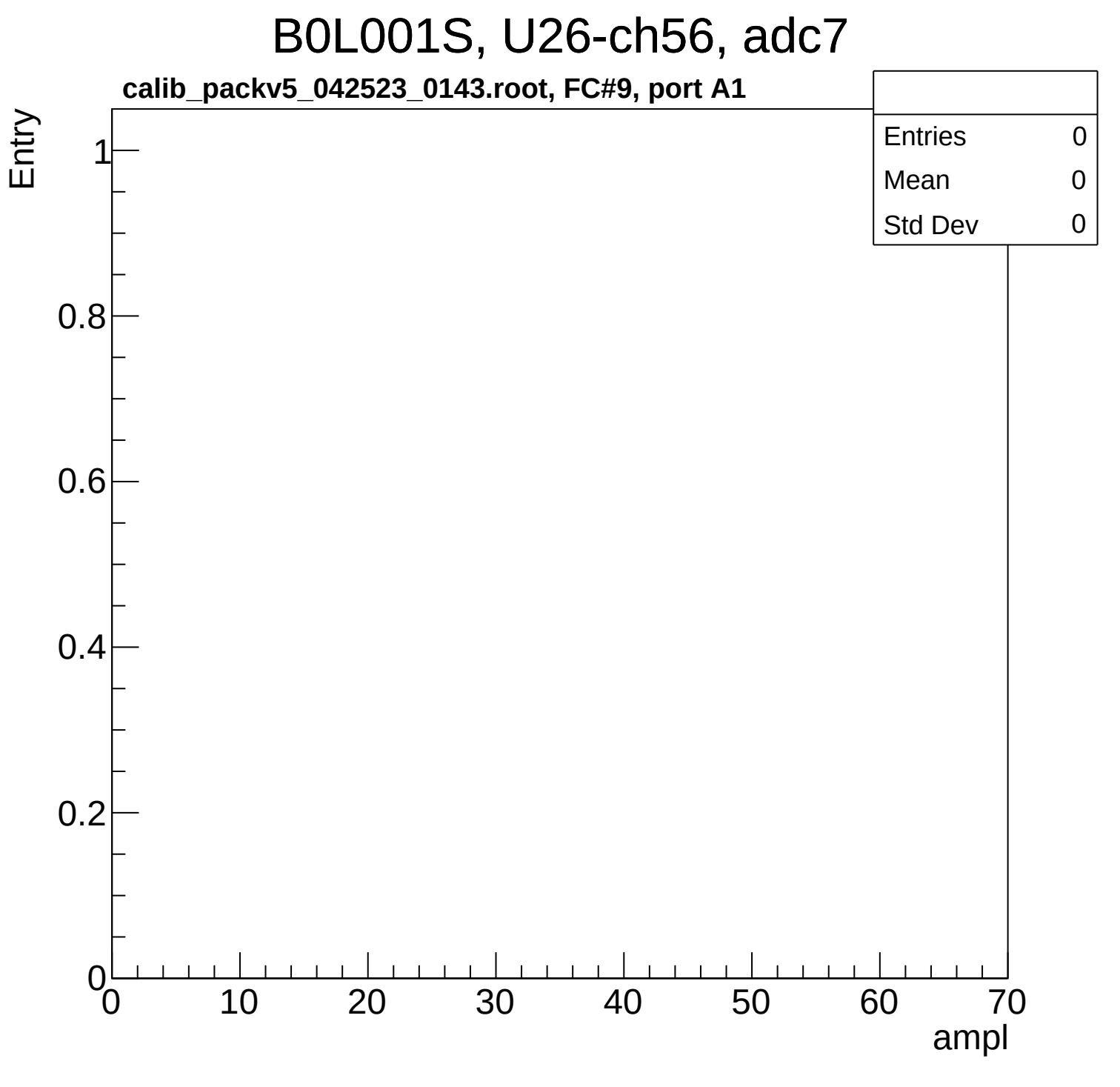
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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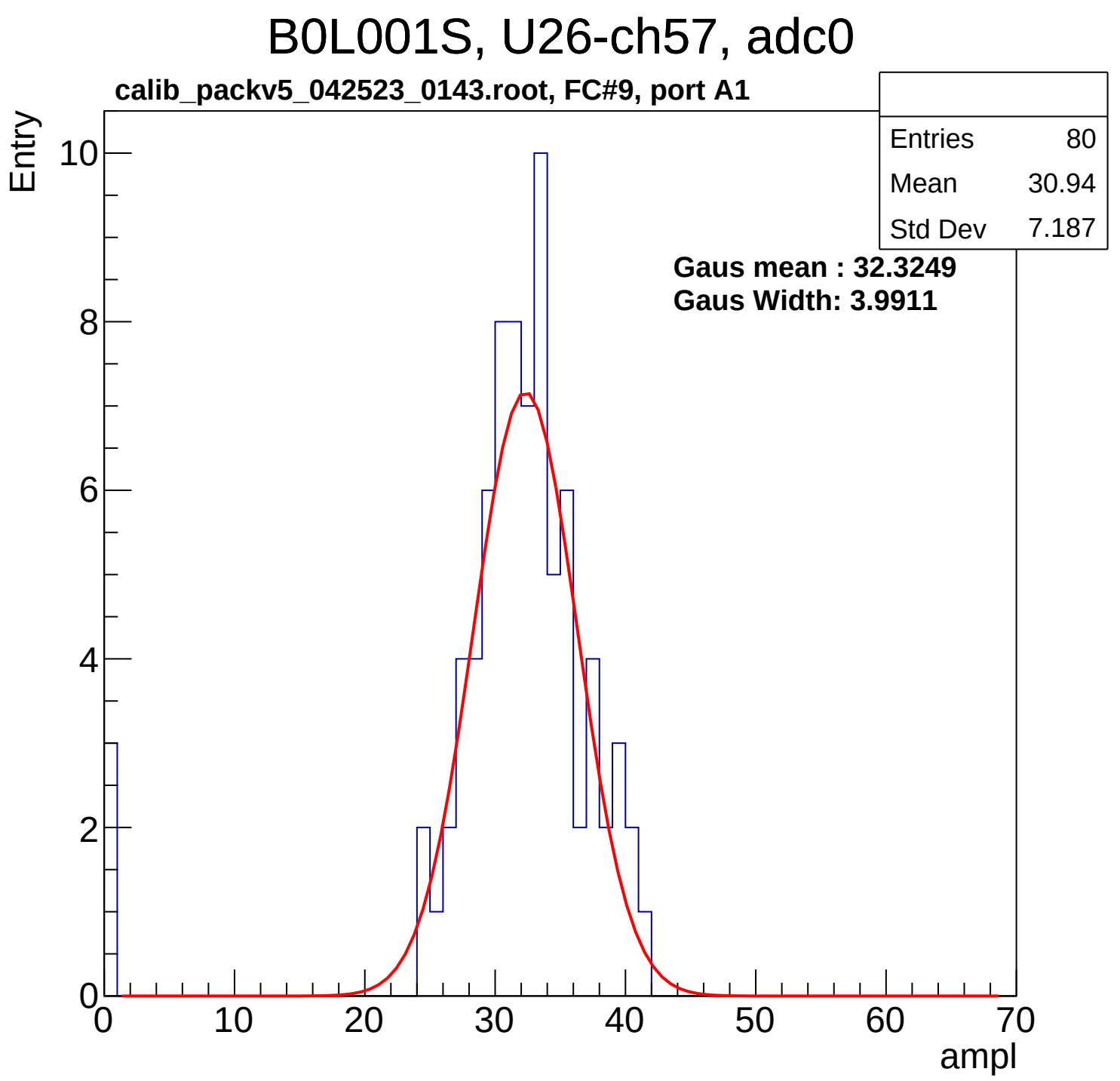
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.94
Std Dev 7.187
Gaus mean : 32.3249
Gaus Width: 3.9911
Entry
ampl
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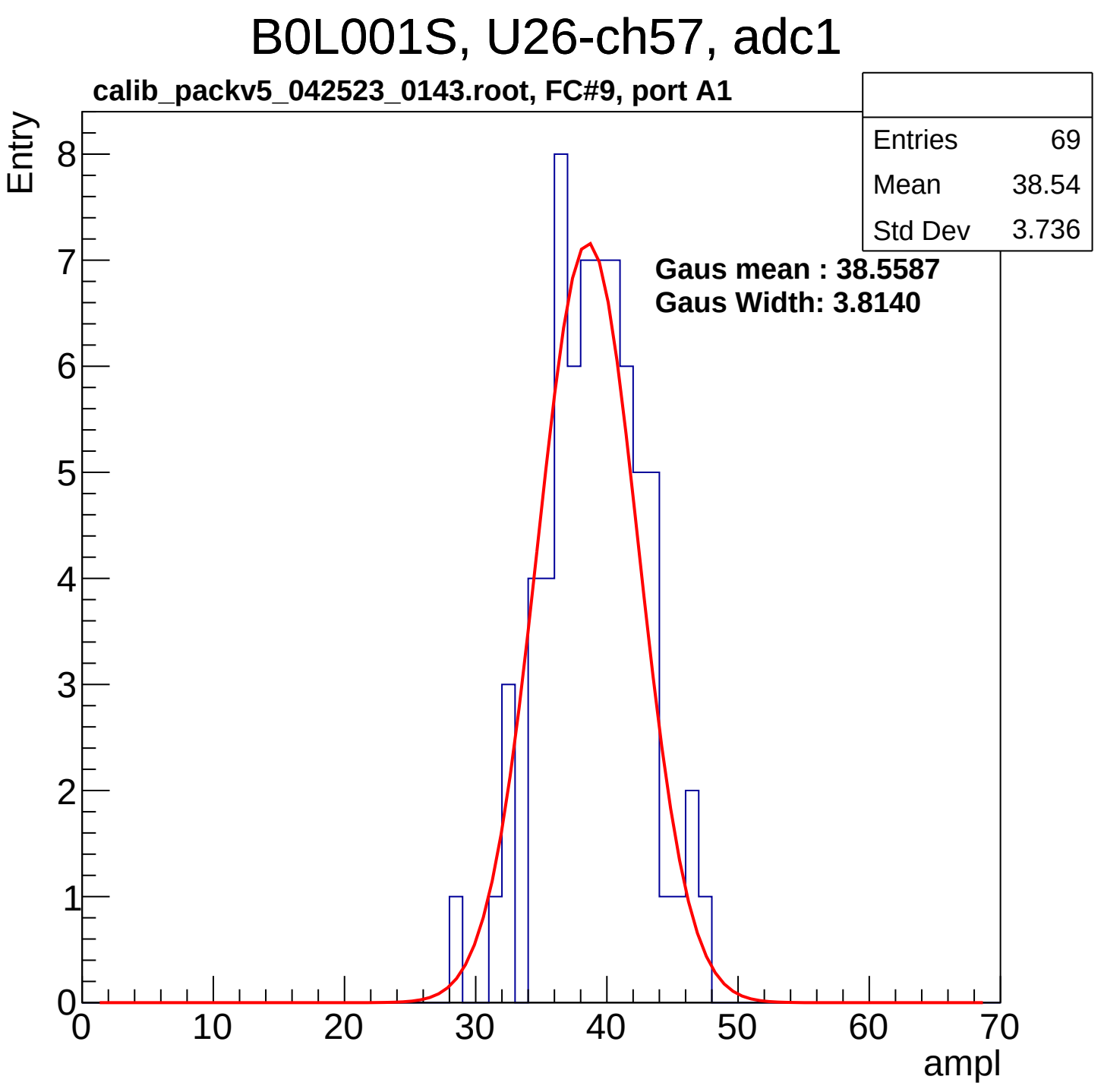
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.54
Std Dev 3.736
Gaus mean : 38.5587
Gaus Width: 3.8140
Entry
ampl
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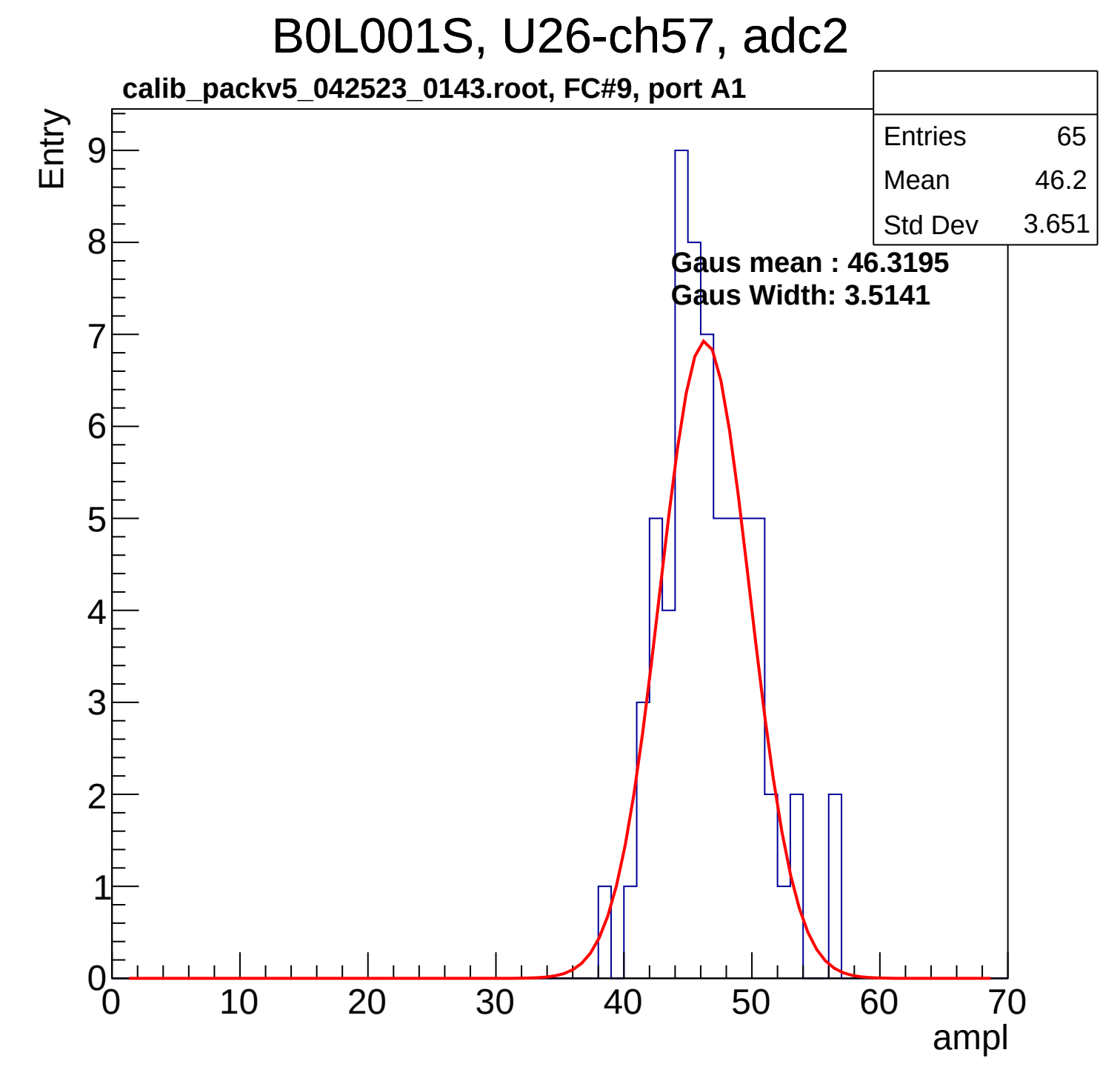
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 46.2
Std Dev 3.651
Gaus mean : 46.3195
Gaus Width: 3.5141
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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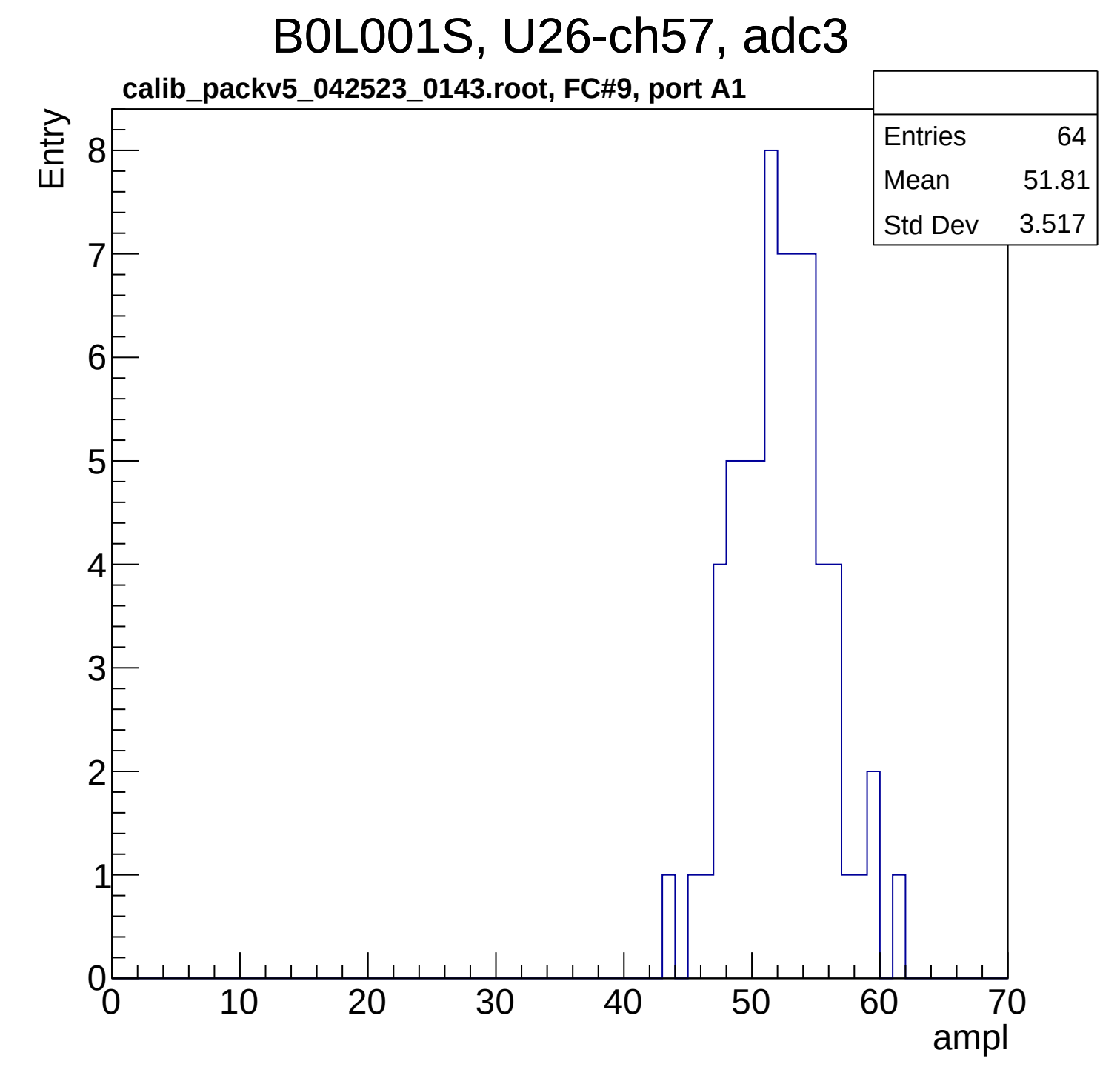
B0L001S, U26-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.81
Std Dev 3.517
Entry
ampl
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5
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7
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0
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20
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40
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60
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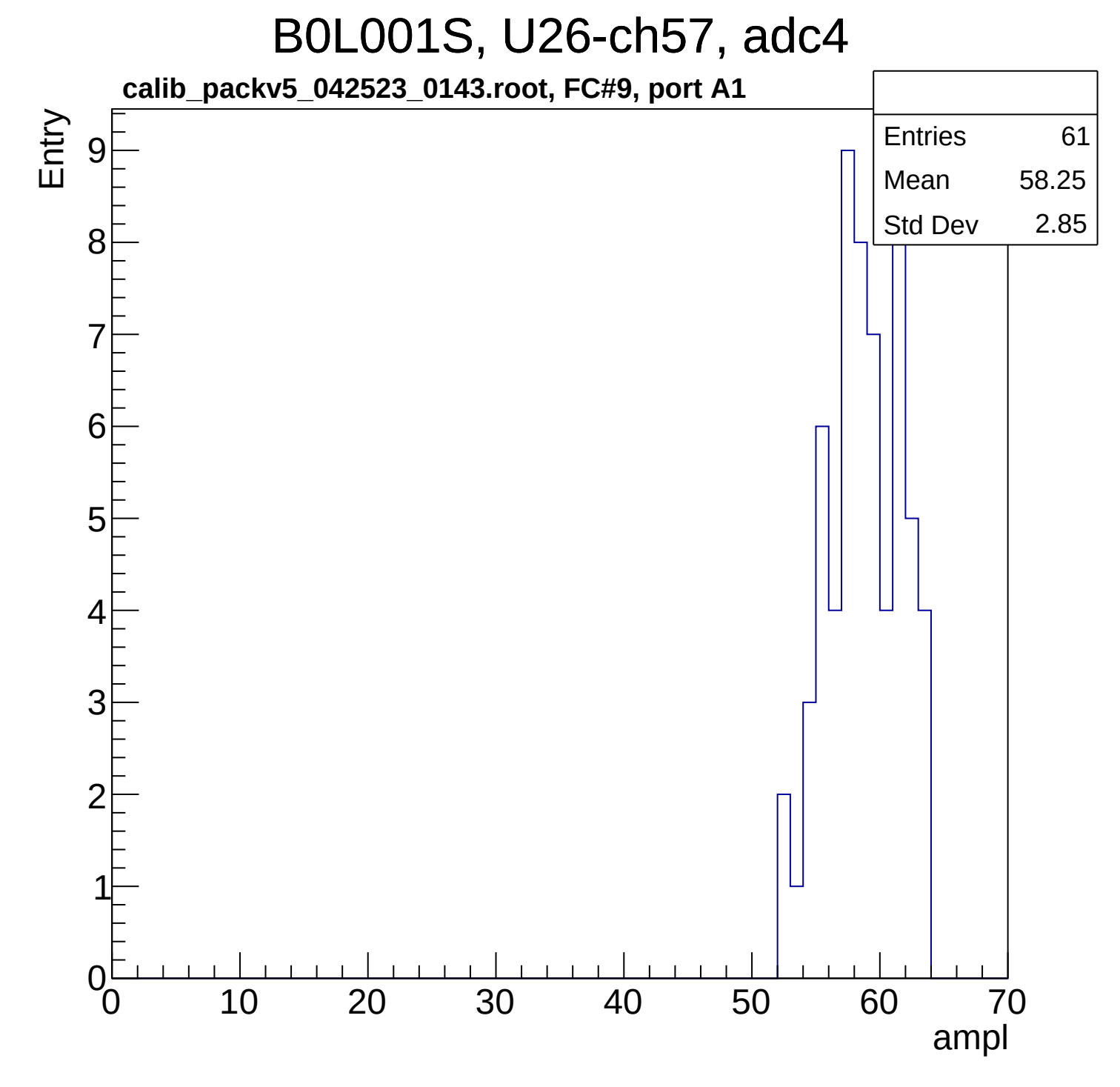
B0L001S, U26-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 58.25
Std Dev 2.85
Entry
ampl
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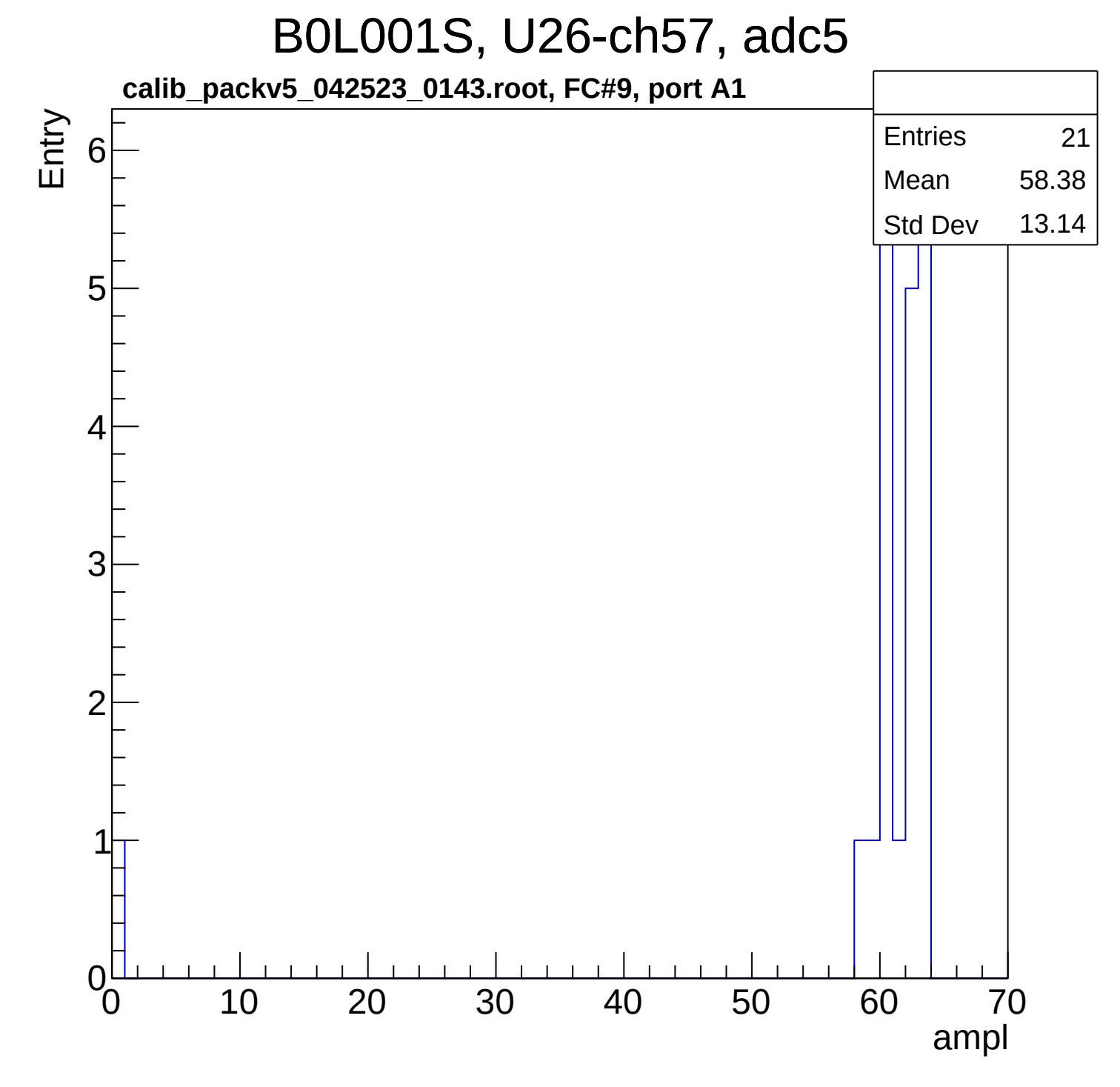
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.38
Std Dev 13.14
Entry
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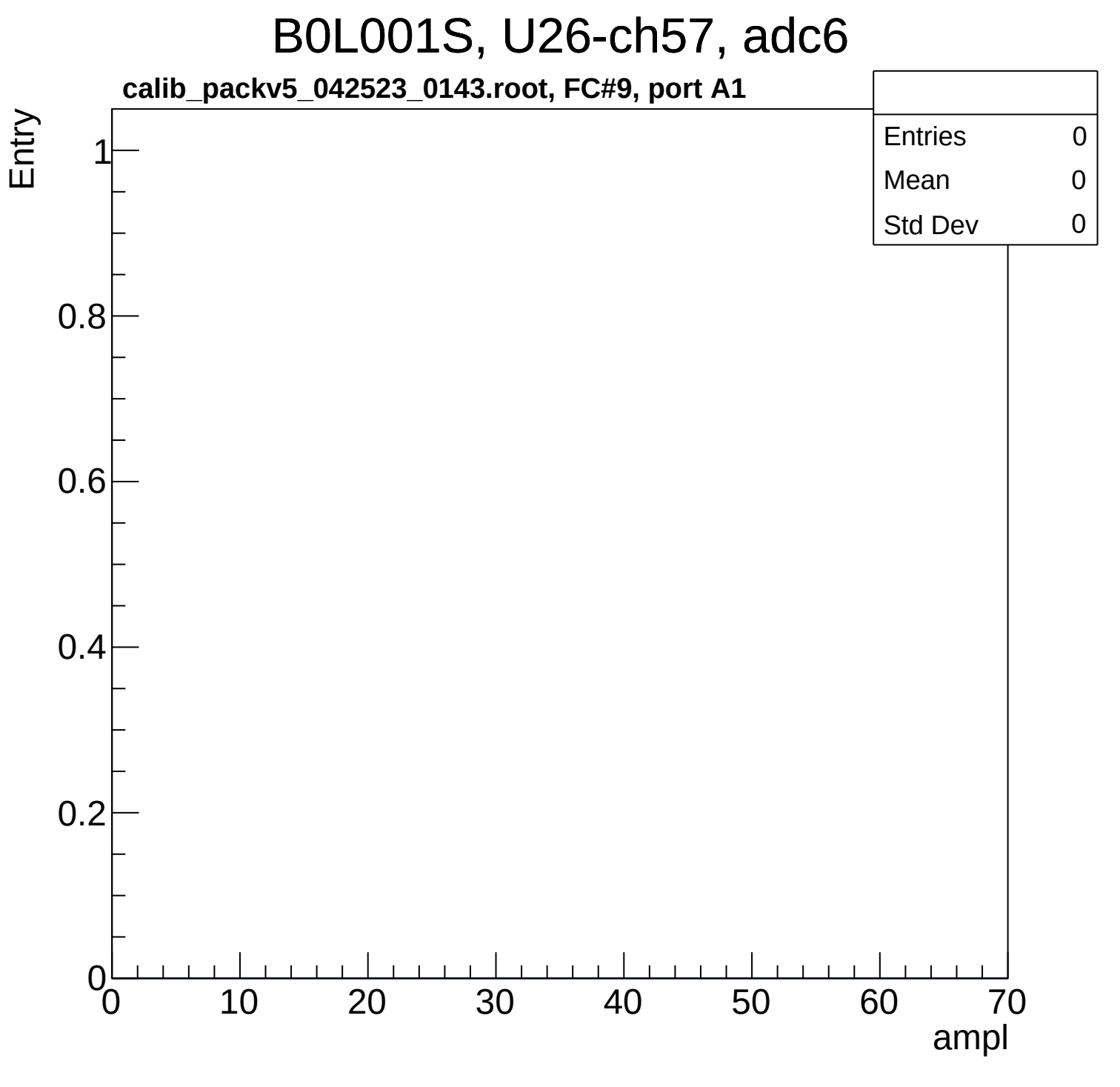
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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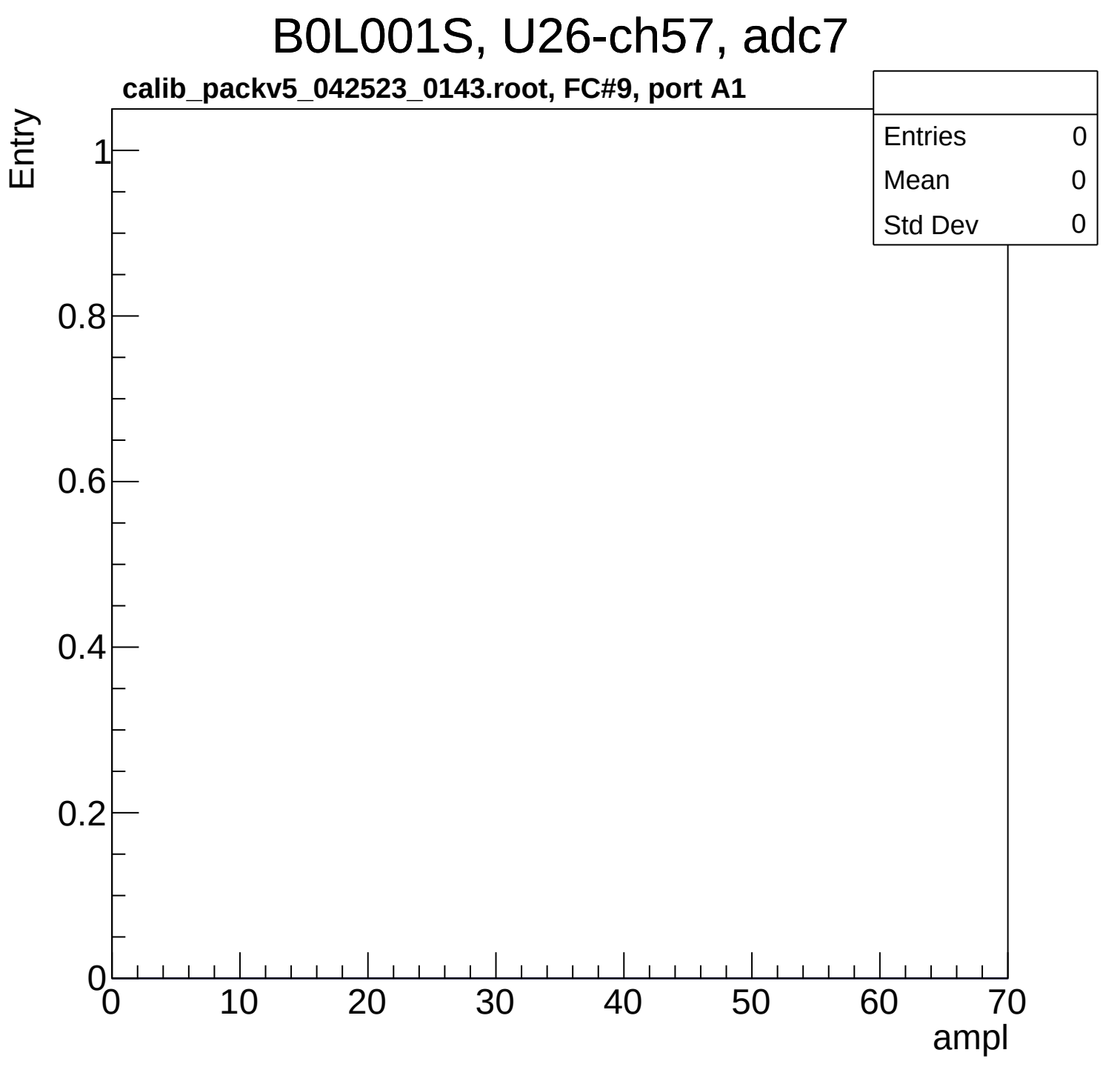
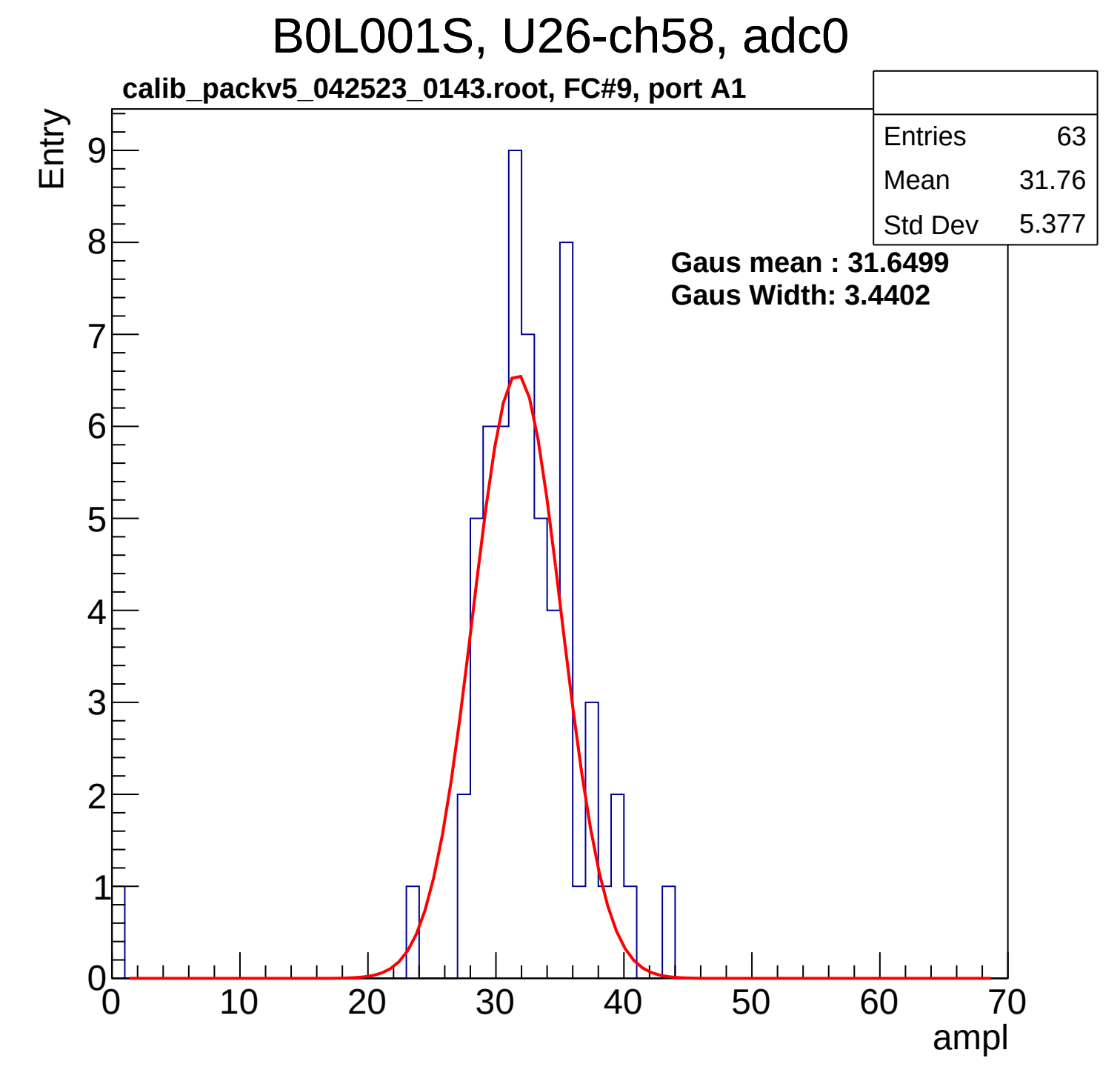
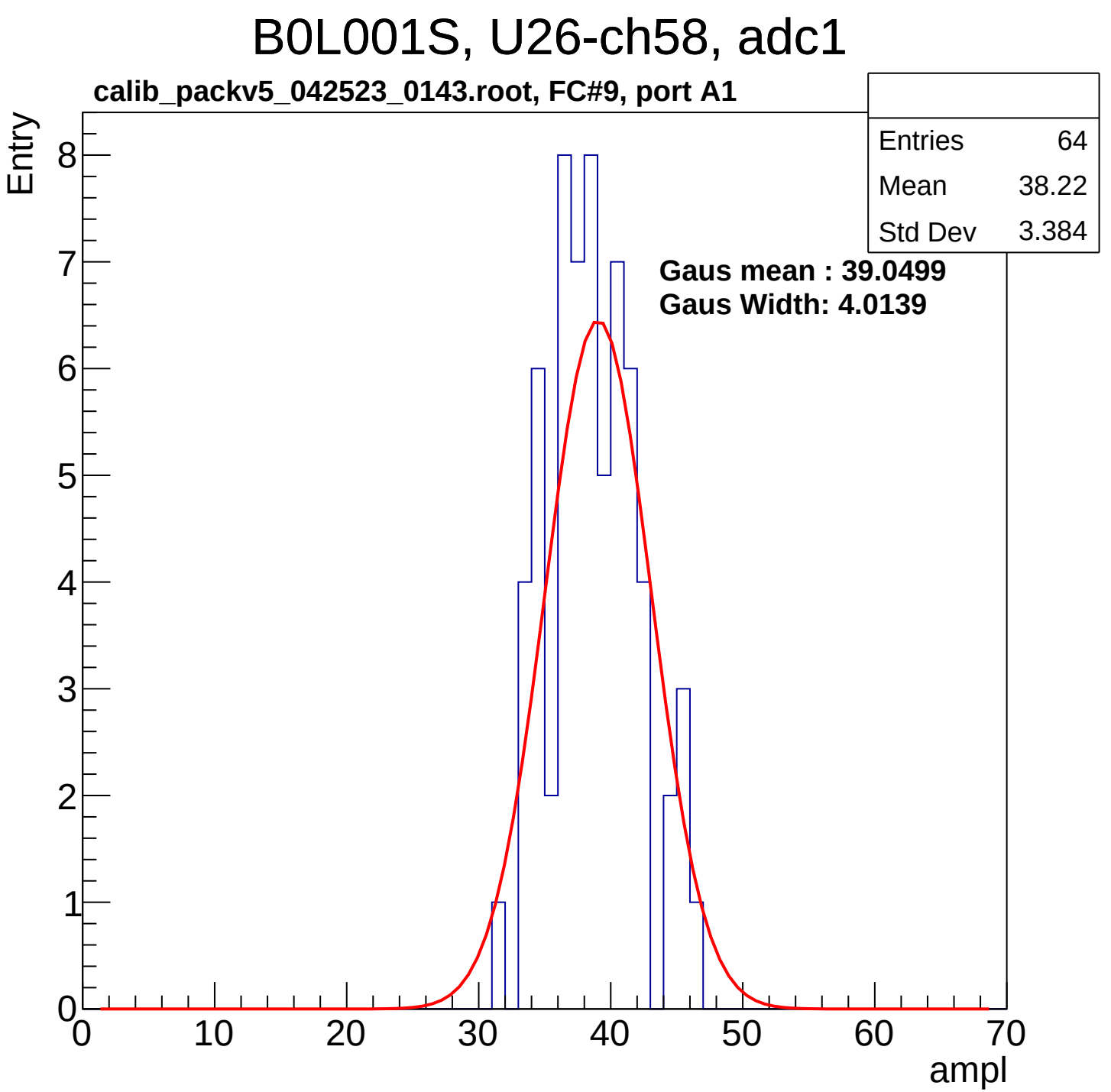
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.76
Std Dev 5.377
Gaus mean : 31.6499
Gaus Width: 3.4402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.22
Std Dev 3.384
Gaus mean : 39.0499
Gaus Width: 4.0139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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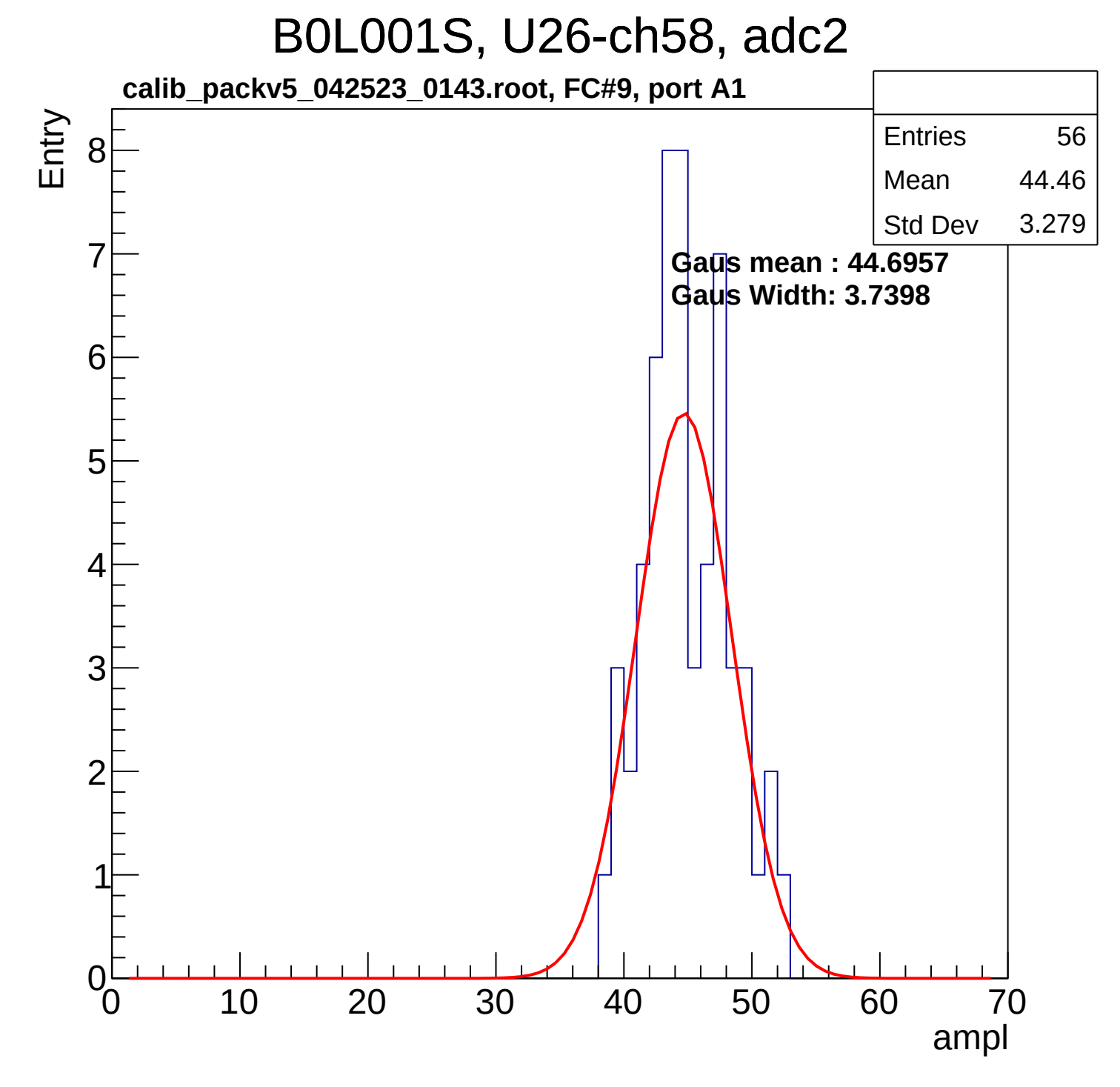
B0L001S, U26-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.46
Std Dev 3.279
Gaus mean : 44.6957
Gaus Width: 3.7398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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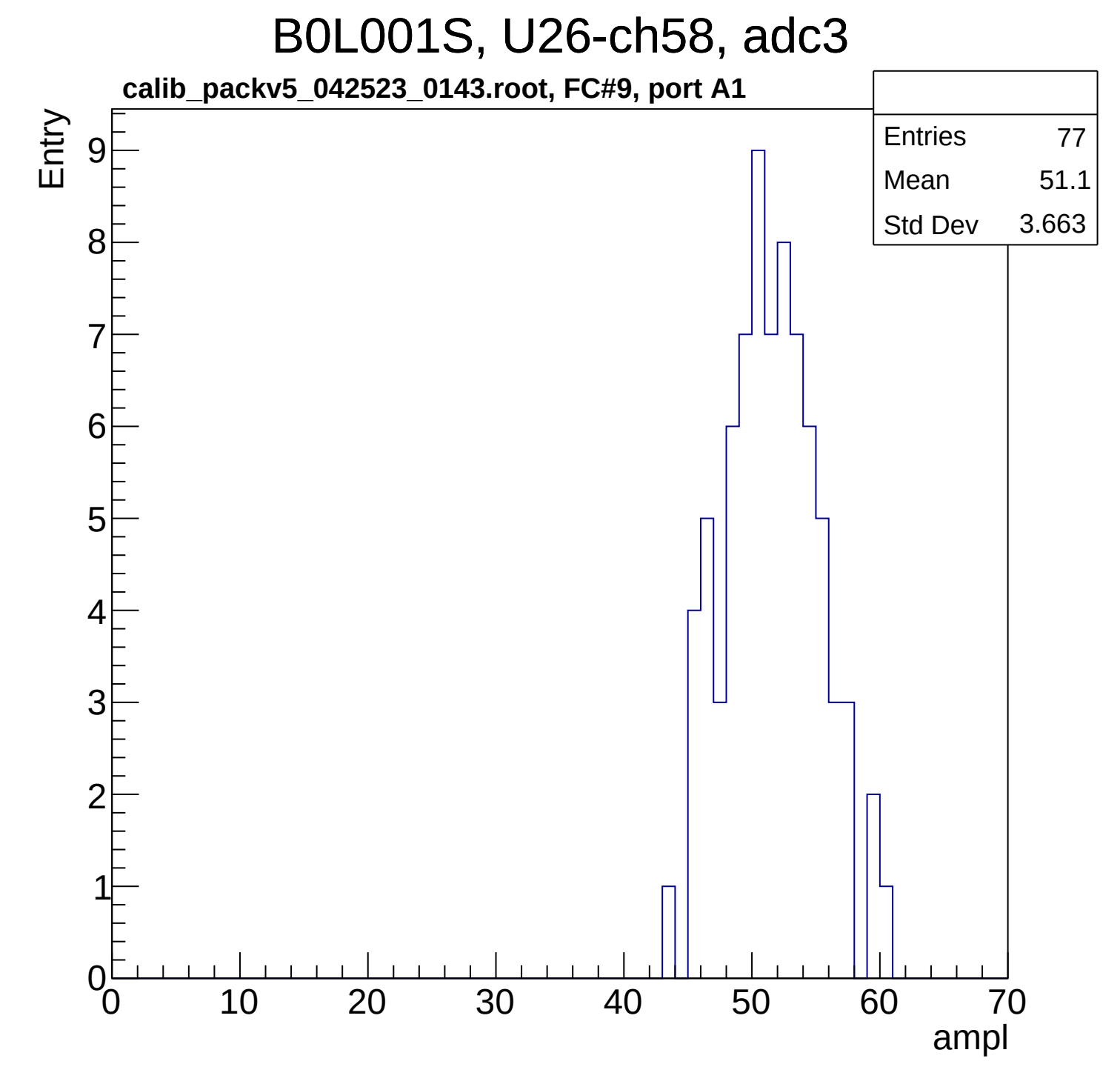
B0L001S, U26-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.1
Std Dev 3.663
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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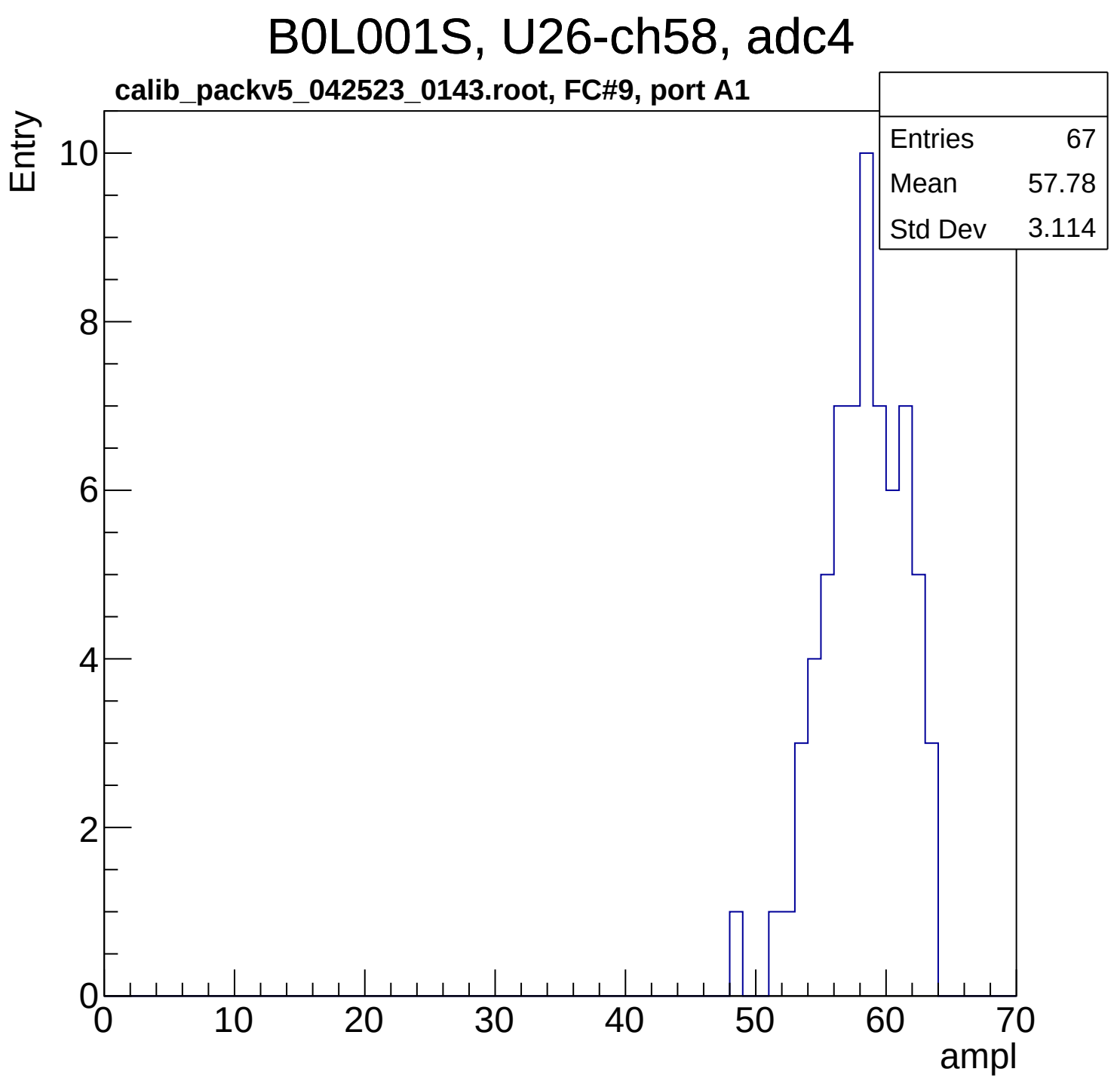
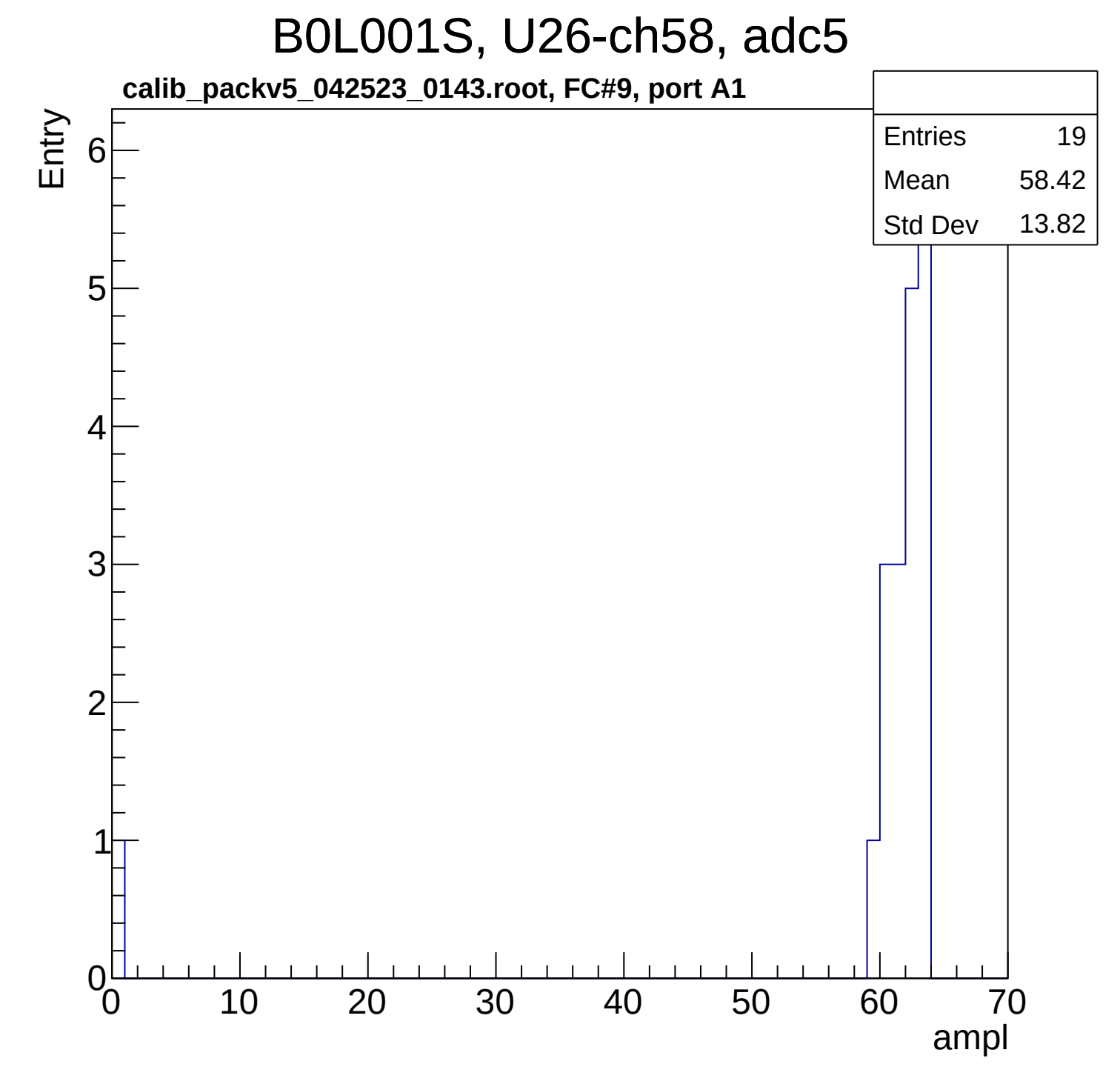
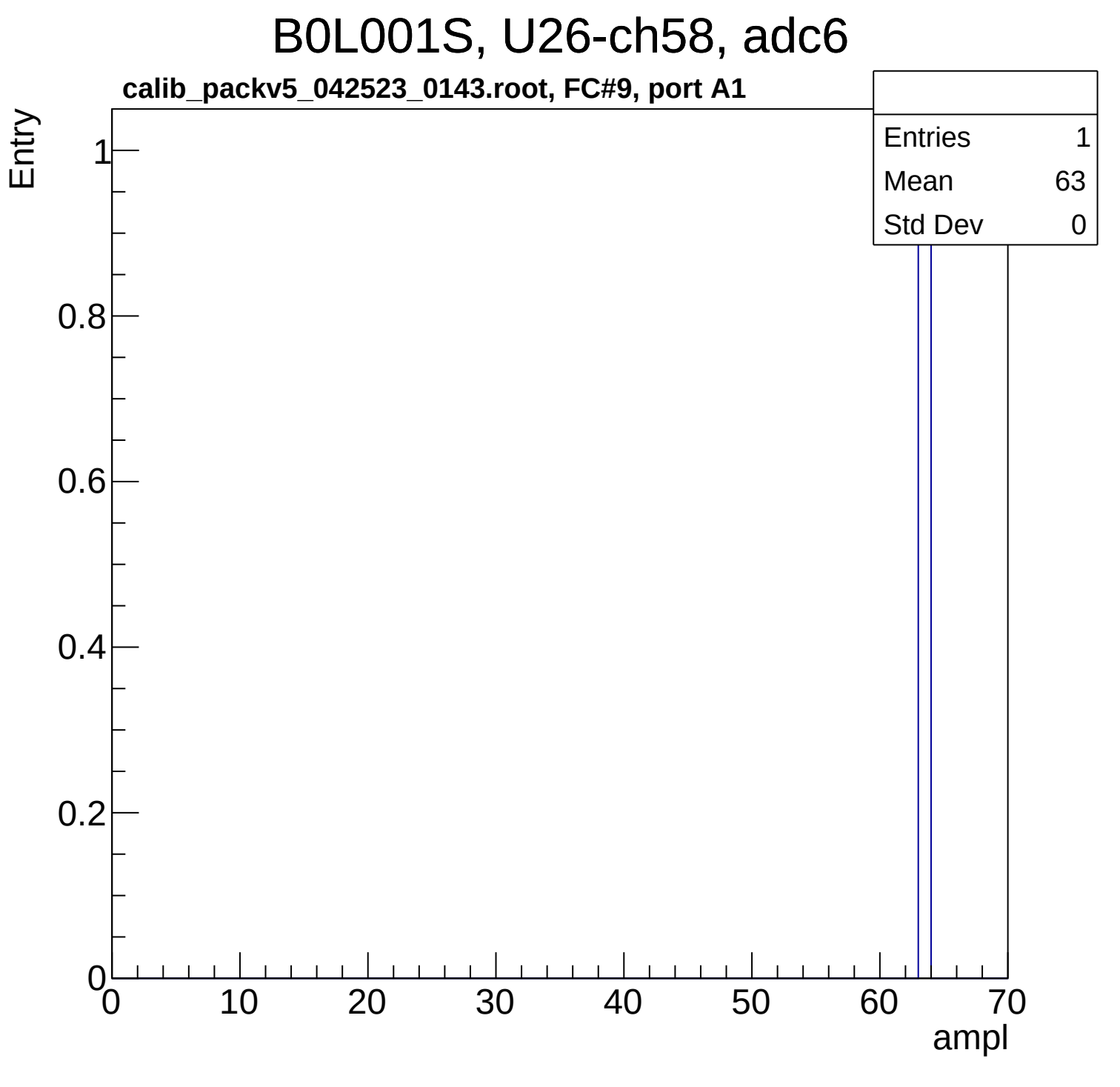
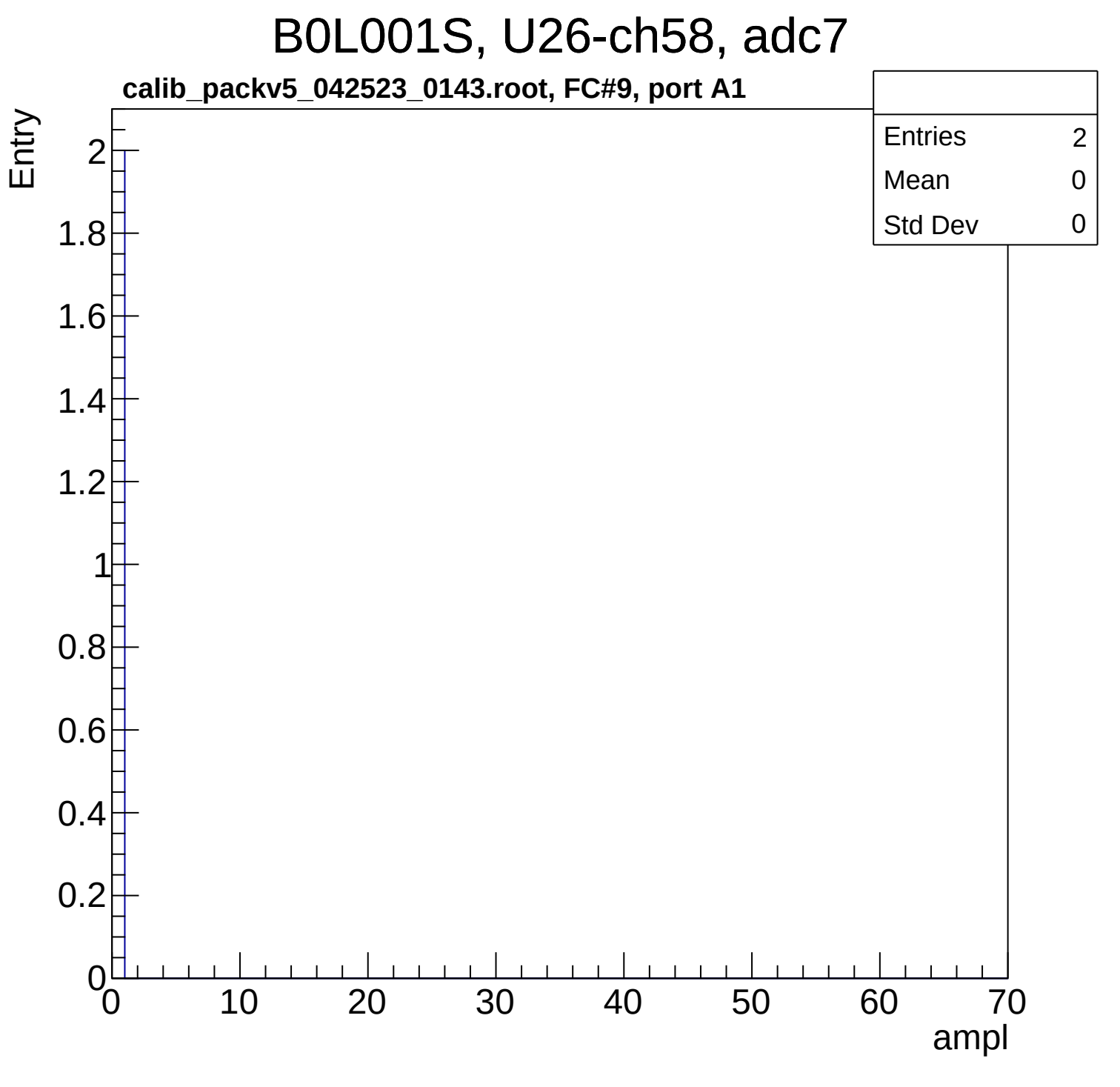
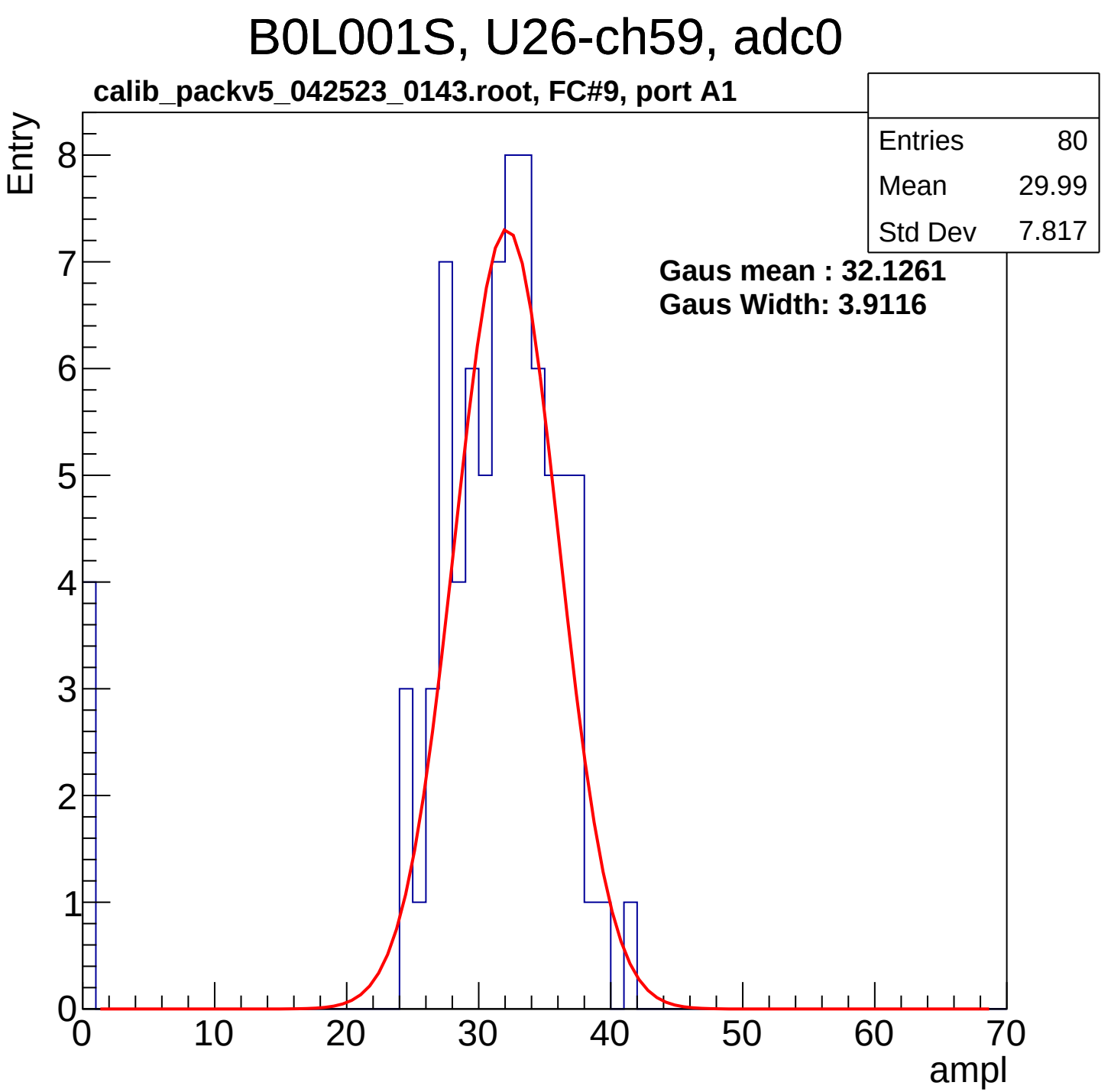
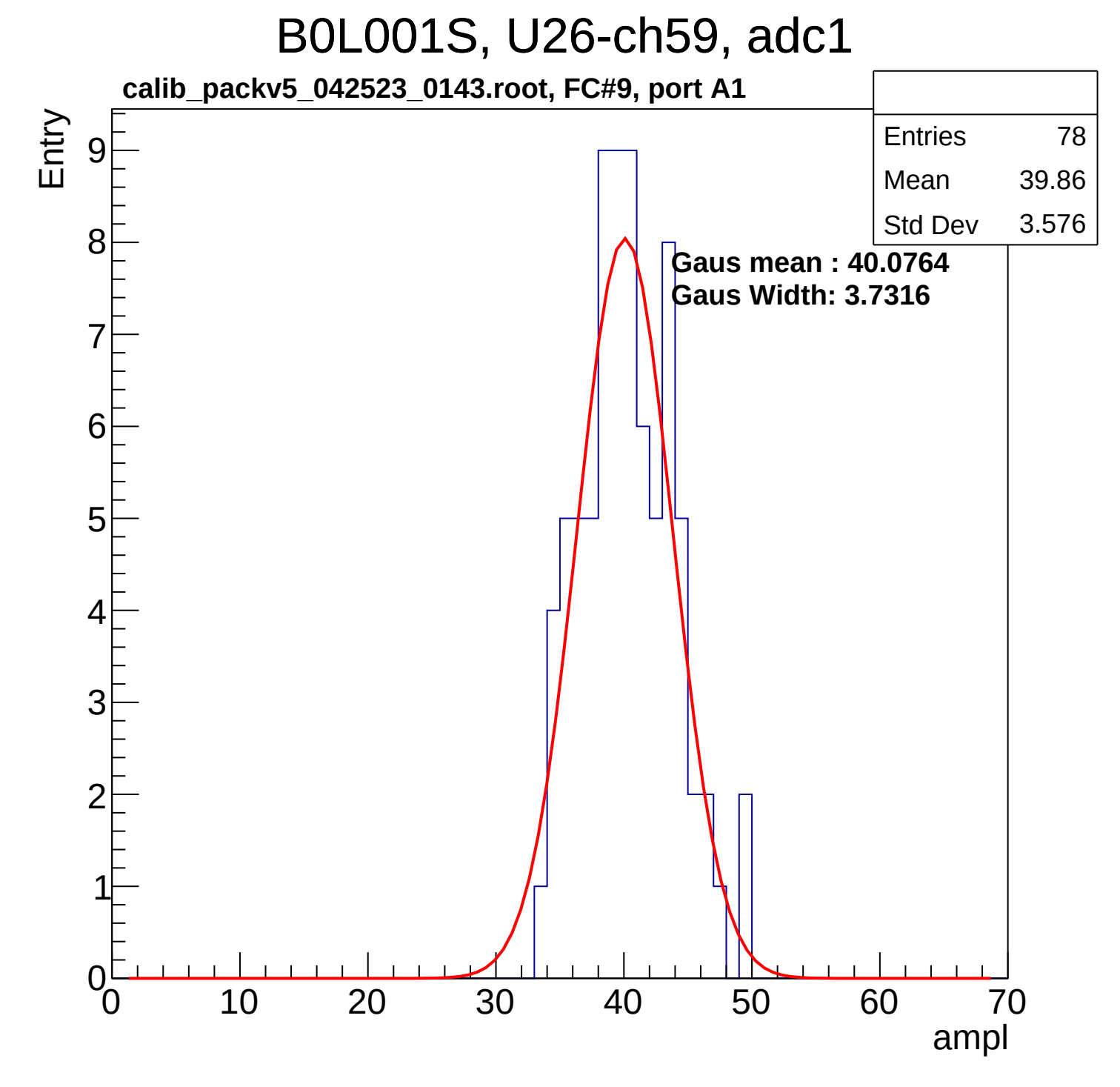

B0L001S, U26-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57.78
Std Dev 3.114
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

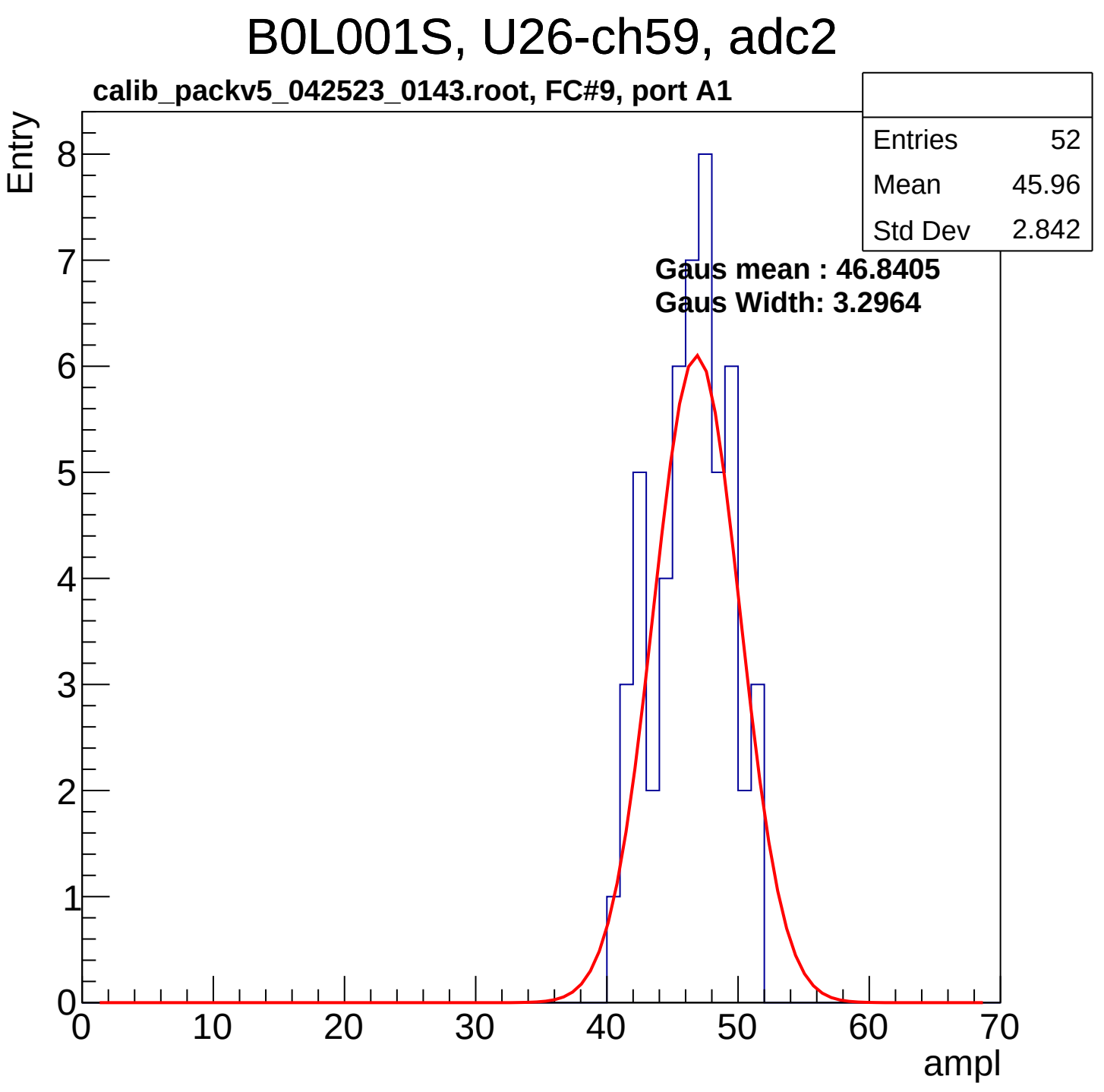

B0L001S, U26-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 58.42
Std Dev 13.82
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

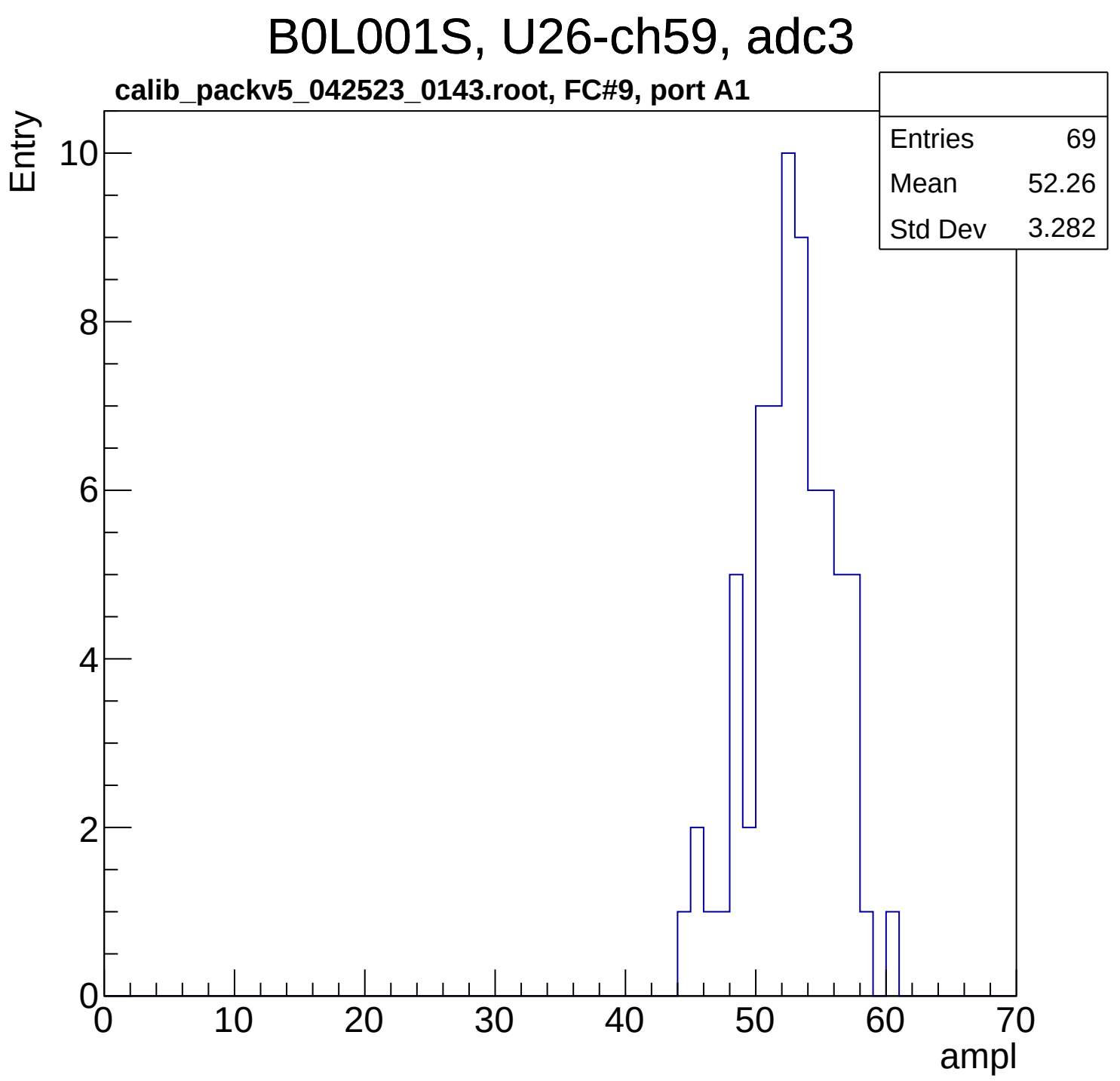

B0L001S, U26-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

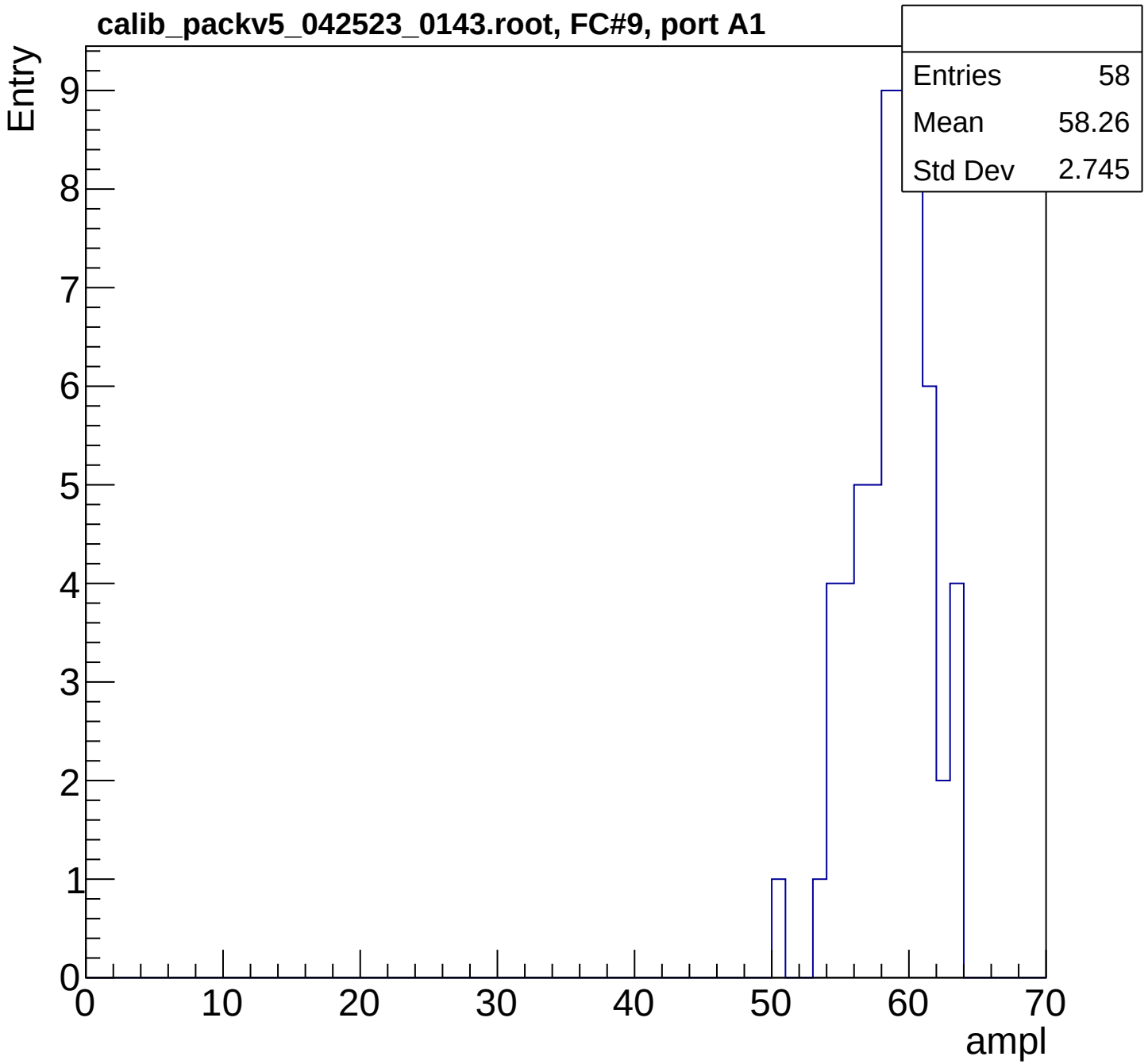

B0L001S, U26-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

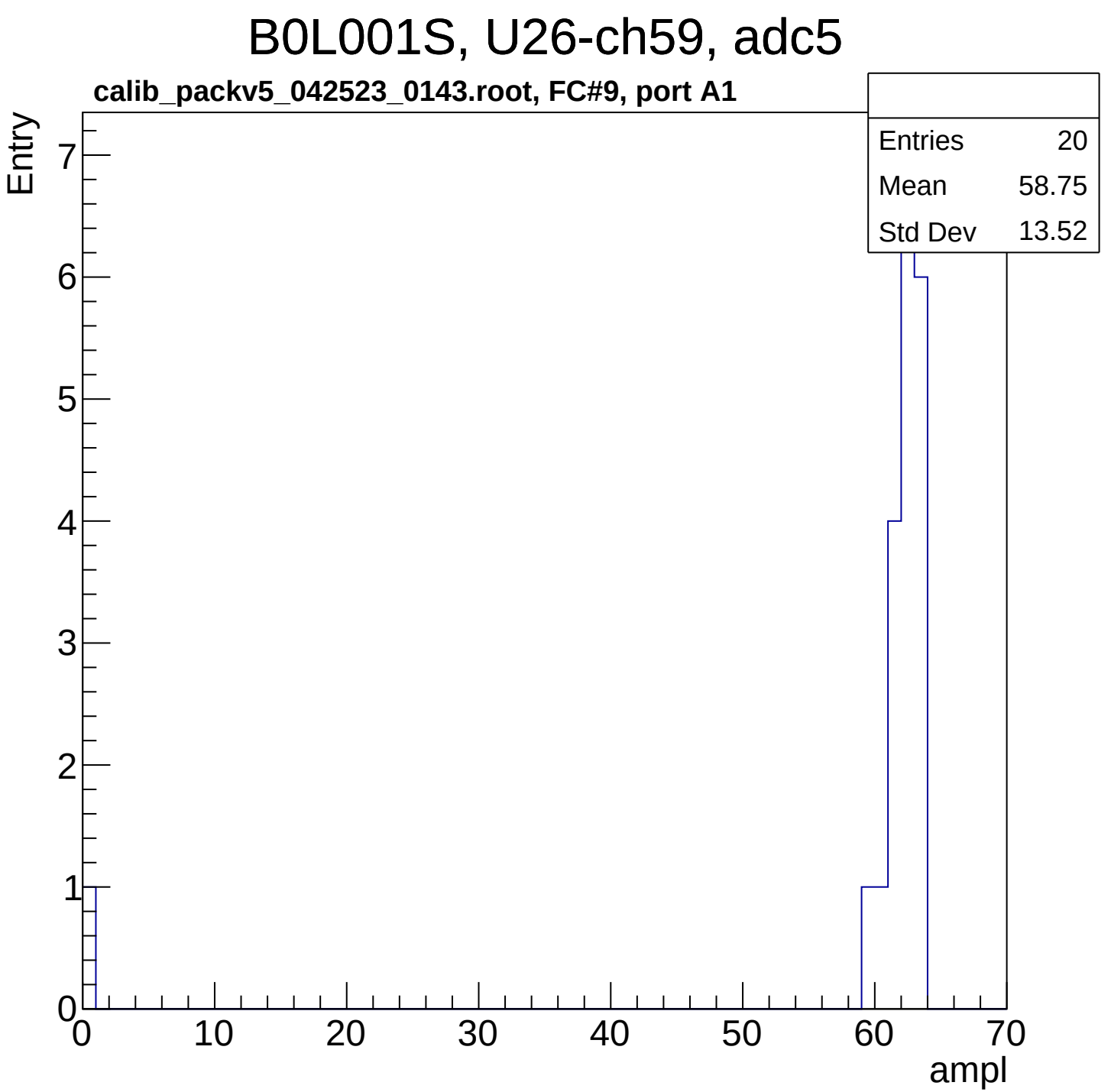

B0L001S, U26-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.99
Std Dev 7.817
Gaus mean : 32.1261
Gaus Width: 3.9116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

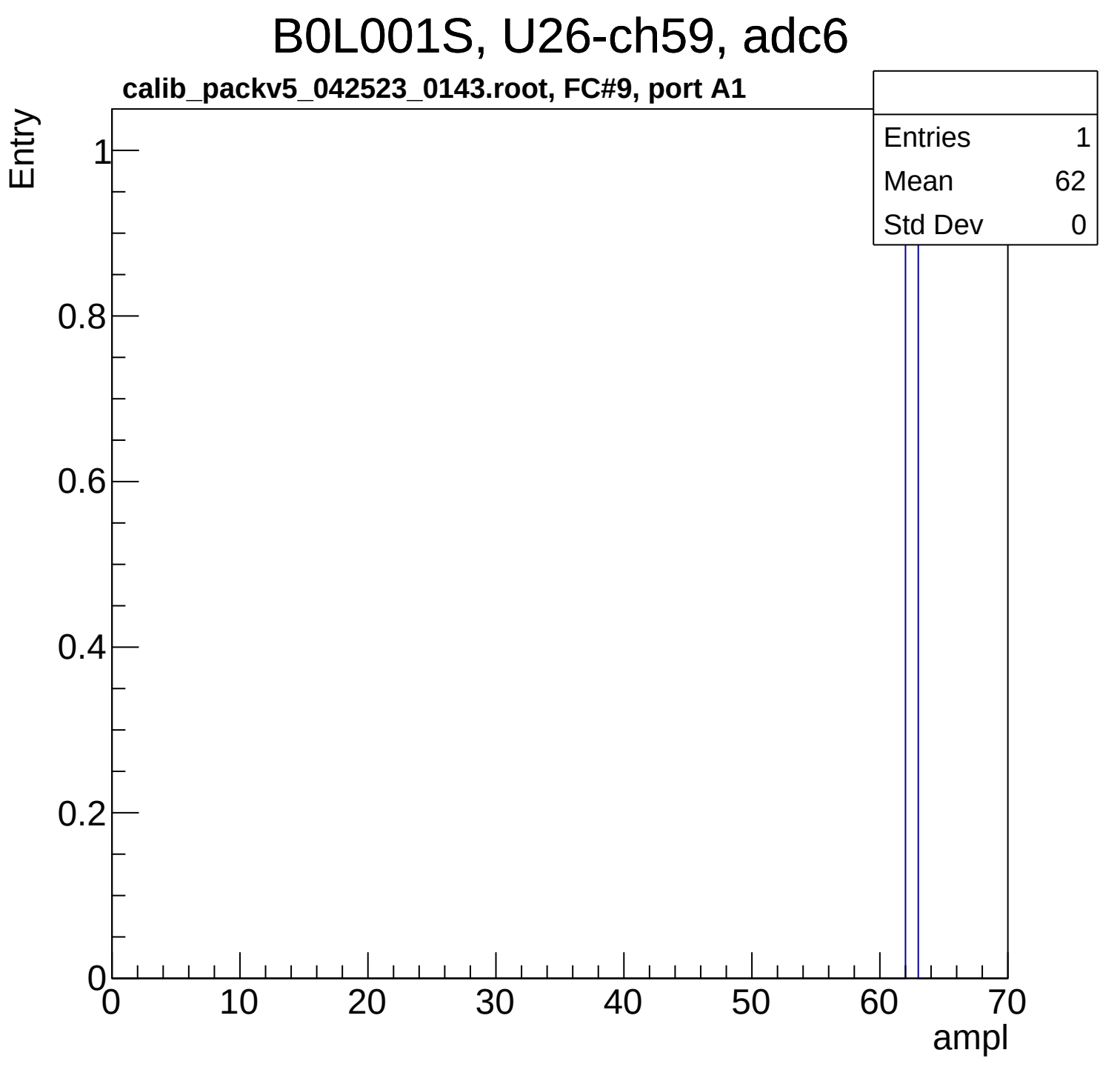

B0L001S, U26-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 39.86
Std Dev 3.576
Gaus mean : 40.0764
Gaus Width: 3.7316
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

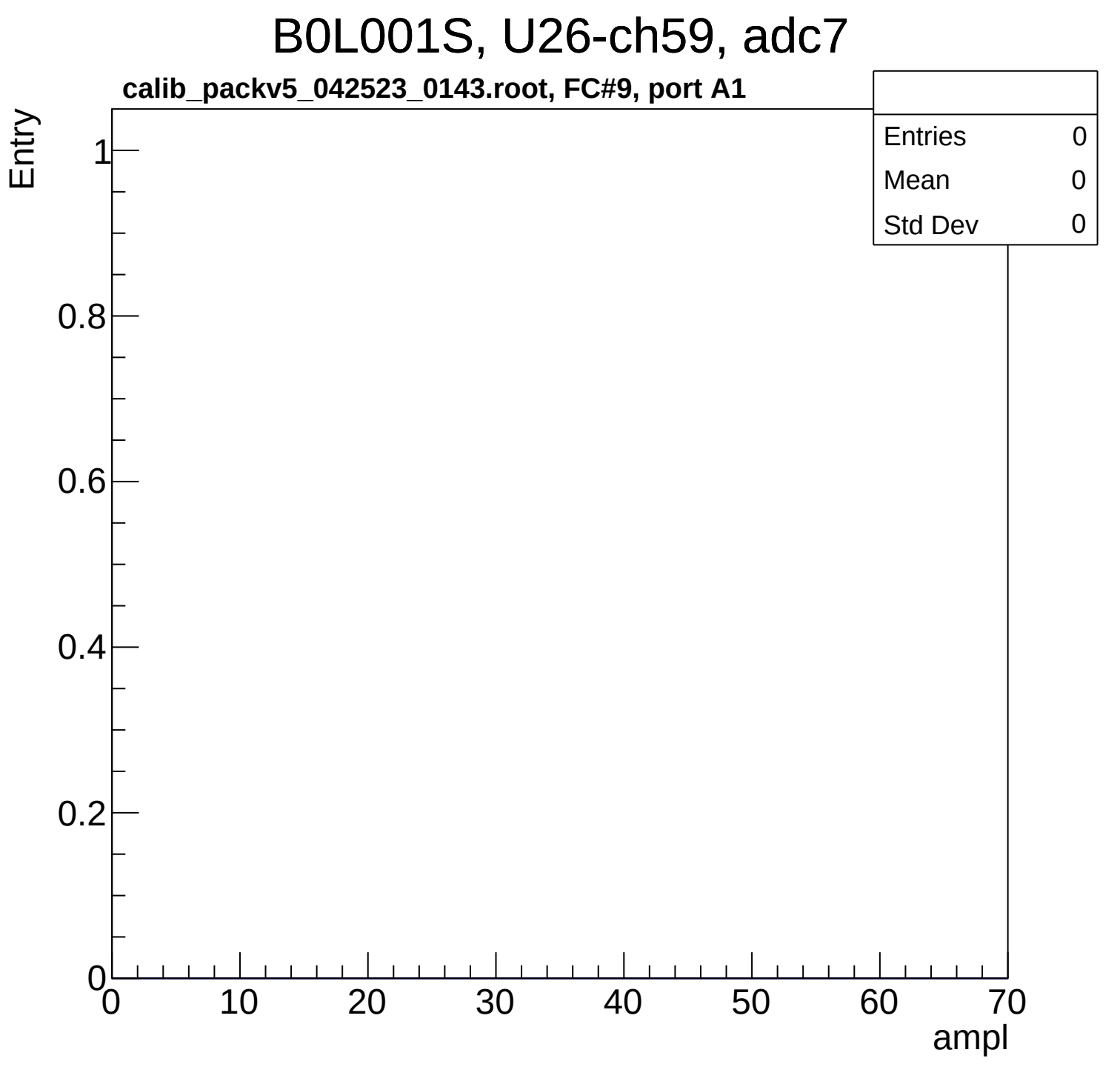

B0L001S, U26-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 45.96
Std Dev 2.842
Gaus mean : 46.8405
Gaus Width: 3.2964
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

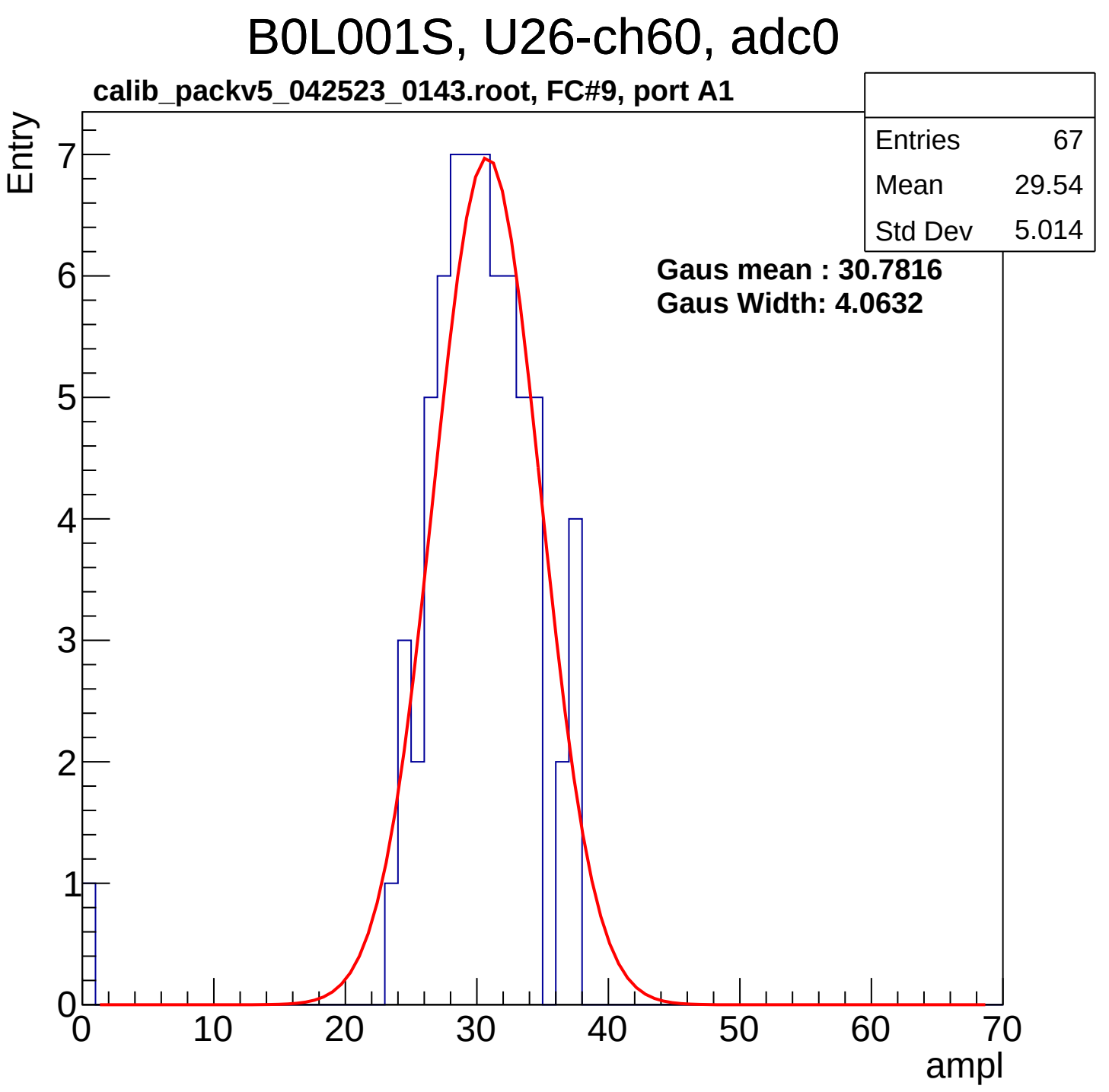

B0L001S, U26-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 52.26
Std Dev 3.282
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

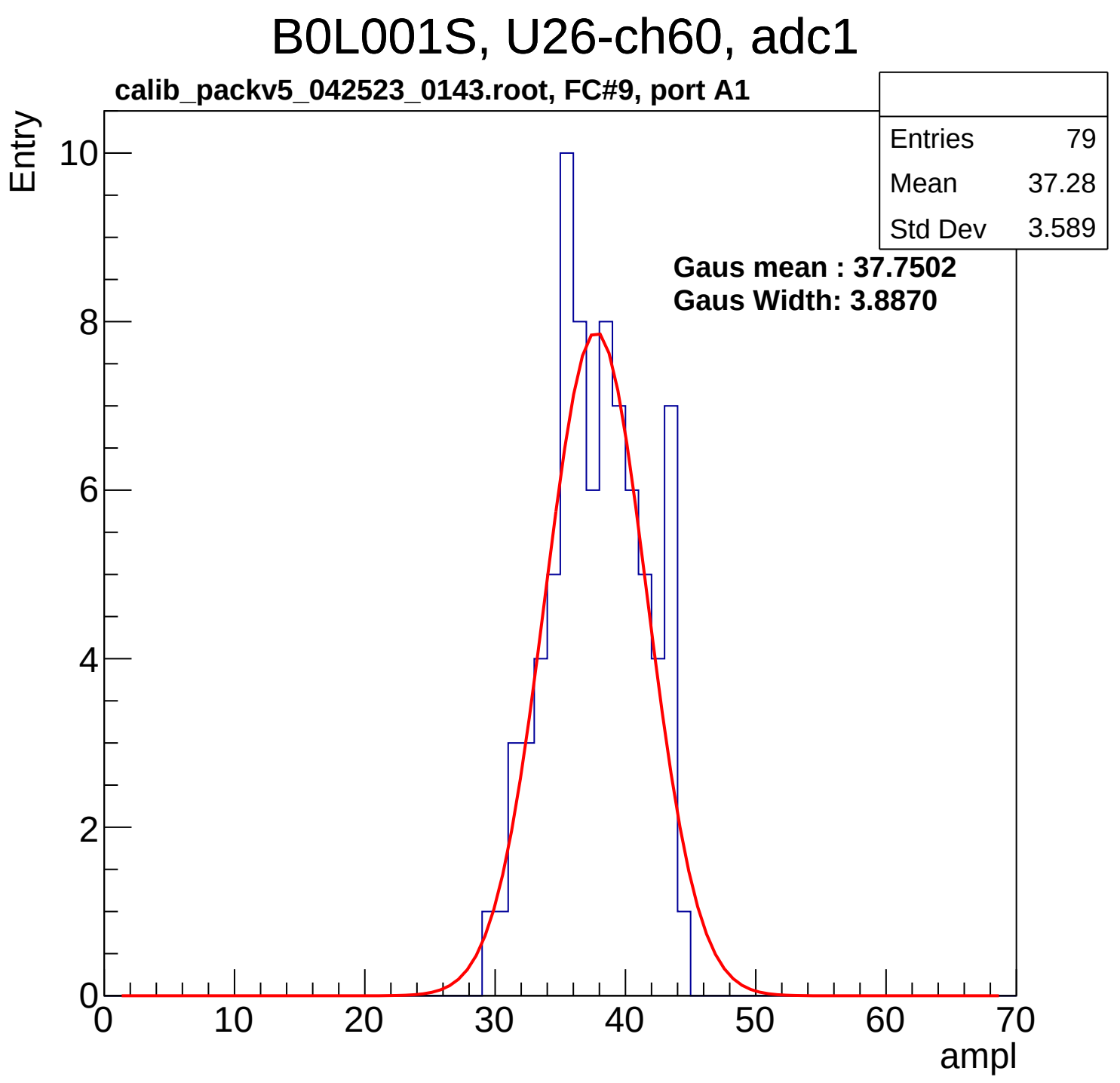

B0L001S, U26-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 58.26
Std Dev 2.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

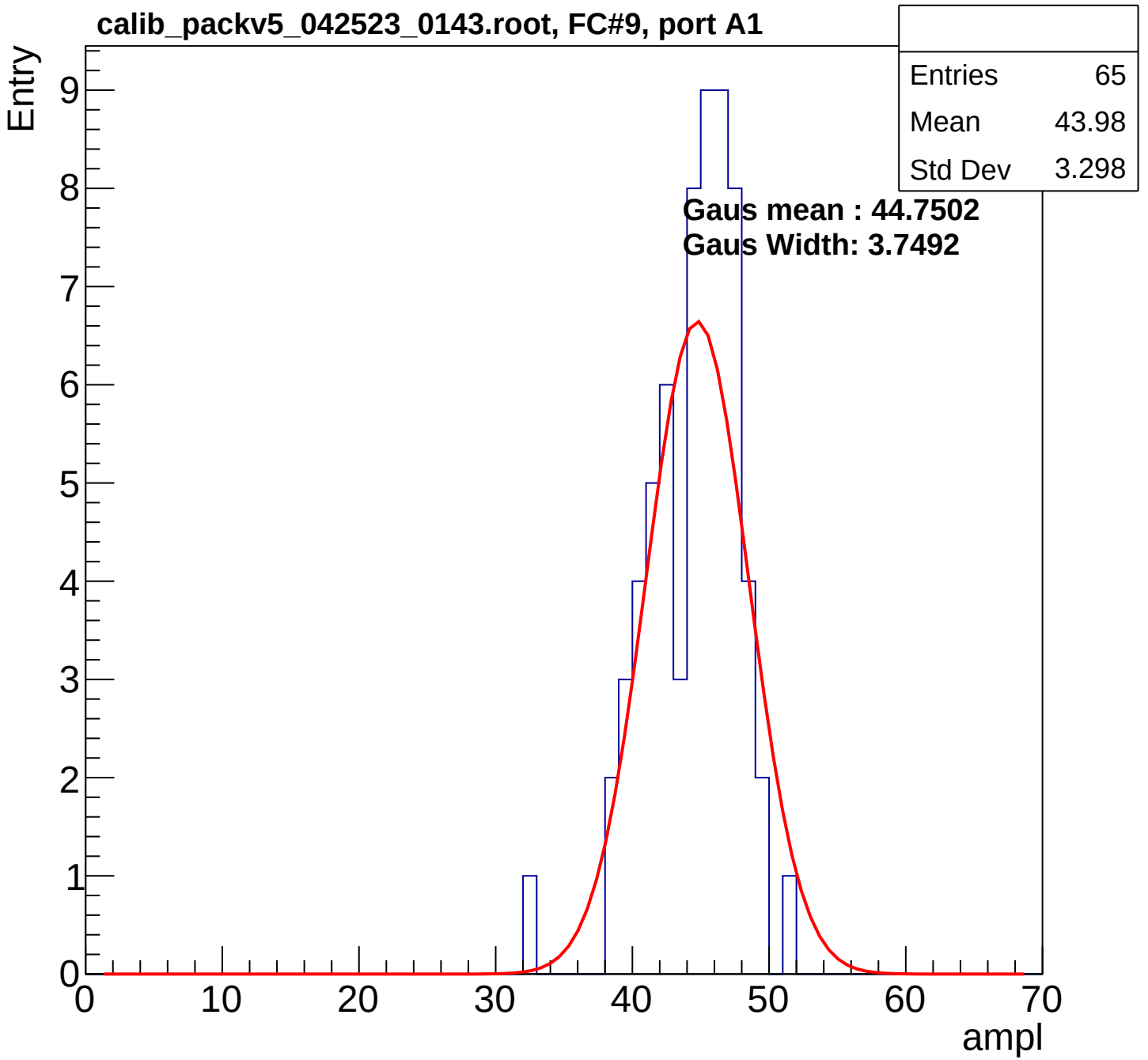

B0L001S, U26-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 58.75
Std Dev 13.52
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.54
Std Dev 5.014
Gaus mean : 30.7816
Gaus Width: 4.0632
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.28
Std Dev 3.589
Gaus mean : 37.7502
Gaus Width: 3.8870
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.98
Std Dev 3.298
Gaus mean : 44.7502
Gaus Width: 3.7492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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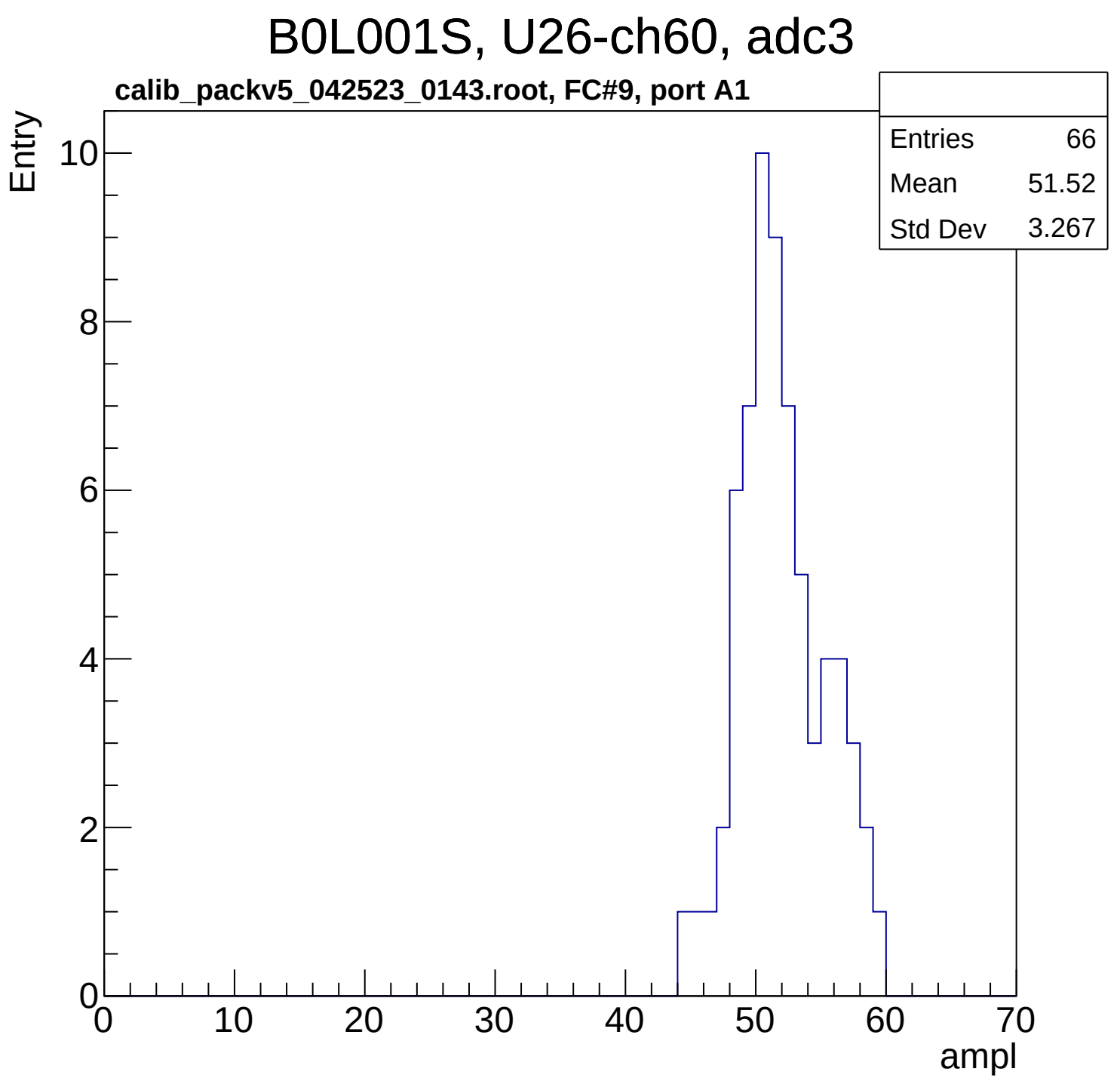
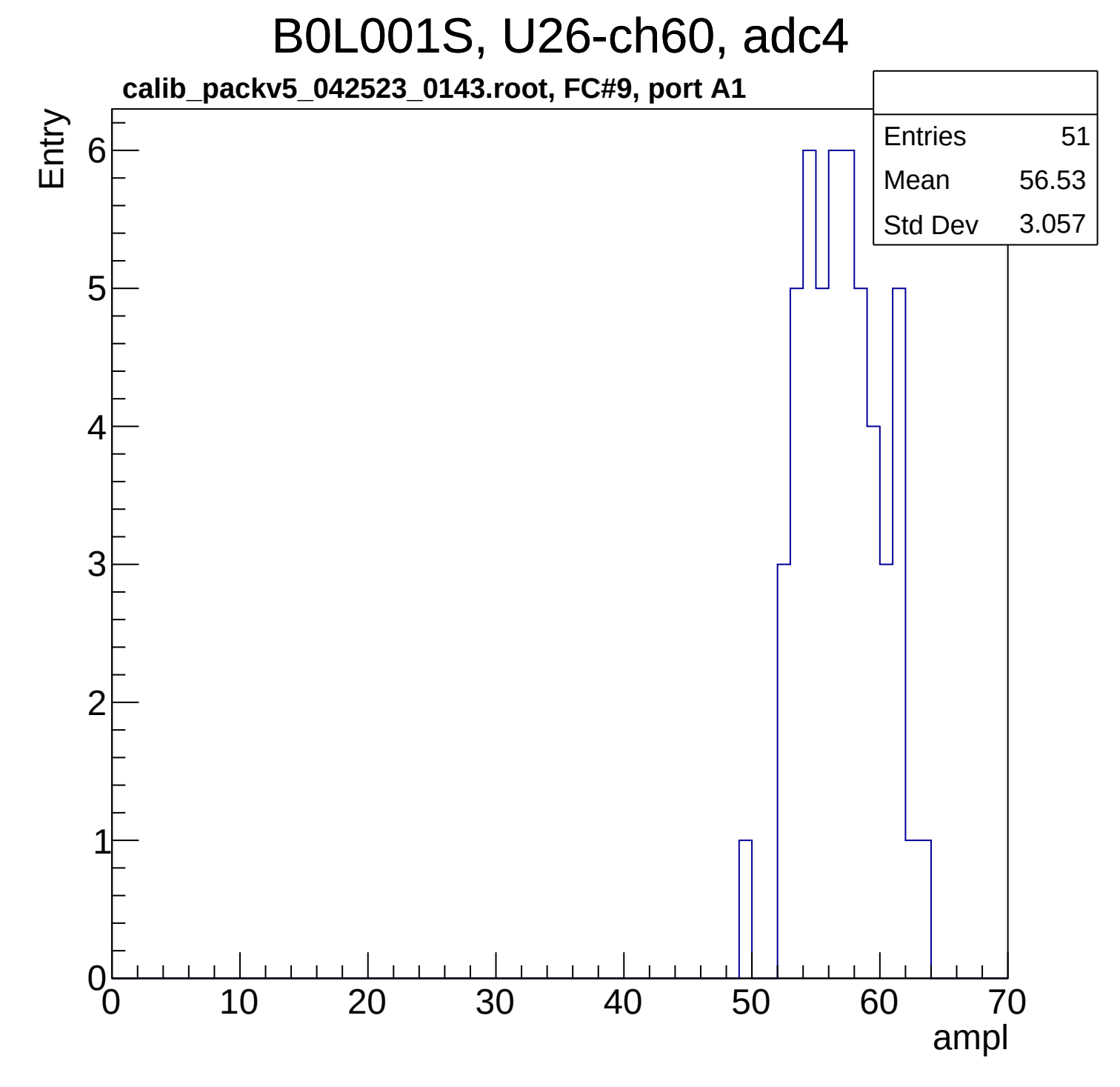
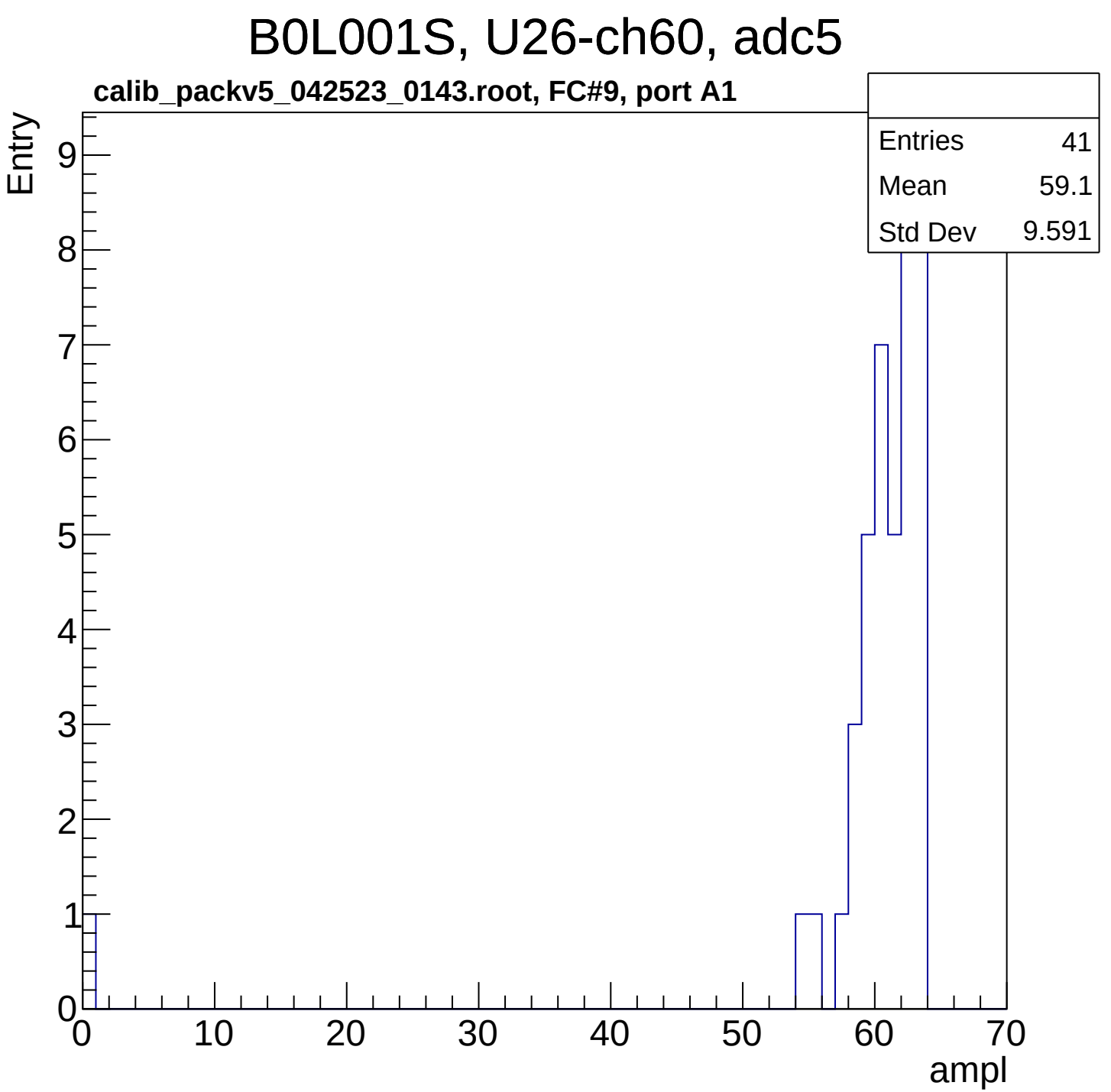
60
70


B0L001S, U26-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.52
Std Dev 3.267
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.53
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U26-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.1
Std Dev 9.591
Entry
ampl
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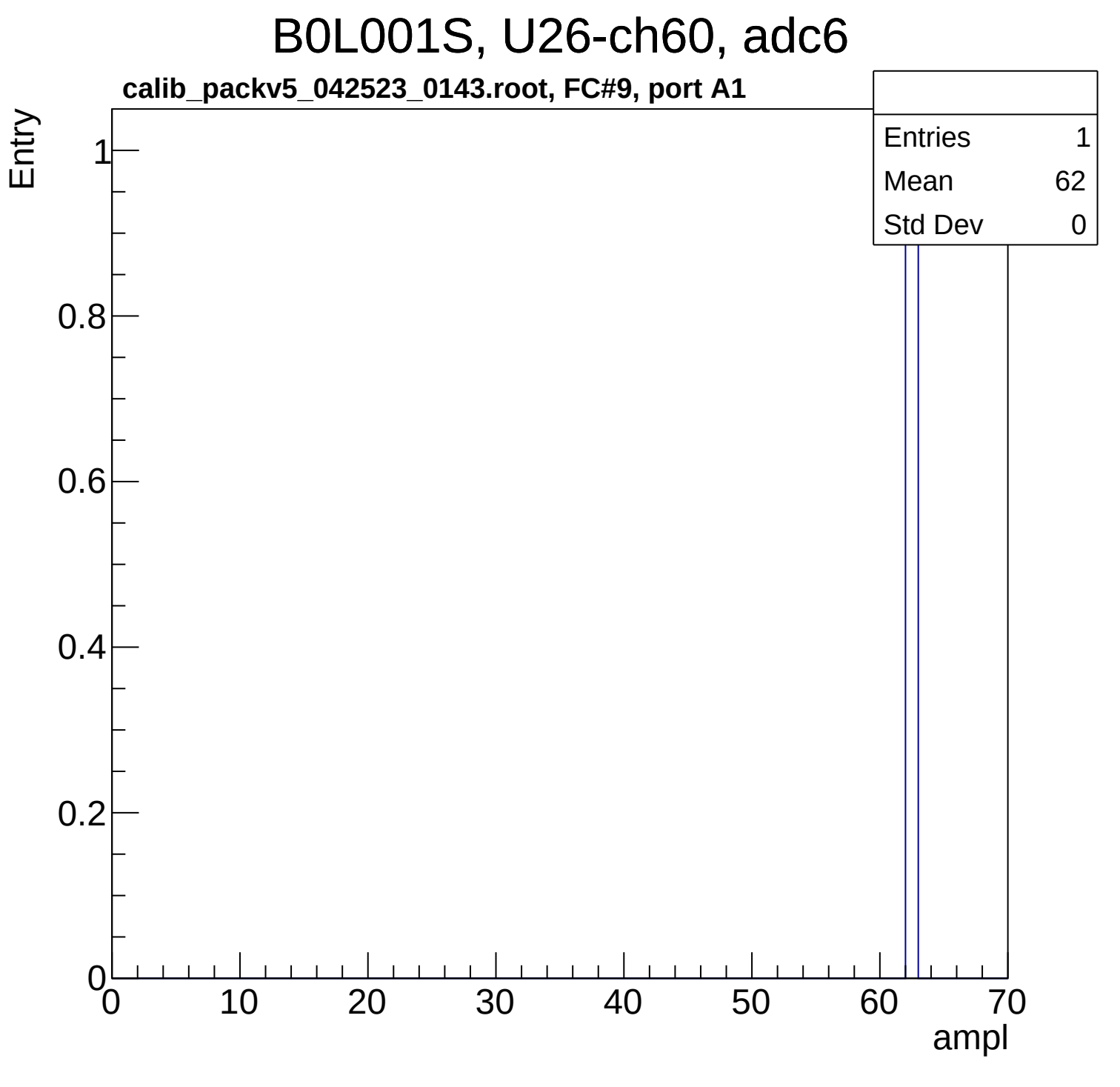
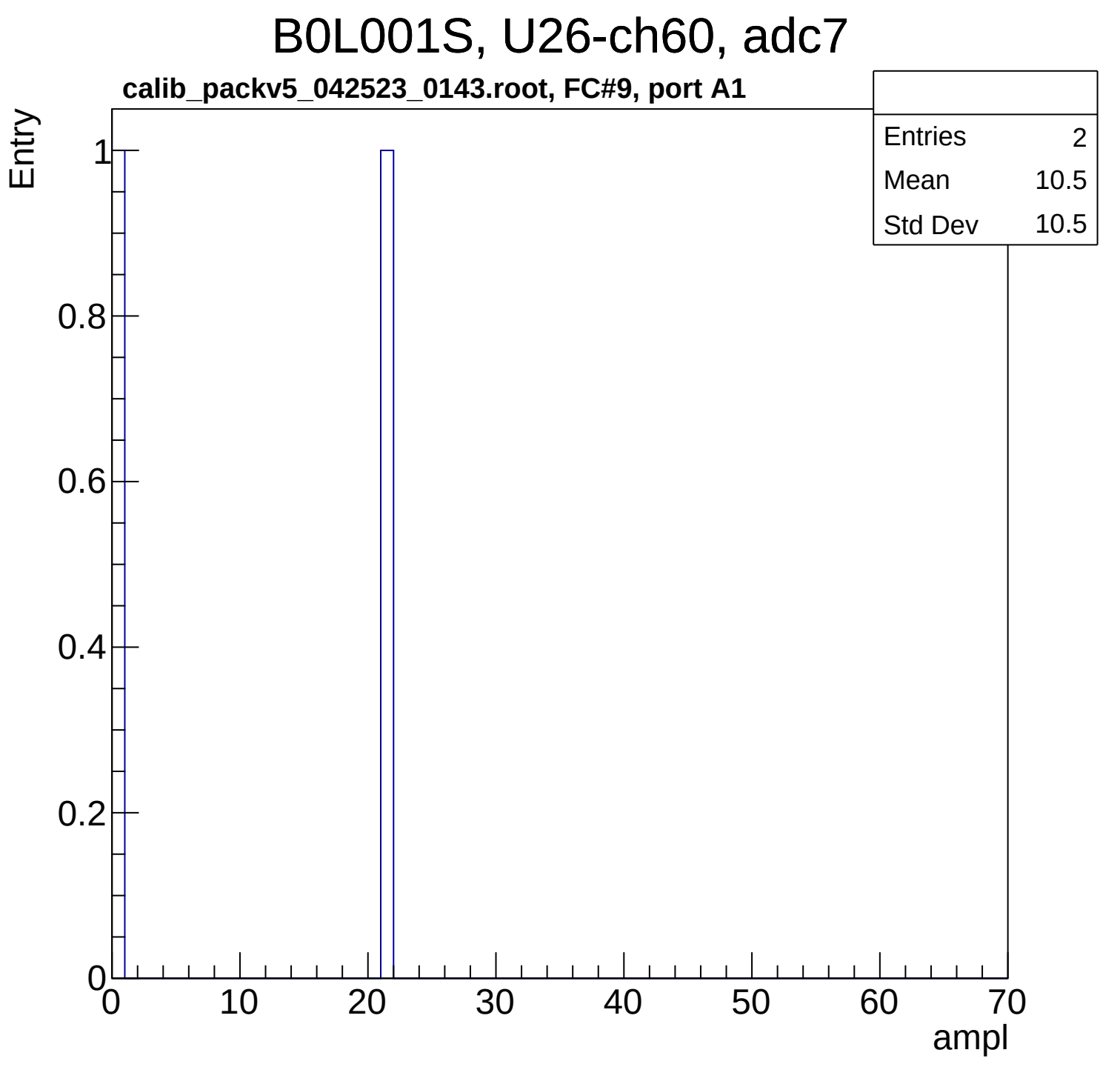
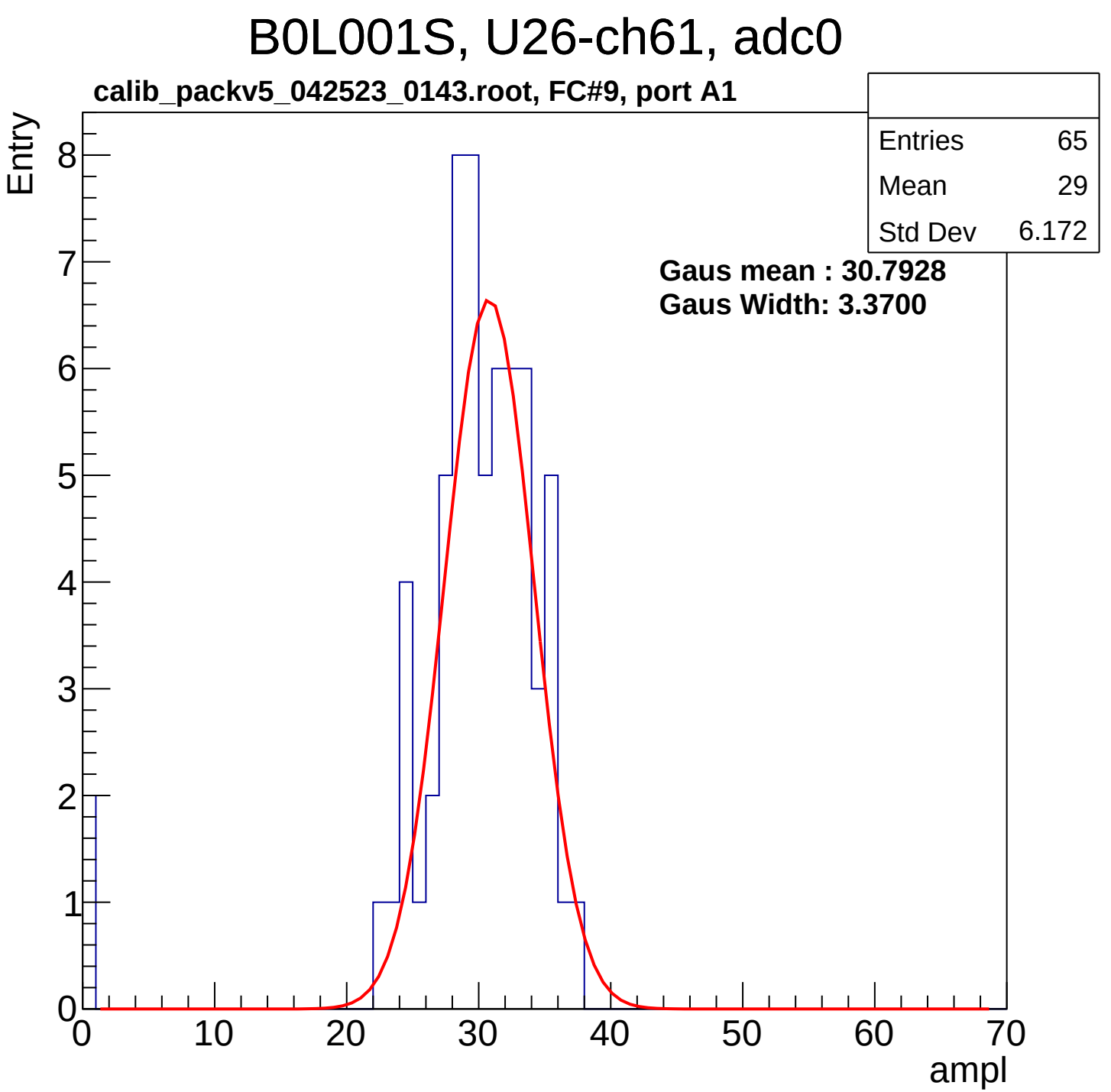
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29
Std Dev 6.172
Gaus mean : 30.7928
Gaus Width: 3.3700
Entry
ampl
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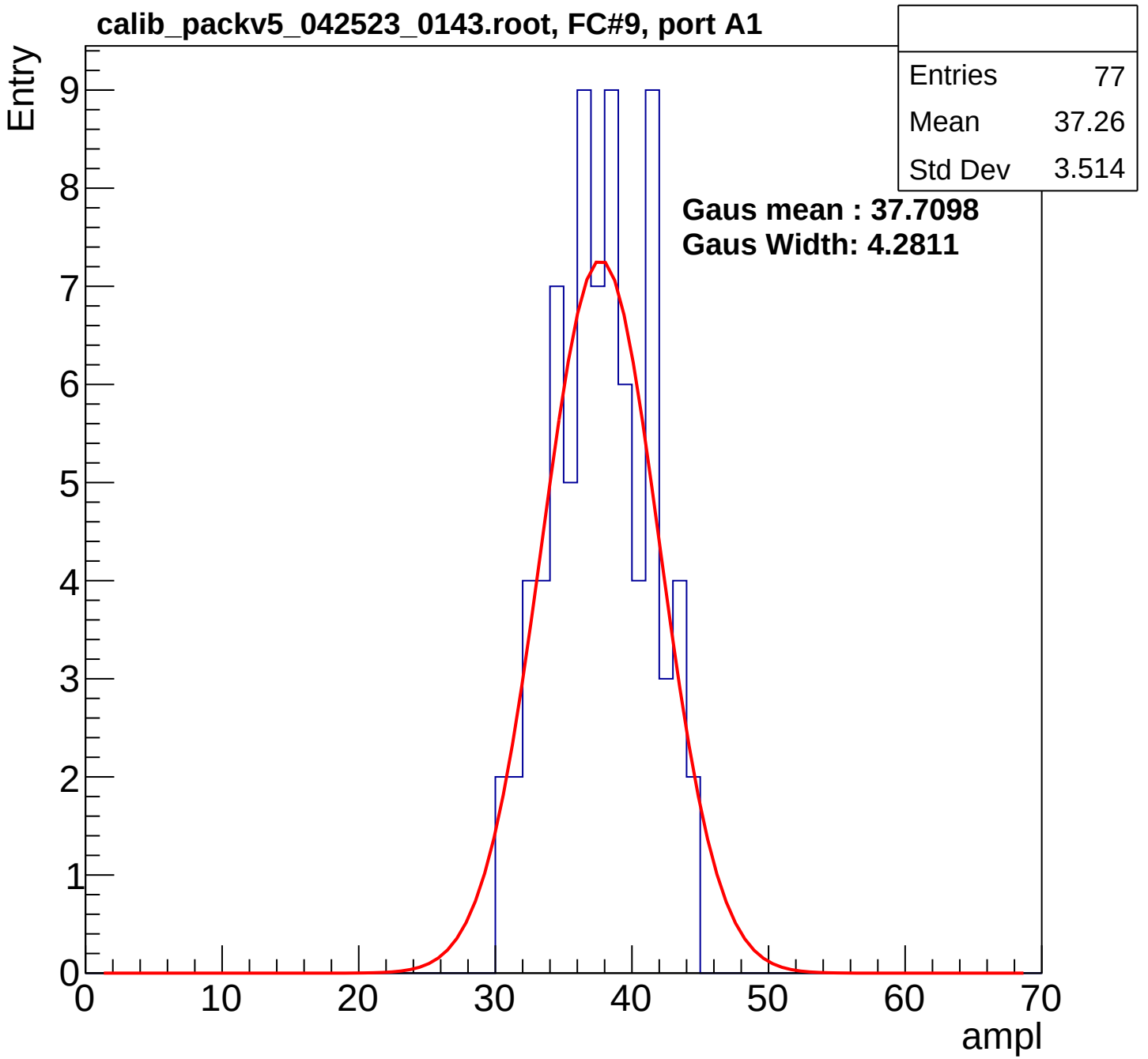
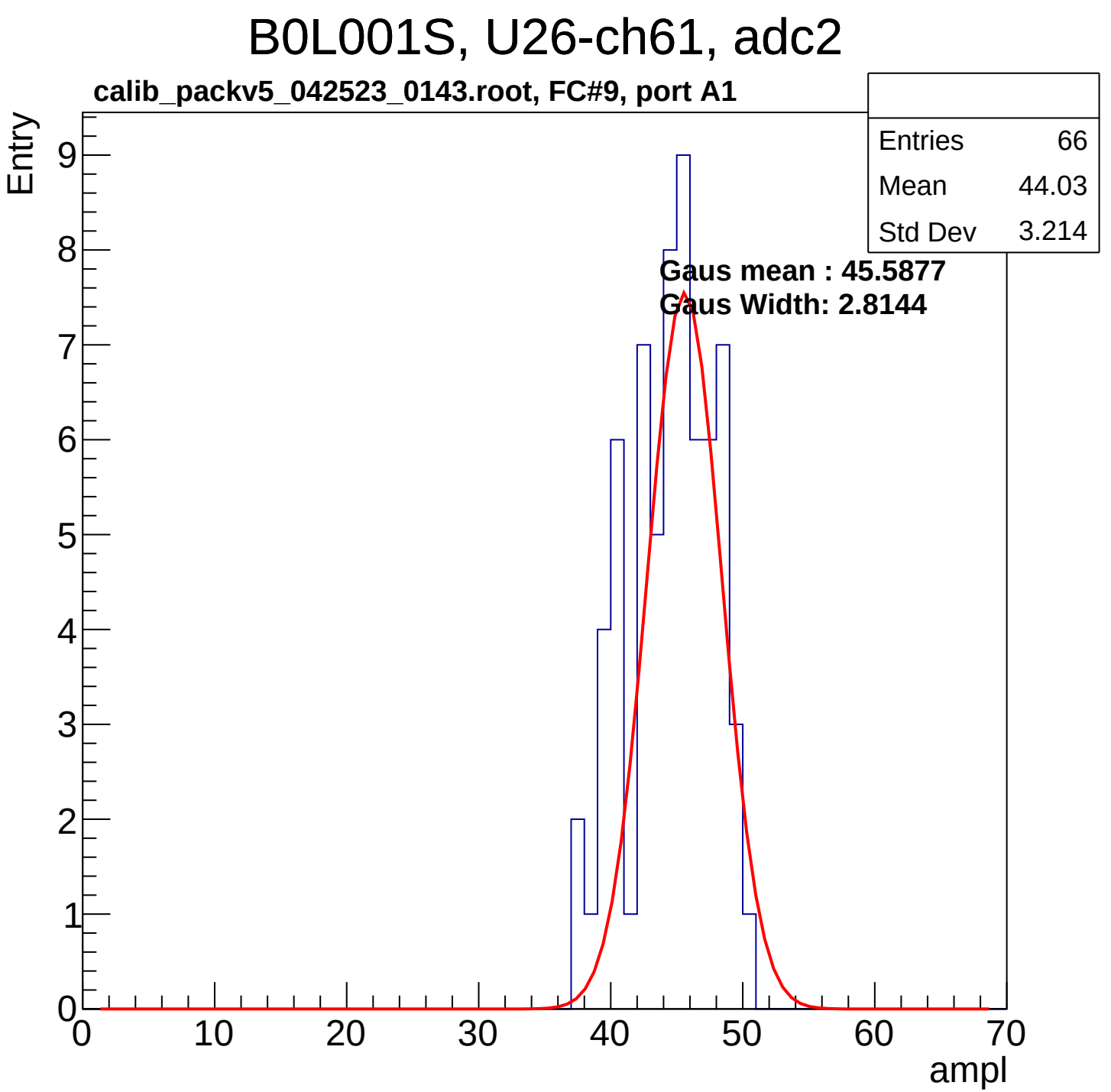
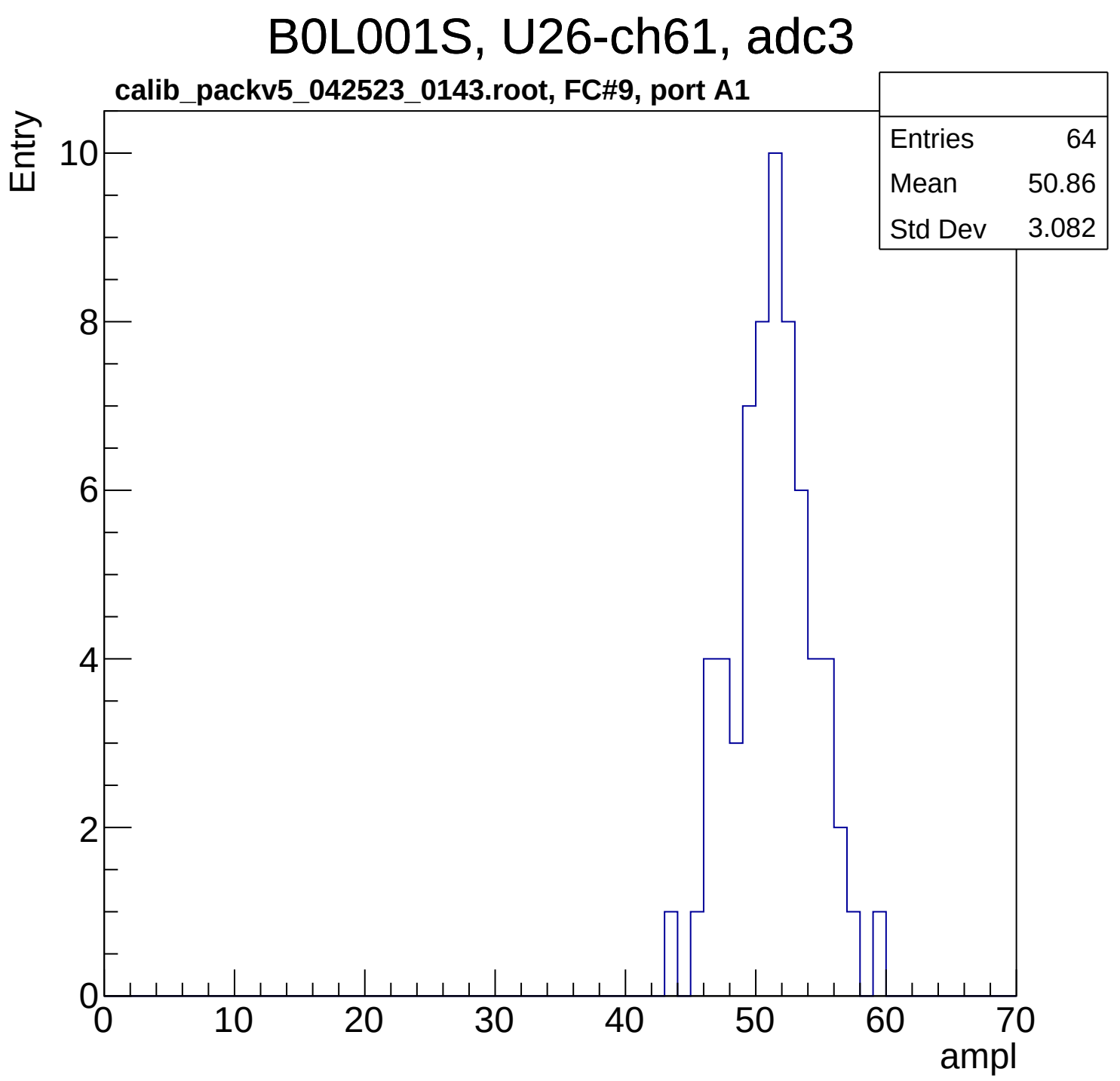
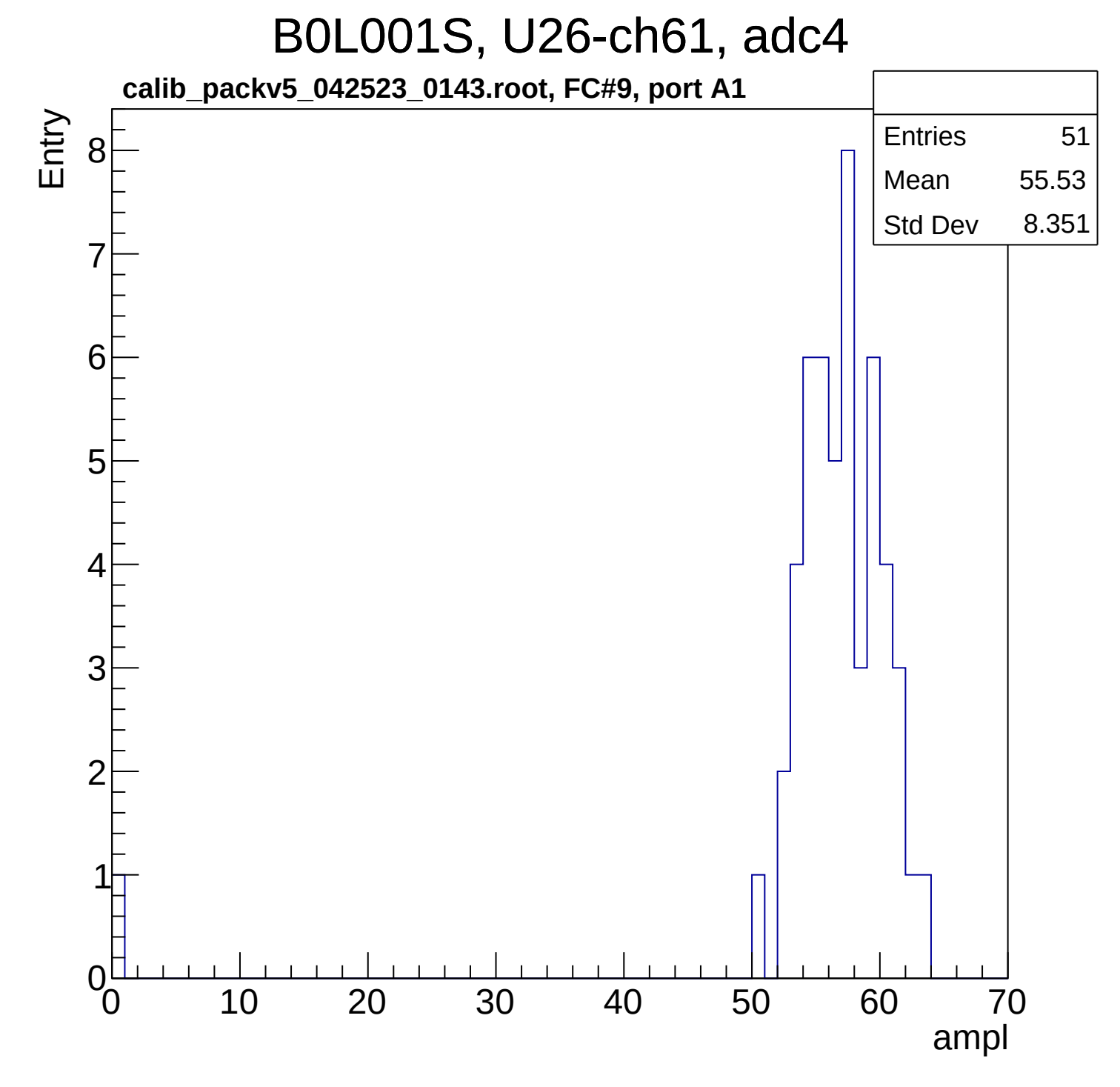
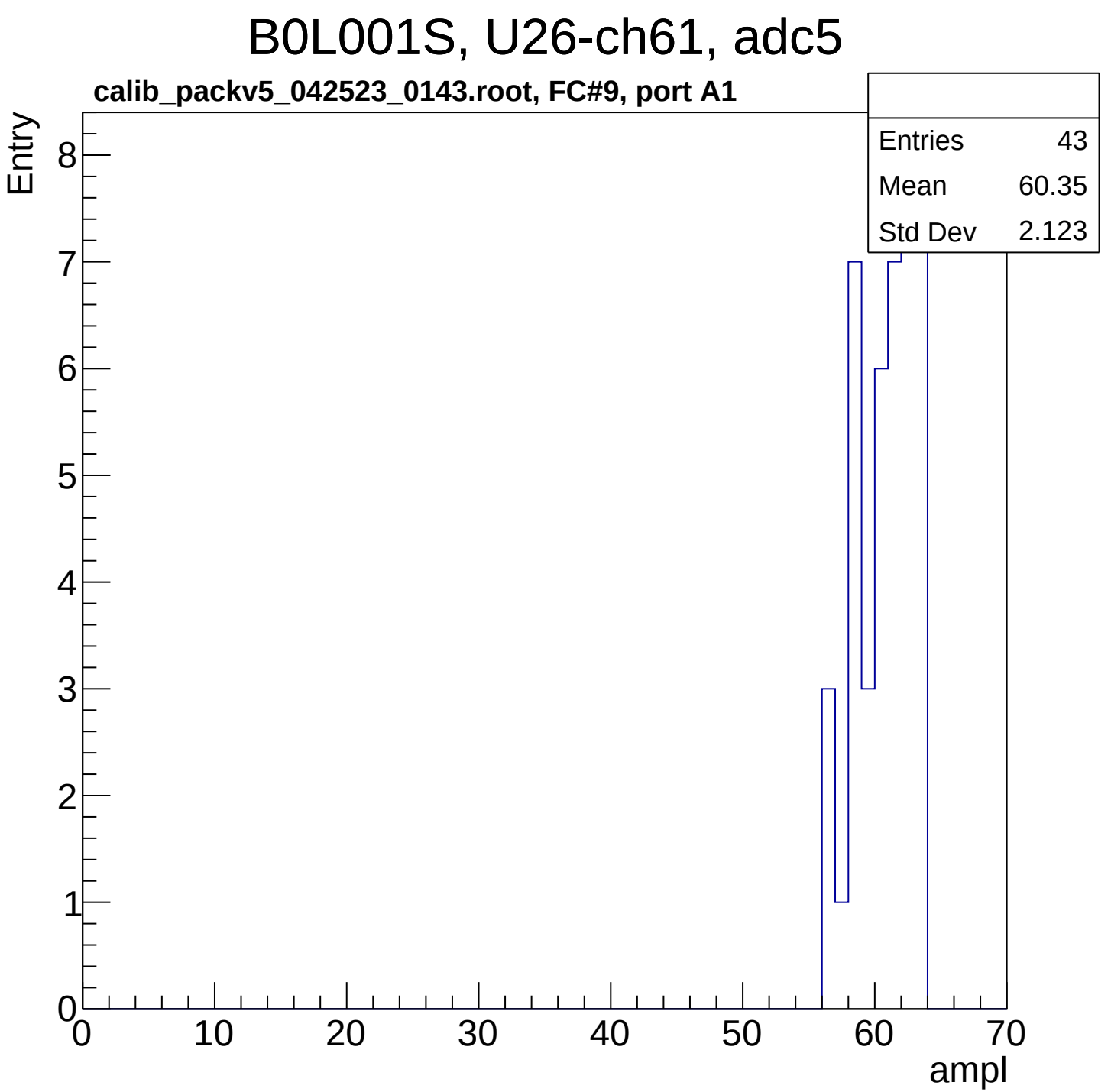
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.26
Std Dev 3.514
Gaus mean : 37.7098
Gaus Width: 4.2811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.03
Std Dev 3.214
Gaus mean : 45.5877
Gaus Width: 2.8144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.86
Std Dev 3.082
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.53
Std Dev 8.351
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 60.35
Std Dev 2.123
Entry
ampl
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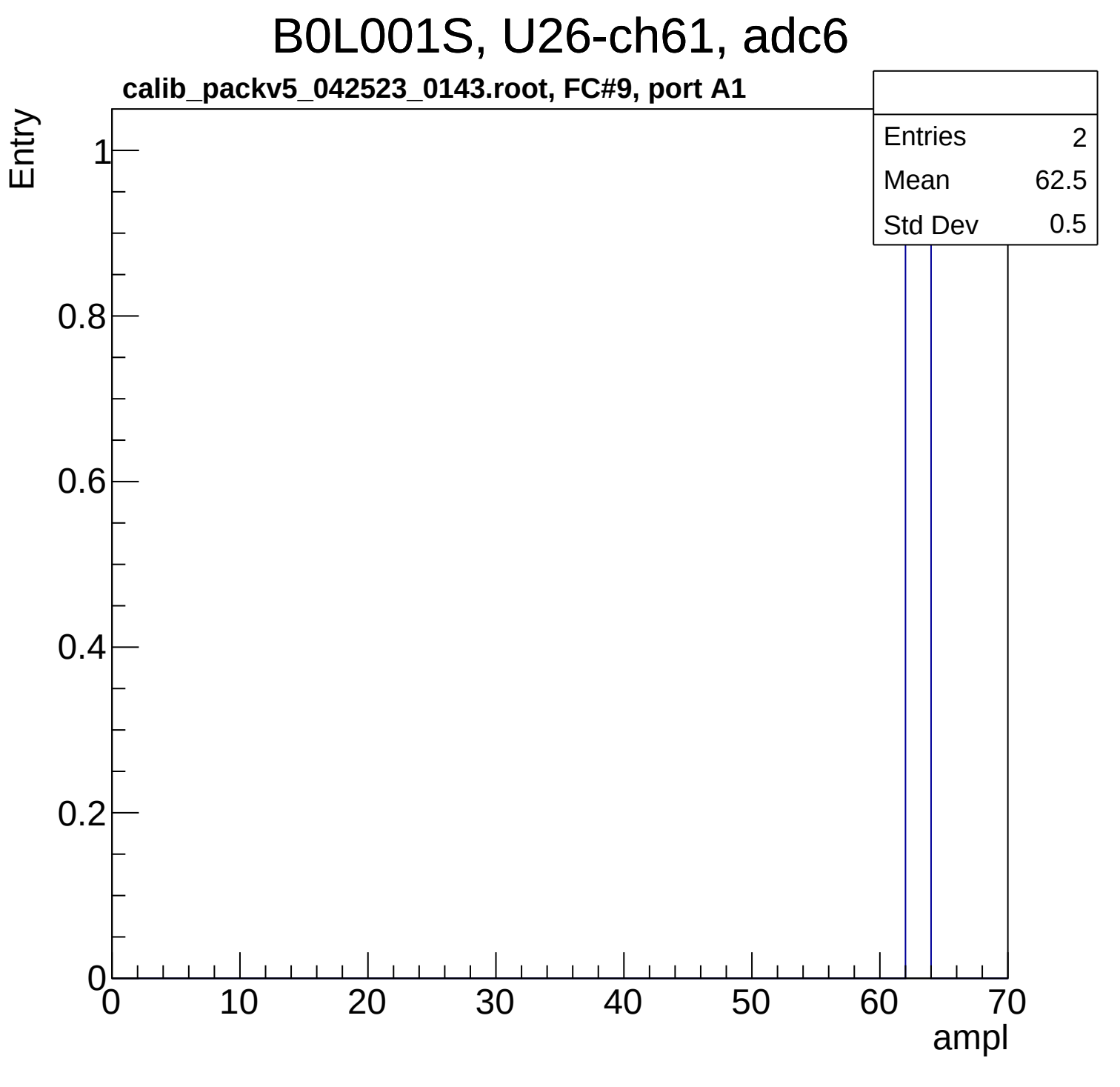
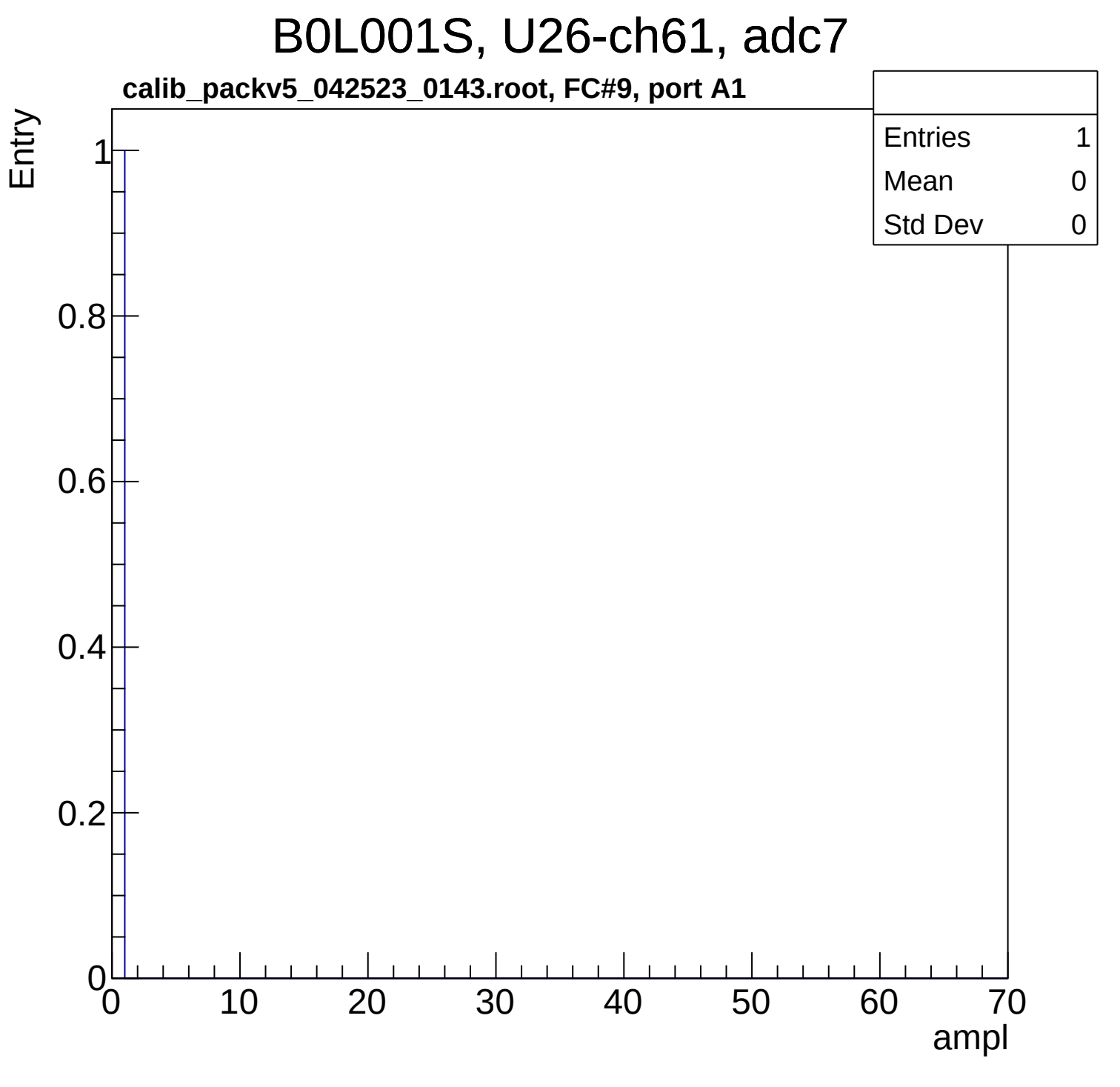
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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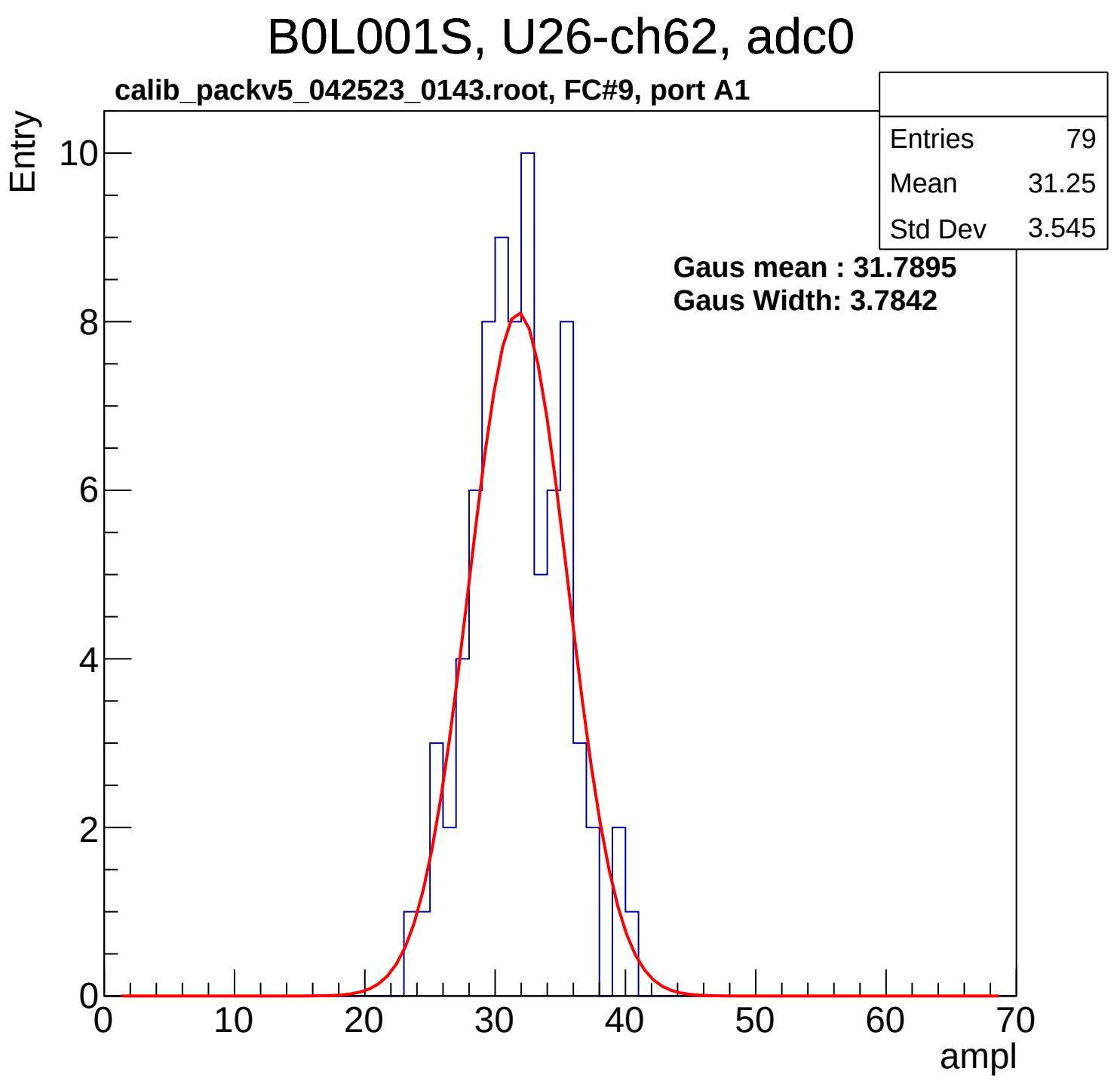
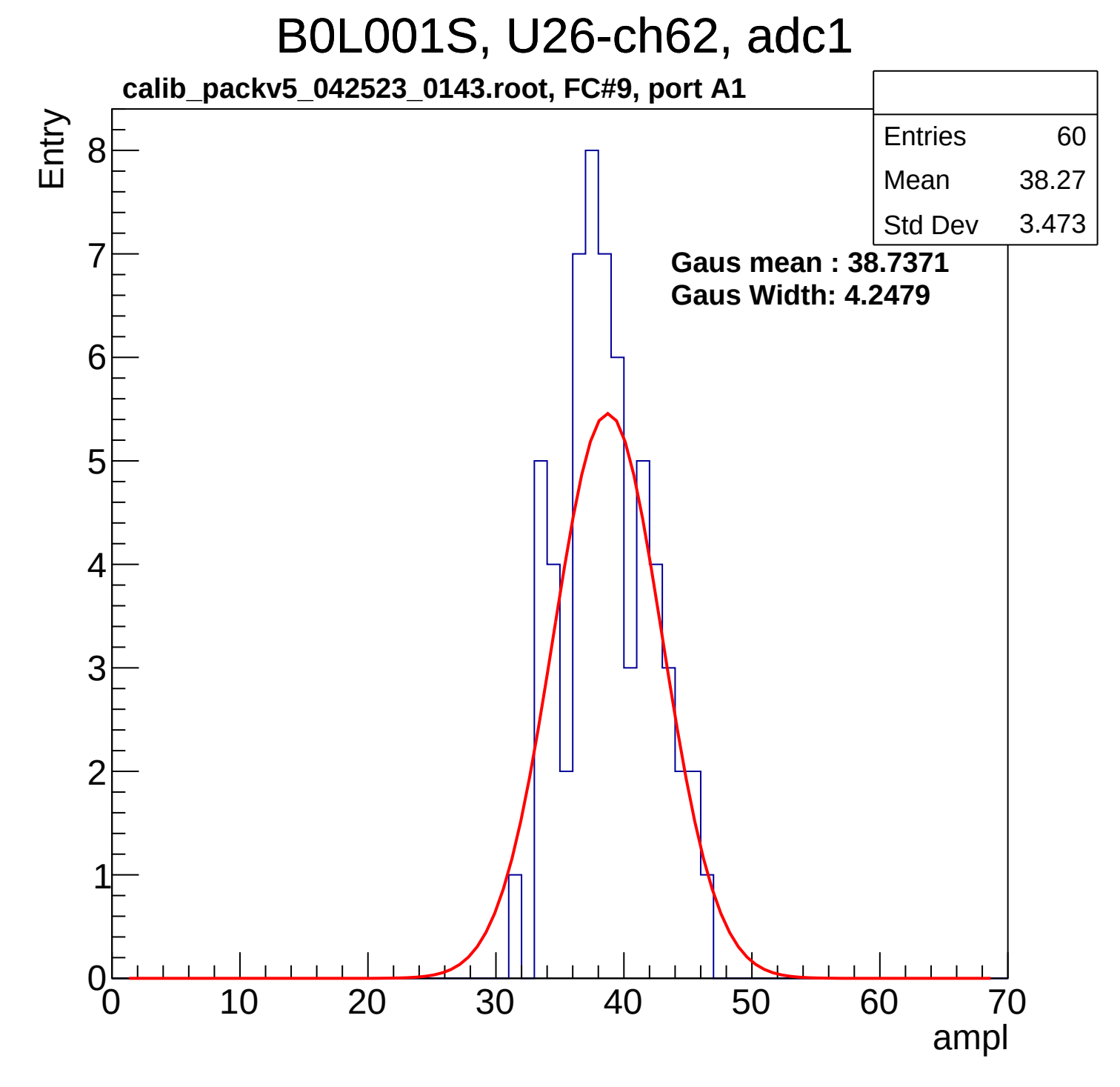
50
60
70
ampl


B0L001S, U26-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 31.25
Std Dev 3.545
Gaus mean : 31.7895
Gaus Width: 3.7842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 38.27
Std Dev 3.473
Gaus mean : 38.7371
Gaus Width: 4.2479
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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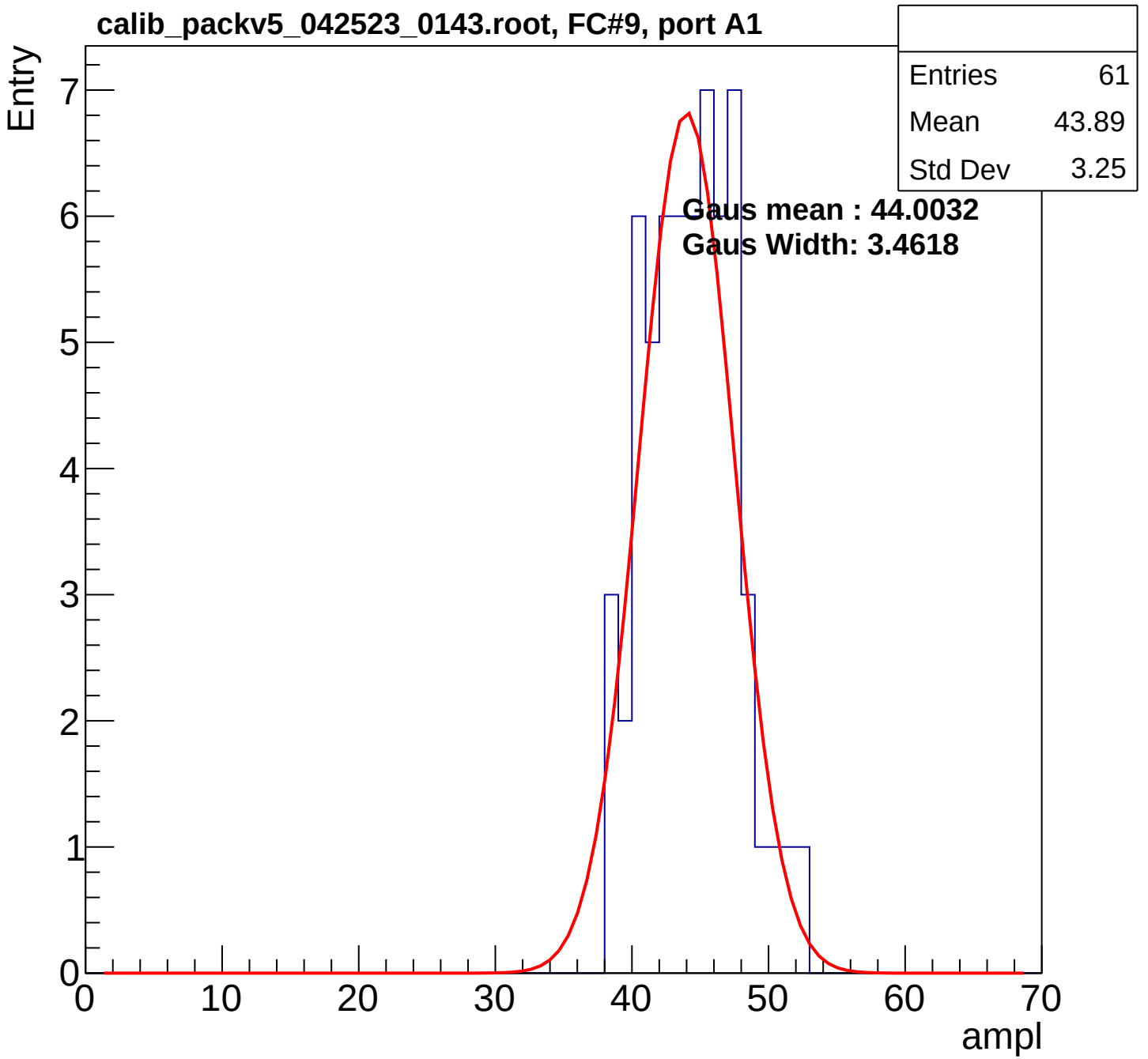
B0L001S, U26-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.89
Std Dev 3.25
Gaus mean : 44.0032
Gaus Width: 3.4618
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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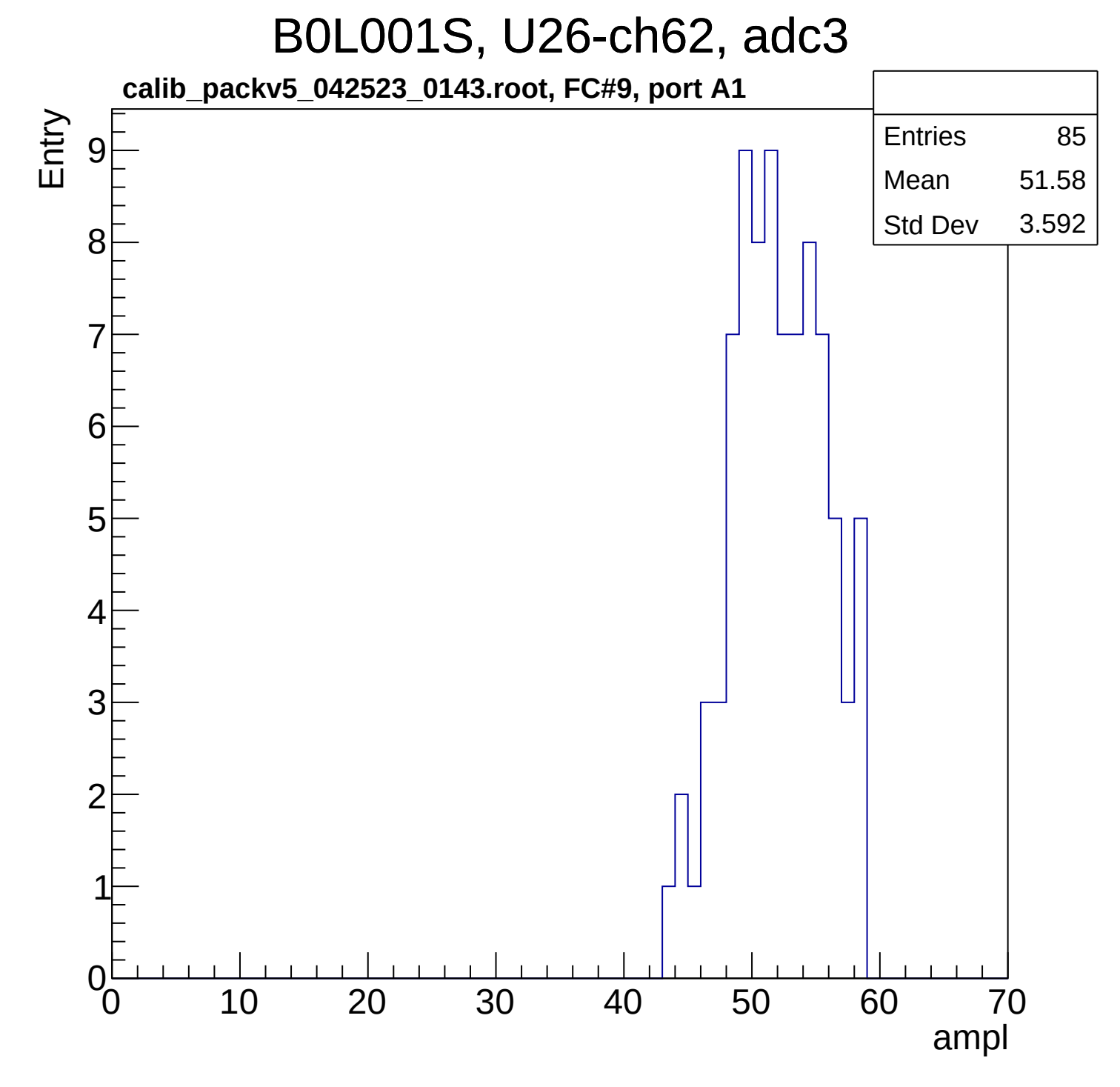
B0L001S, U26-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 51.58
Std Dev 3.592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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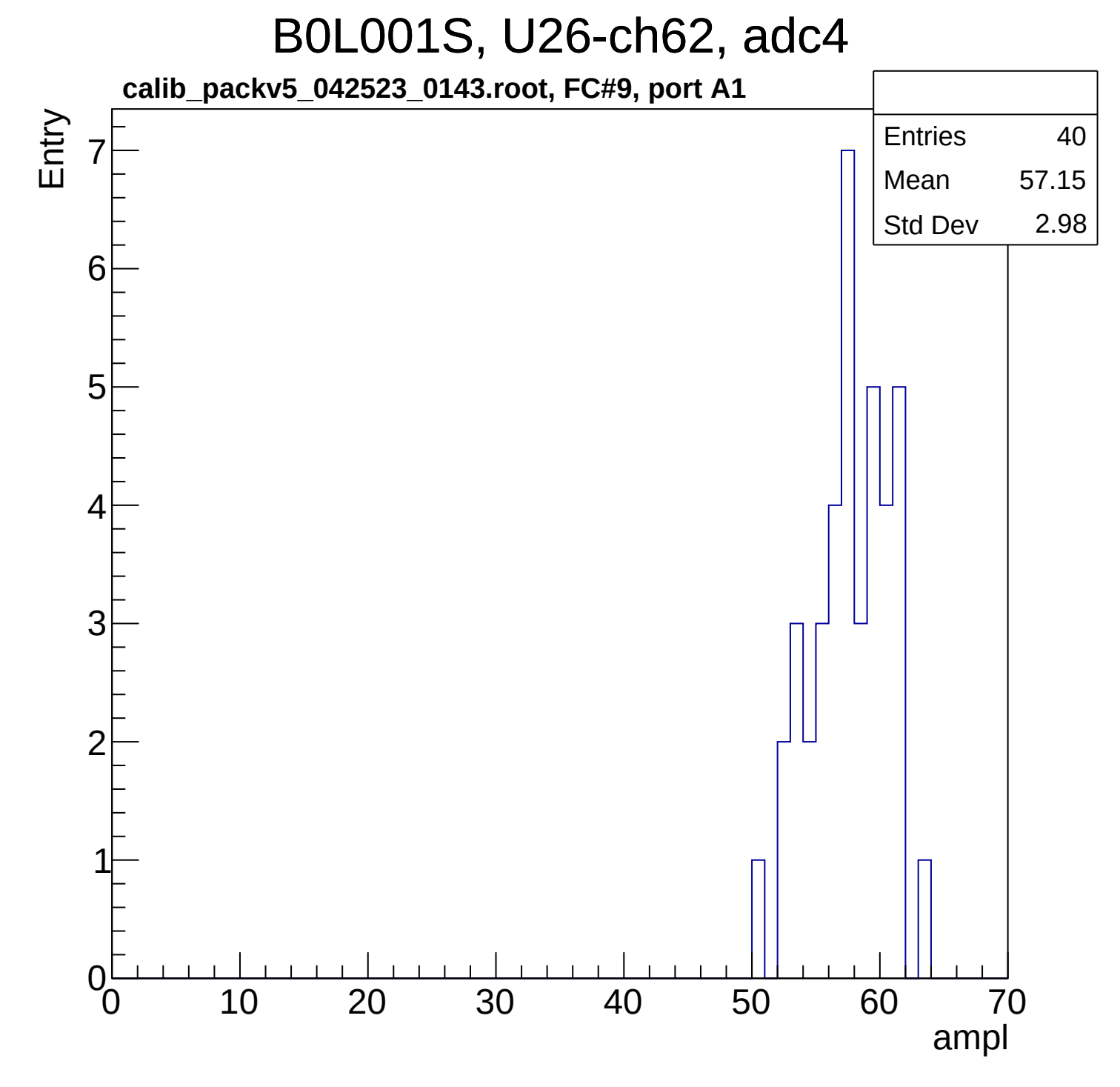
B0L001S, U26-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 57.15
Std Dev 2.98
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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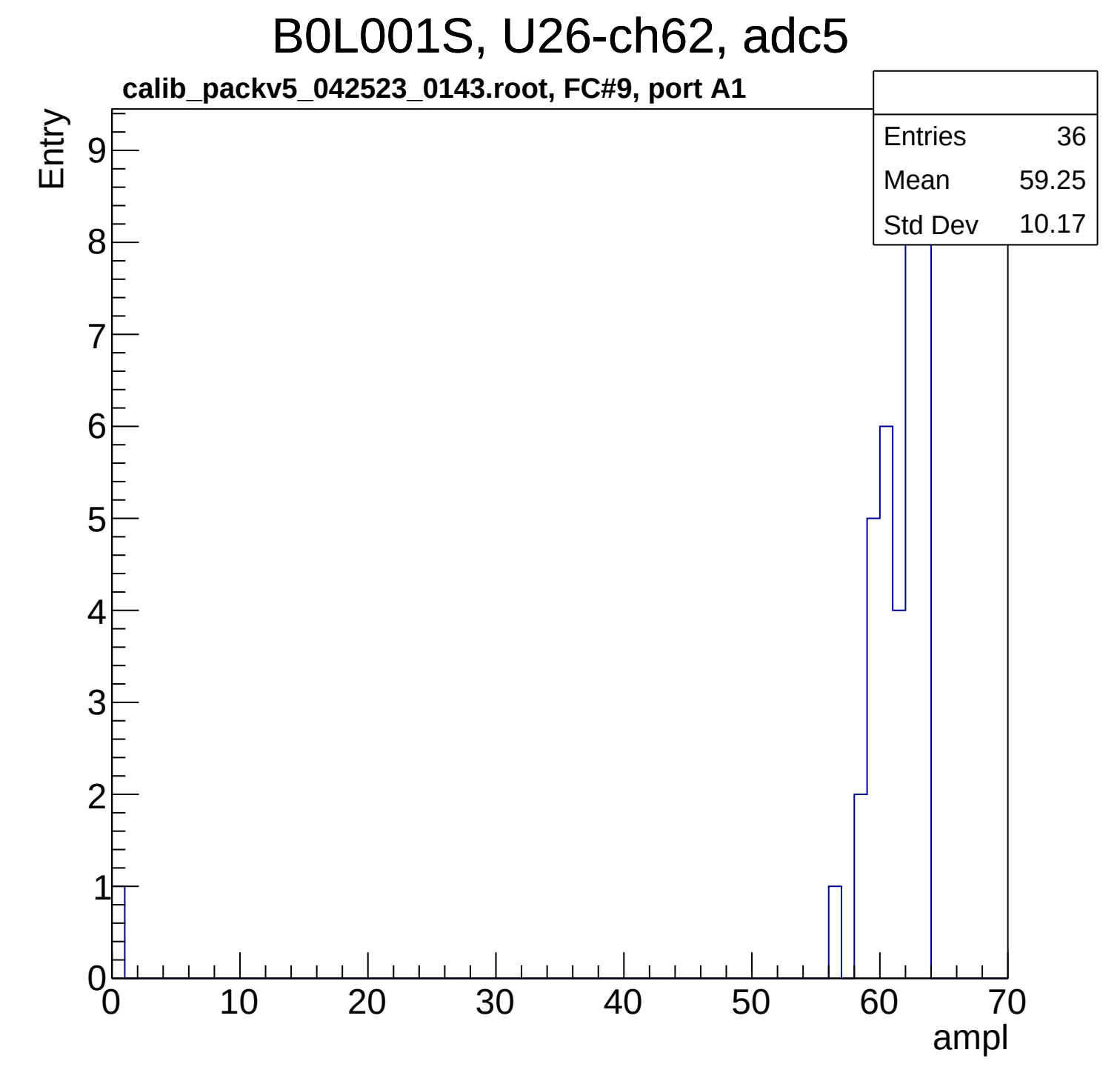
B0L001S, U26-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.25
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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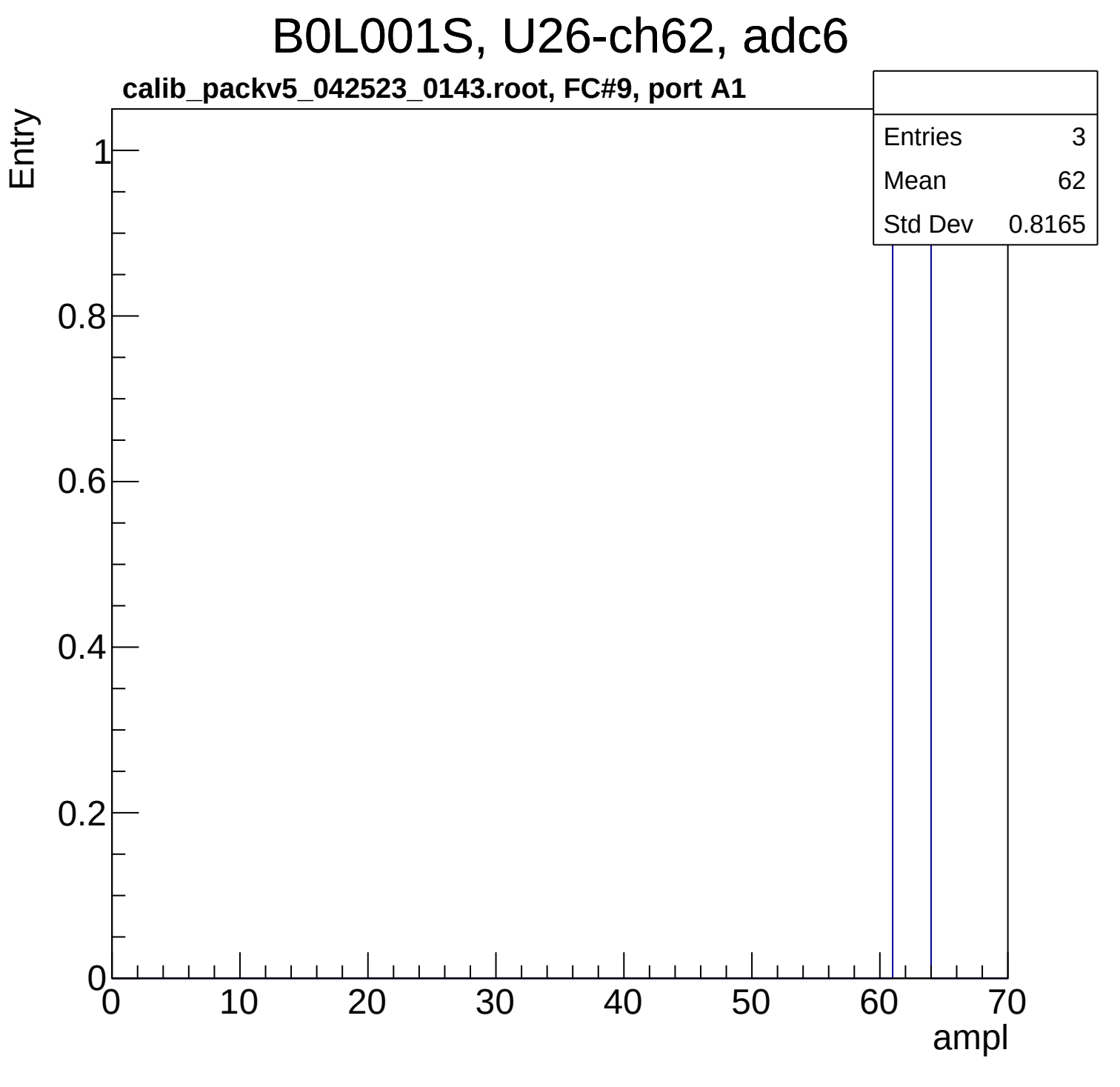
B0L001S, U26-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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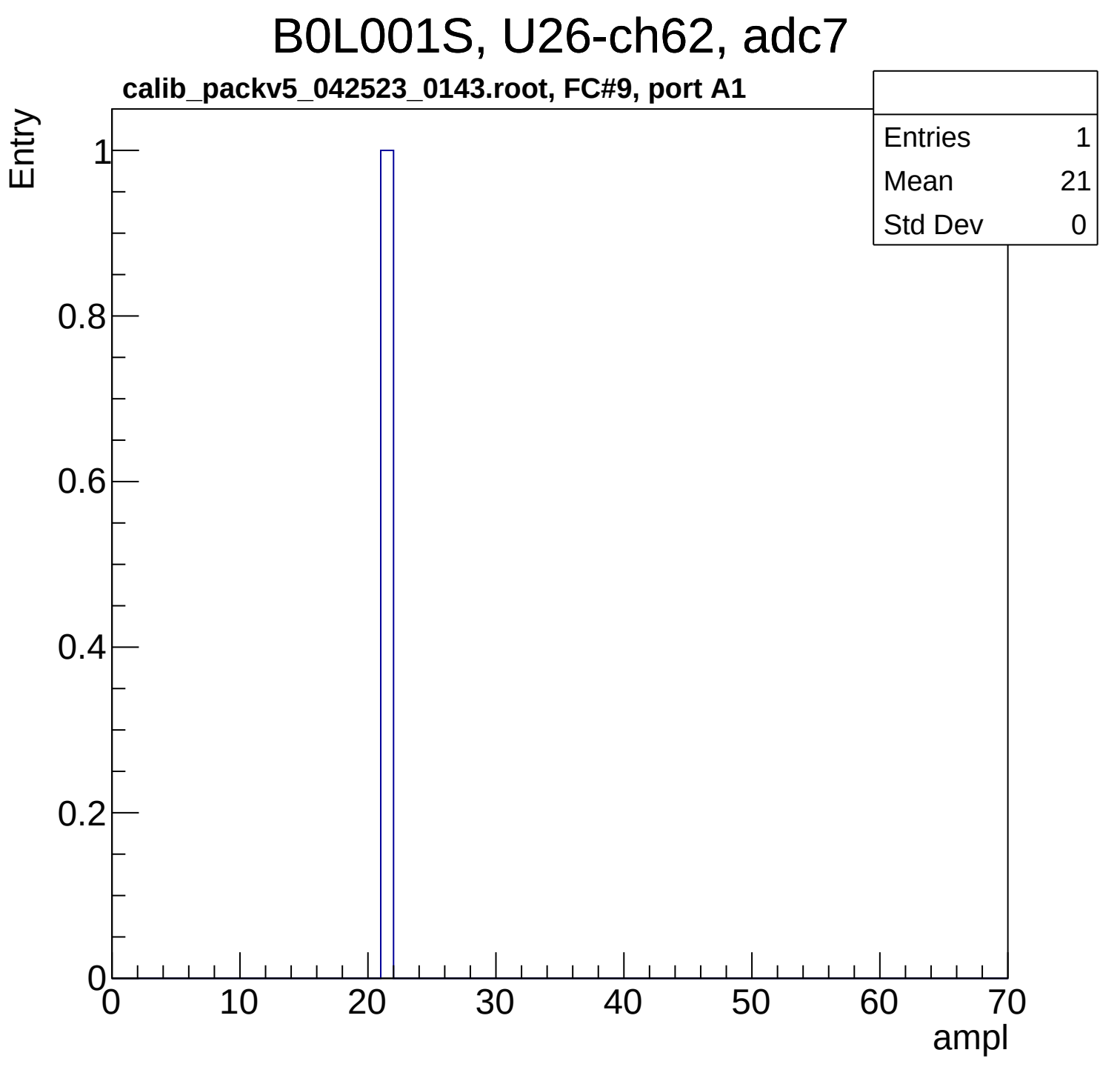
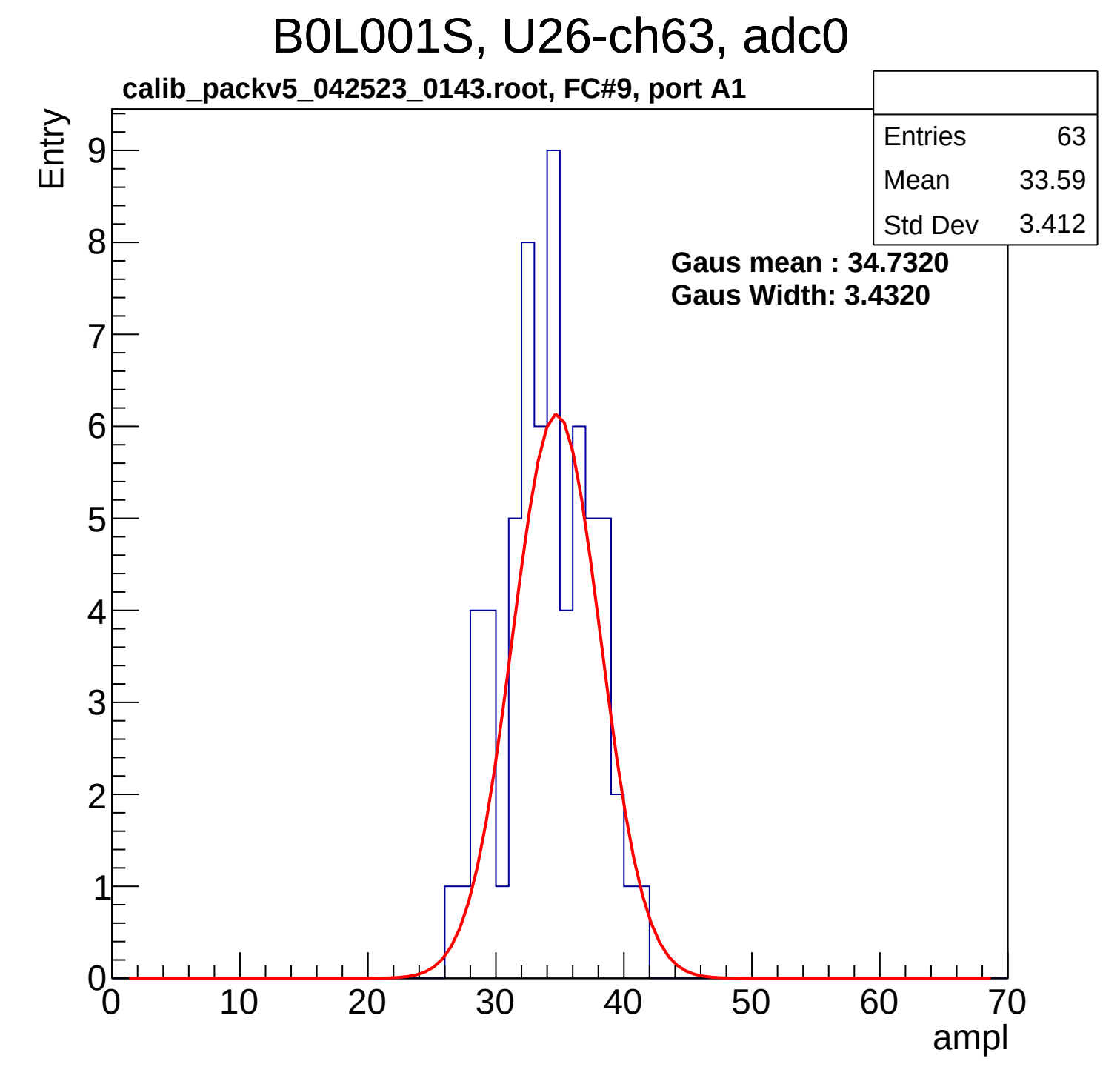
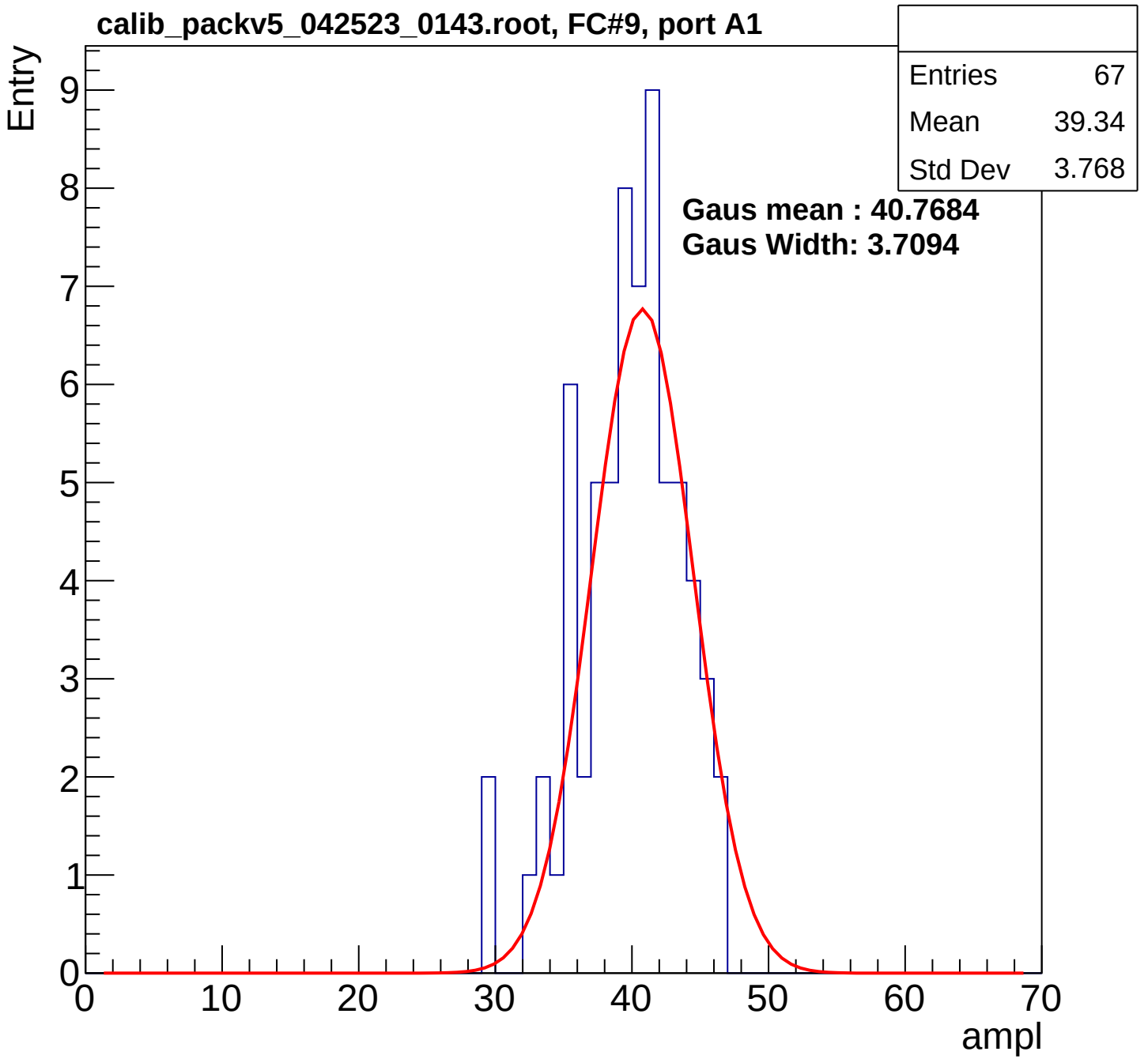
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 33.59
Std Dev 3.412
Gaus mean : 34.7320
Gaus Width: 3.4320
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 39.34
Std Dev 3.768
Gaus mean : 40.7684
Gaus Width: 3.7094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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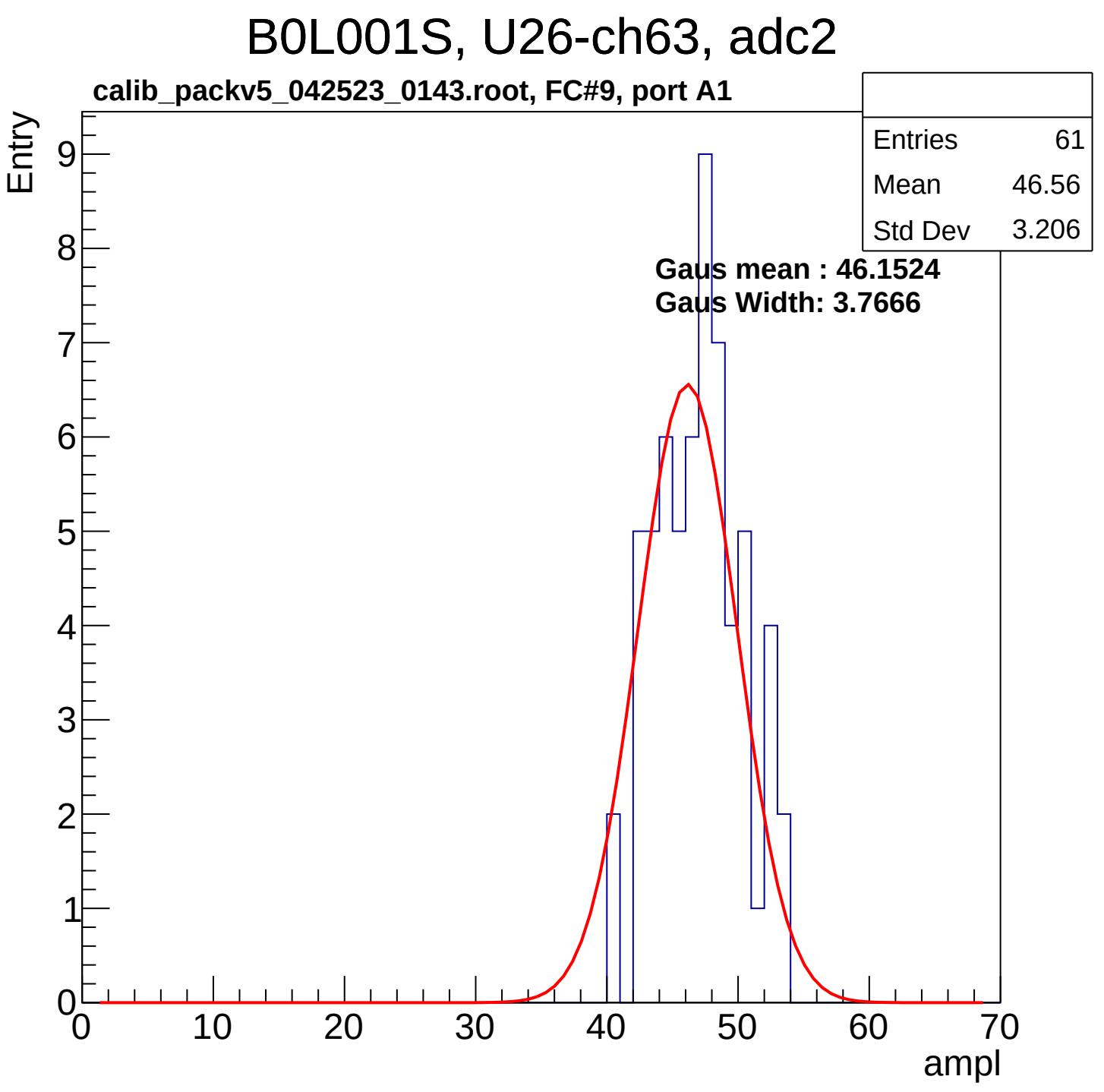
B0L001S, U26-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 46.56
Std Dev 3.206
Gaus mean : 46.1524
Gaus Width: 3.7666
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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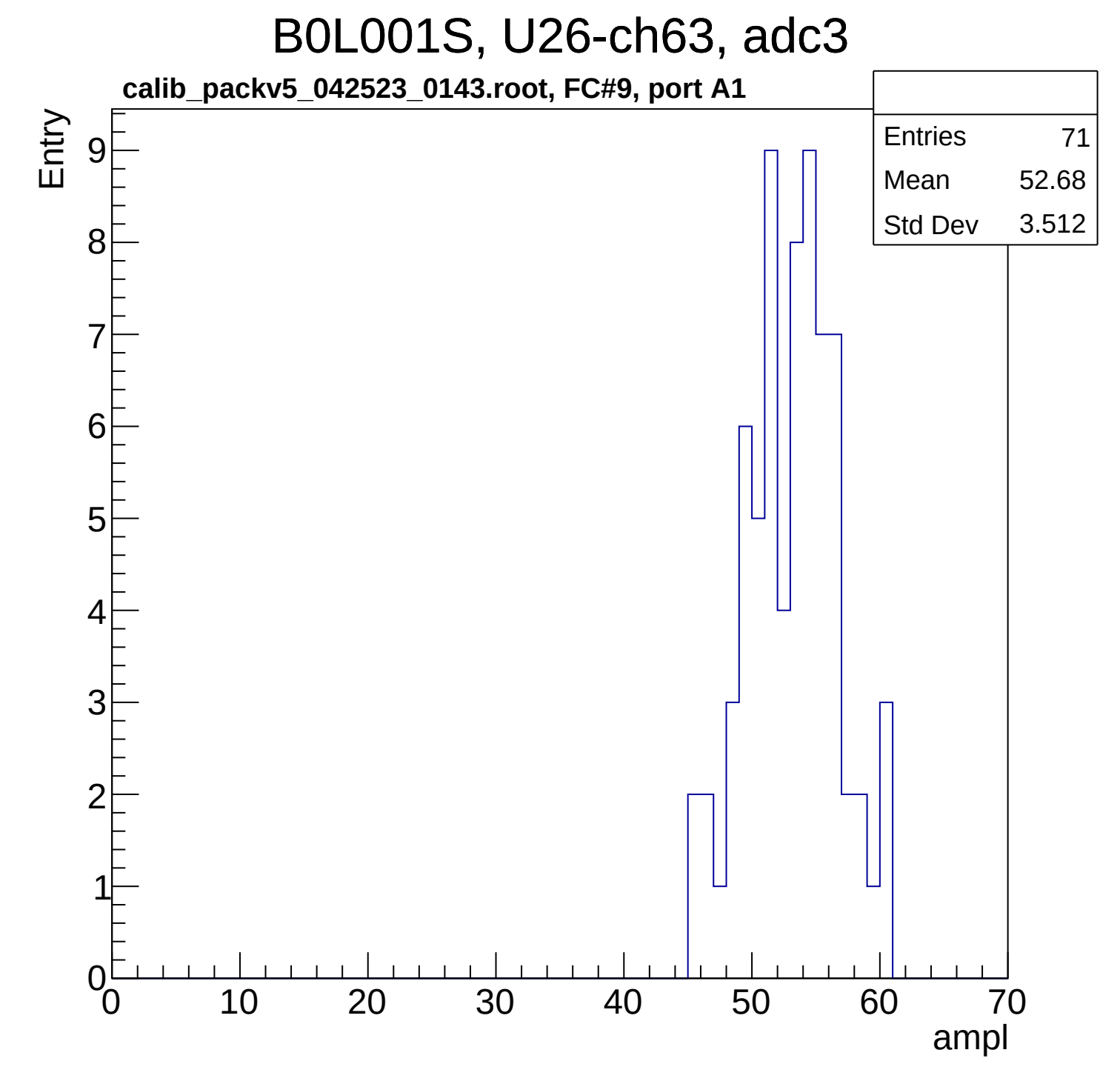
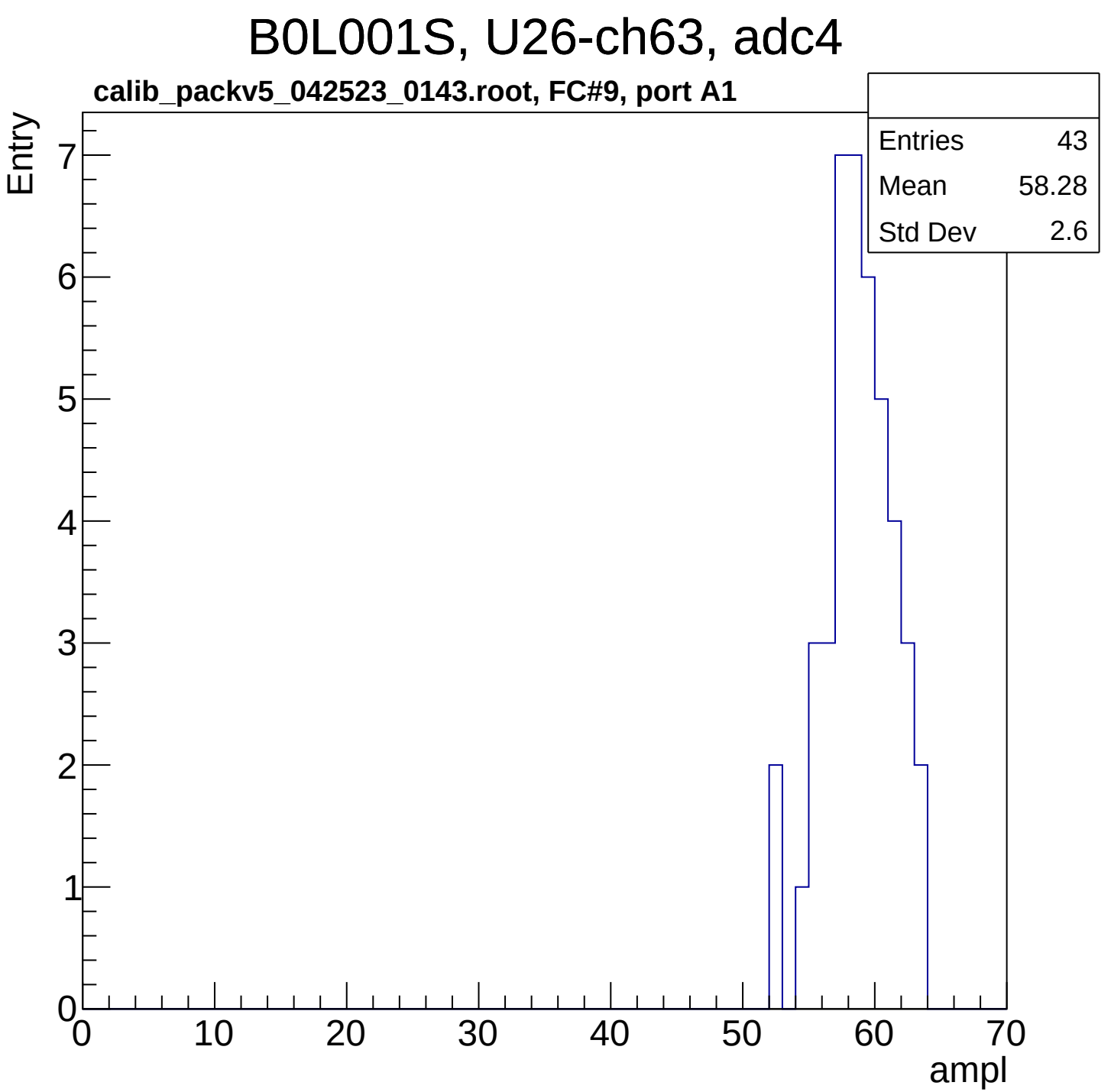
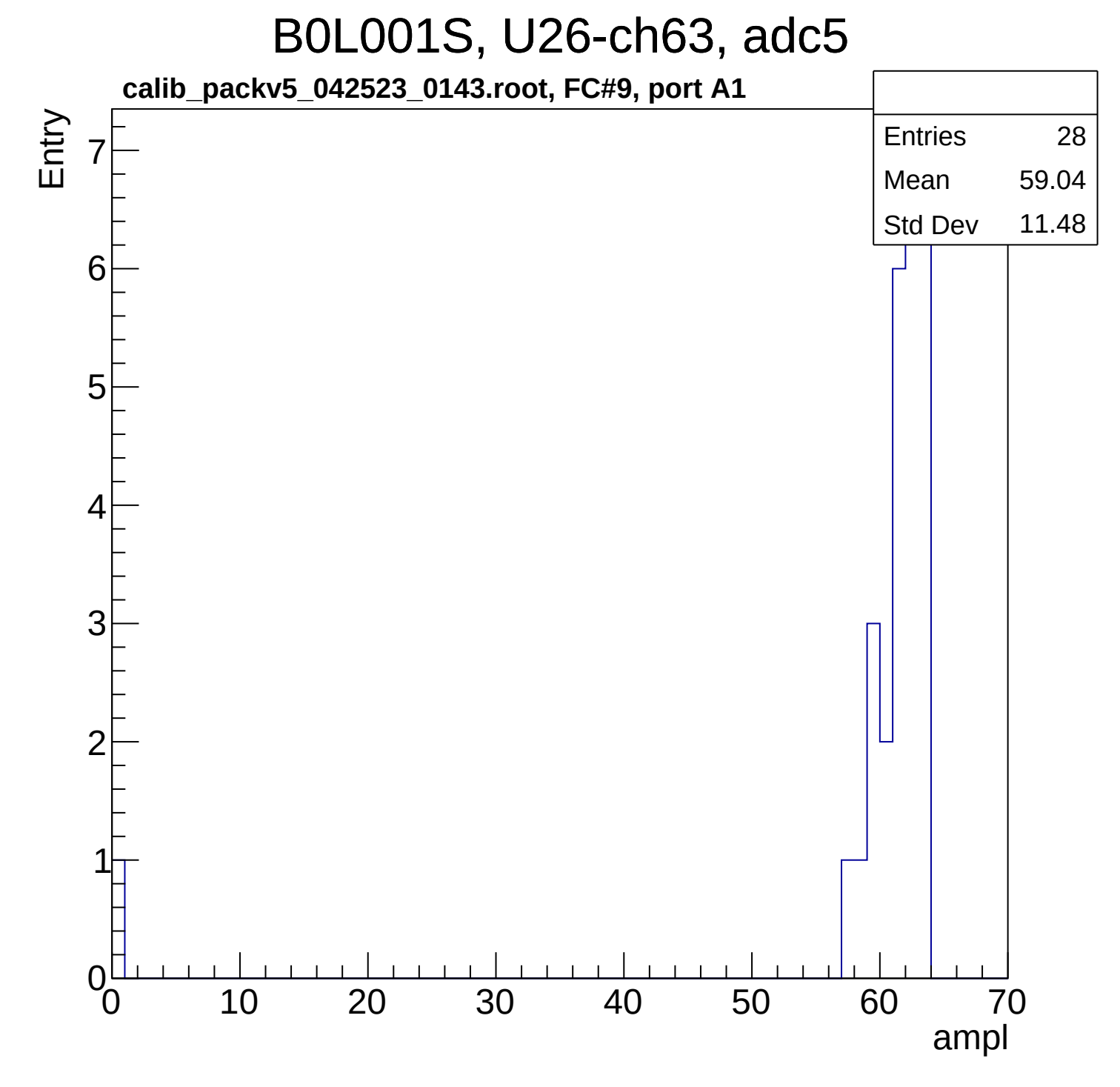
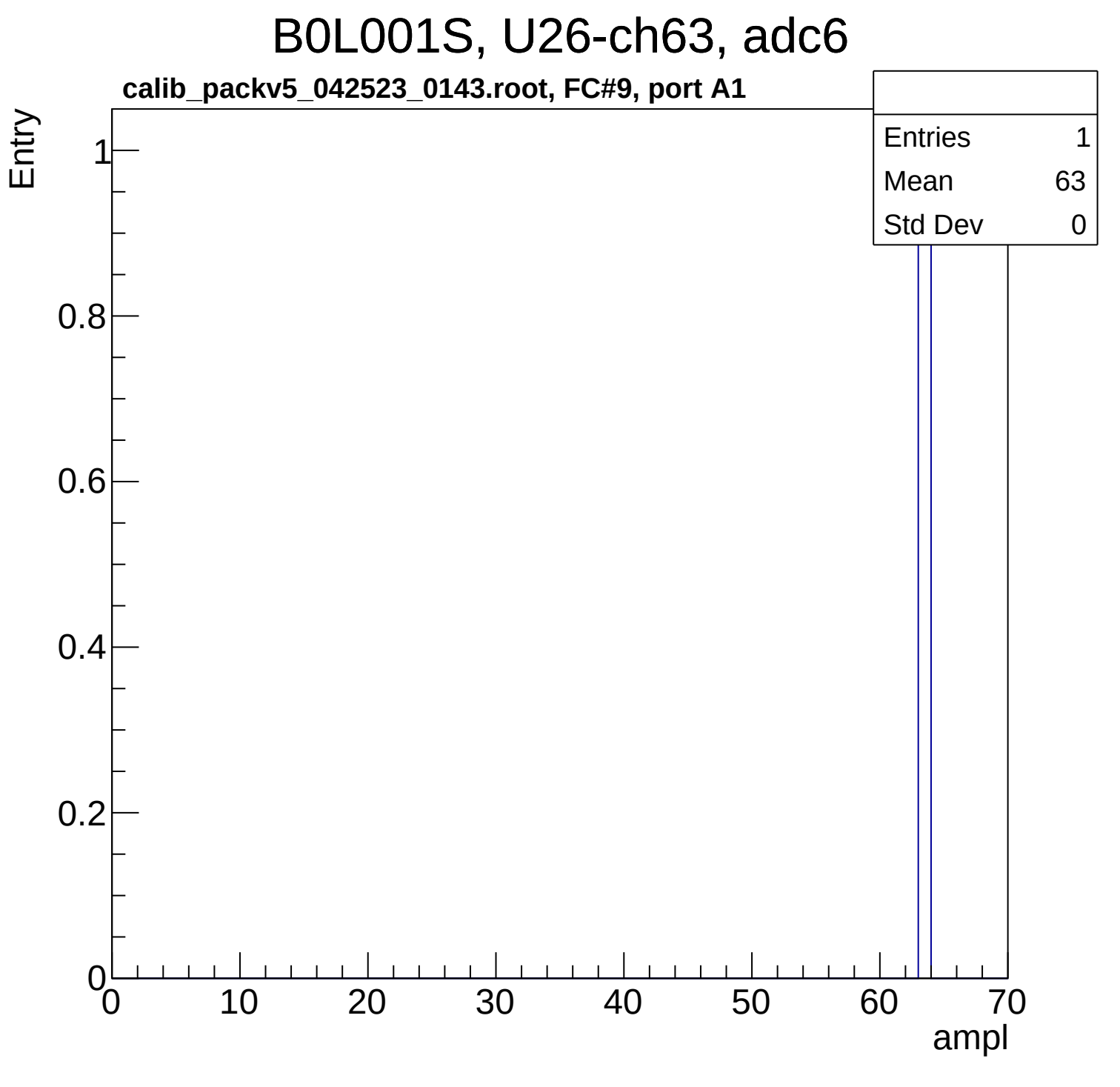
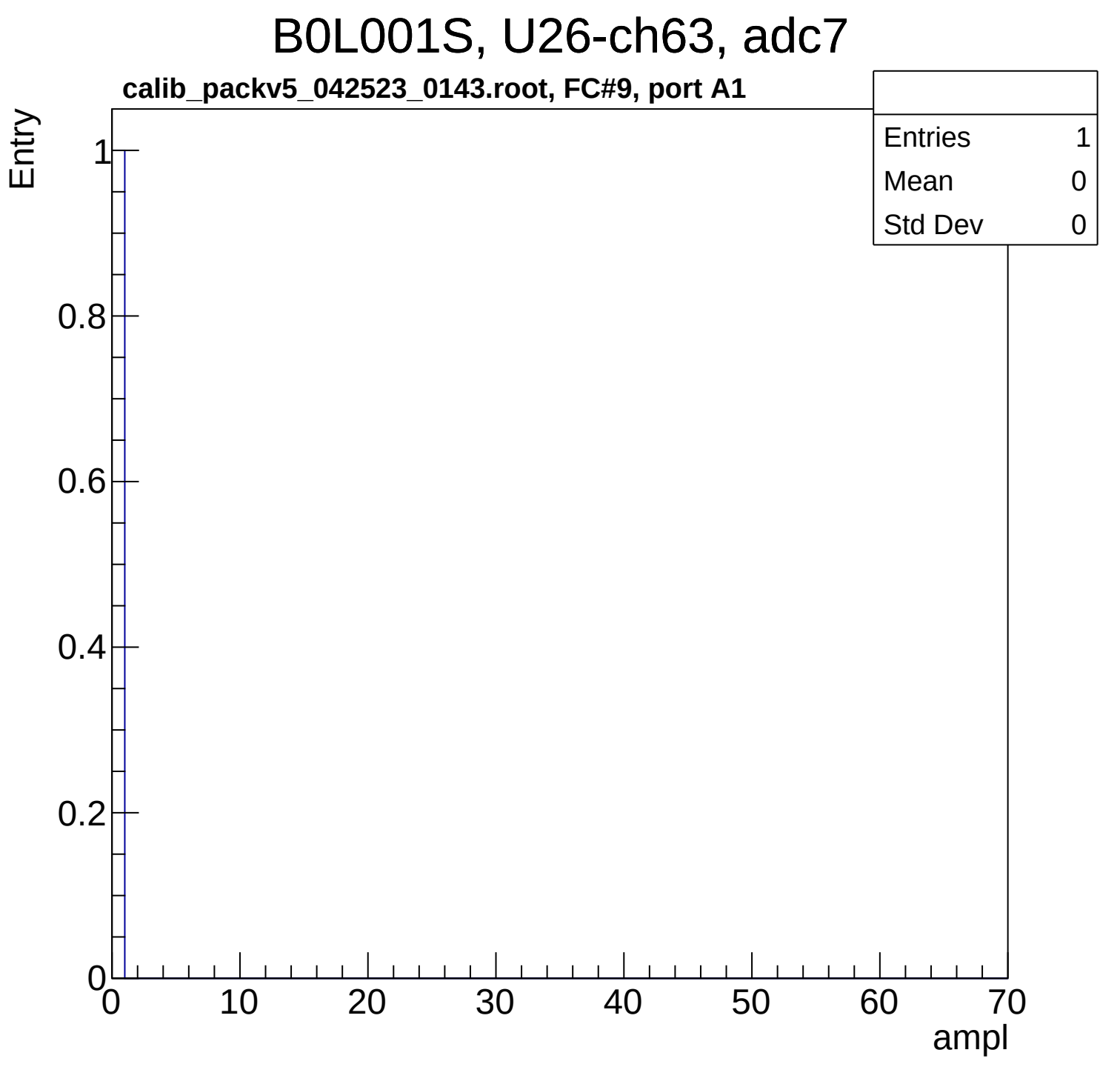
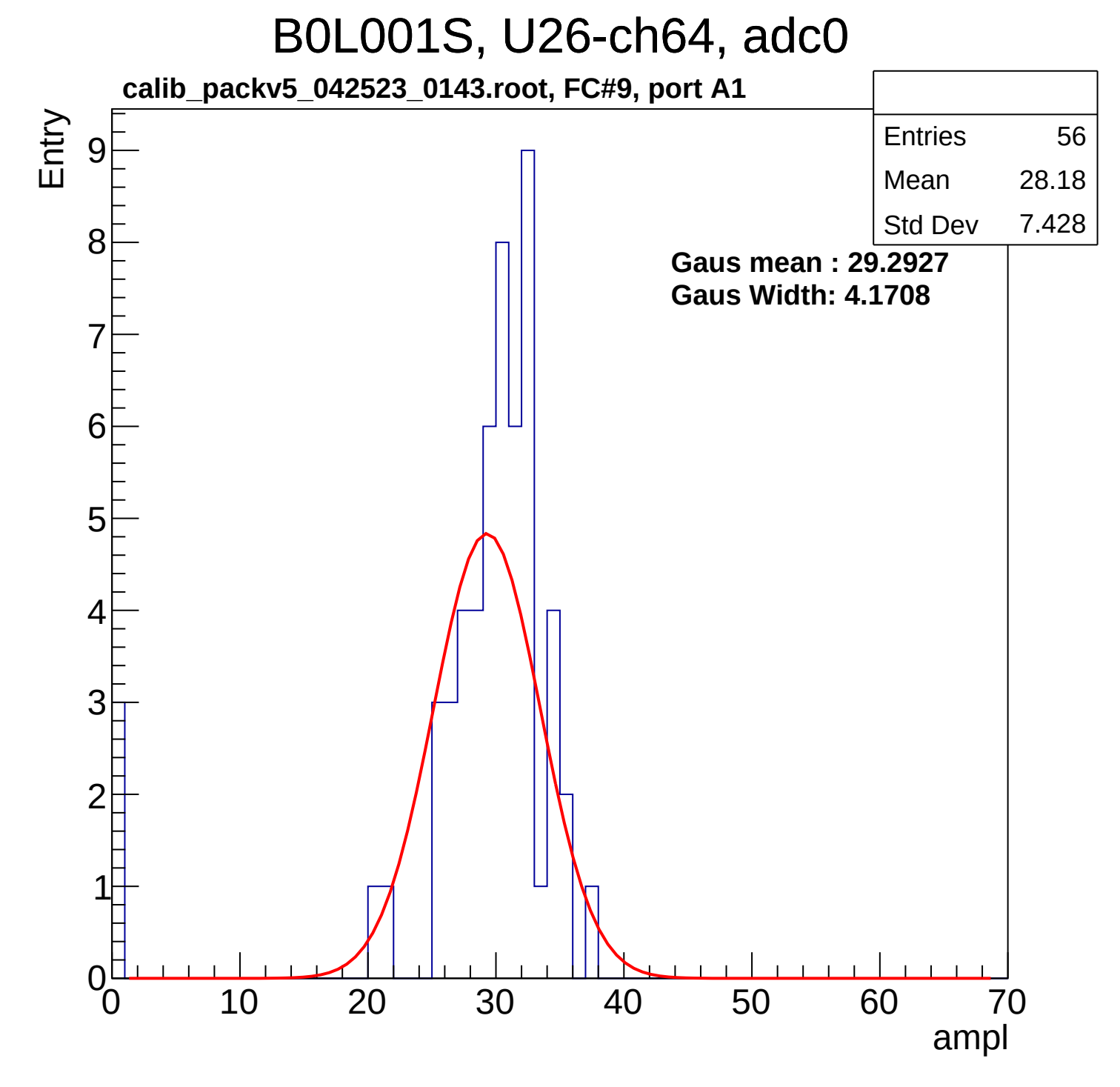

B0L001S, U26-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 52.68
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

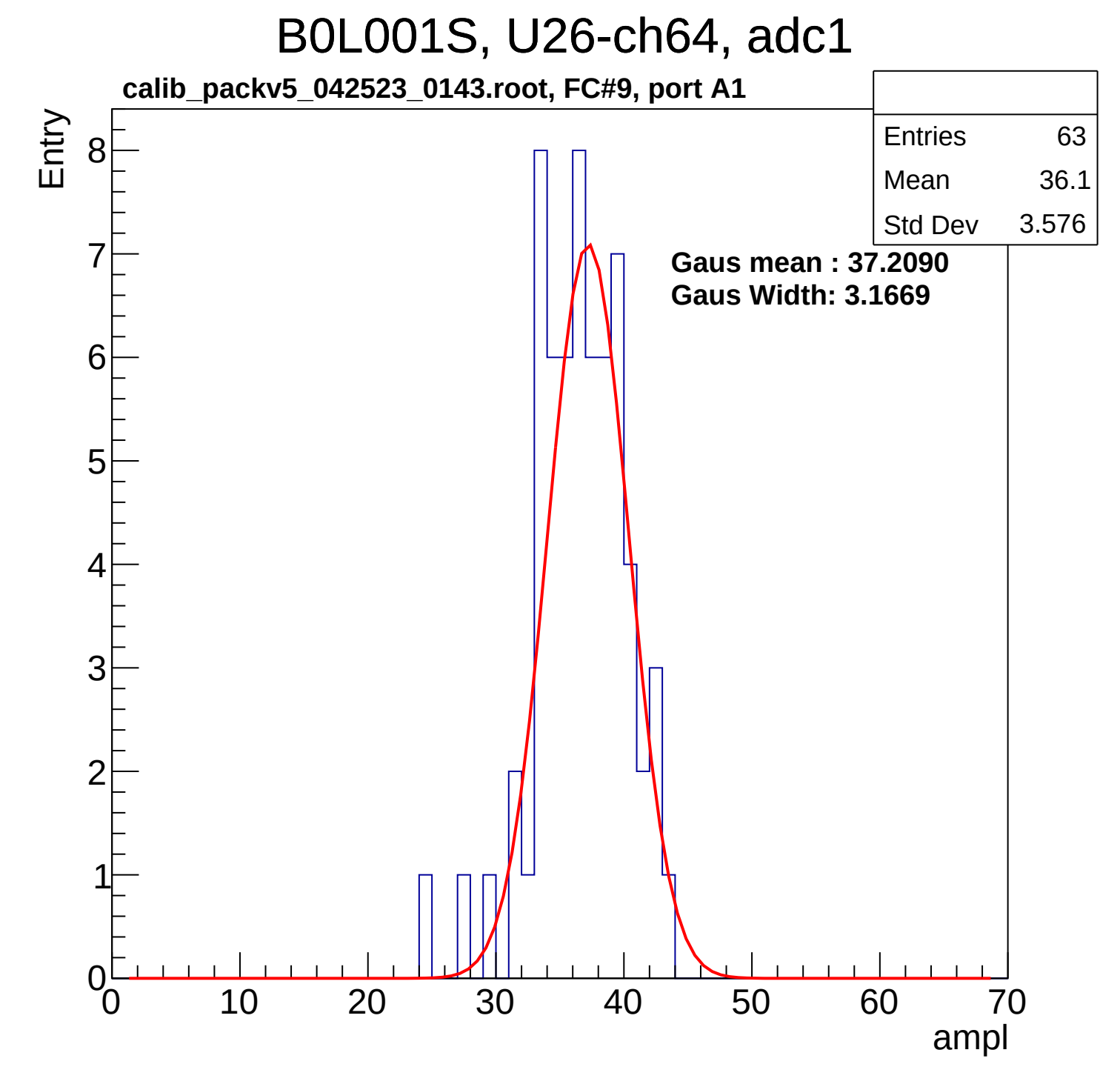

B0L001S, U26-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.28
Std Dev 2.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 59.04
Std Dev 11.48
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 28.18
Std Dev 7.428
Gaus mean : 29.2927
Gaus Width: 4.1708
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.1
Std Dev 3.576
Gaus mean : 37.2090
Gaus Width: 3.1669
Entry
ampl
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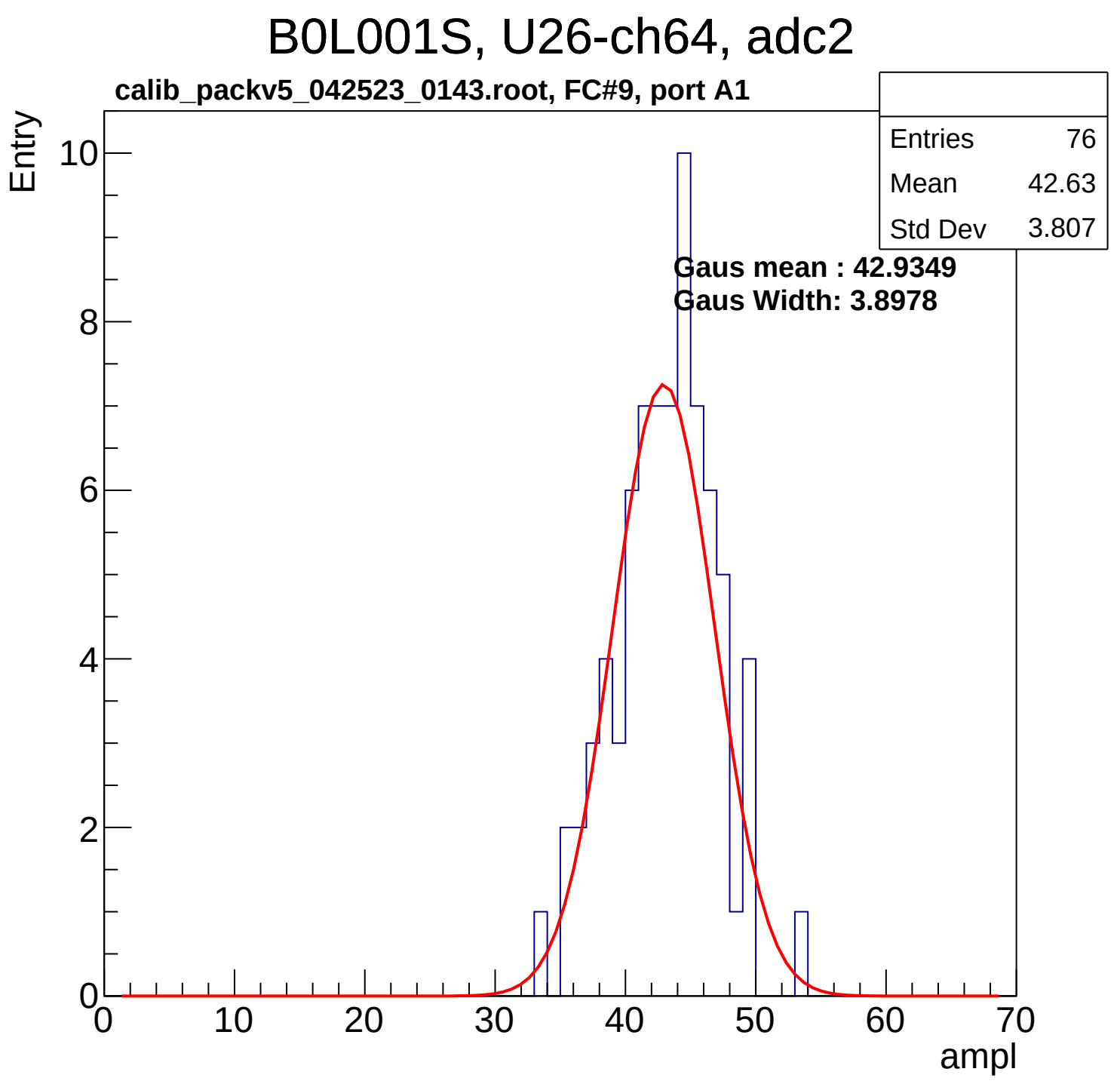
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 42.63
Std Dev 3.807
Gaus mean : 42.9349
Gaus Width: 3.8978
Entry
ampl
0
2
4
6
8
10
0
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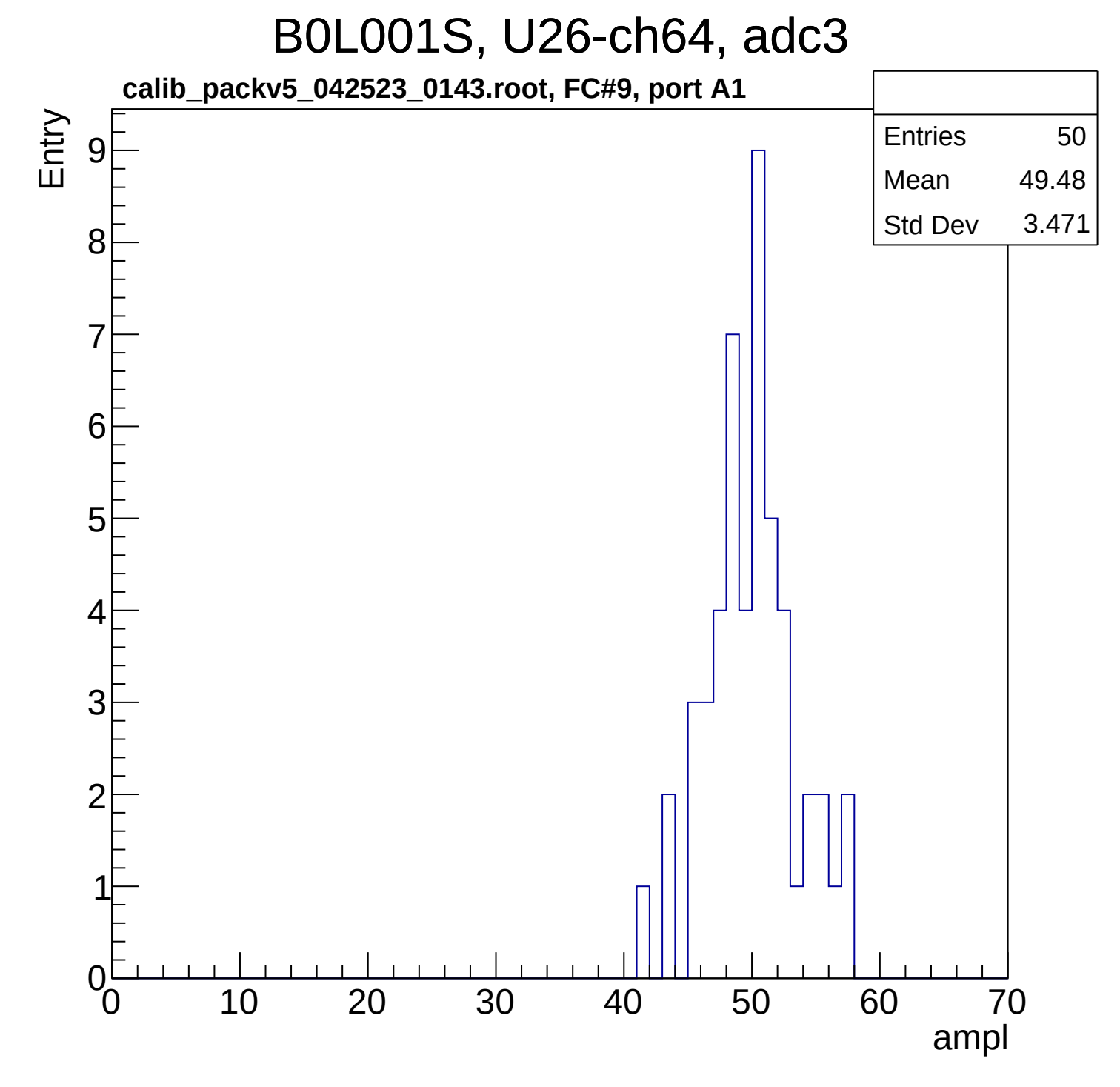
20
30
40
50
60
70


B0L001S, U26-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 49.48
Std Dev 3.471
Entry
ampl
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2
3
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6
7
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0
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60
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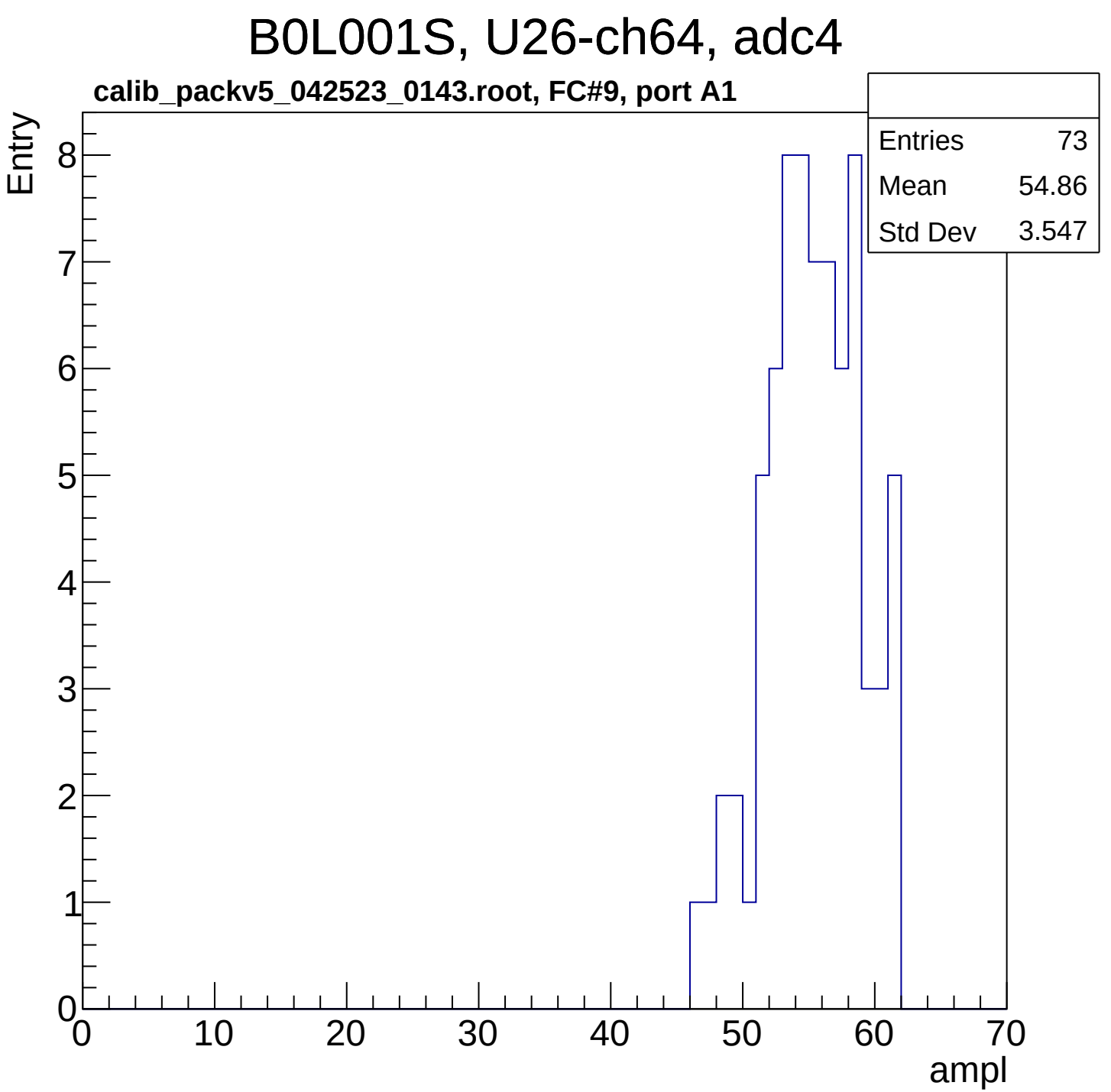
B0L001S, U26-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 54.86
Std Dev 3.547
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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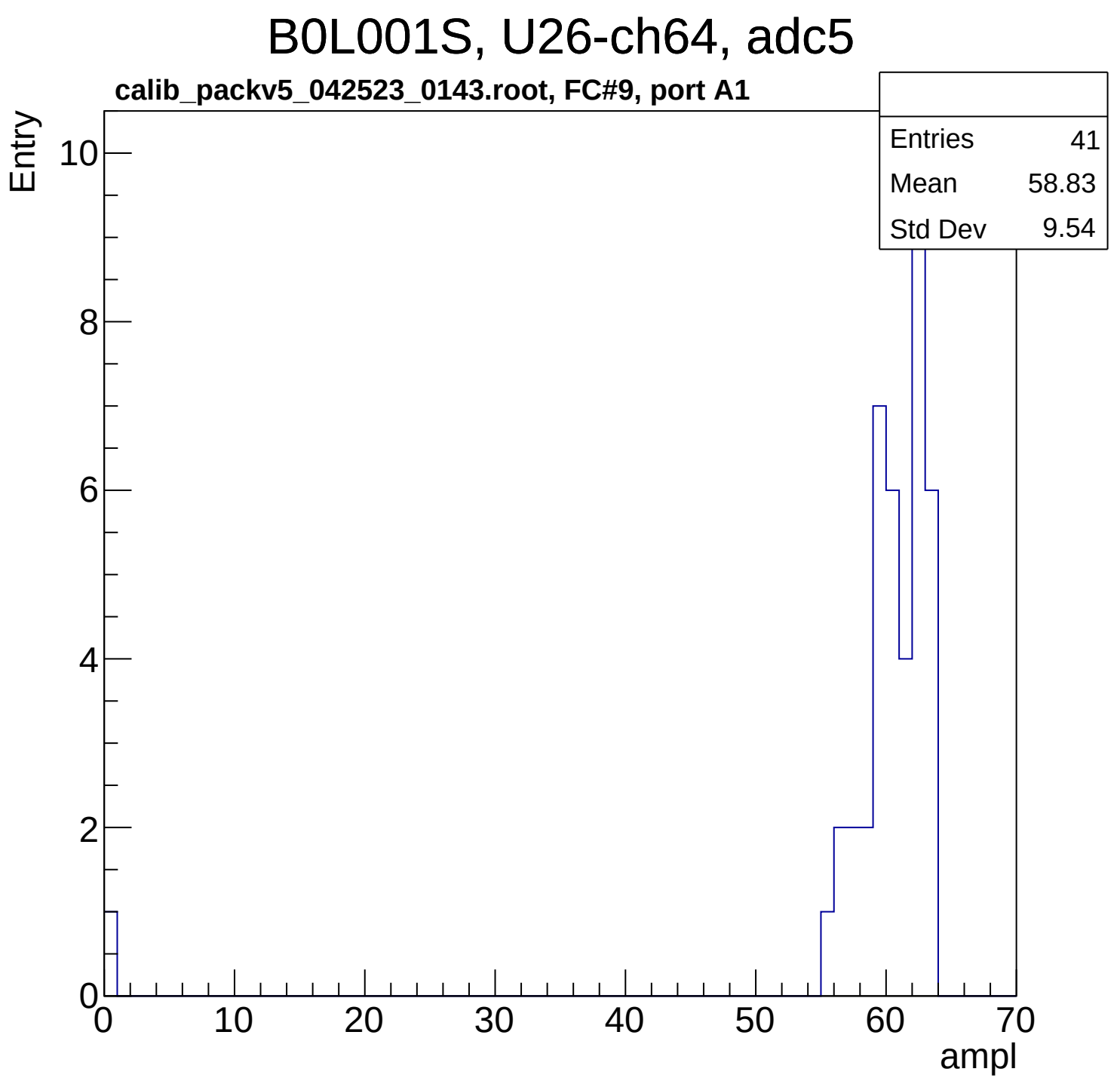
B0L001S, U26-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.83
Std Dev 9.54
Entry
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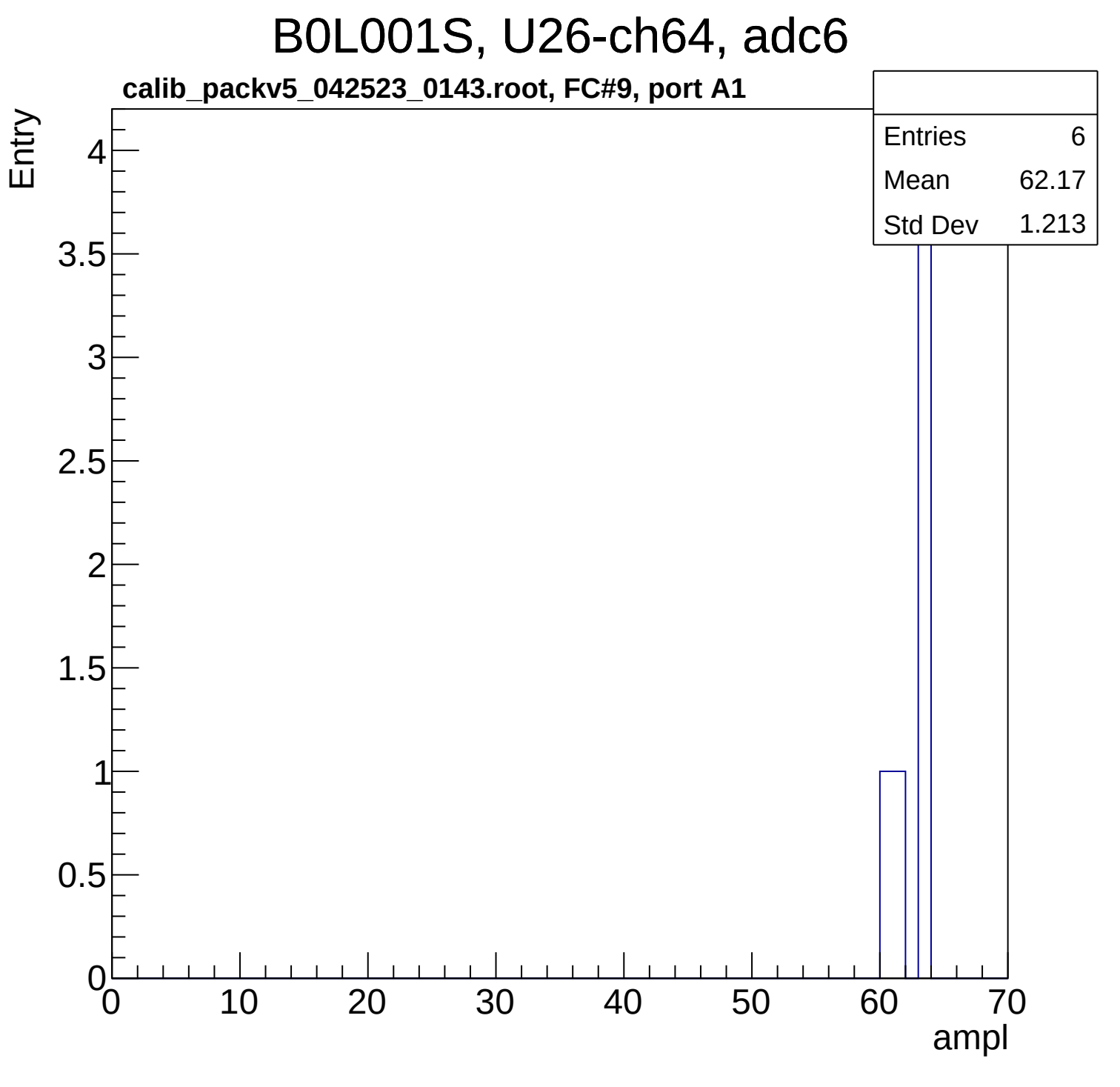
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
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0.5
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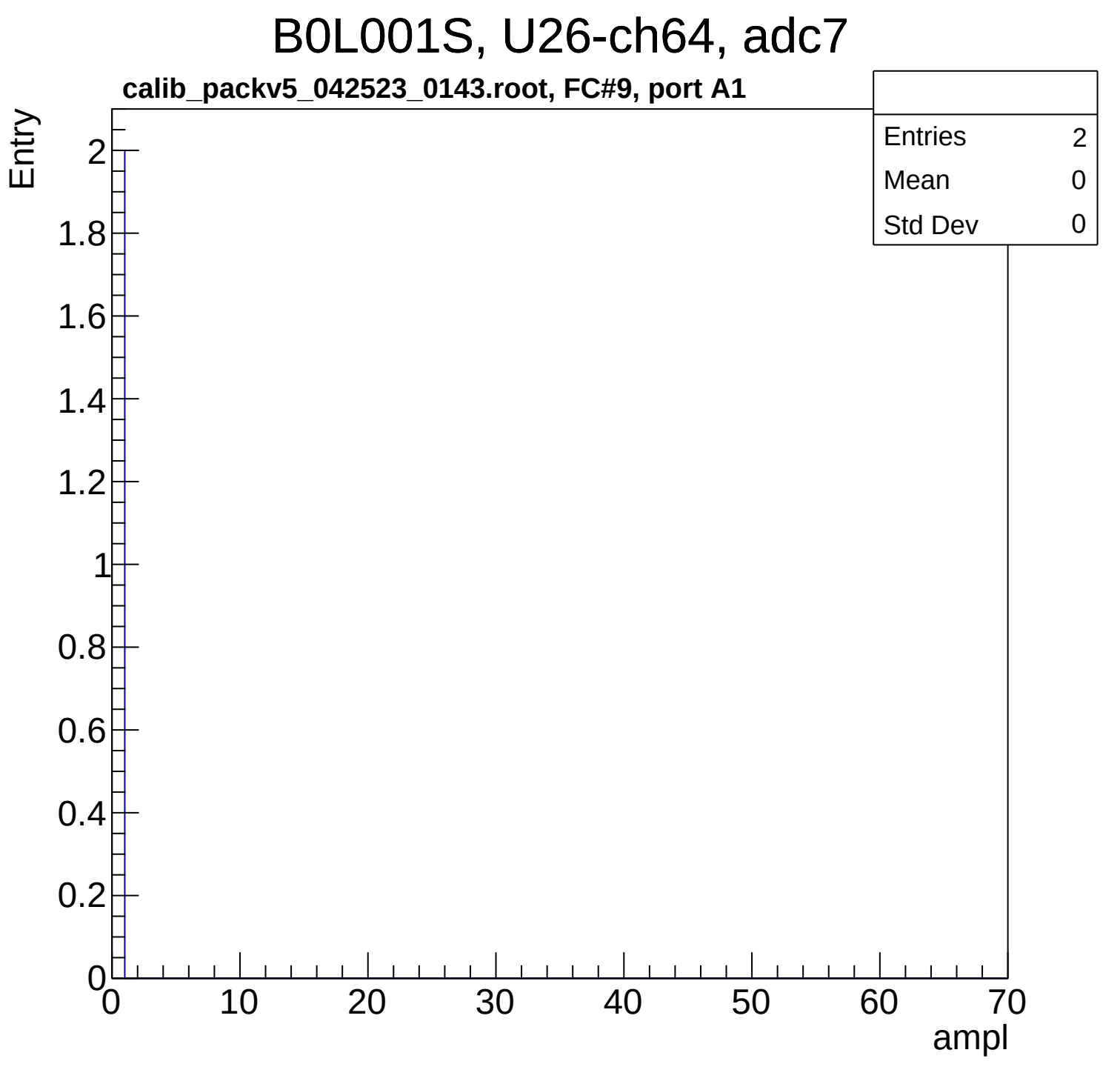
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U26-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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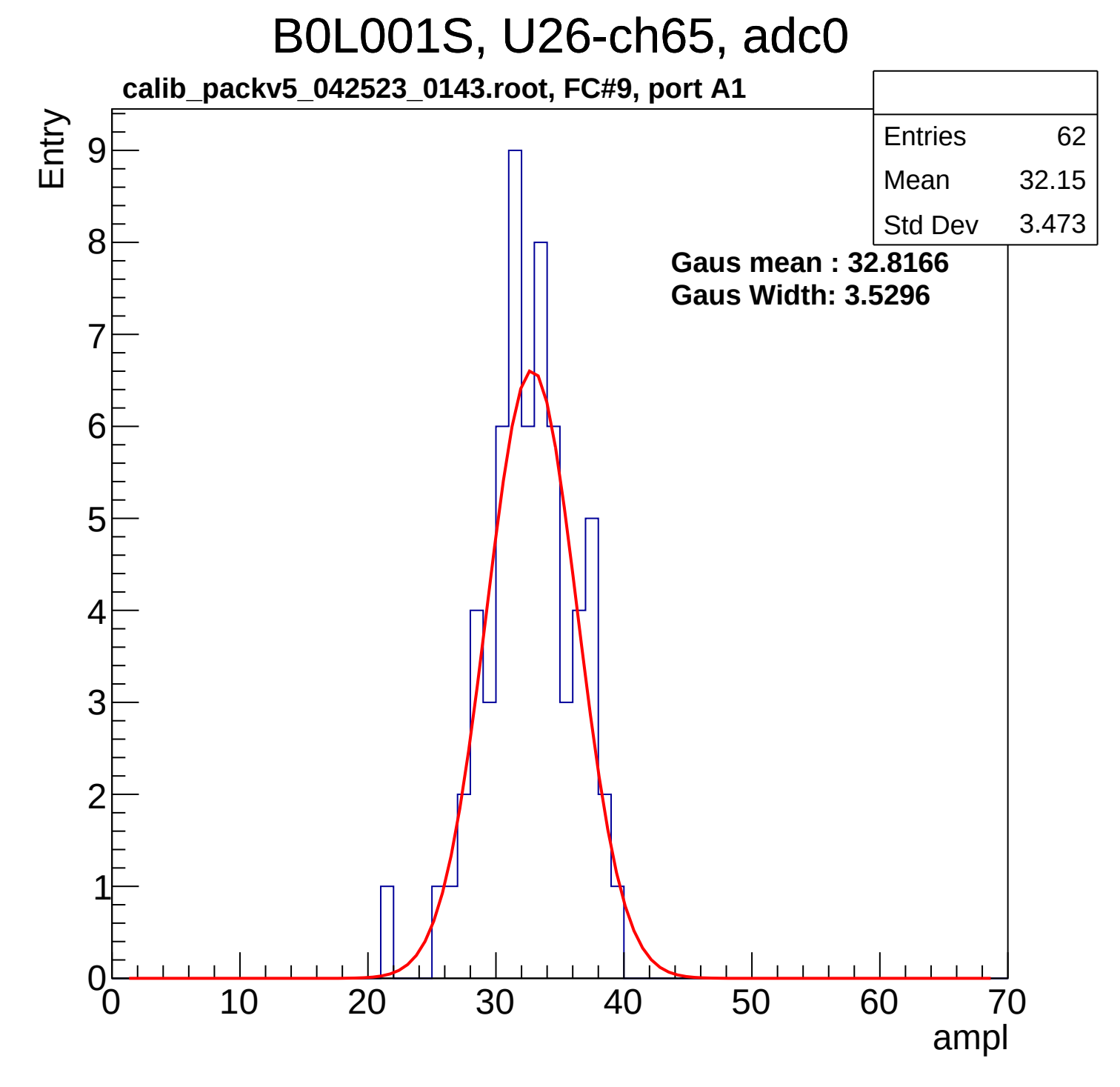
50
60
70
ampl


B0L001S, U26-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 32.15
Std Dev 3.473
Gaus mean : 32.8166
Gaus Width: 3.5296
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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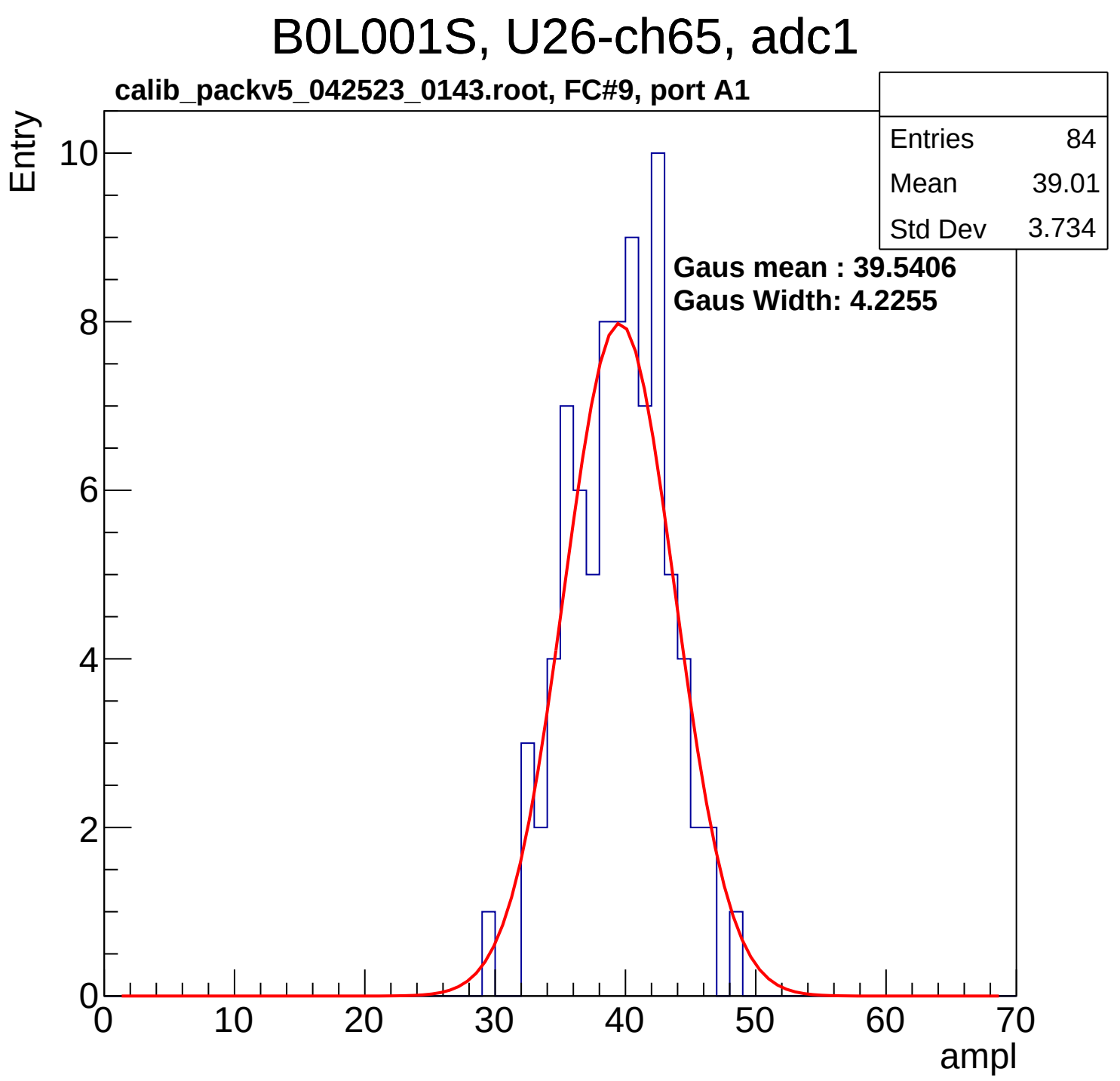
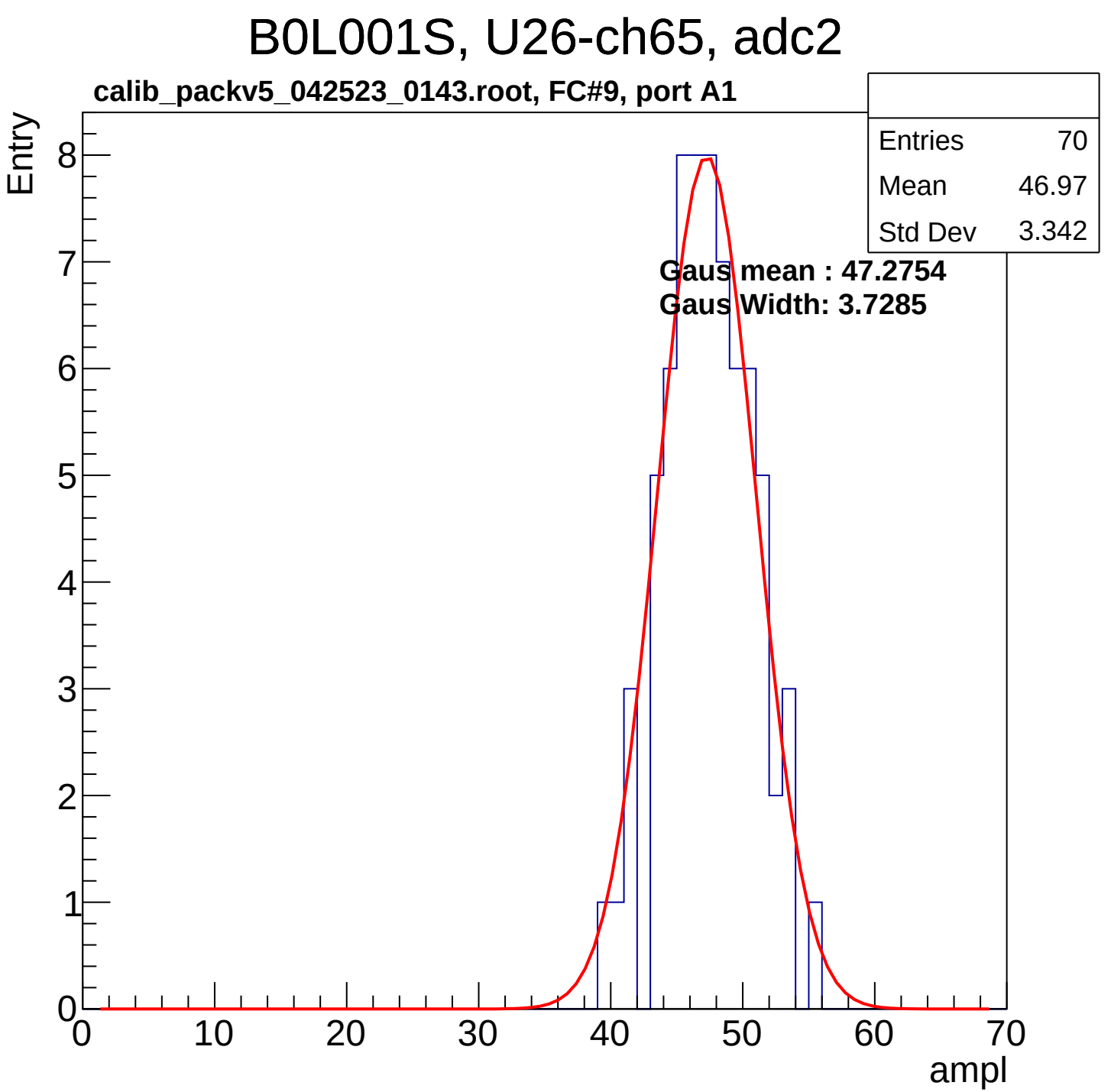

B0L001S, U26-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 39.01
Std Dev 3.734
Gaus mean : 39.5406
Gaus Width: 4.2255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 46.97
Std Dev 3.342
Gaus mean : 47.2754
Gaus Width: 3.7285
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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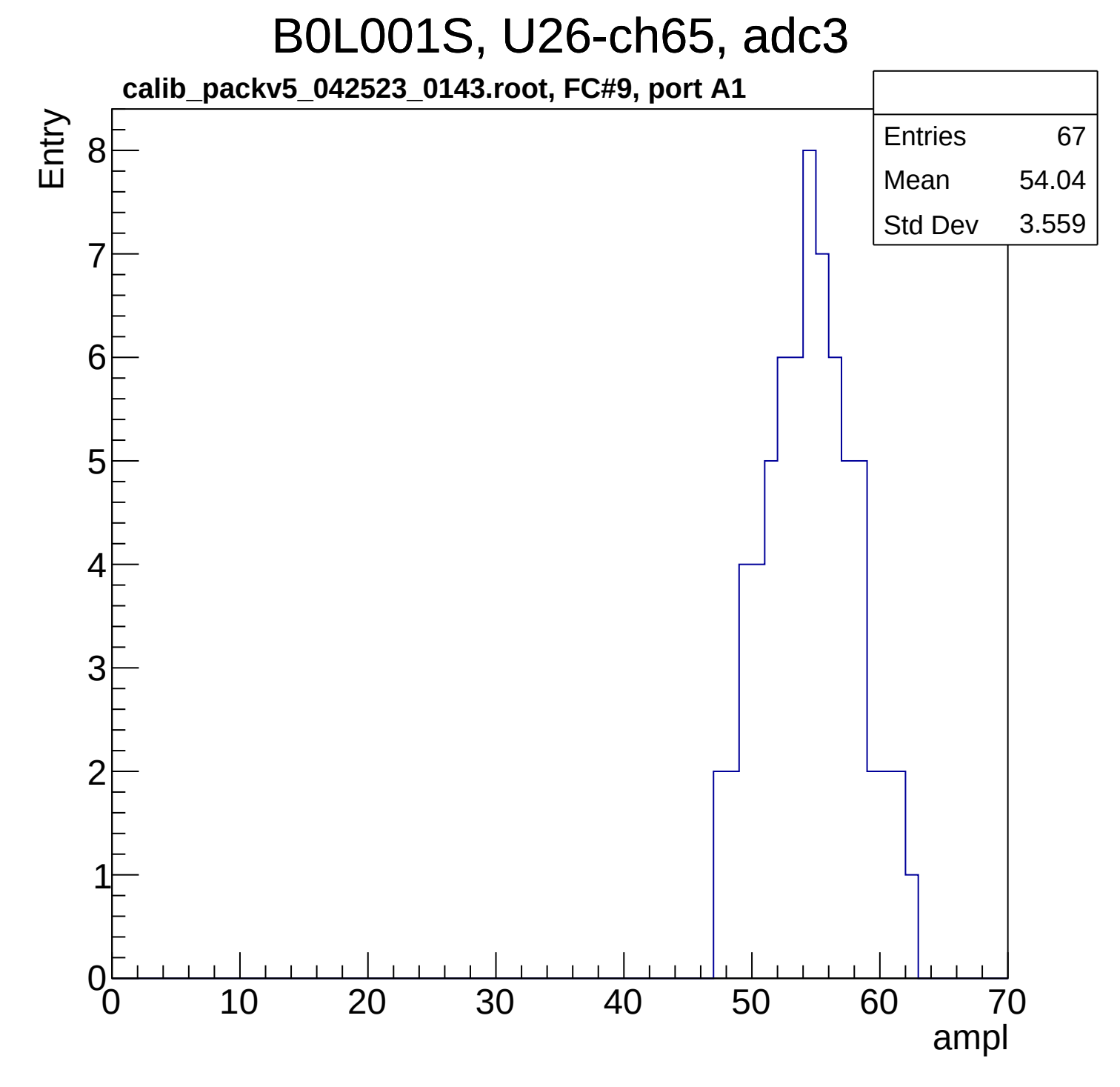
B0L001S, U26-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 54.04
Std Dev 3.559
Entry
ampl
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1
2
3
4
5
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7
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0
10
20
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40
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60
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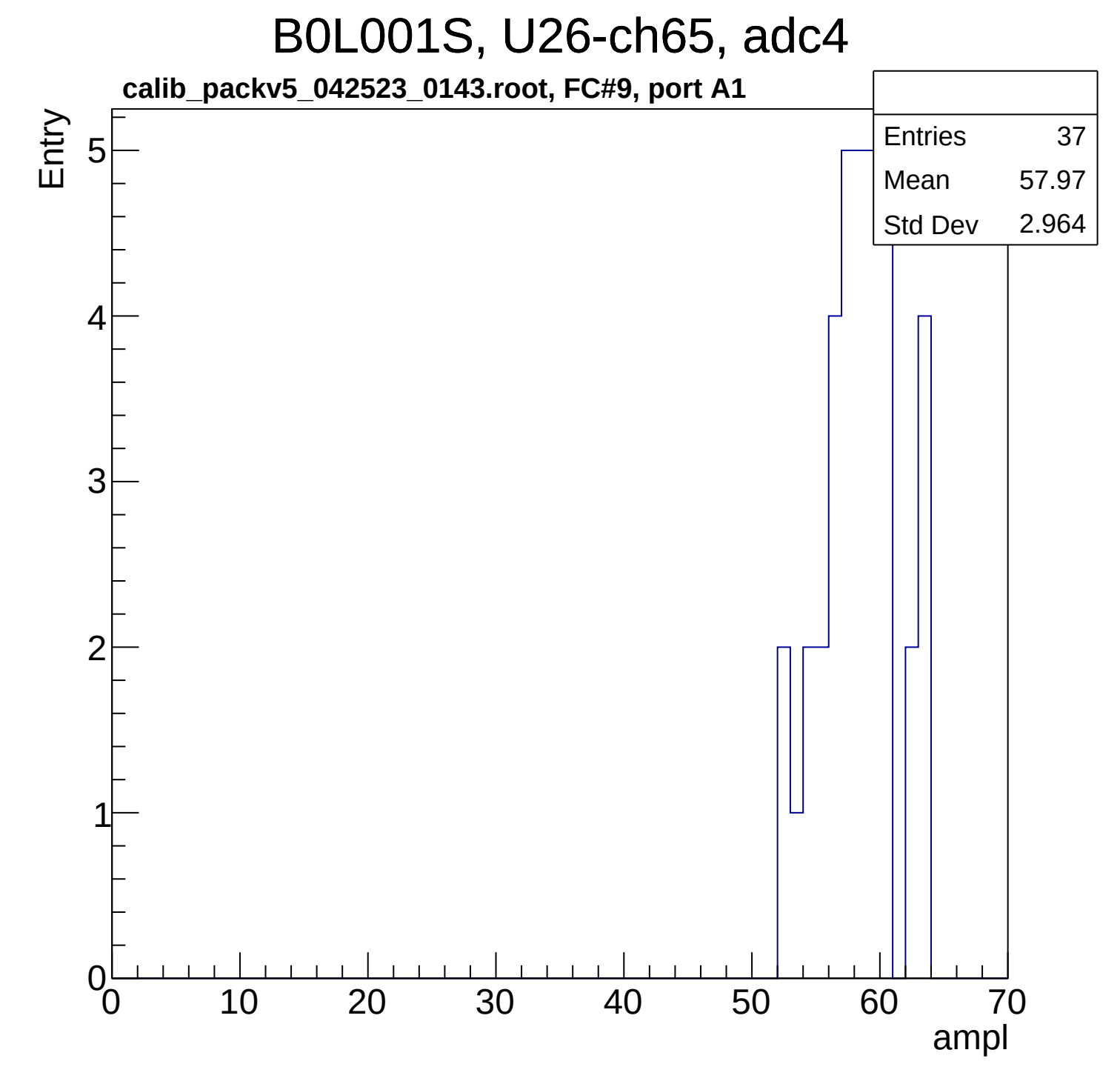
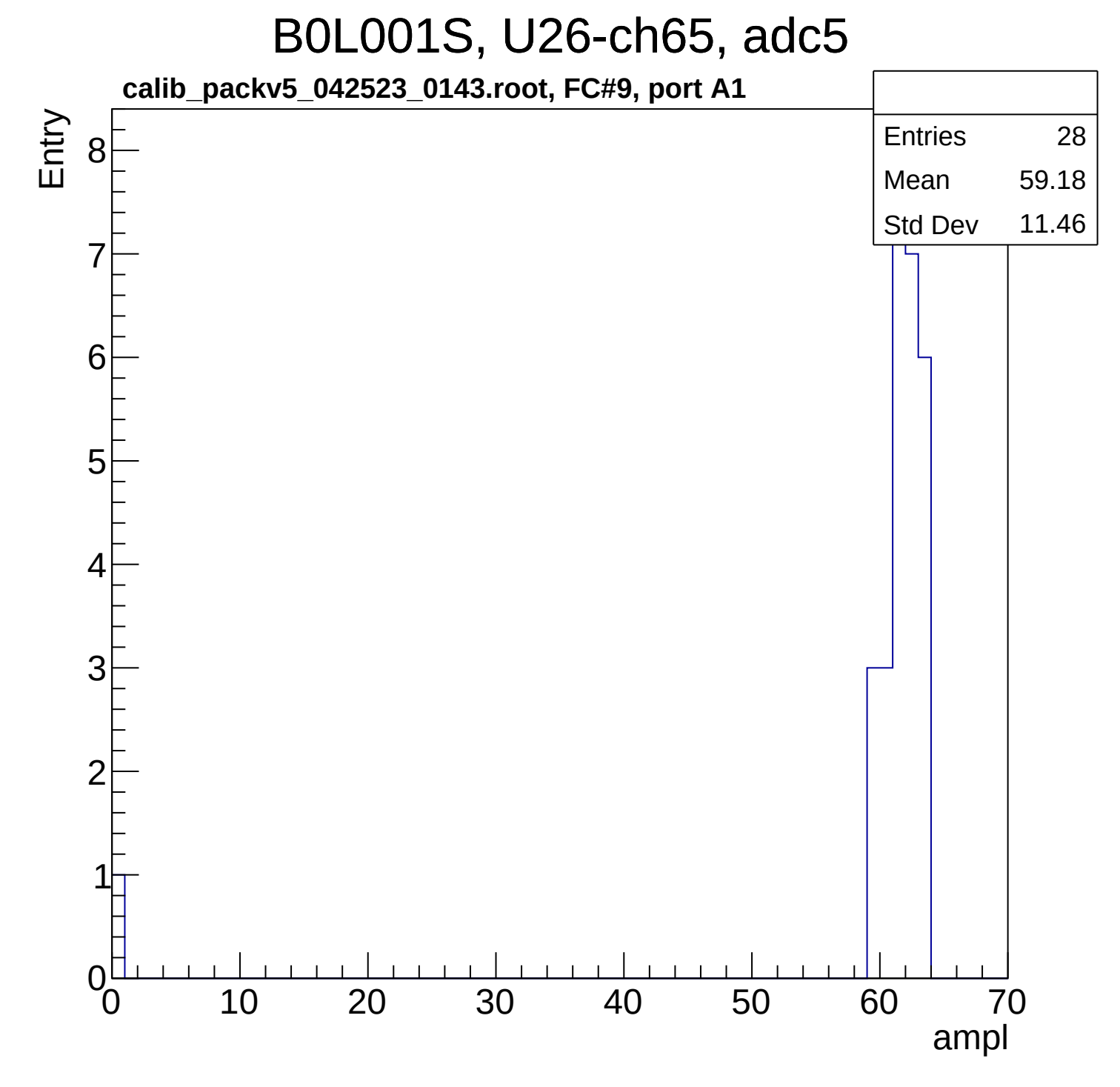
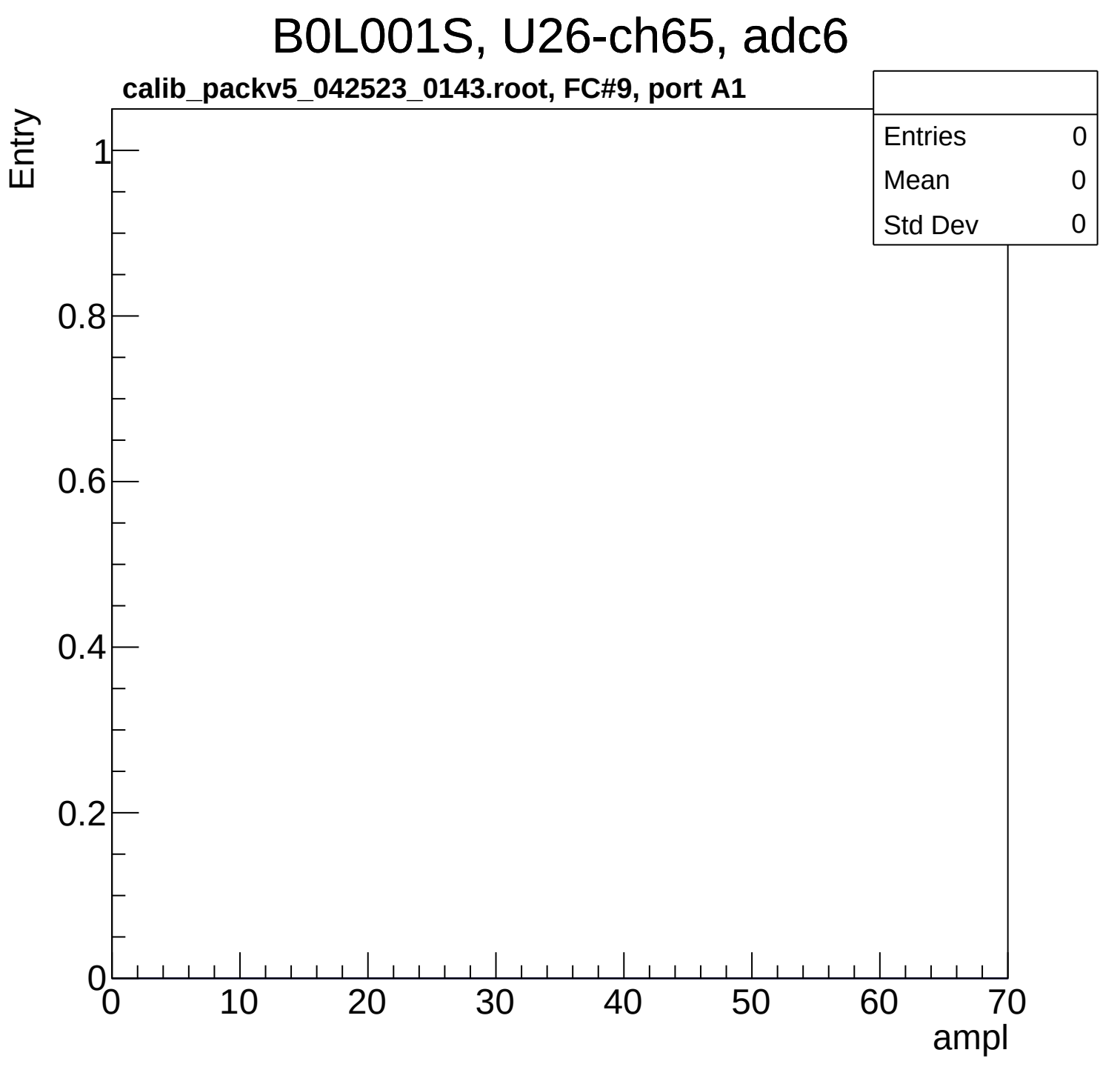

B0L001S, U26-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 57.97
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U26-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 59.18
Std Dev 11.46
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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50
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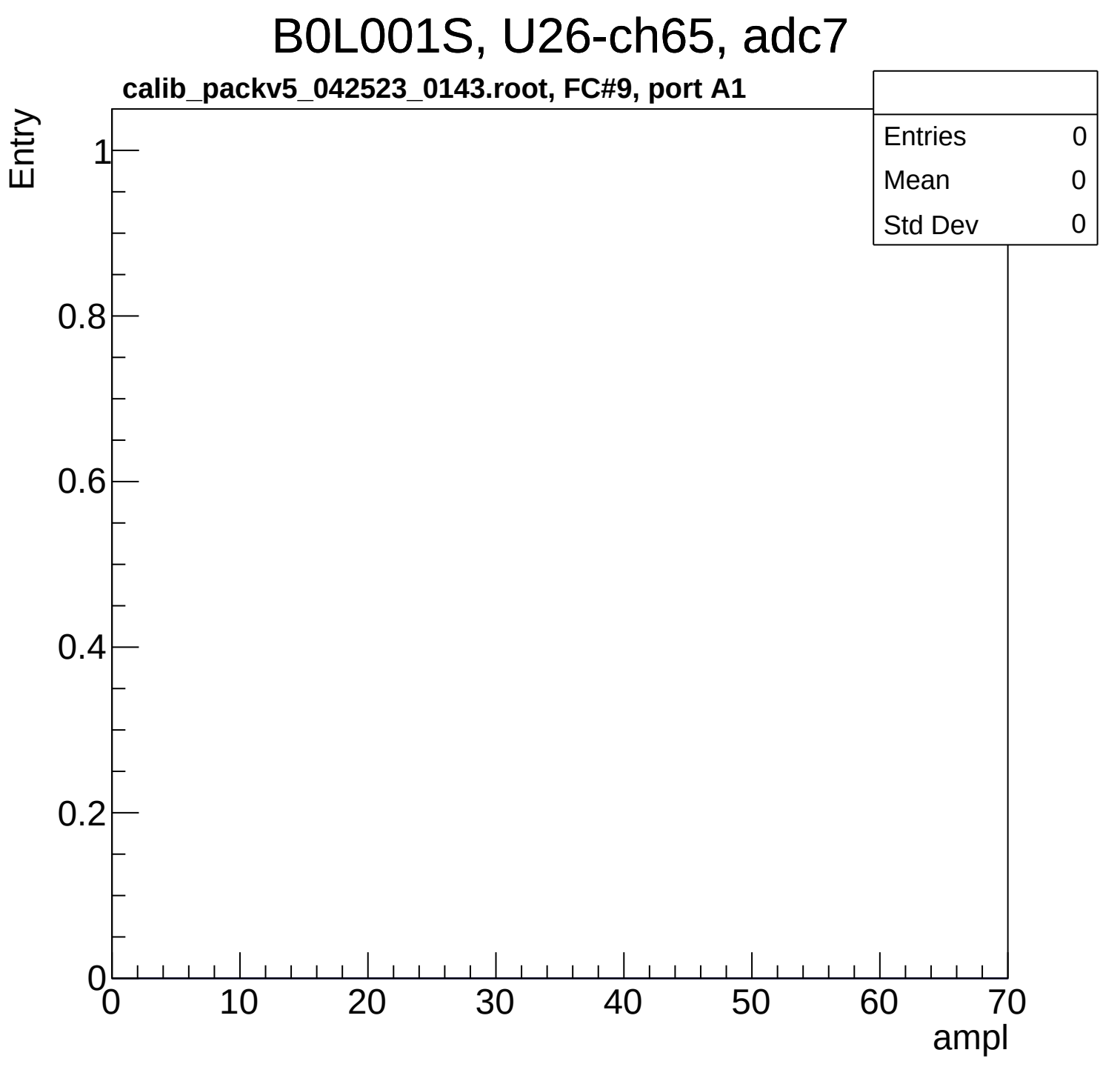
70
ampl


B0L001S, U26-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
20
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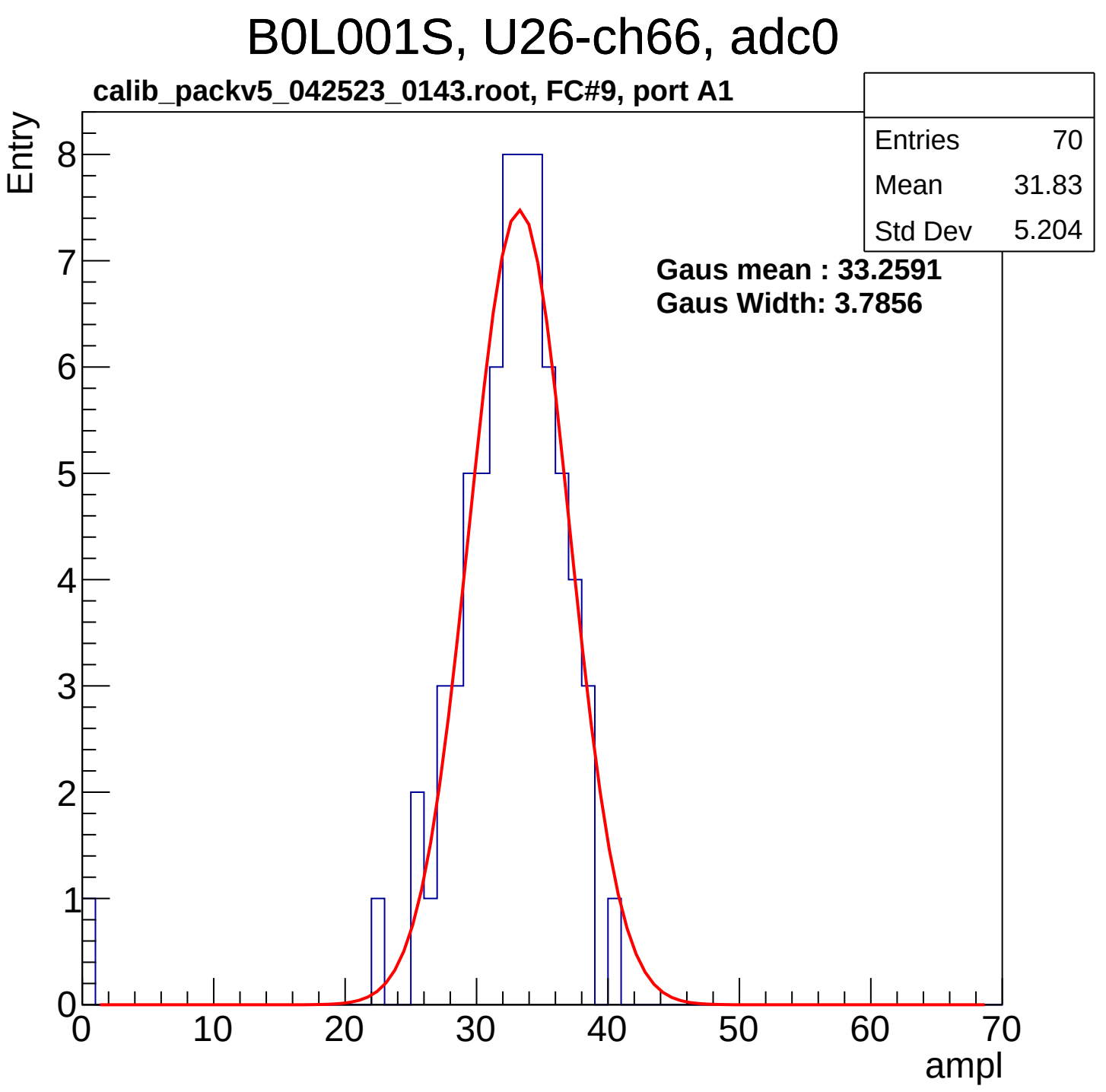
40
50
60
70
ampl


B0L001S, U26-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.83
Std Dev 5.204
Gaus mean : 33.2591
Gaus Width: 3.7856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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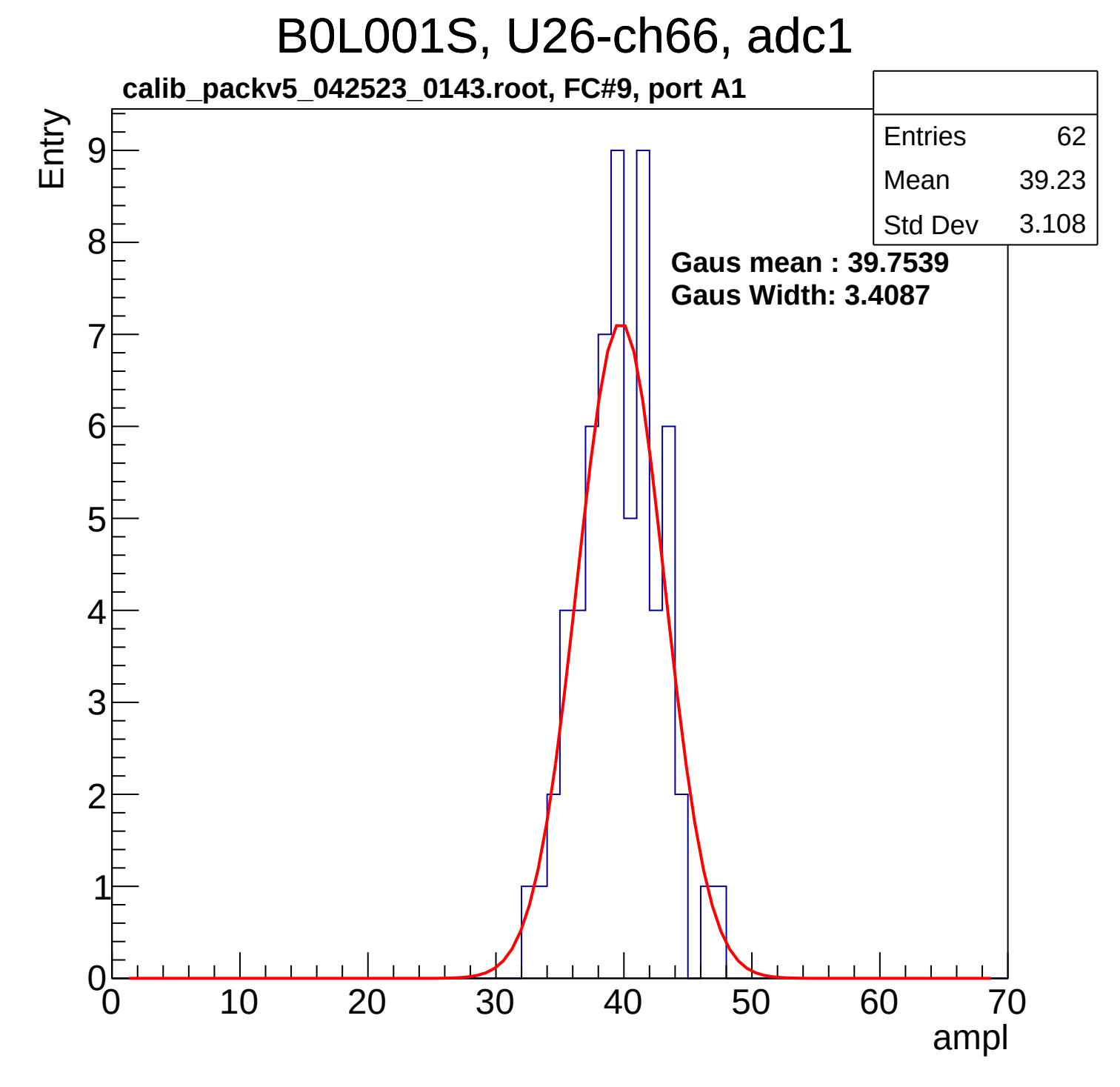
B0L001S, U26-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 39.23
Std Dev 3.108
Gaus mean : 39.7539
Gaus Width: 3.4087
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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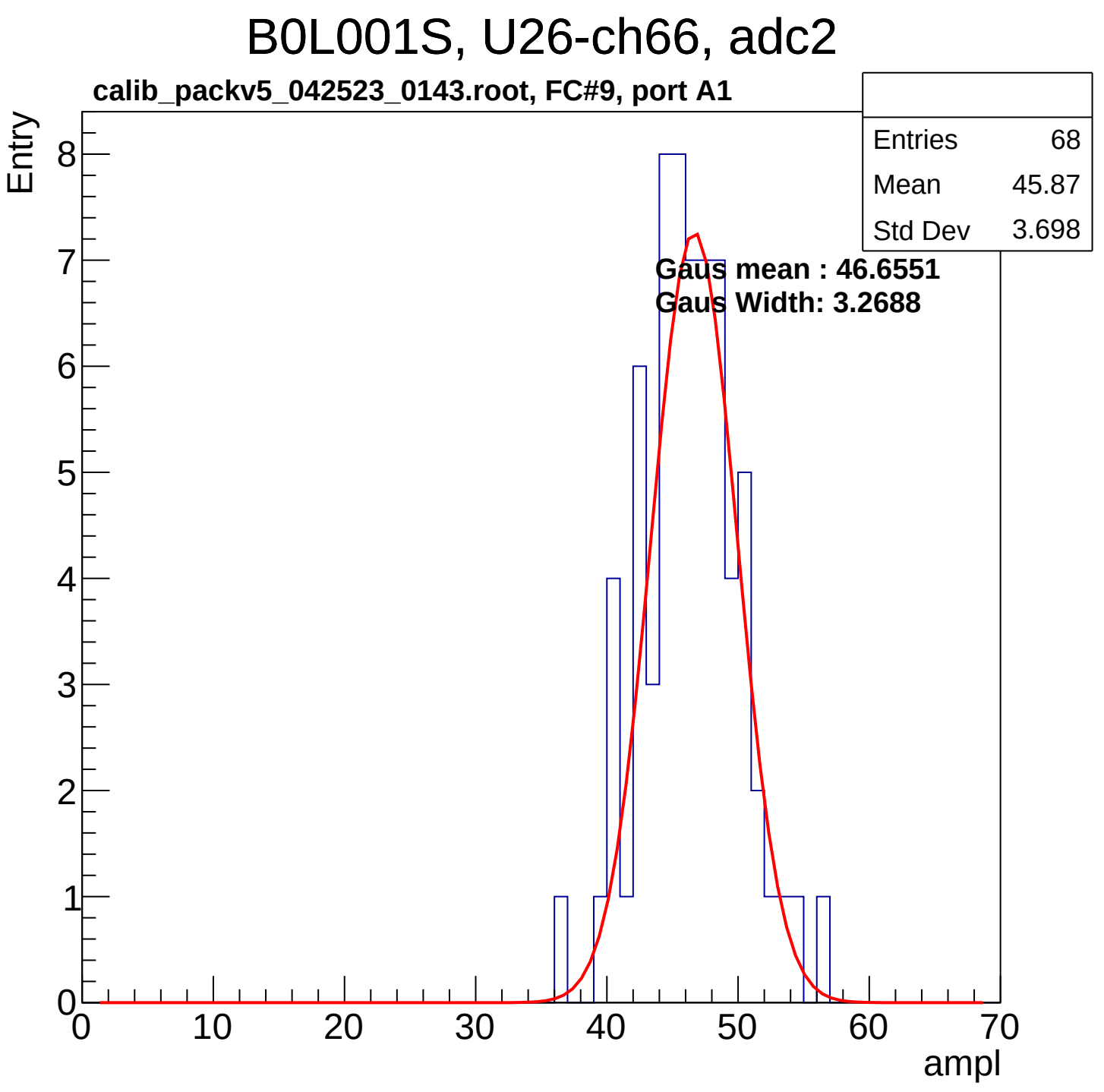
B0L001S, U26-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 45.87
Std Dev 3.698
Gaus mean : 46.6551
Gaus Width: 3.2688
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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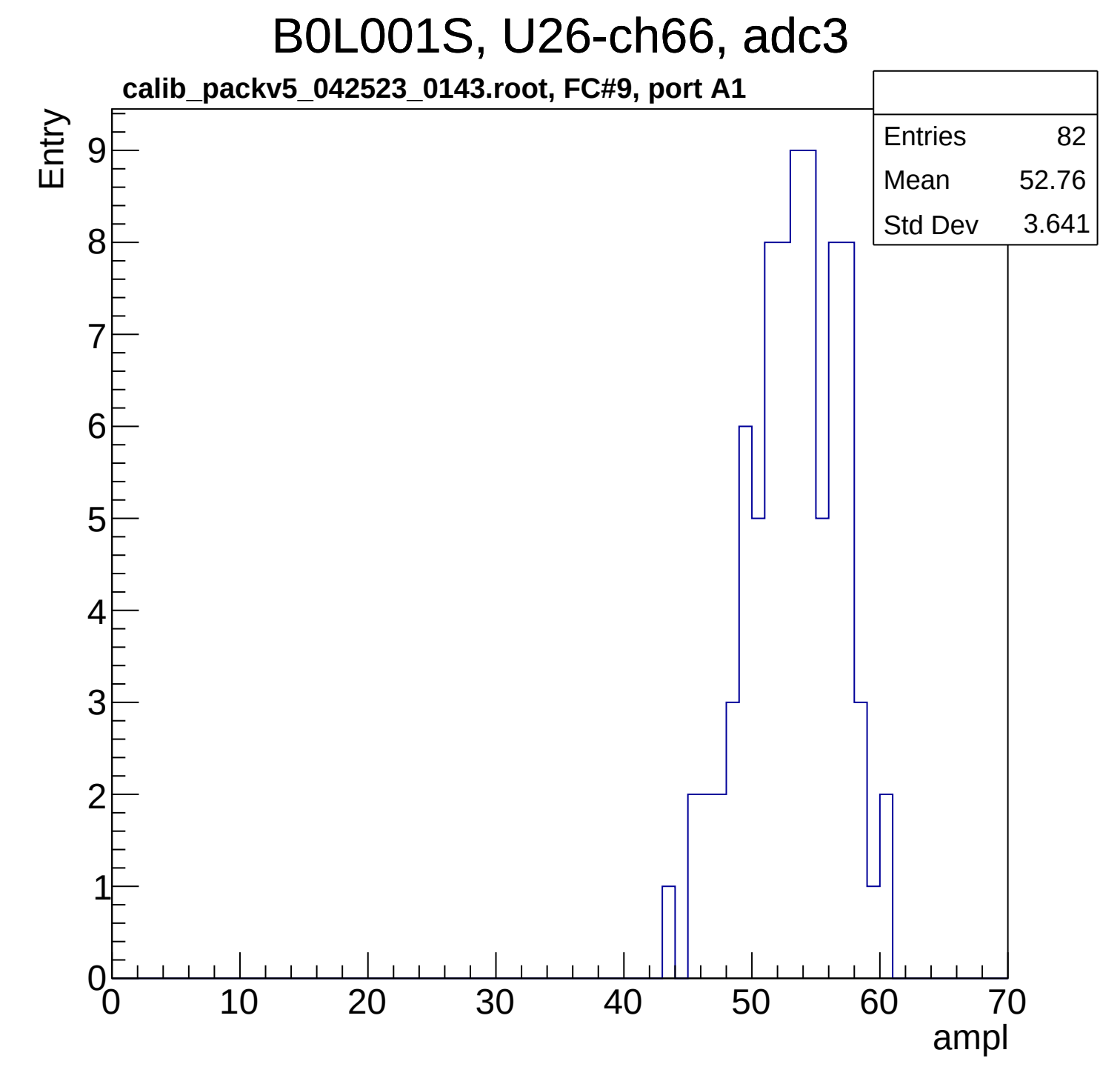
B0L001S, U26-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 52.76
Std Dev 3.641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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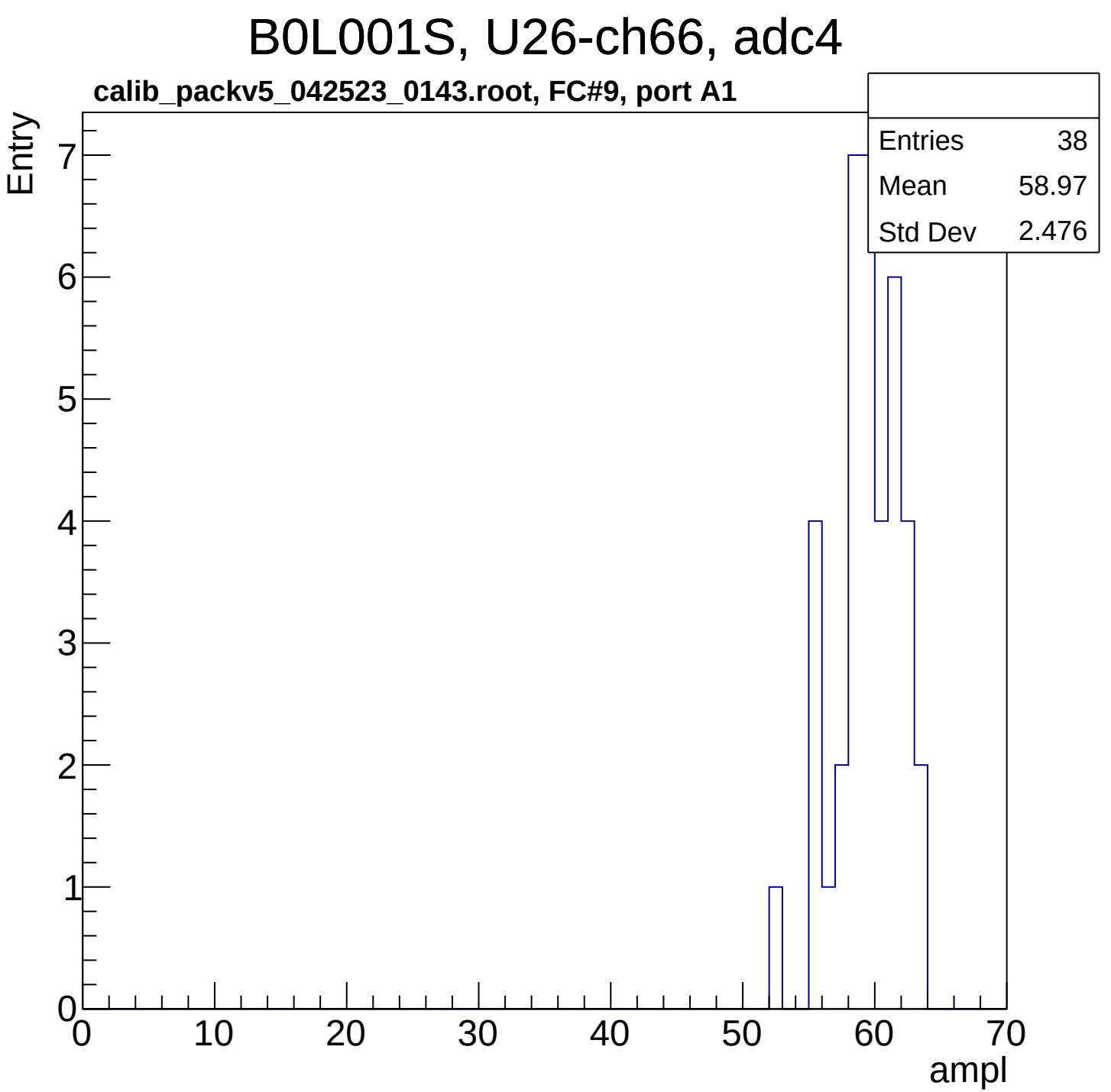
B0L001S, U26-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.97
Std Dev 2.476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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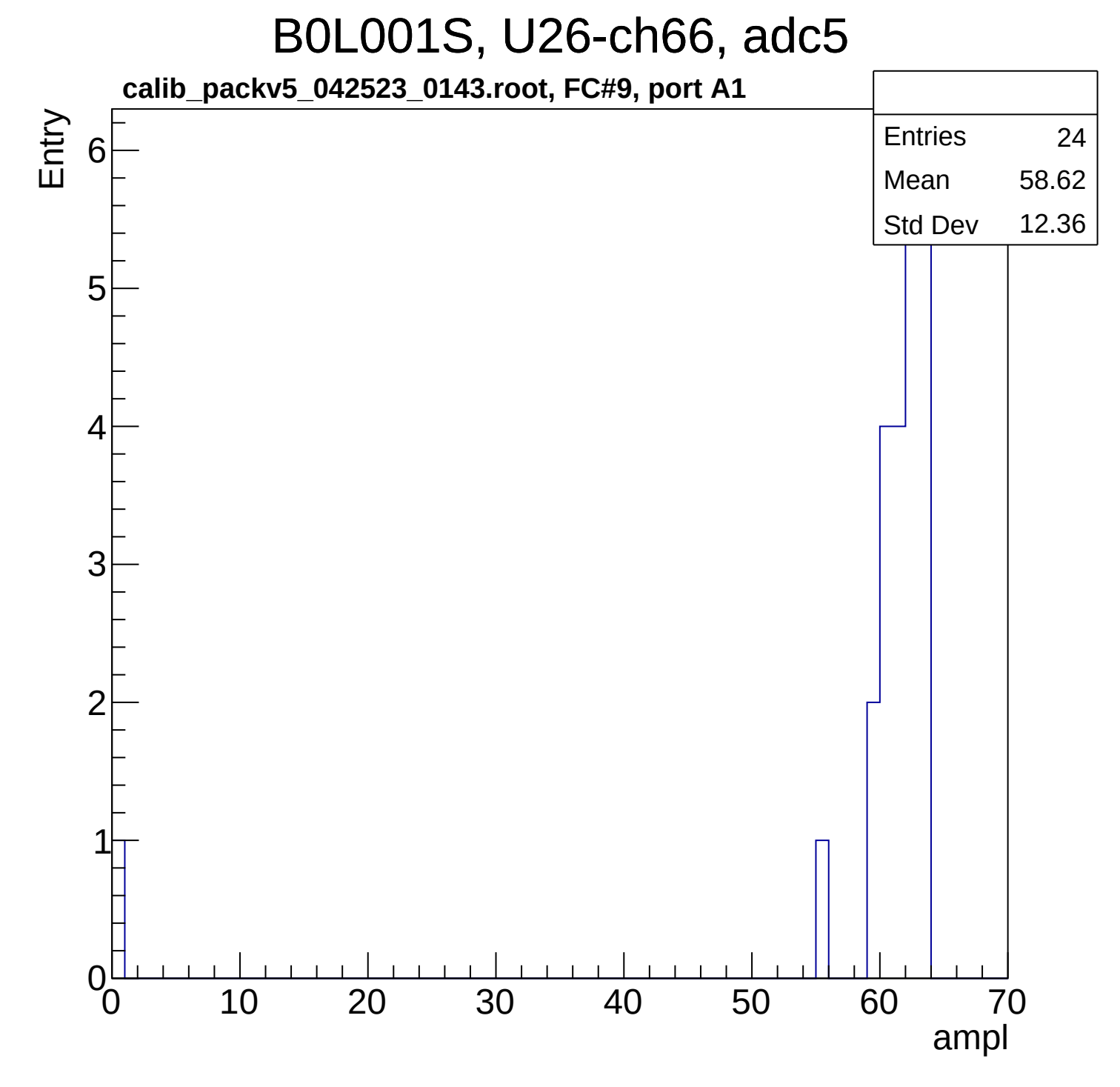
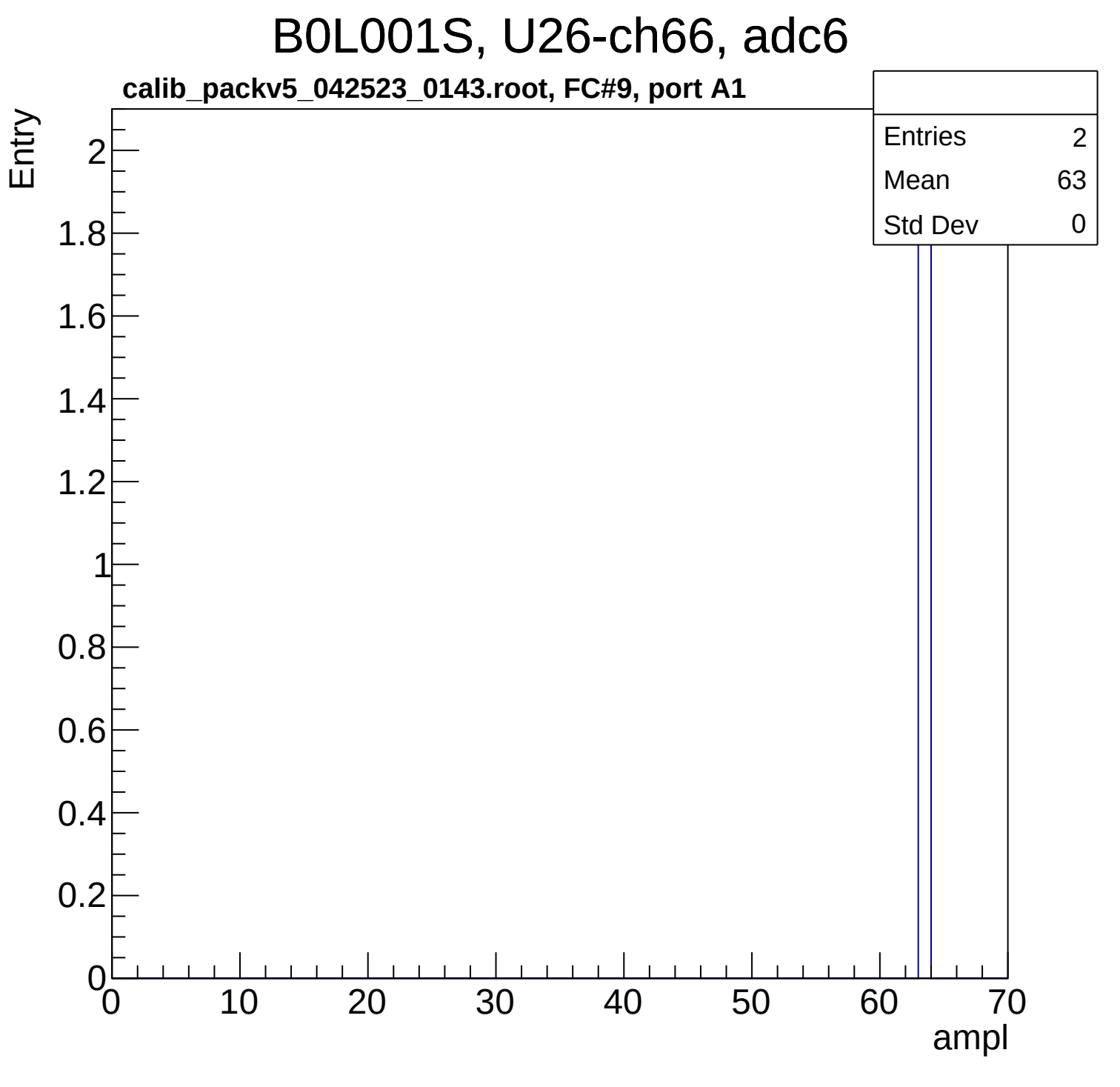
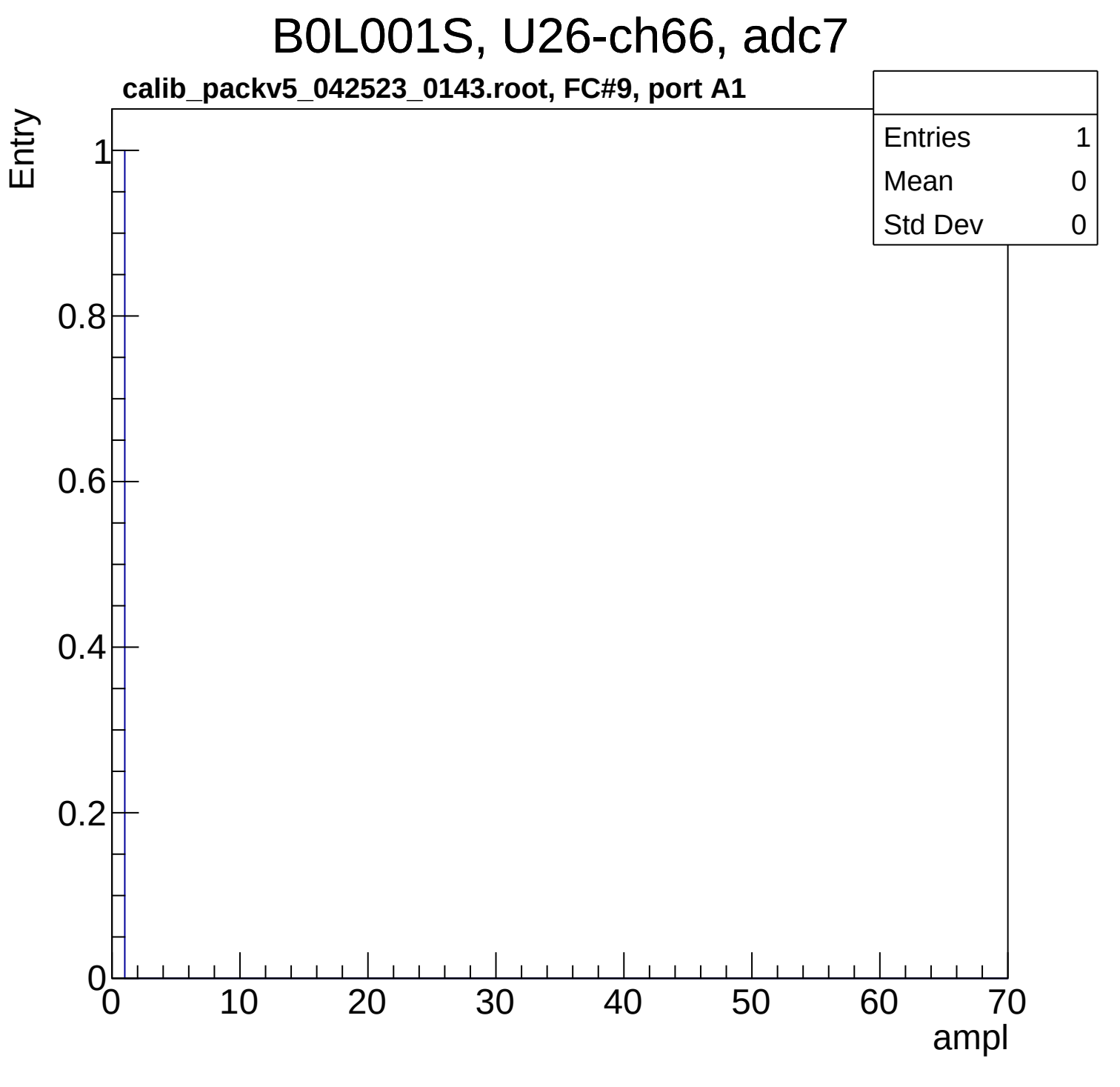
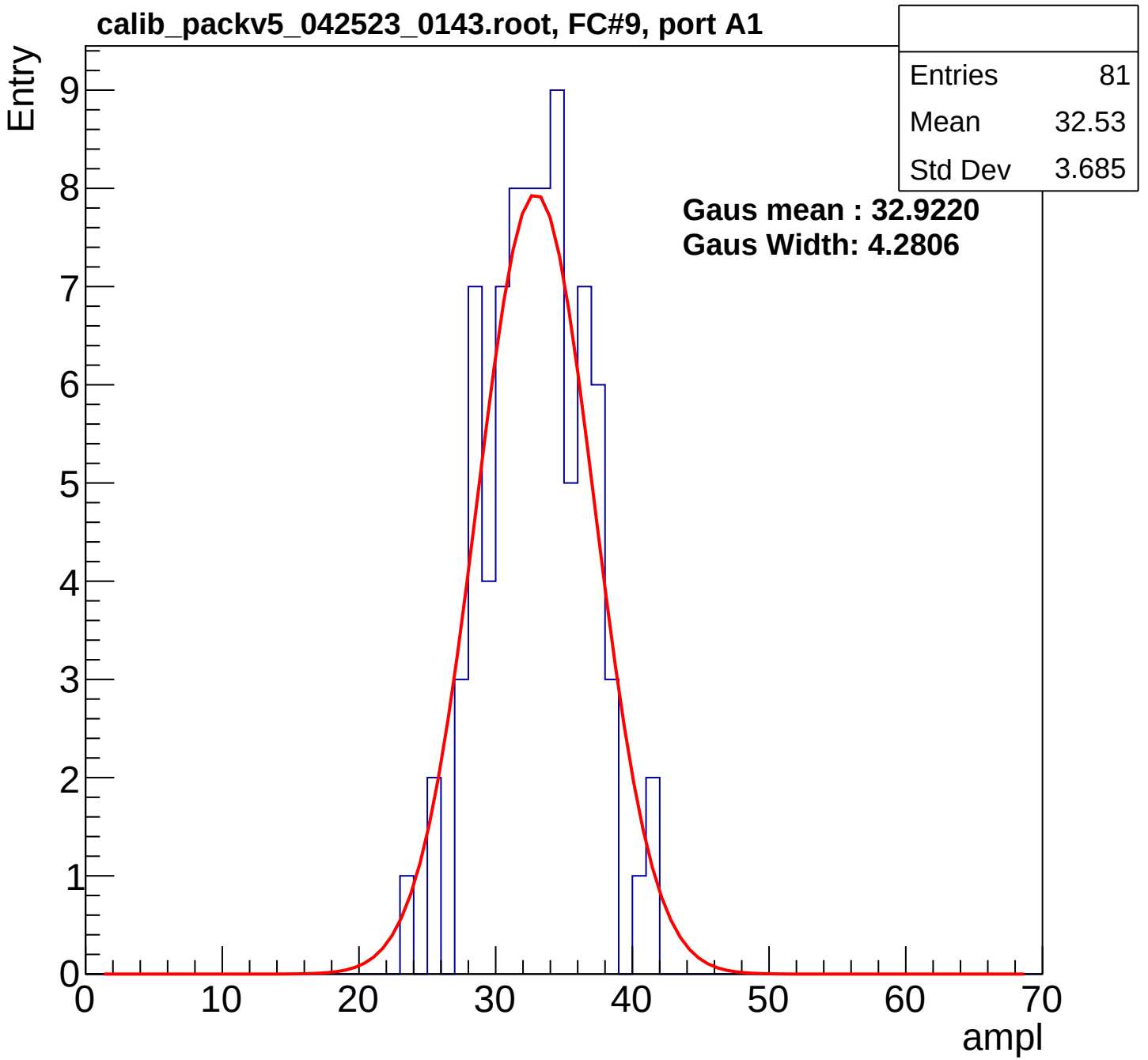
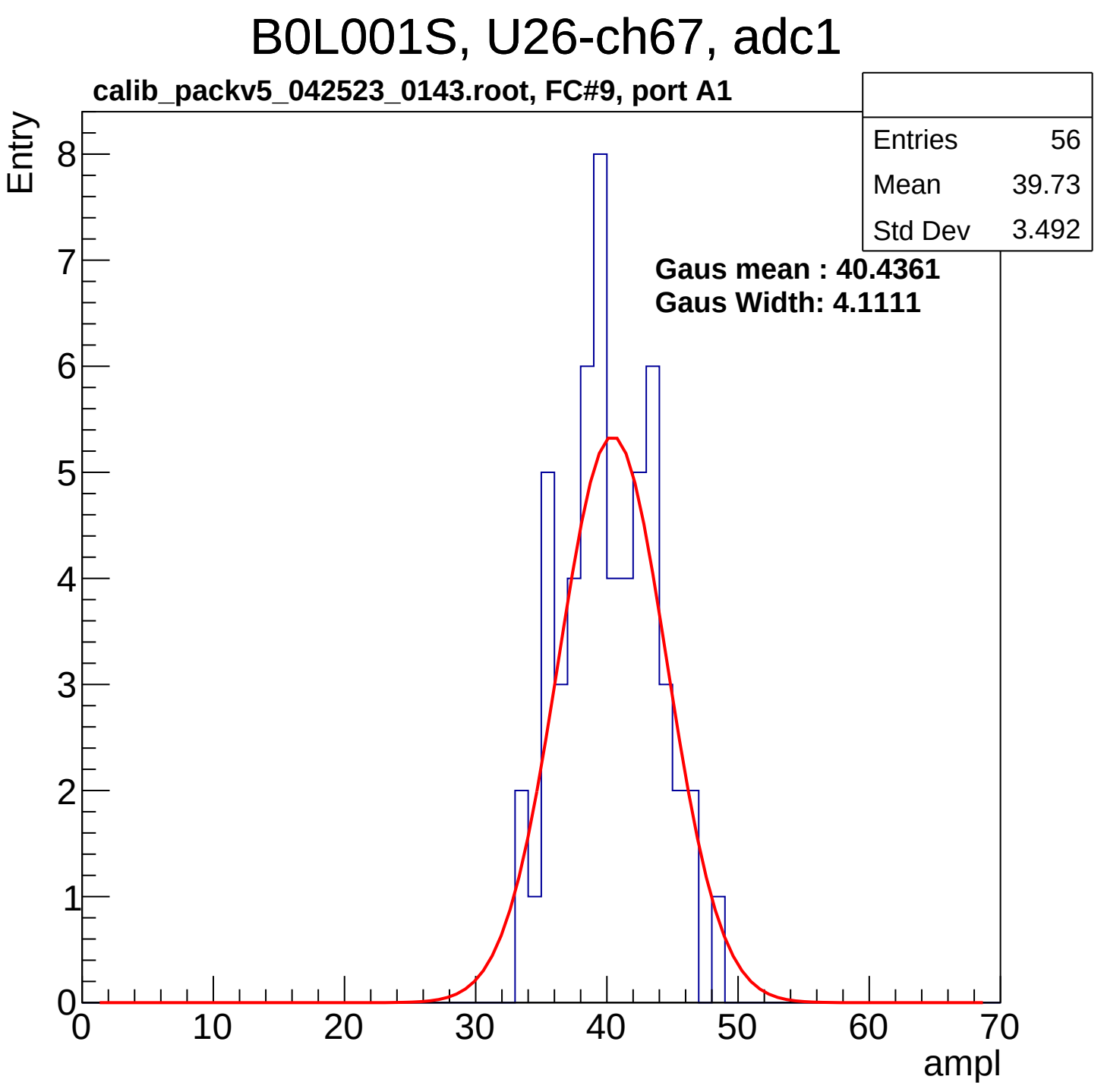

B0L001S, U26-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.62
Std Dev 12.36
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 32.53
Std Dev 3.685
Gaus mean : 32.9220
Gaus Width: 4.2806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 39.73
Std Dev 3.492
Gaus mean : 40.4361
Gaus Width: 4.1111
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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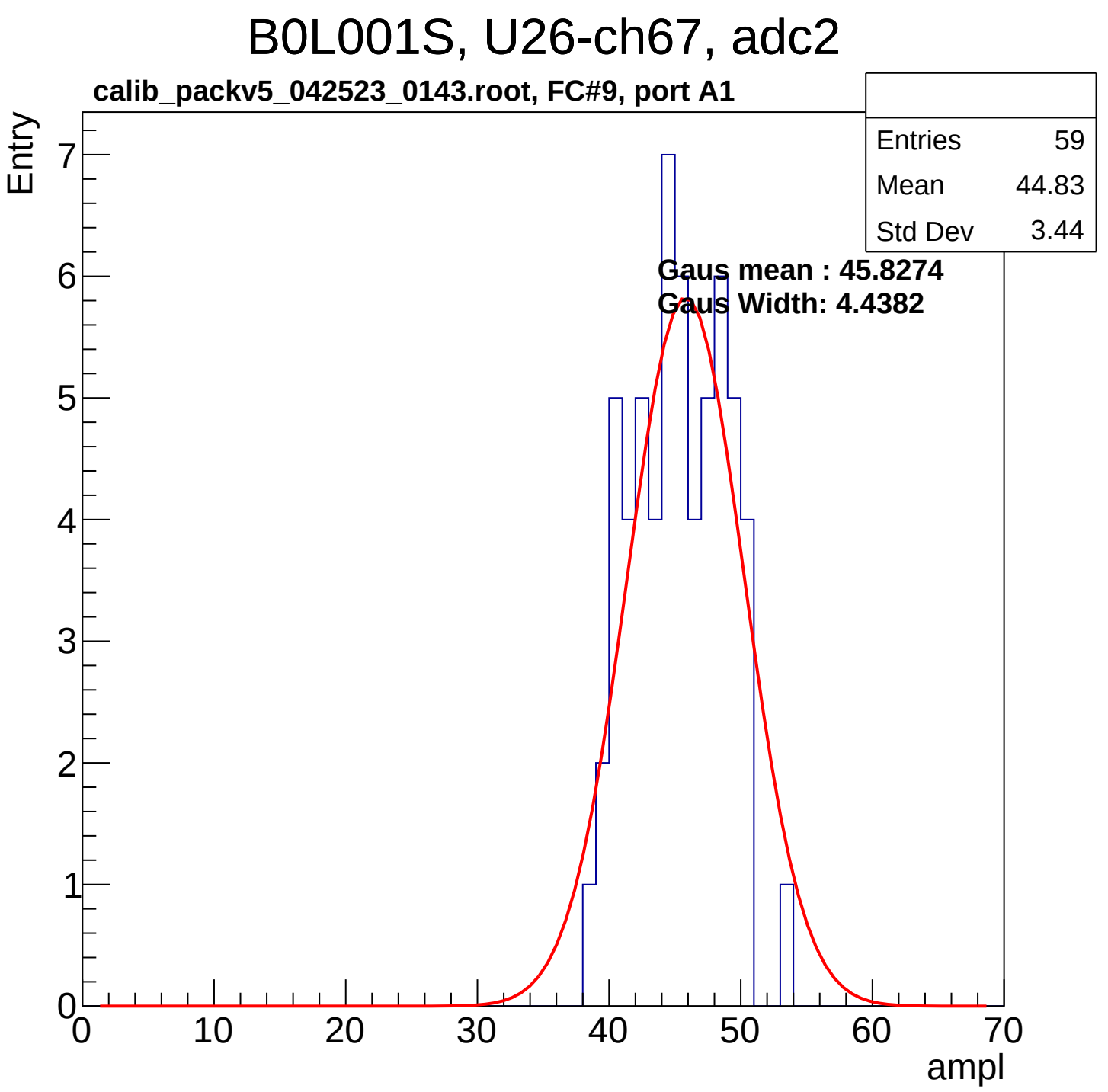
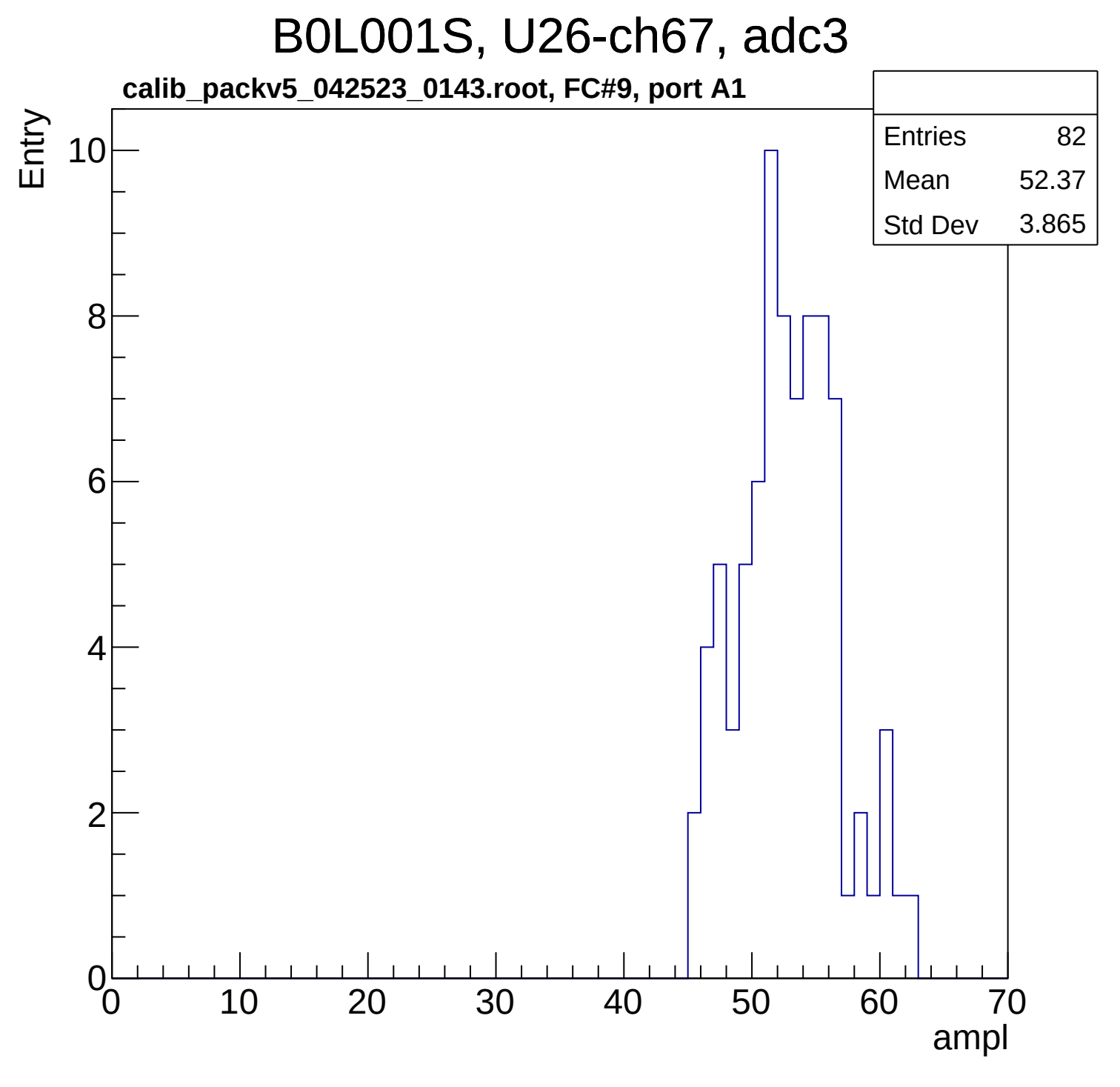

B0L001S, U26-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.83
Std Dev 3.44
Gaus mean : 45.8274
Gaus Width: 4.4382
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 52.37
Std Dev 3.865
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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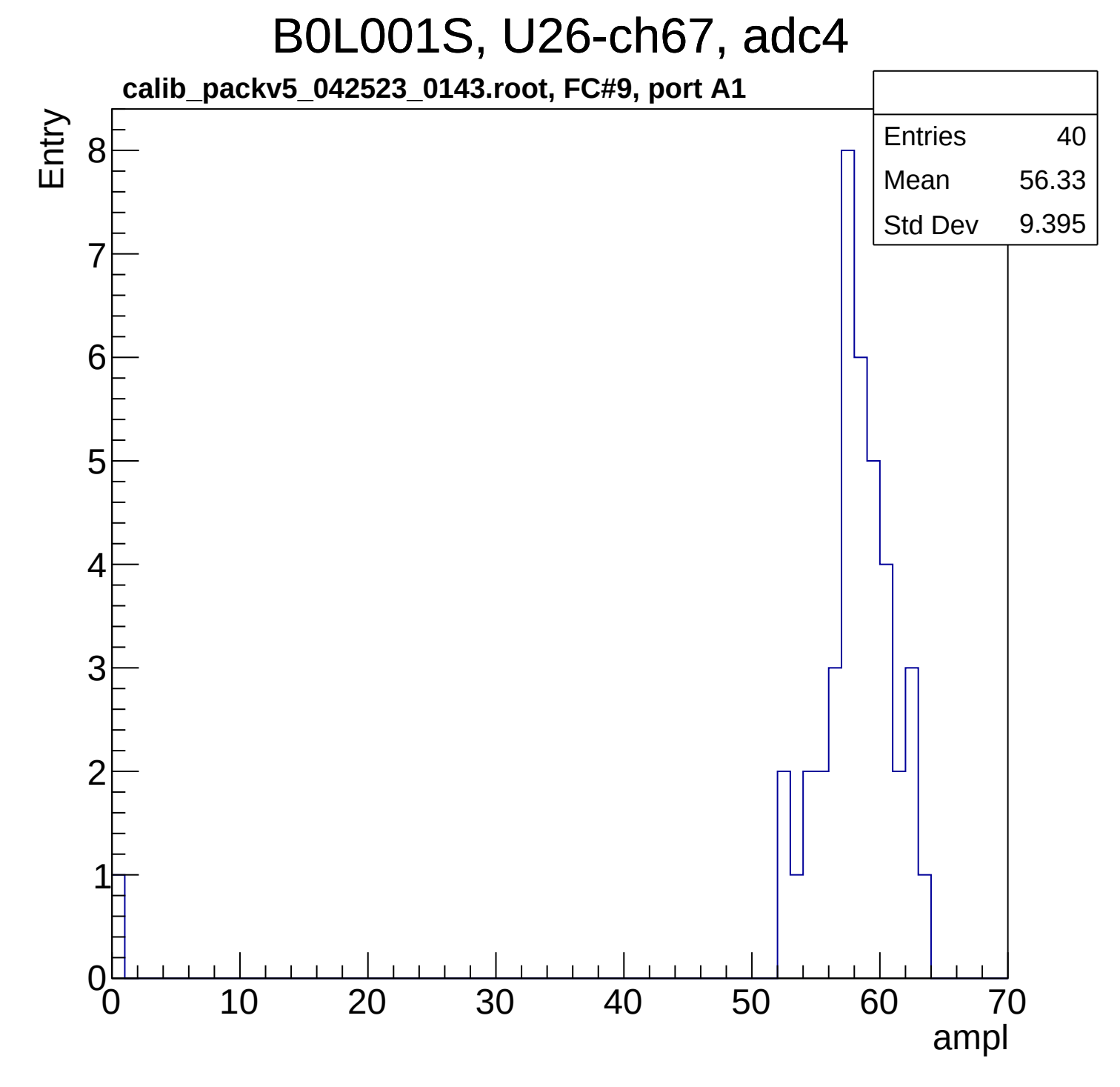
B0L001S, U26-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 56.33
Std Dev 9.395
Entry
ampl
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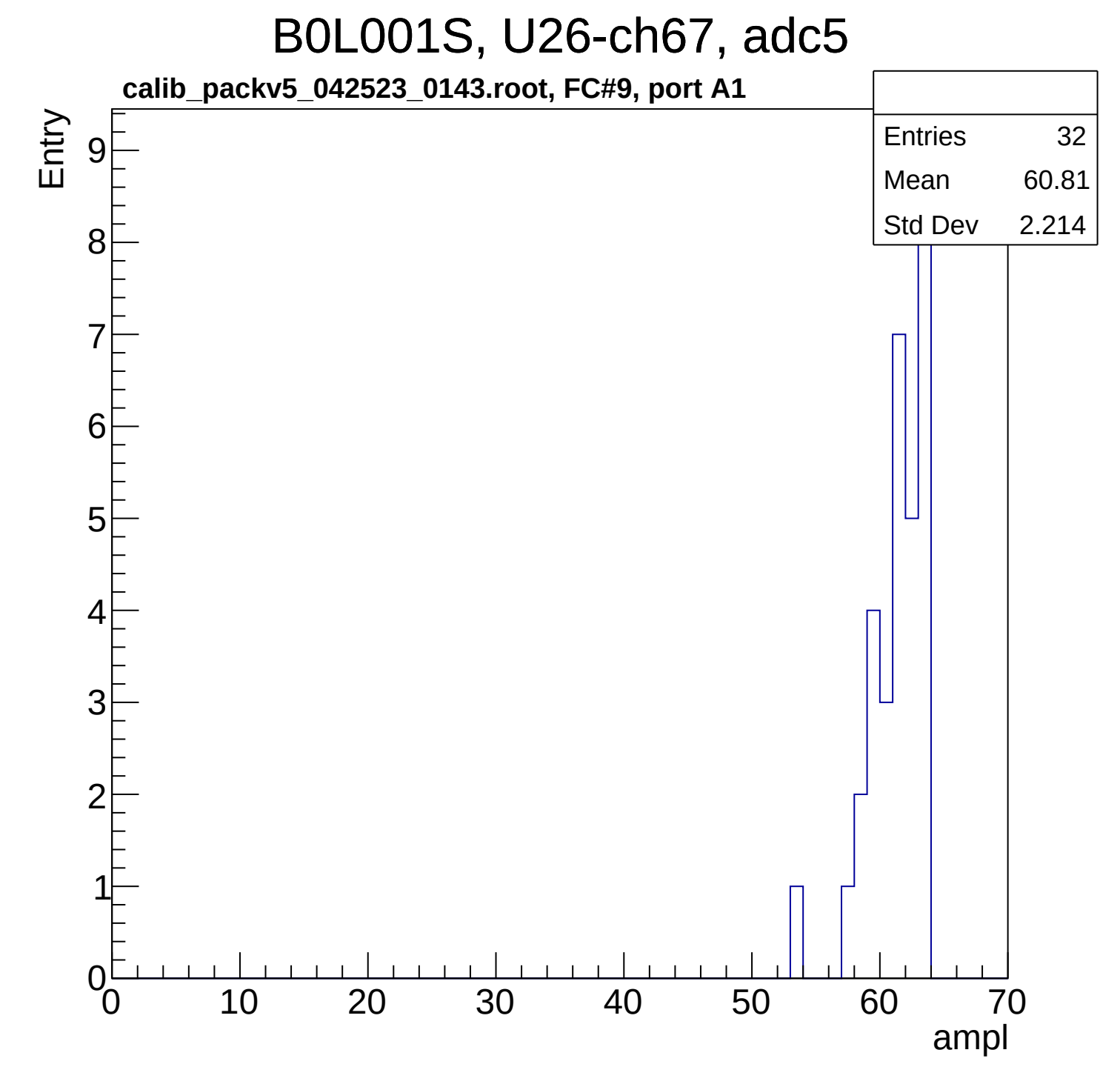
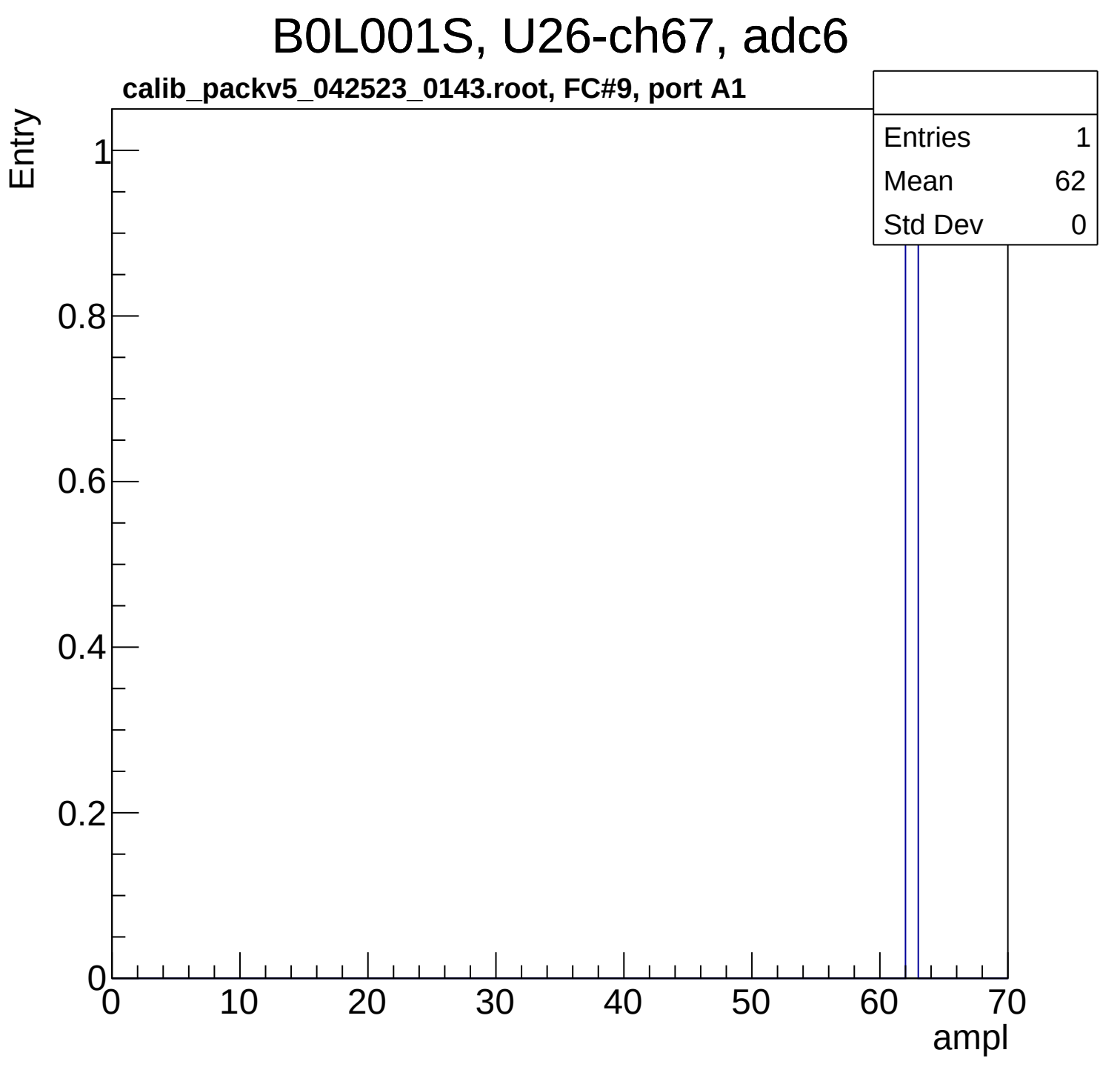
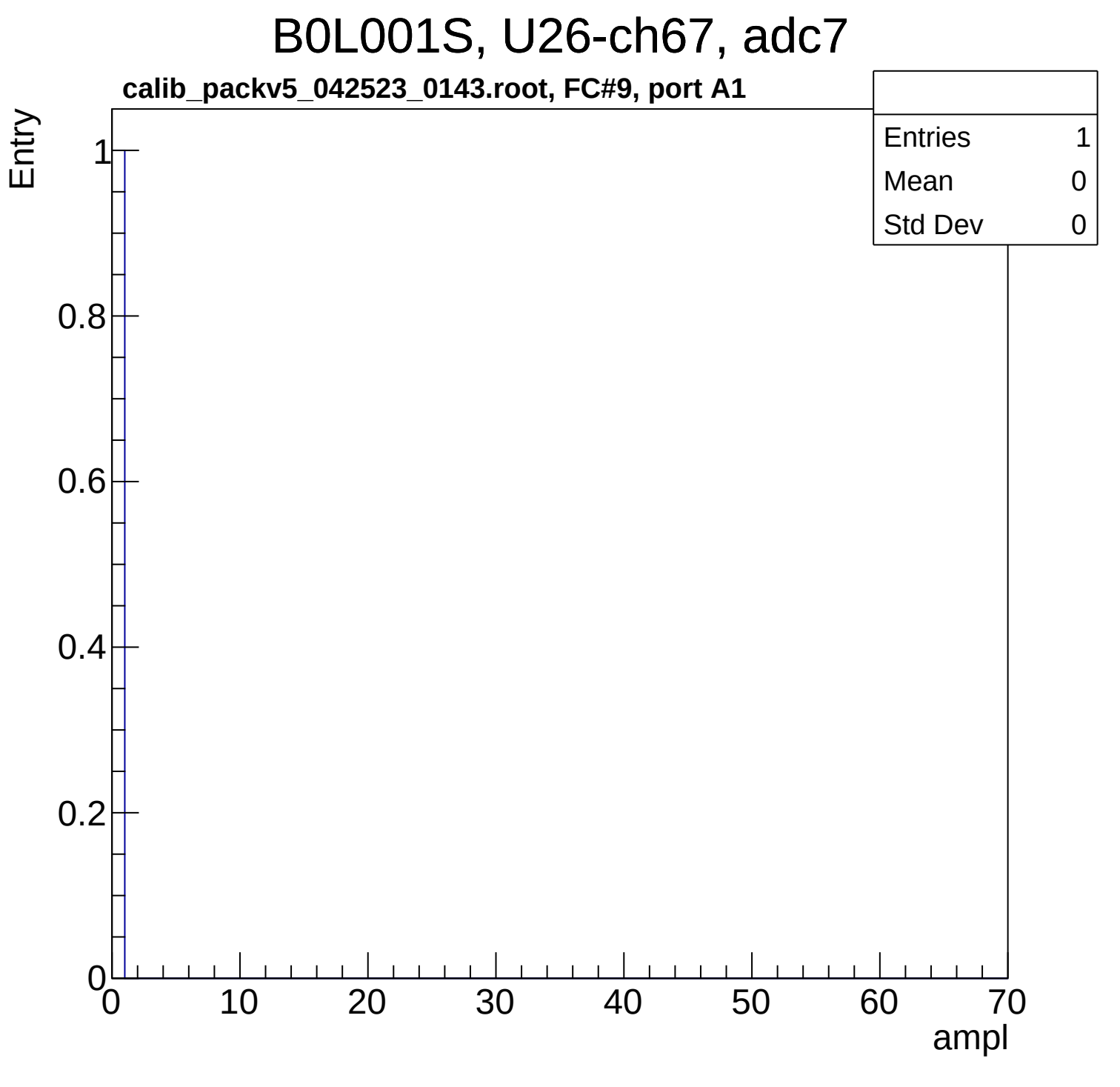
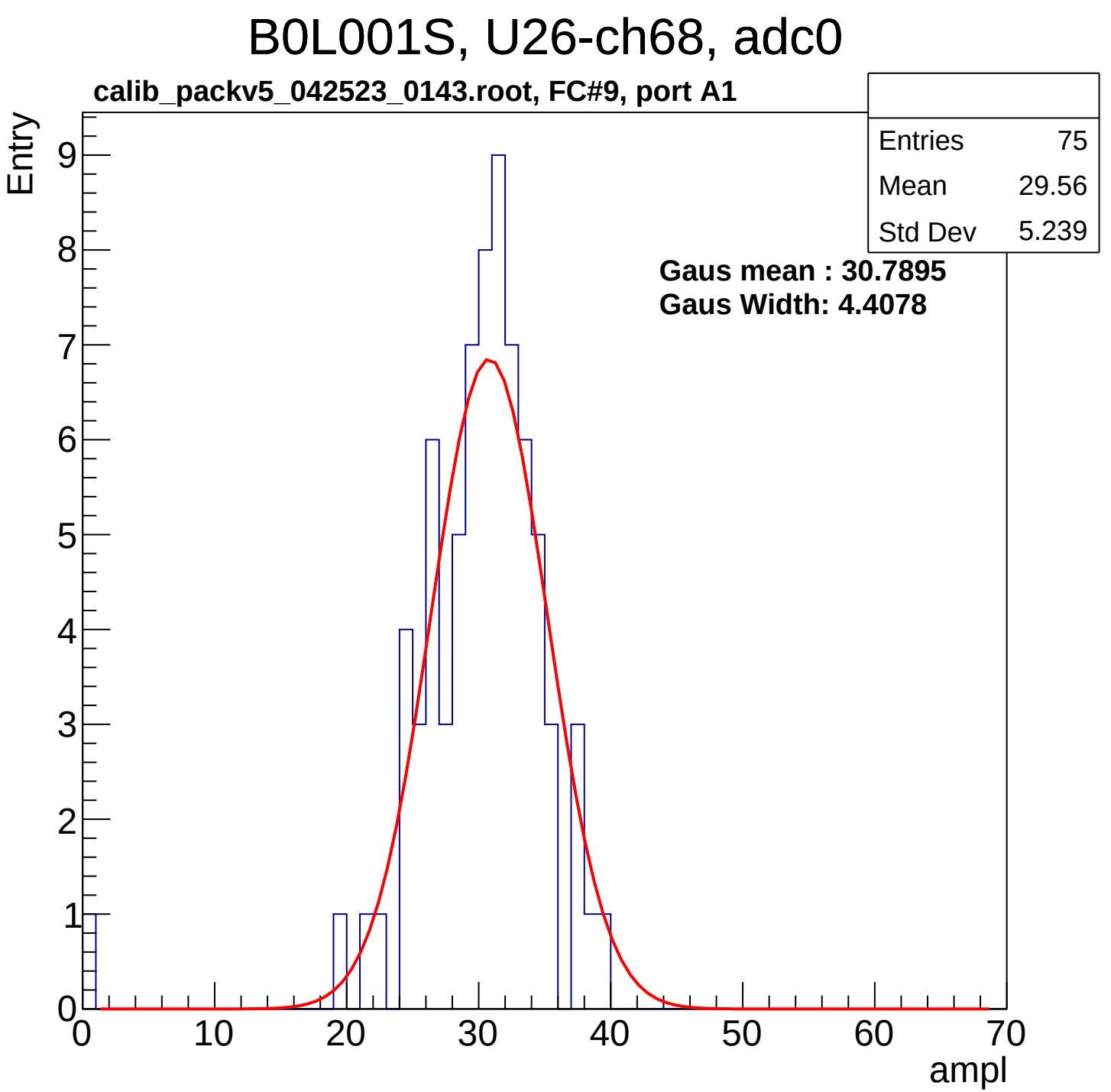
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.81
Std Dev 2.214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.56
Std Dev 5.239
Gaus mean : 30.7895
Gaus Width: 4.4078
Entry
ampl
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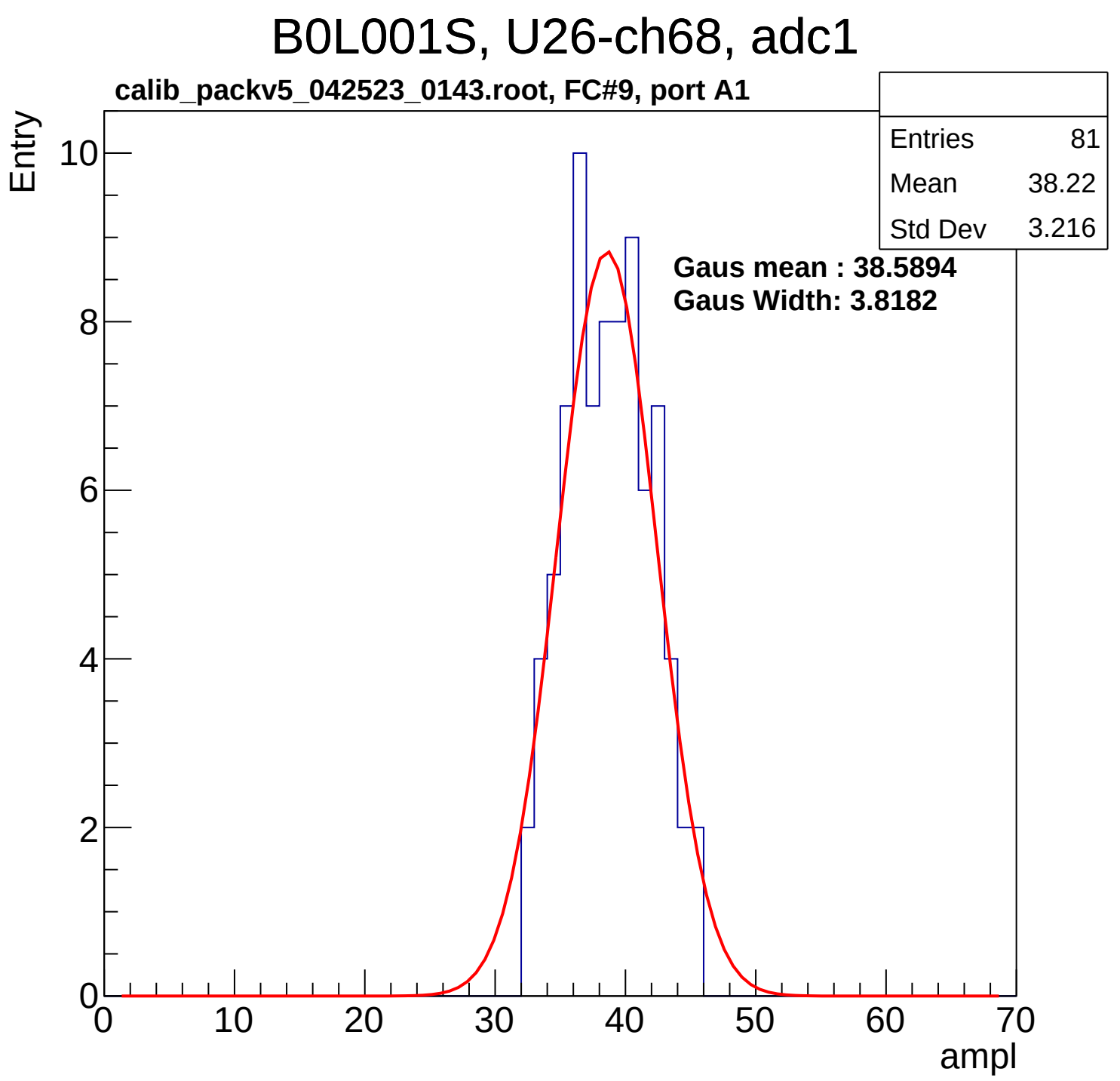
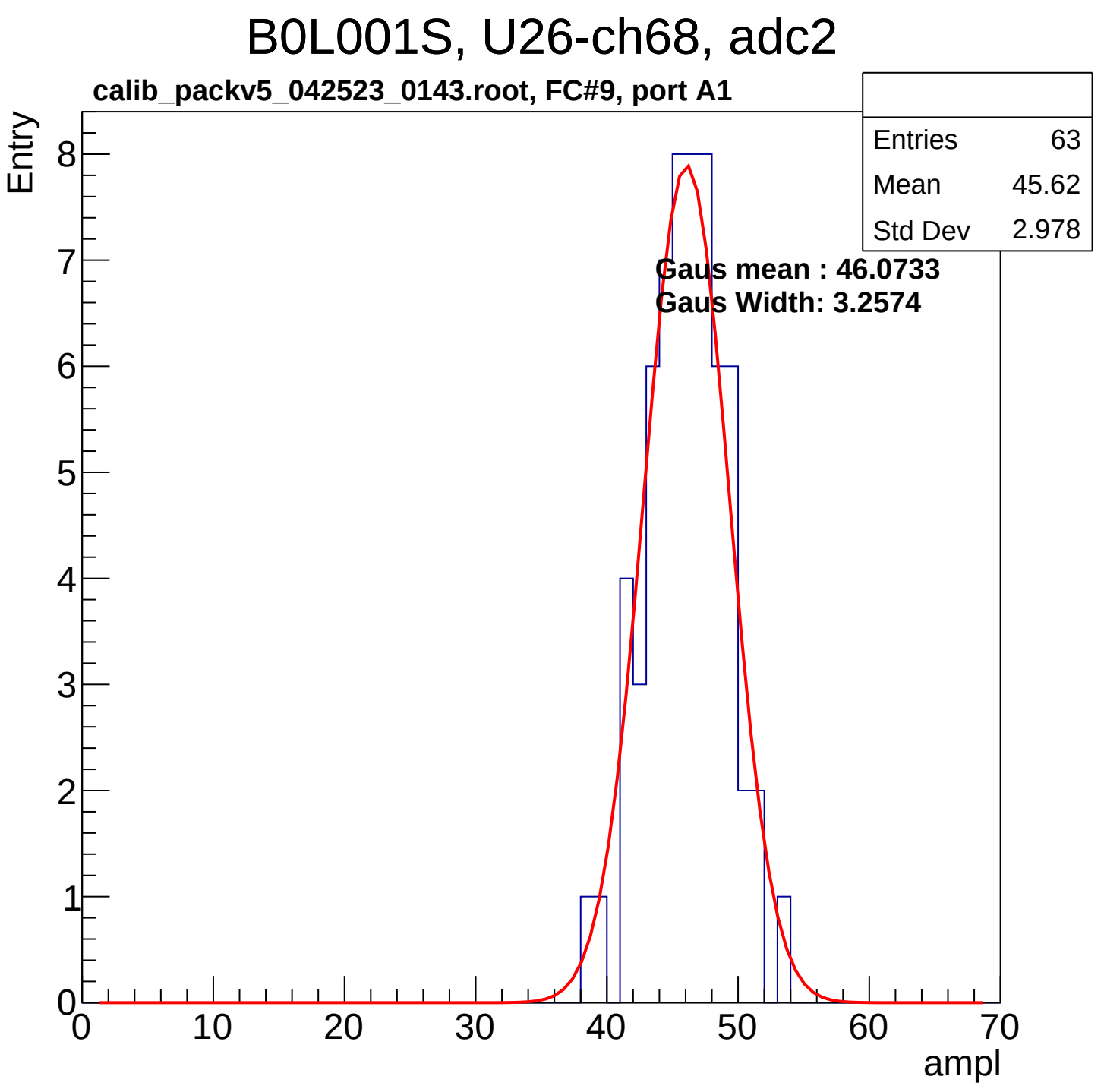
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 38.22
Std Dev 3.216
Gaus mean : 38.5894
Gaus Width: 3.8182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 45.62
Std Dev 2.978
Gaus mean : 46.0733
Gaus Width: 3.2574
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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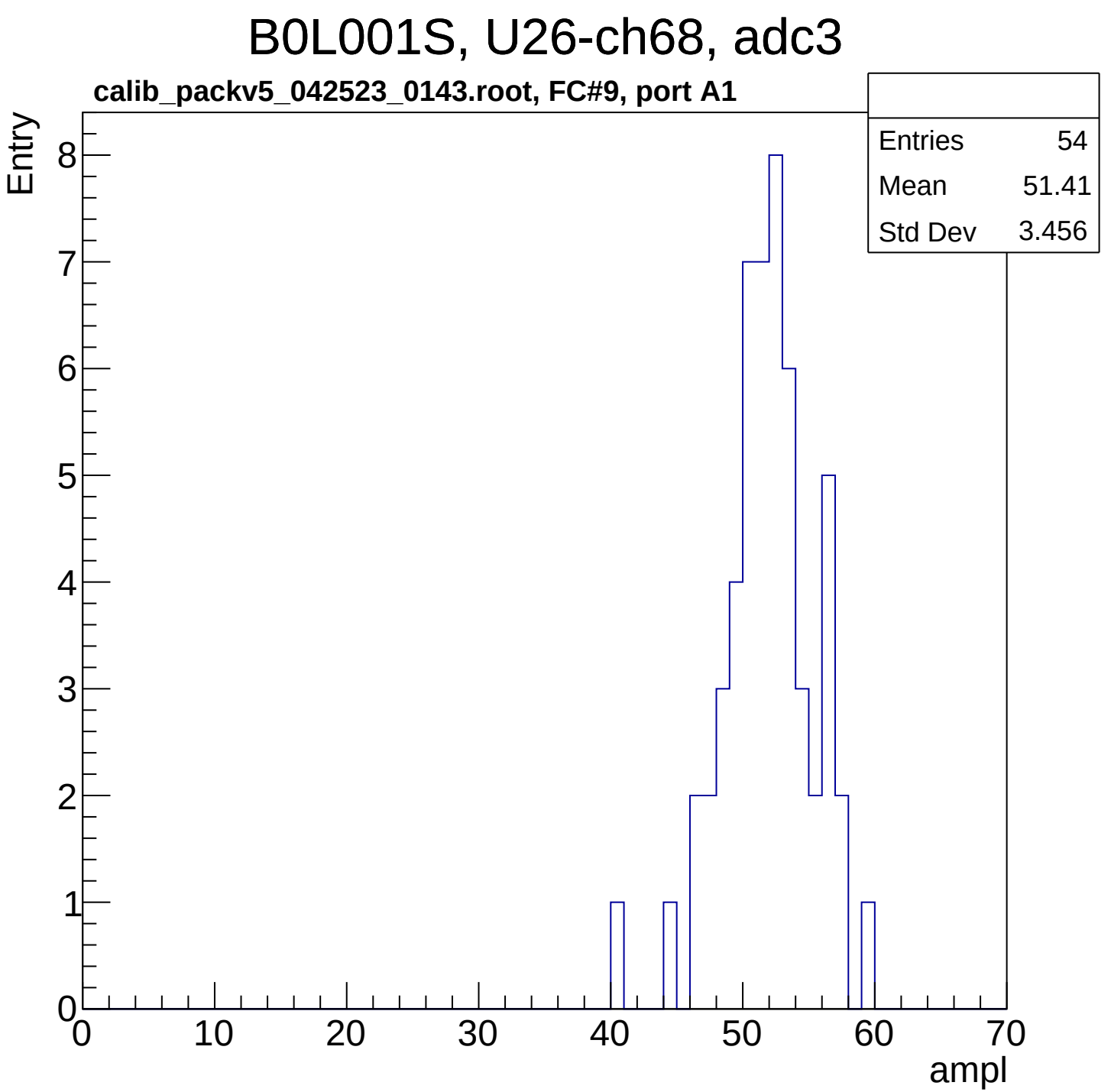
40
50
60
70


B0L001S, U26-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.41
Std Dev 3.456
Entry
ampl
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3
4
5
6
7
8
0
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40
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60
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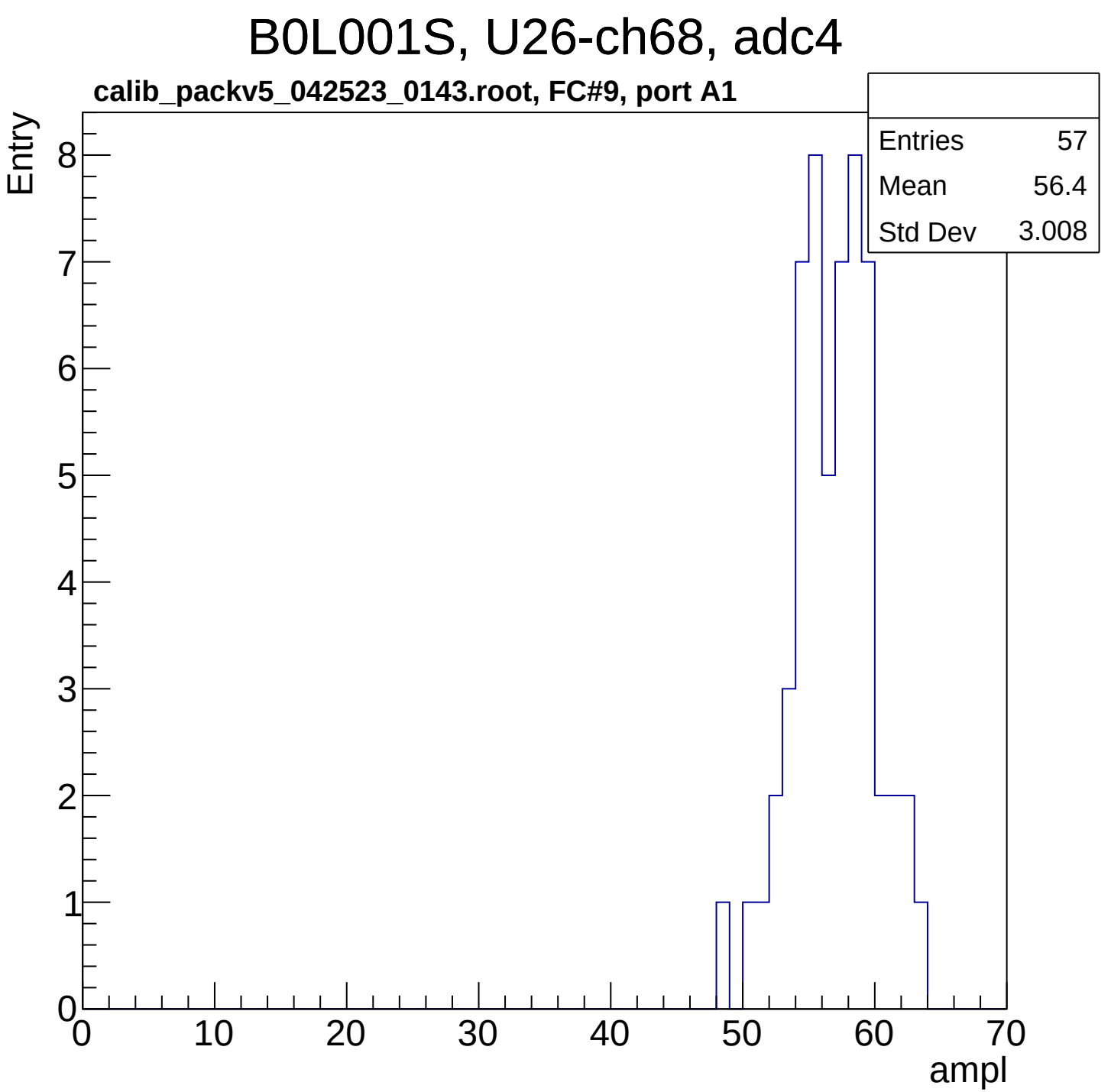
B0L001S, U26-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.4
Std Dev 3.008
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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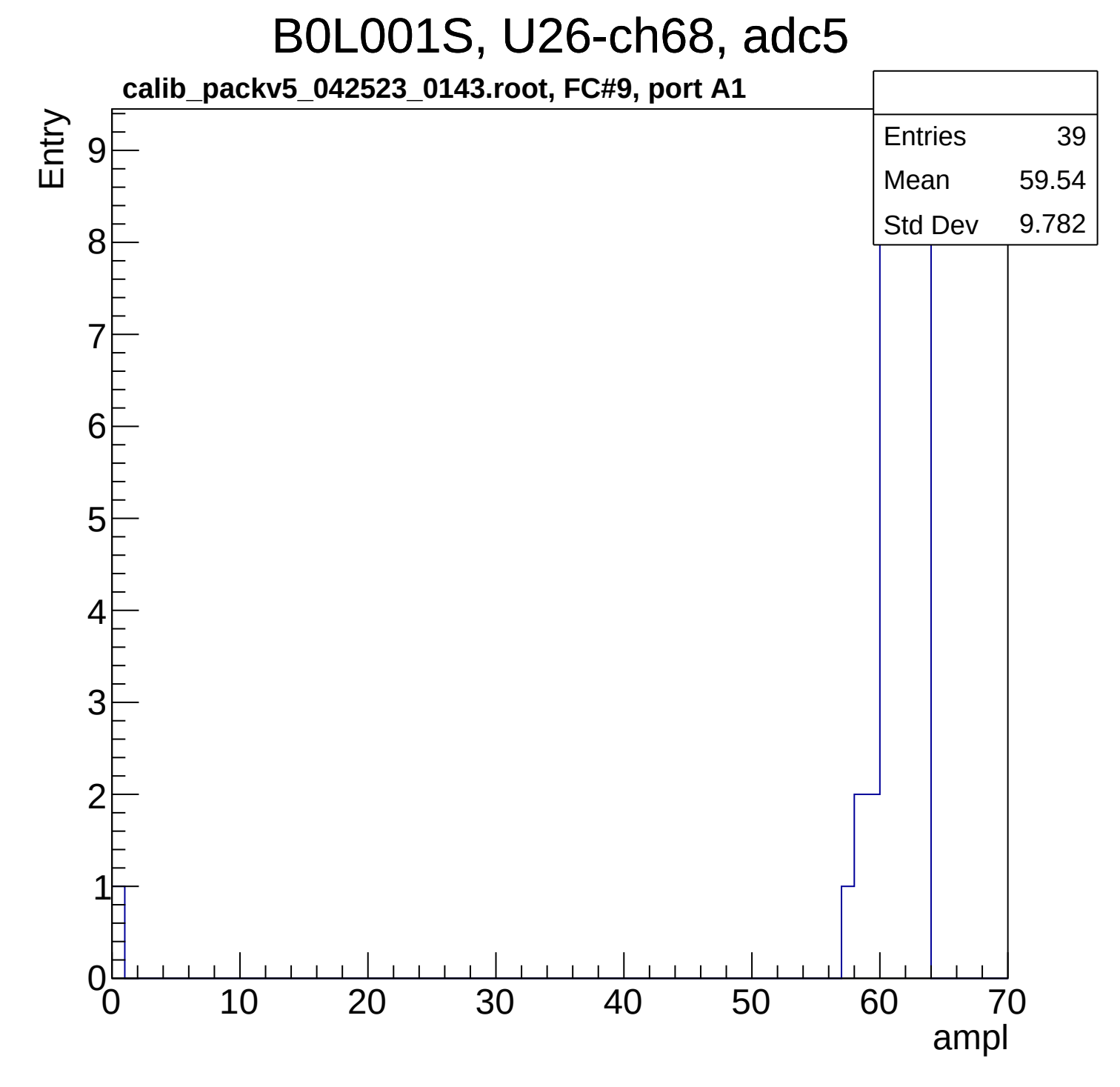
B0L001S, U26-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.54
Std Dev 9.782
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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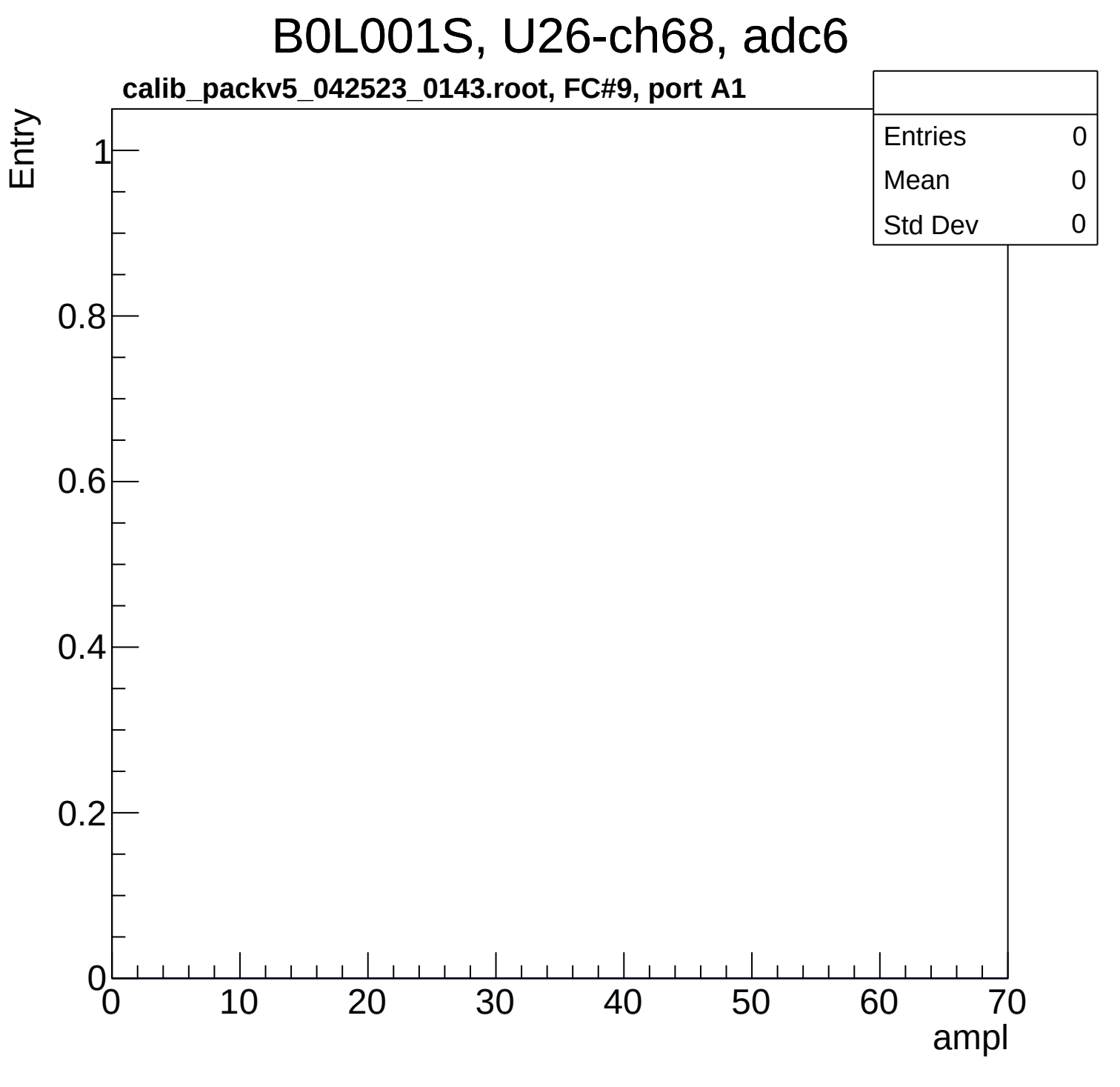
B0L001S, U26-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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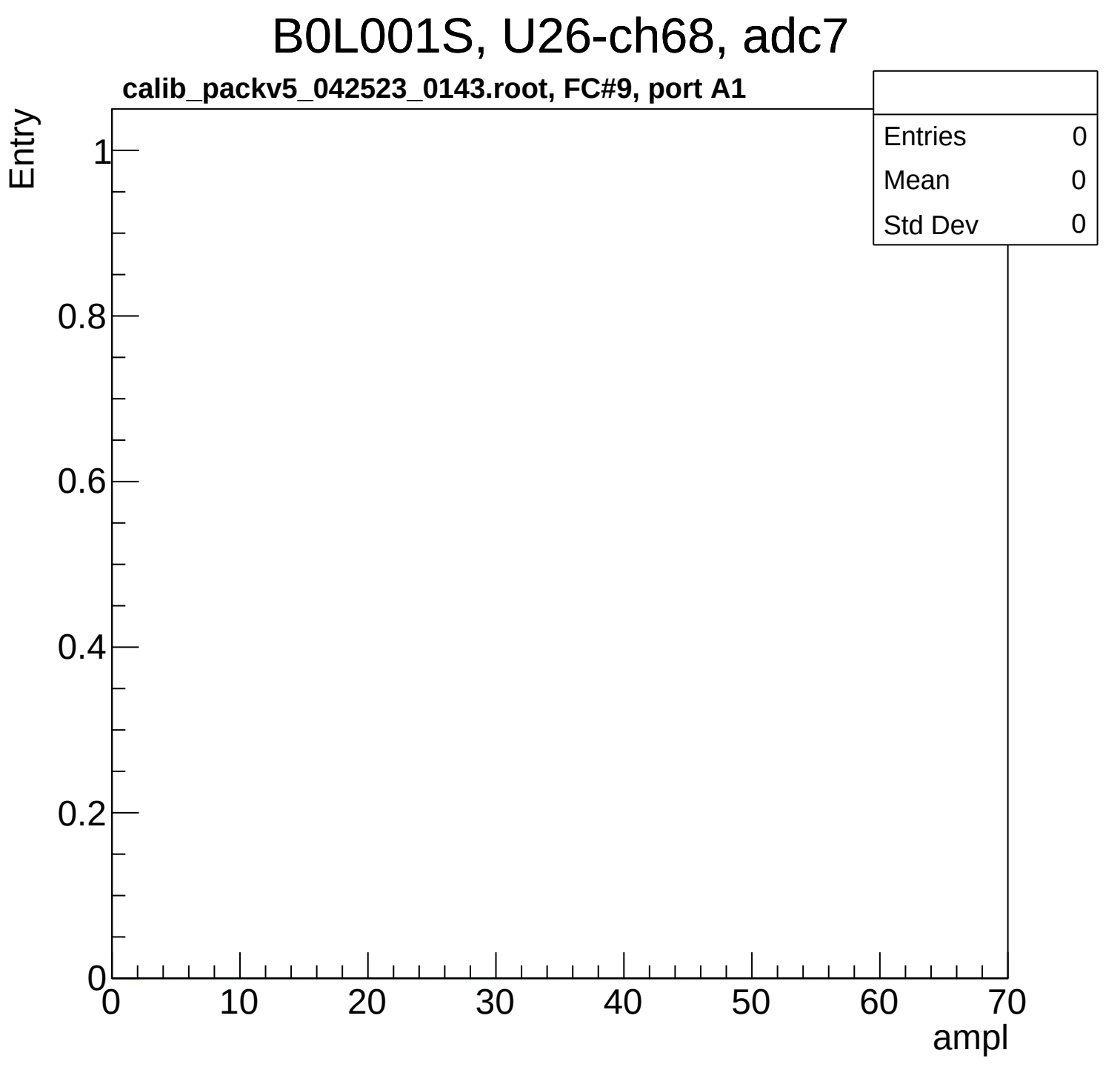
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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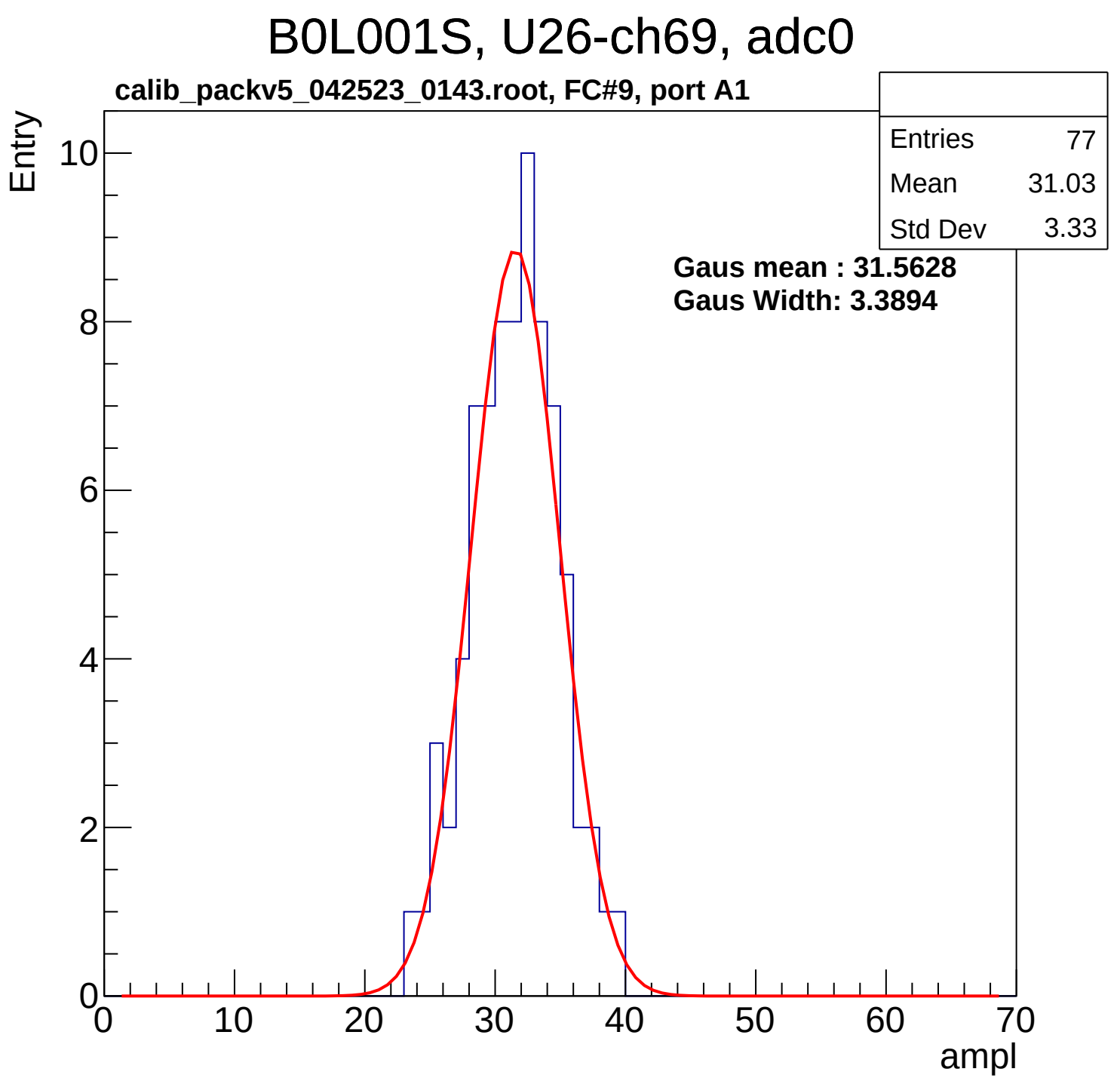
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.03
Std Dev 3.33
Gaus mean : 31.5628
Gaus Width: 3.3894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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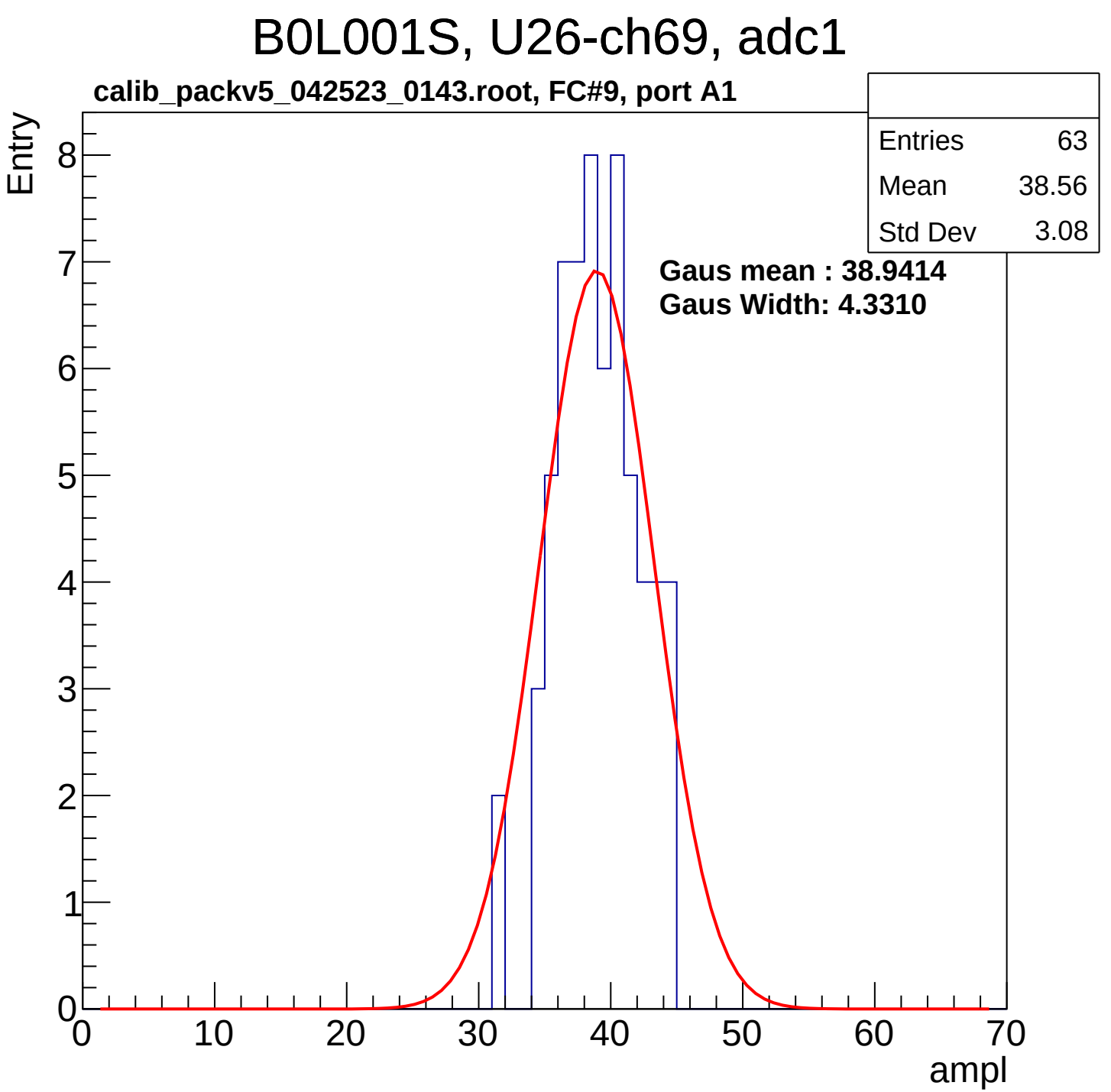
B0L001S, U26-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.56
Std Dev 3.08
Gaus mean : 38.9414
Gaus Width: 4.3310
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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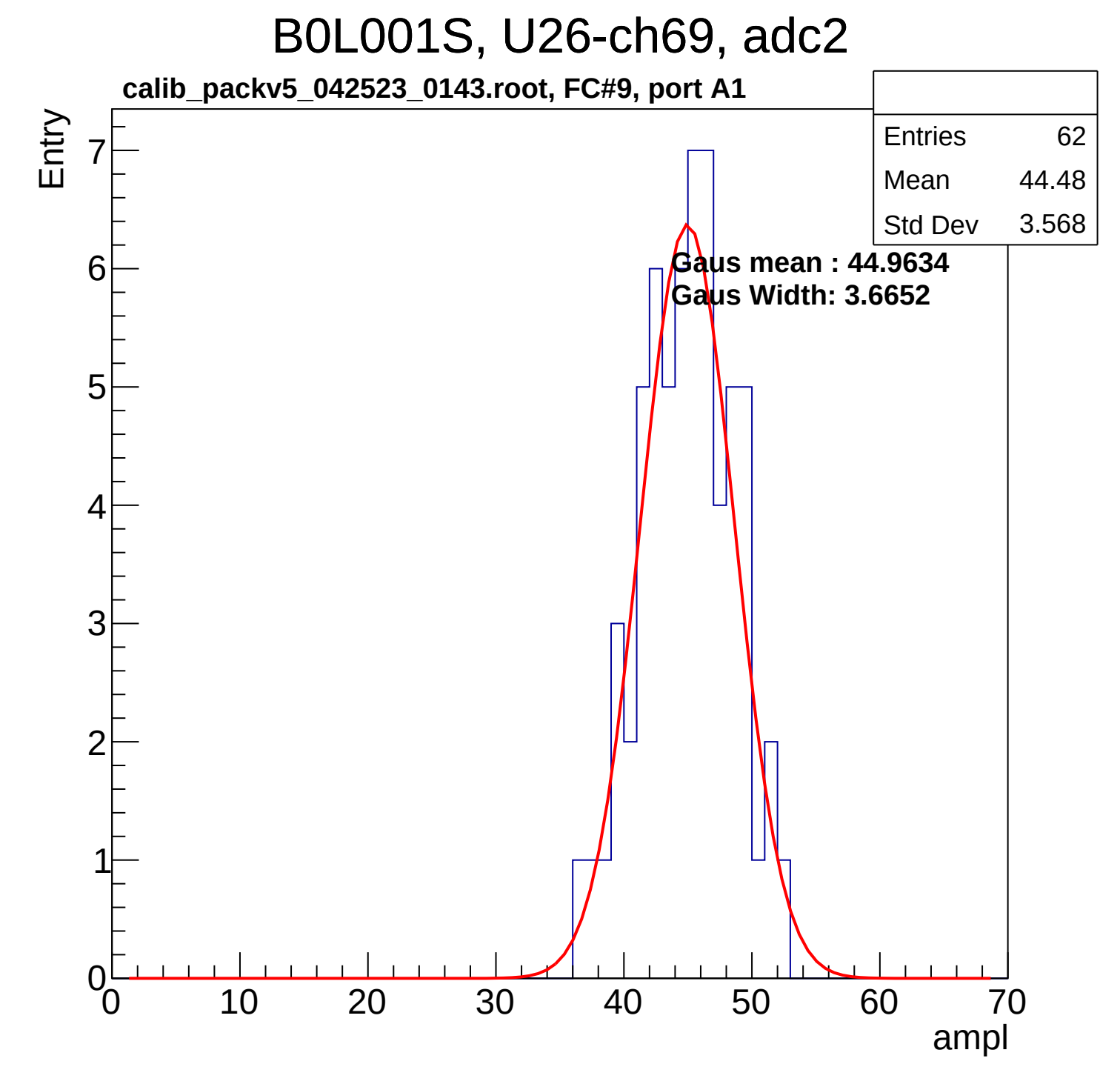
B0L001S, U26-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.48
Std Dev 3.568
Gaus mean : 44.9634
Gaus Width: 3.6652
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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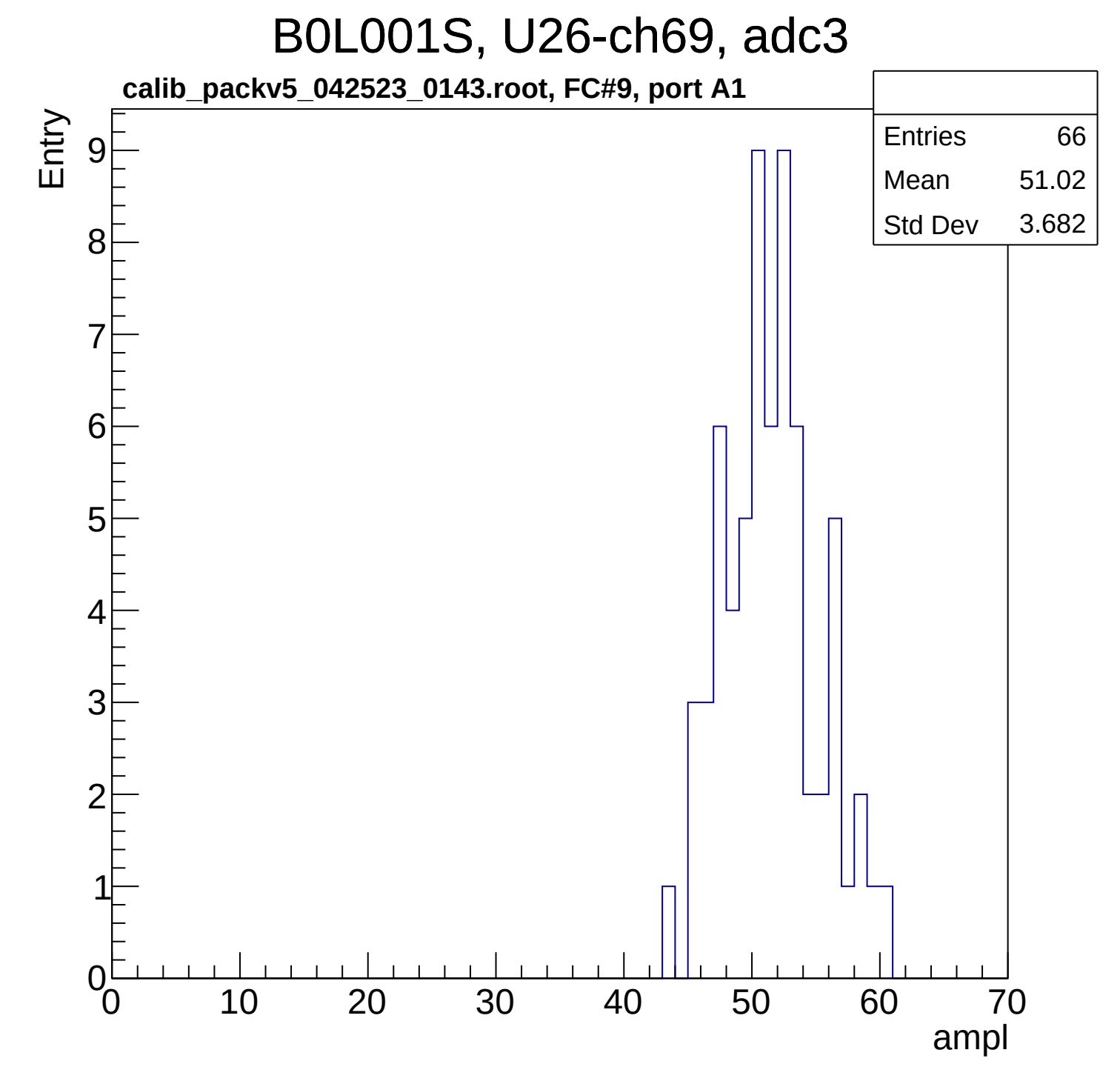
B0L001S, U26-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.02
Std Dev 3.682
Entry
ampl
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2
3
4
5
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0
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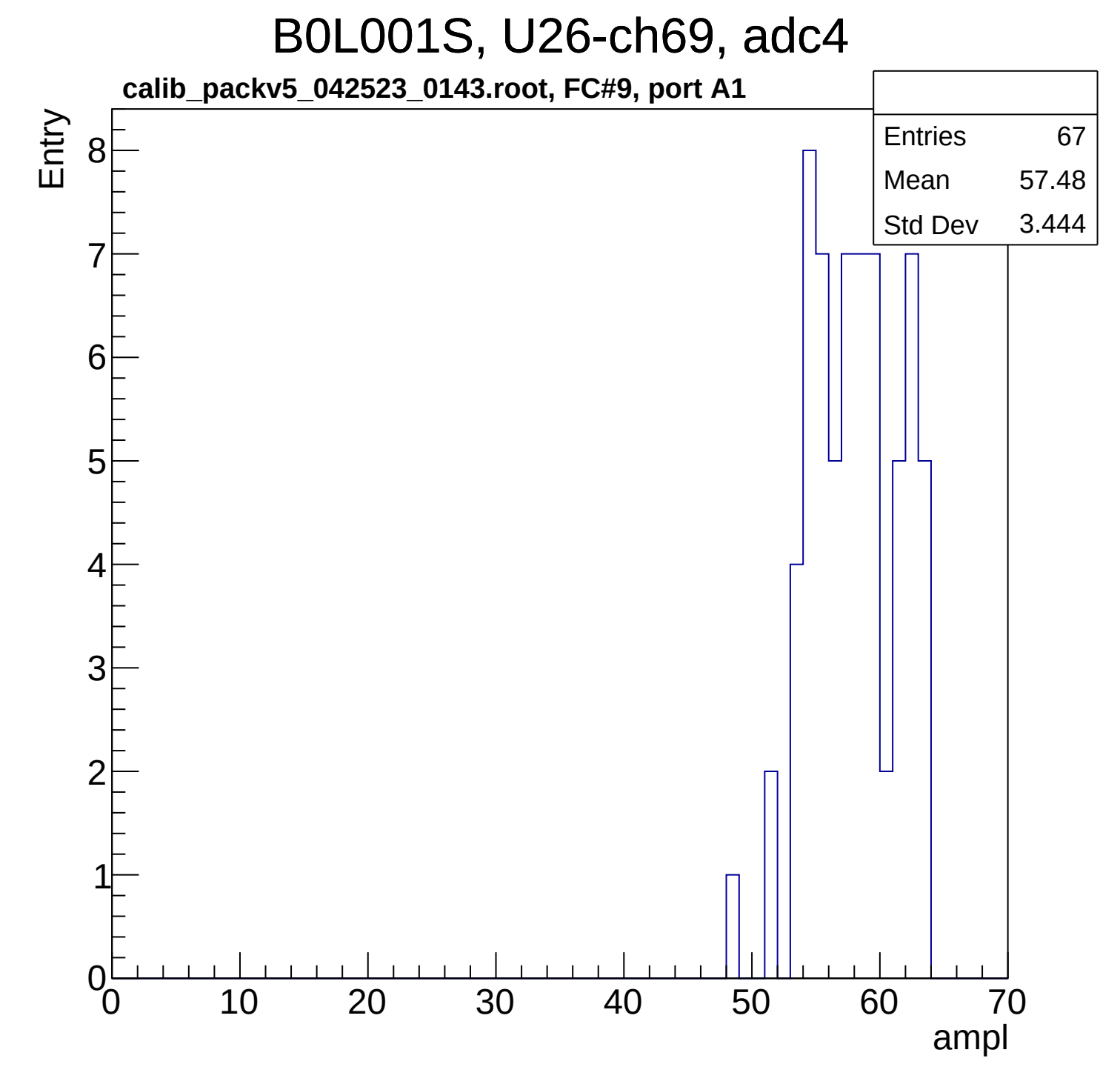
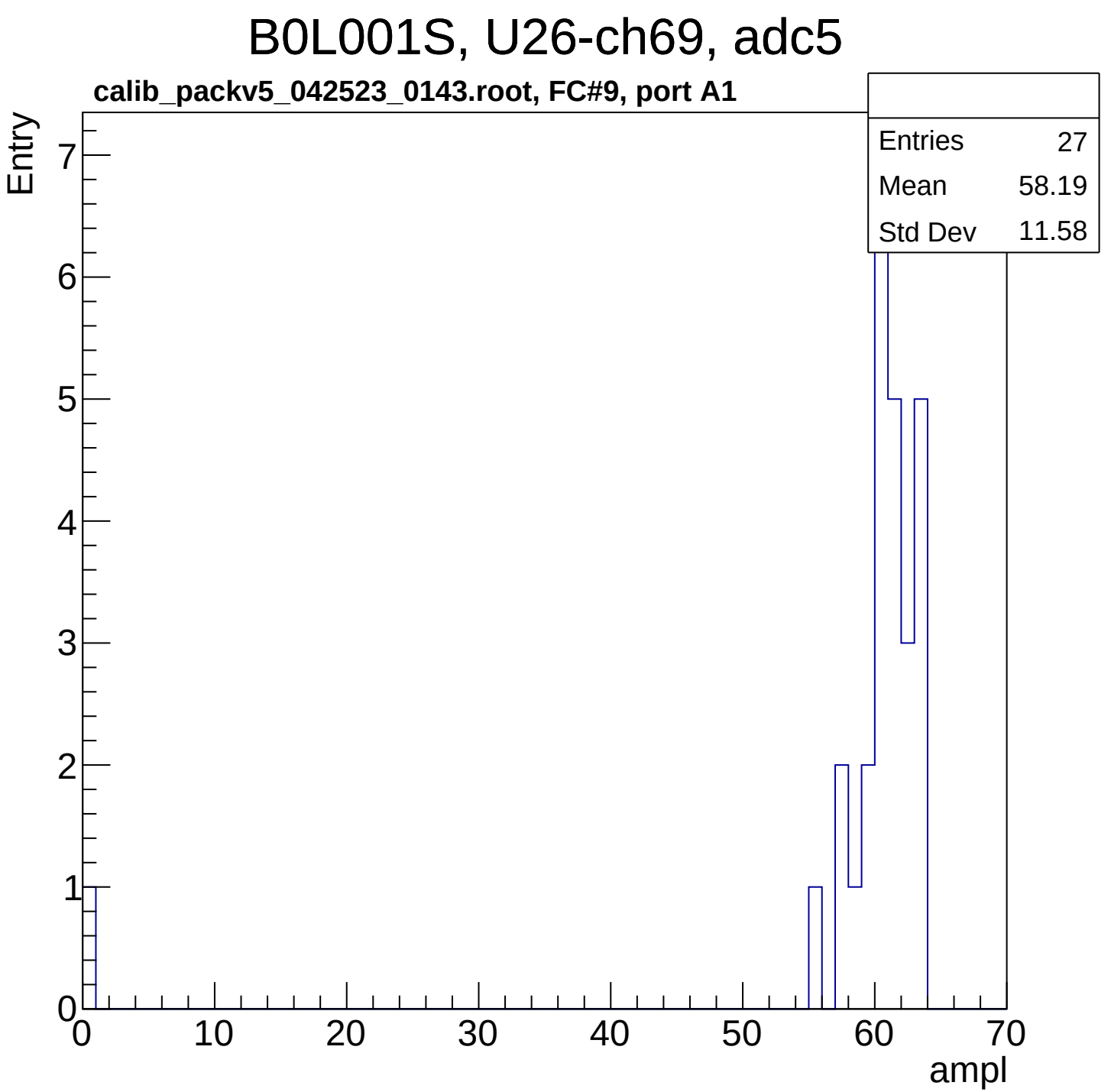

B0L001S, U26-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57.48
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.19
Std Dev 11.58
Entry
0
1
2
3
4
5
6
7
0
10
20
30
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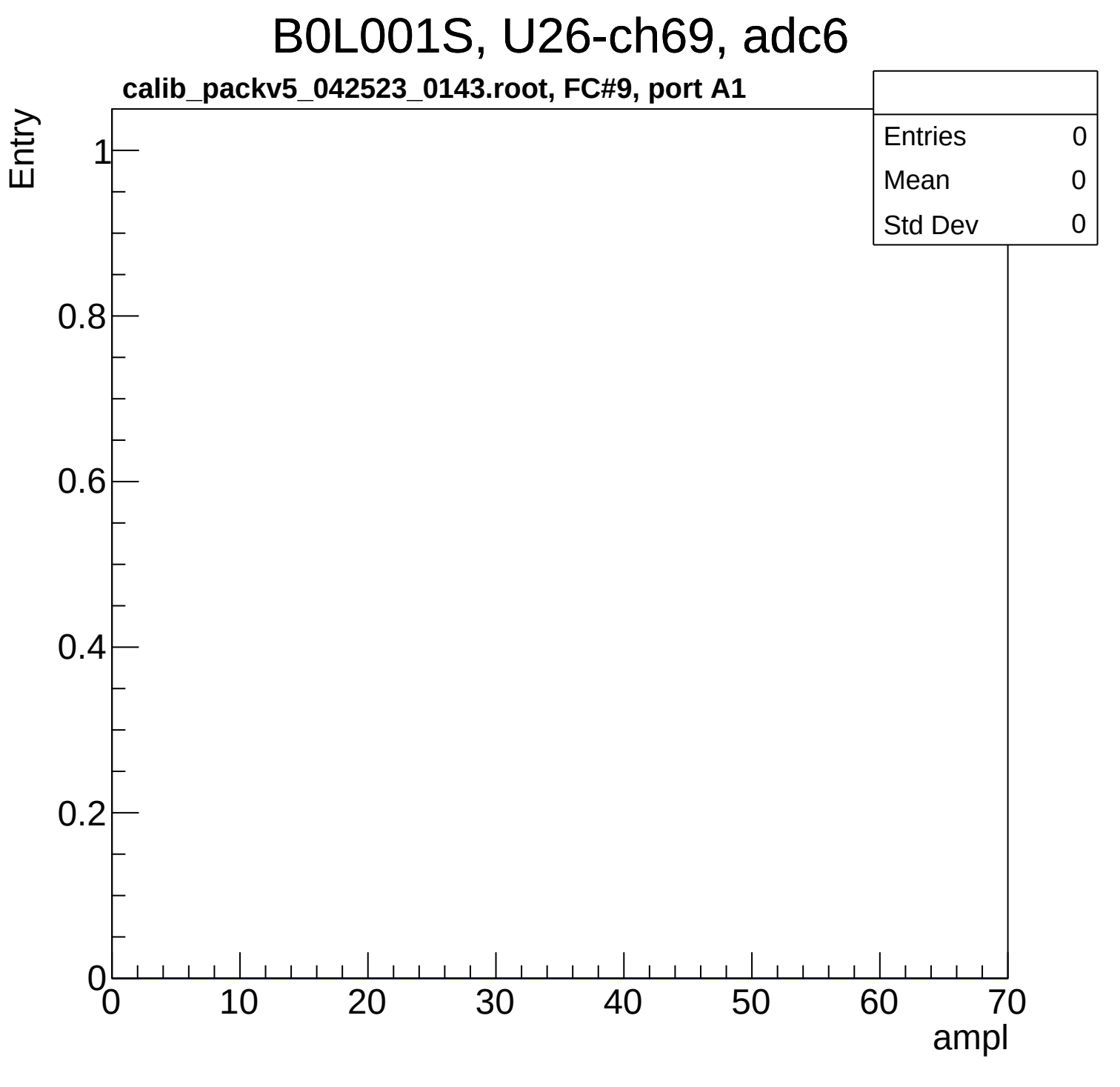
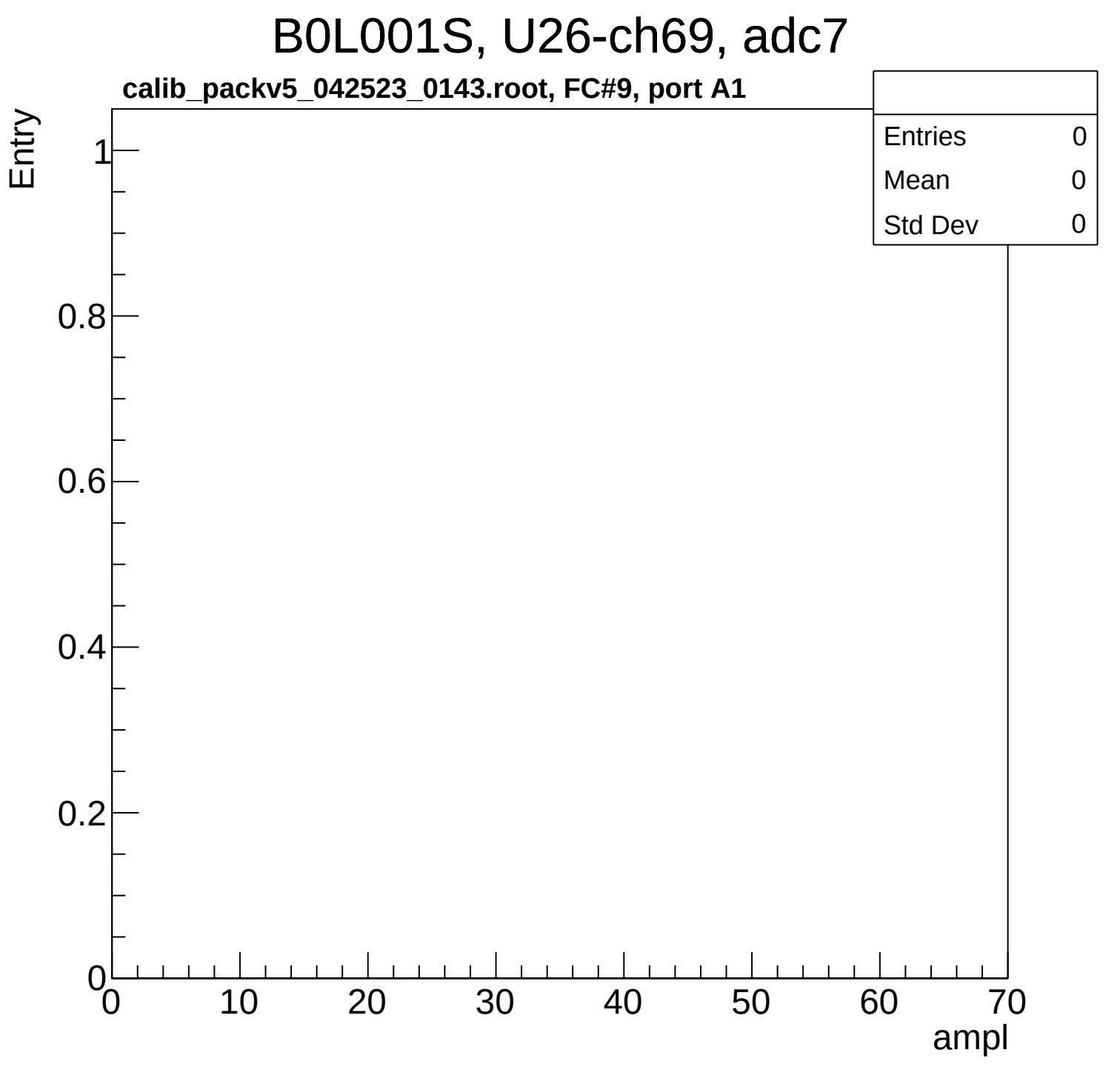
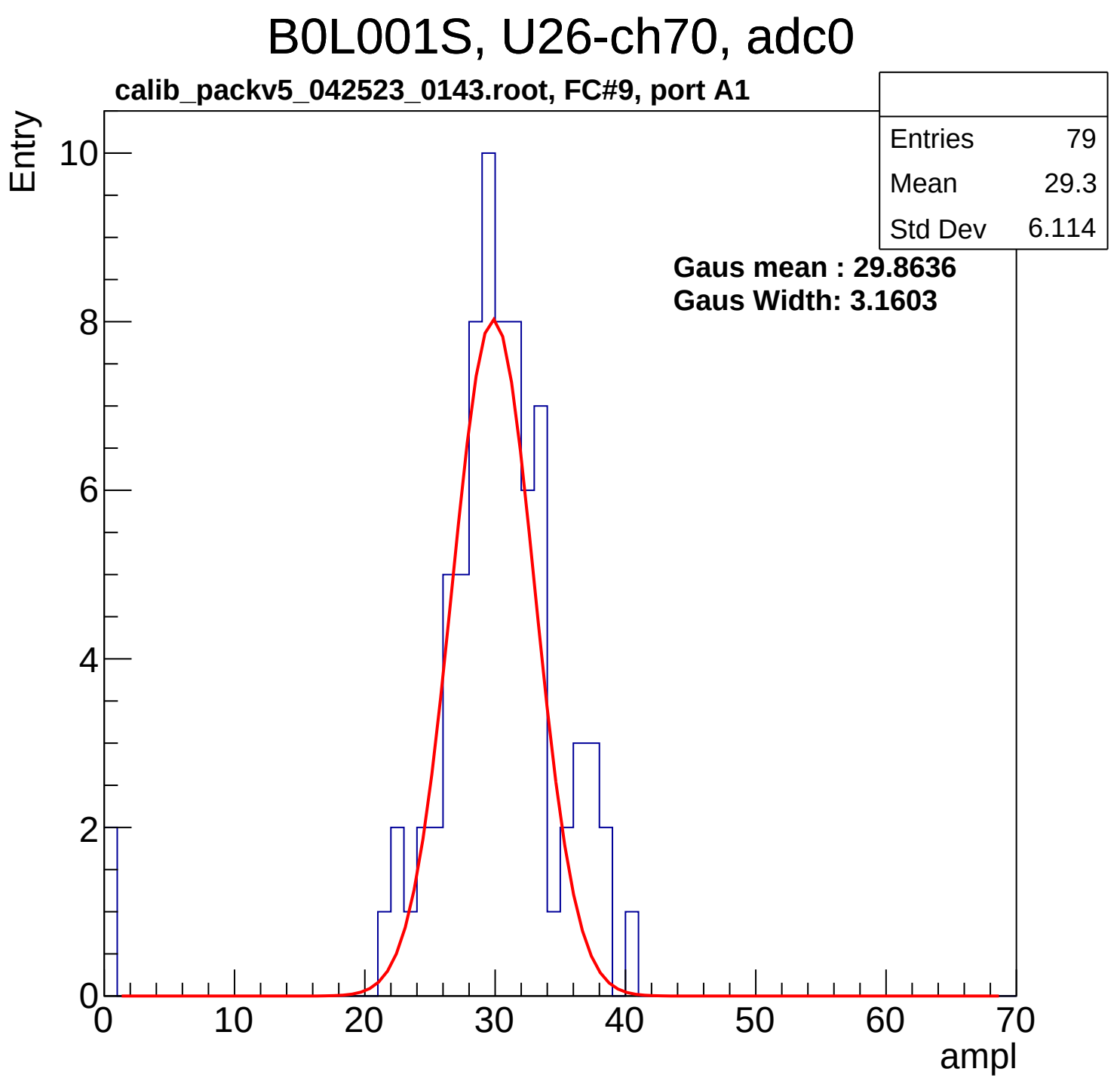
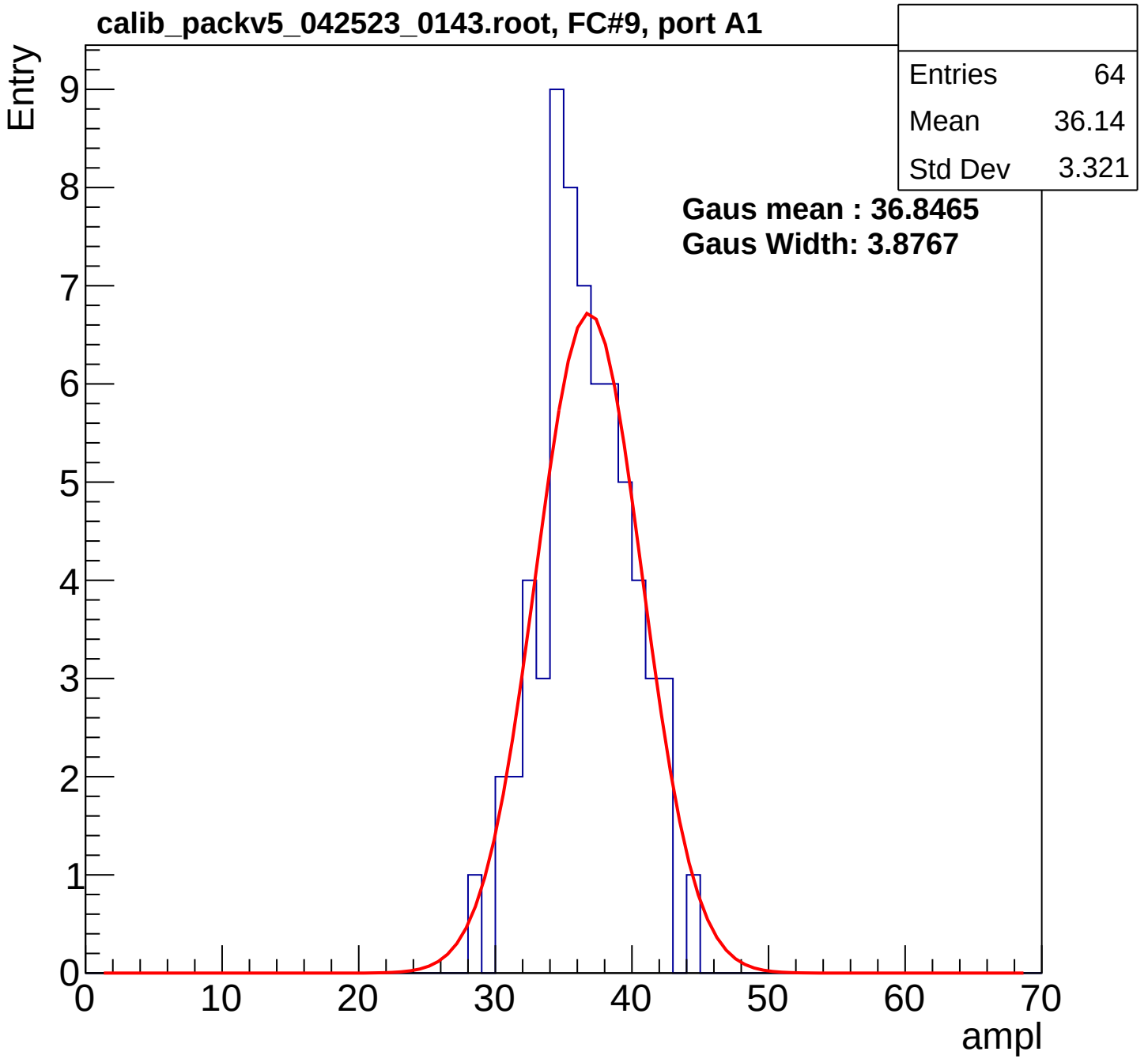
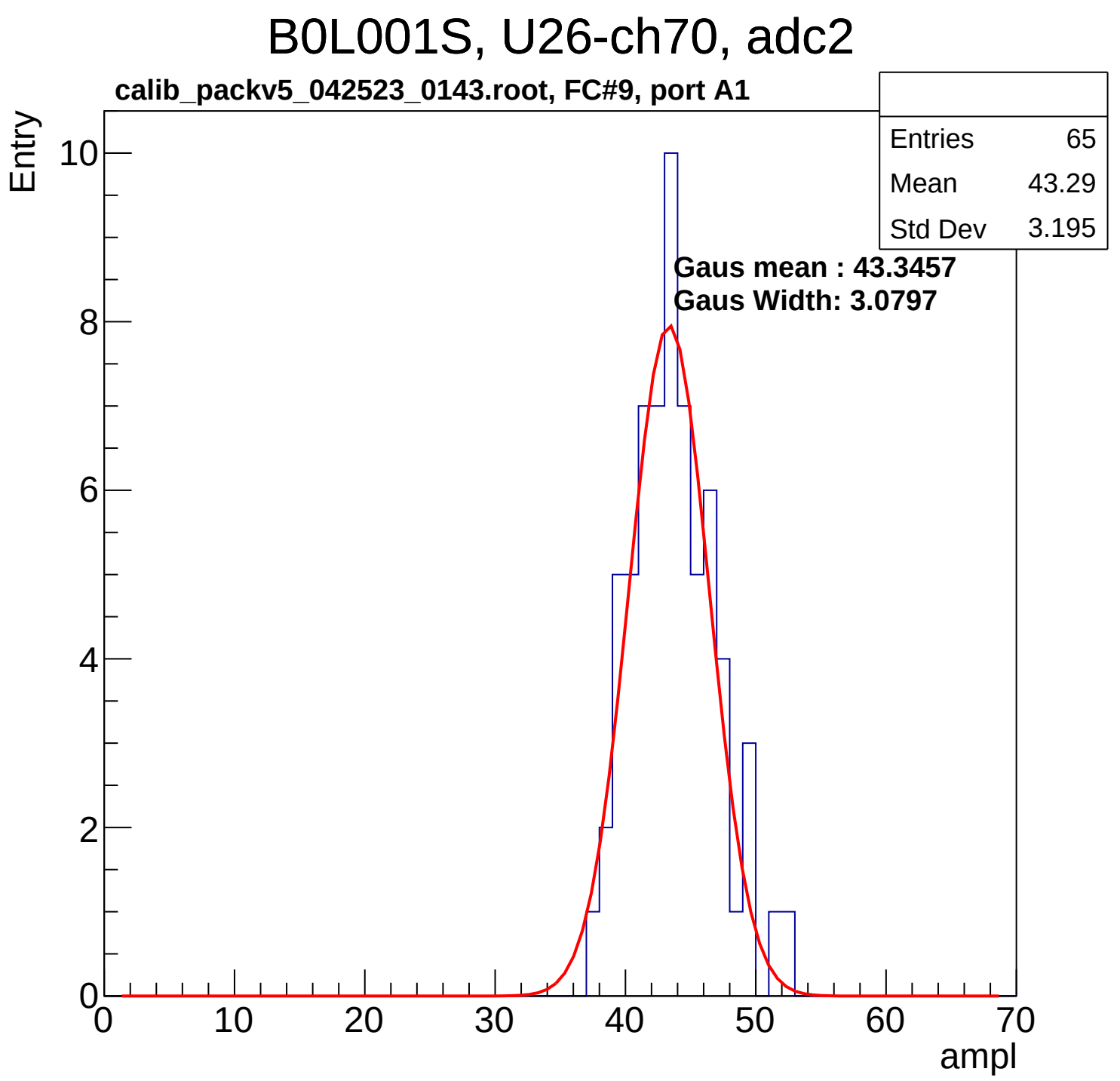
50
60
70
ampl

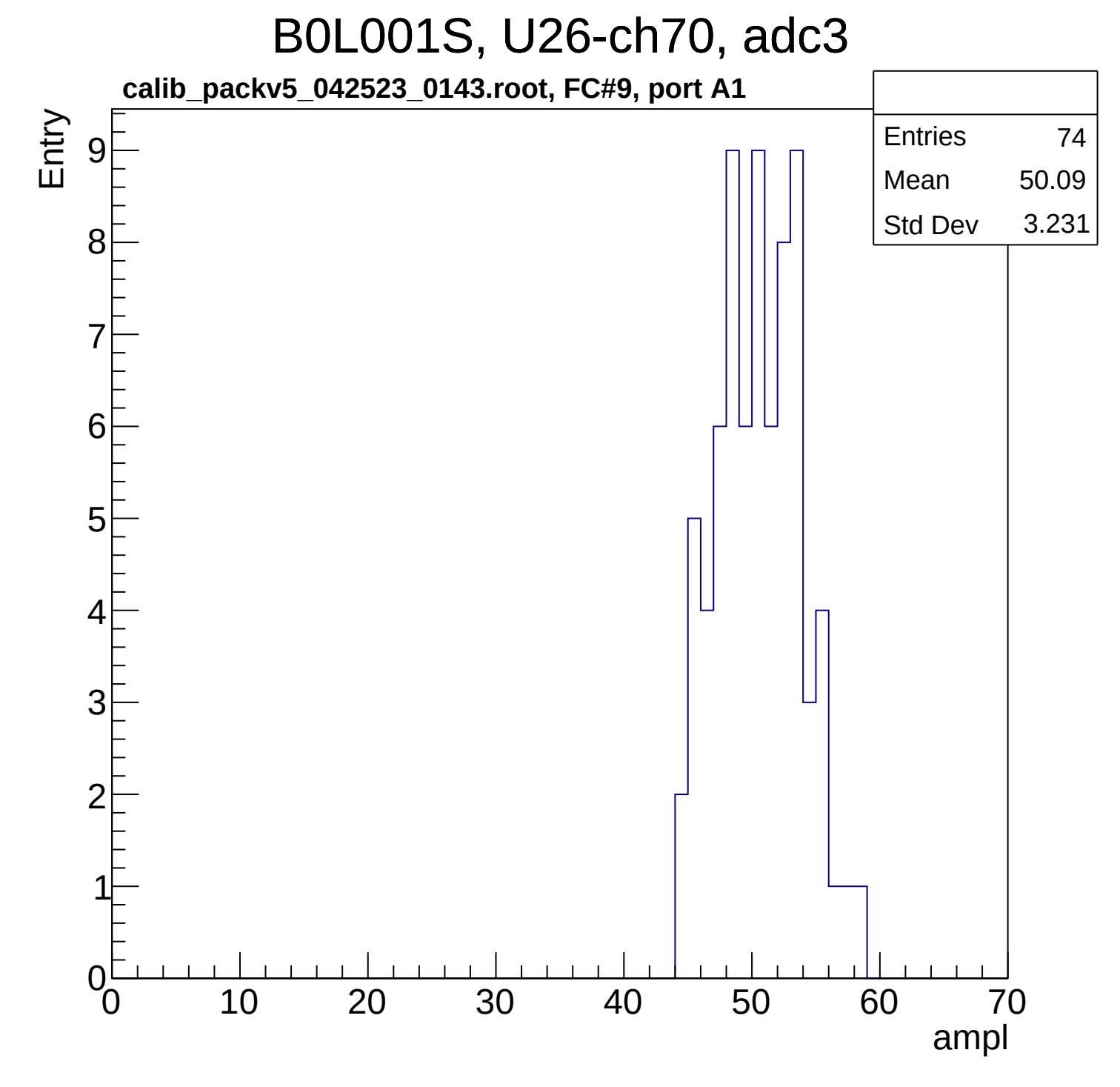

B0L001S, U26-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

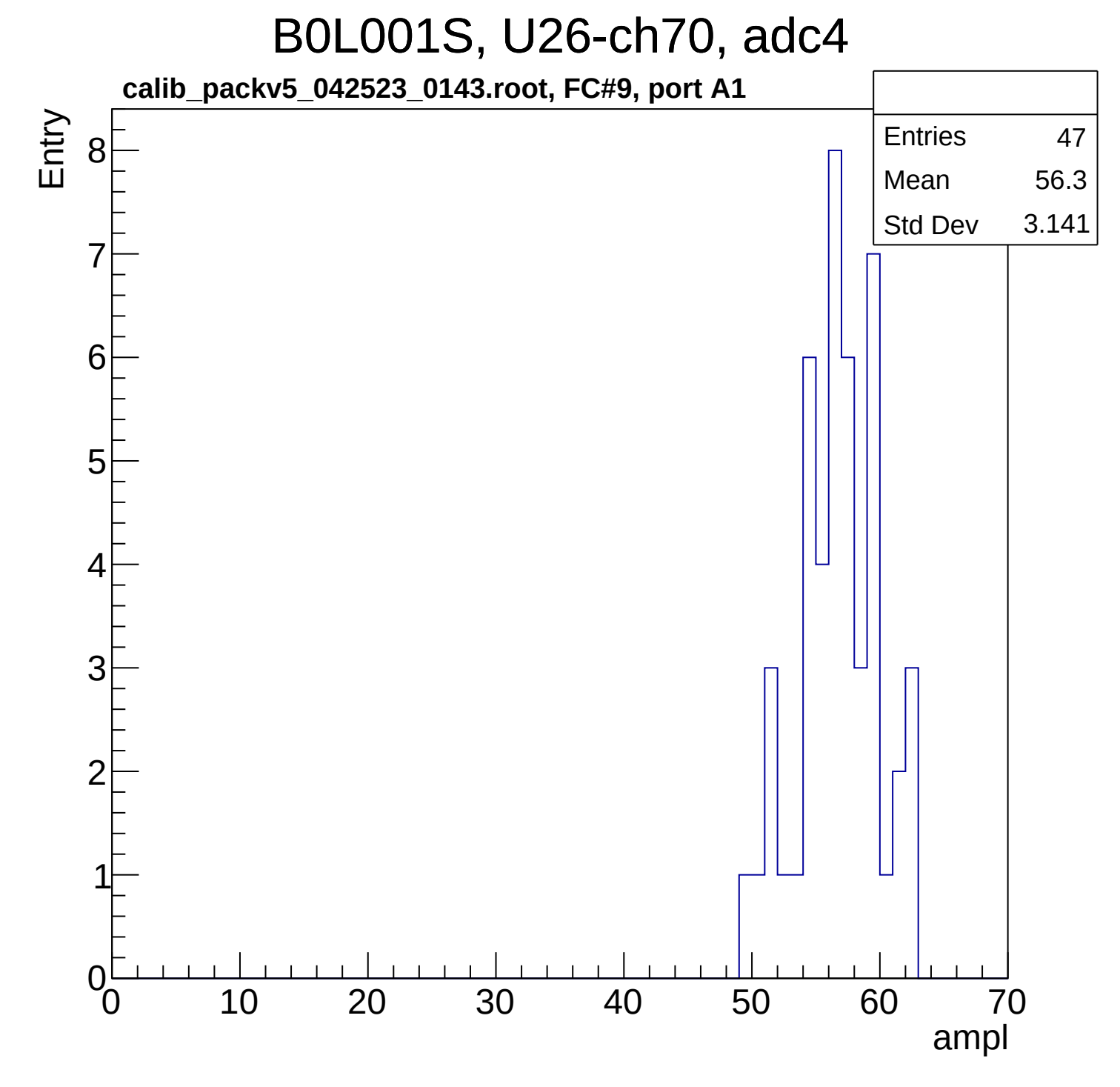

B0L001S, U26-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

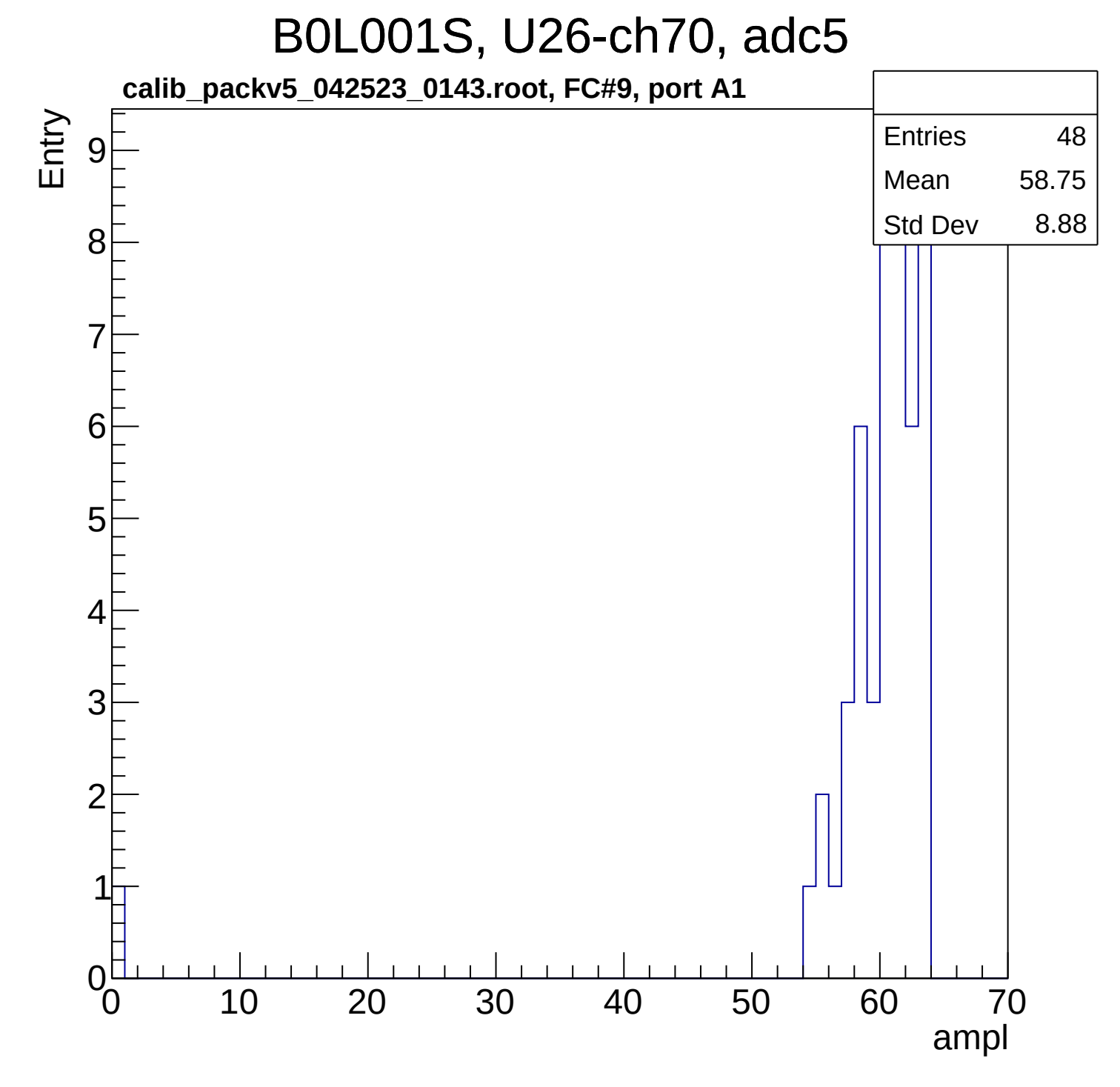

B0L001S, U26-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 29.3
Std Dev 6.114
Gaus mean : 29.8636
Gaus Width: 3.1603
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.14
Std Dev 3.321
Gaus mean : 36.8465
Gaus Width: 3.8767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.29
Std Dev 3.195
Gaus mean : 43.3457
Gaus Width: 3.0797
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.09
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.3
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.75
Std Dev 8.88
Entry
ampl
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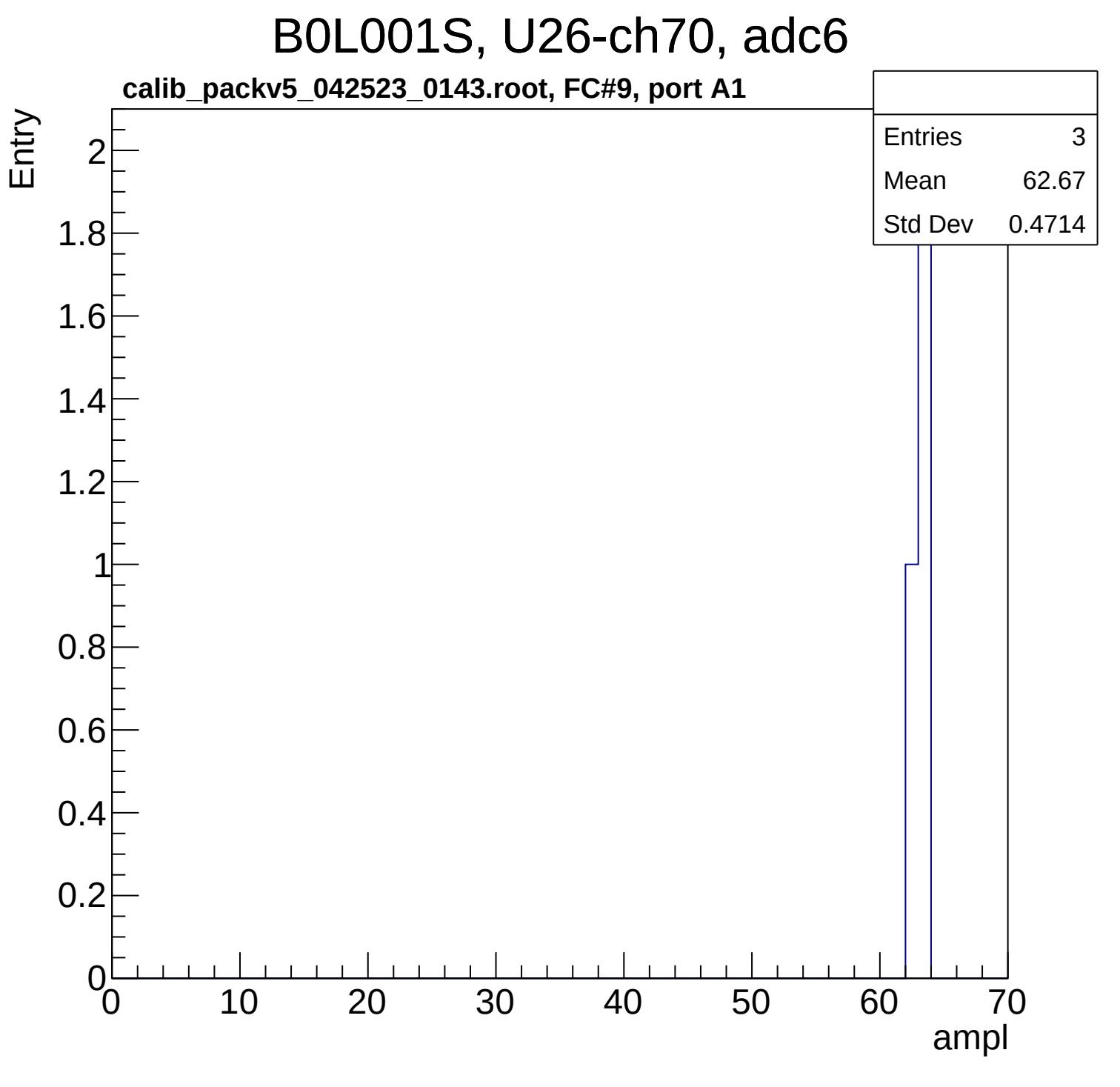
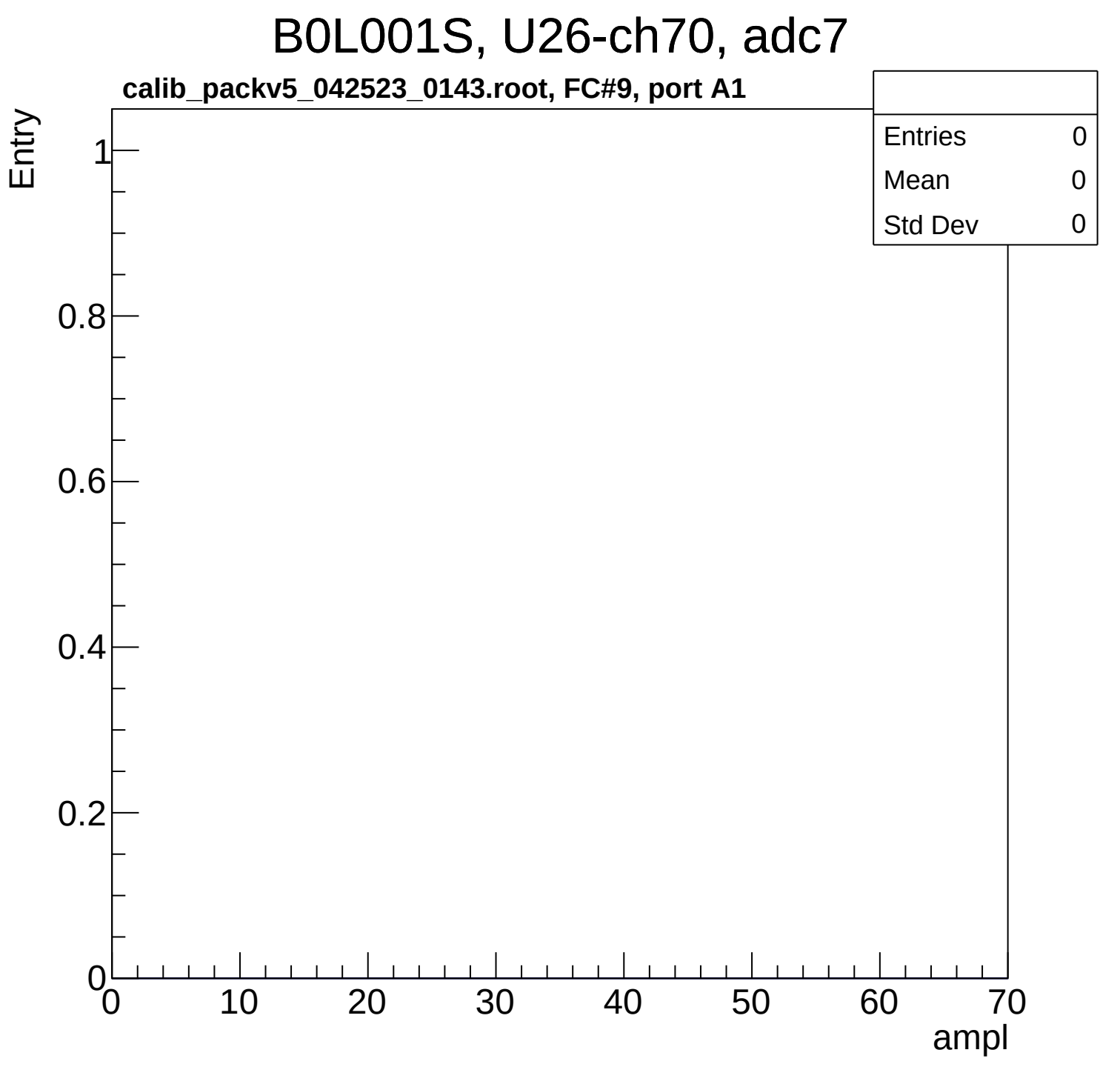
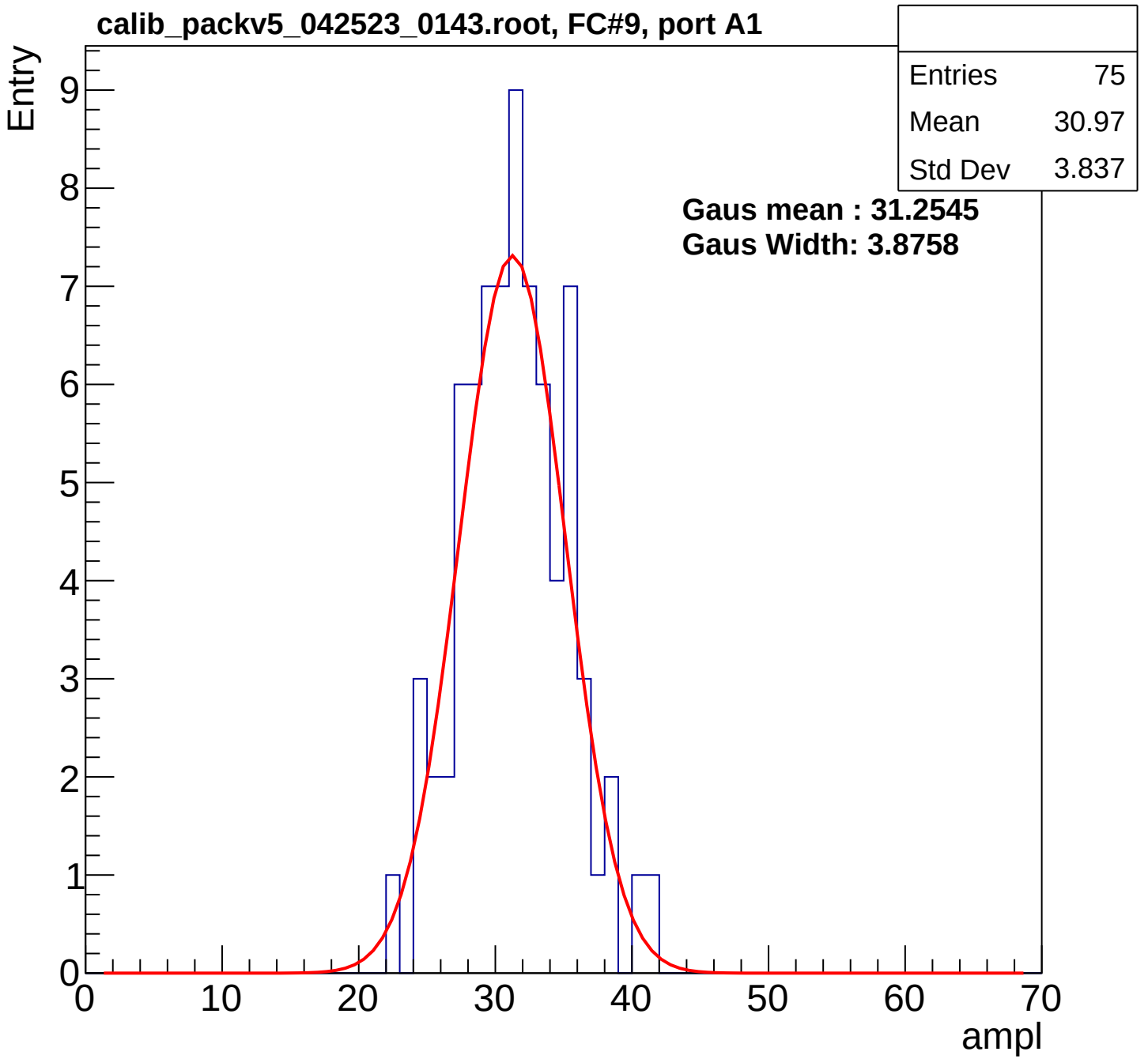
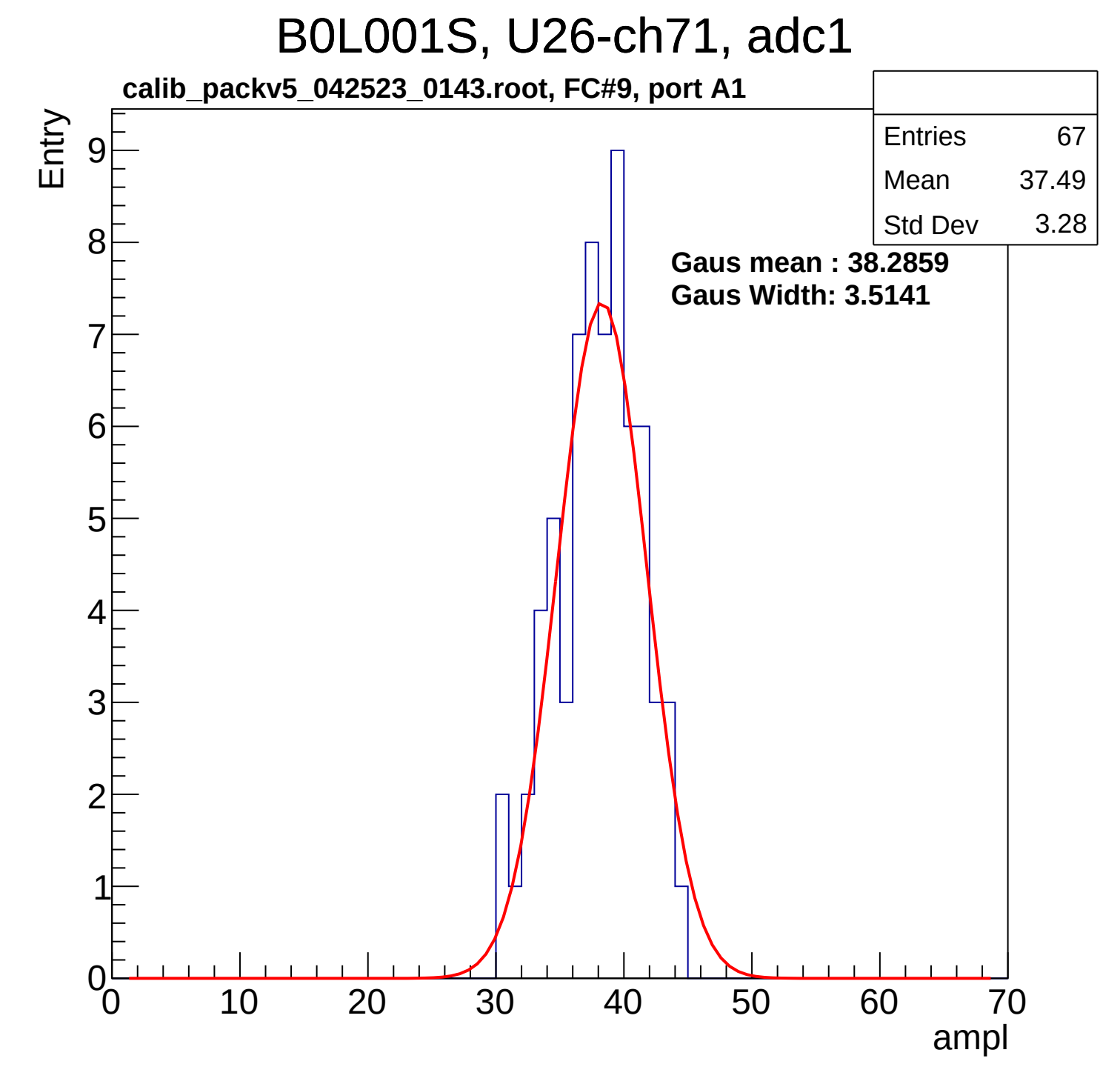
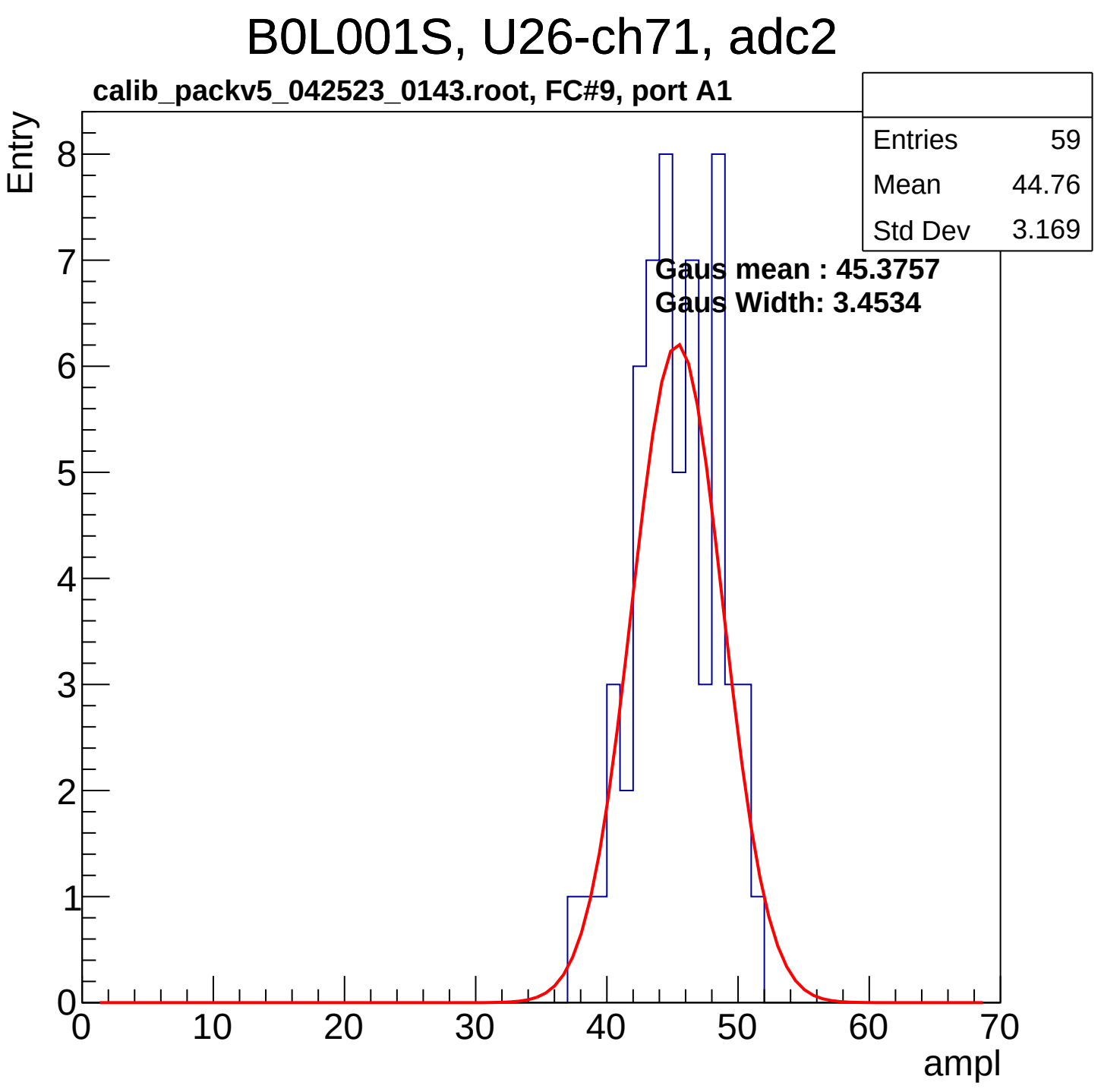
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.97
Std Dev 3.837
Gaus mean : 31.2545
Gaus Width: 3.8758
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.49
Std Dev 3.28
Gaus mean : 38.2859
Gaus Width: 3.5141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.76
Std Dev 3.169
Gaus mean : 45.3757
Gaus Width: 3.4534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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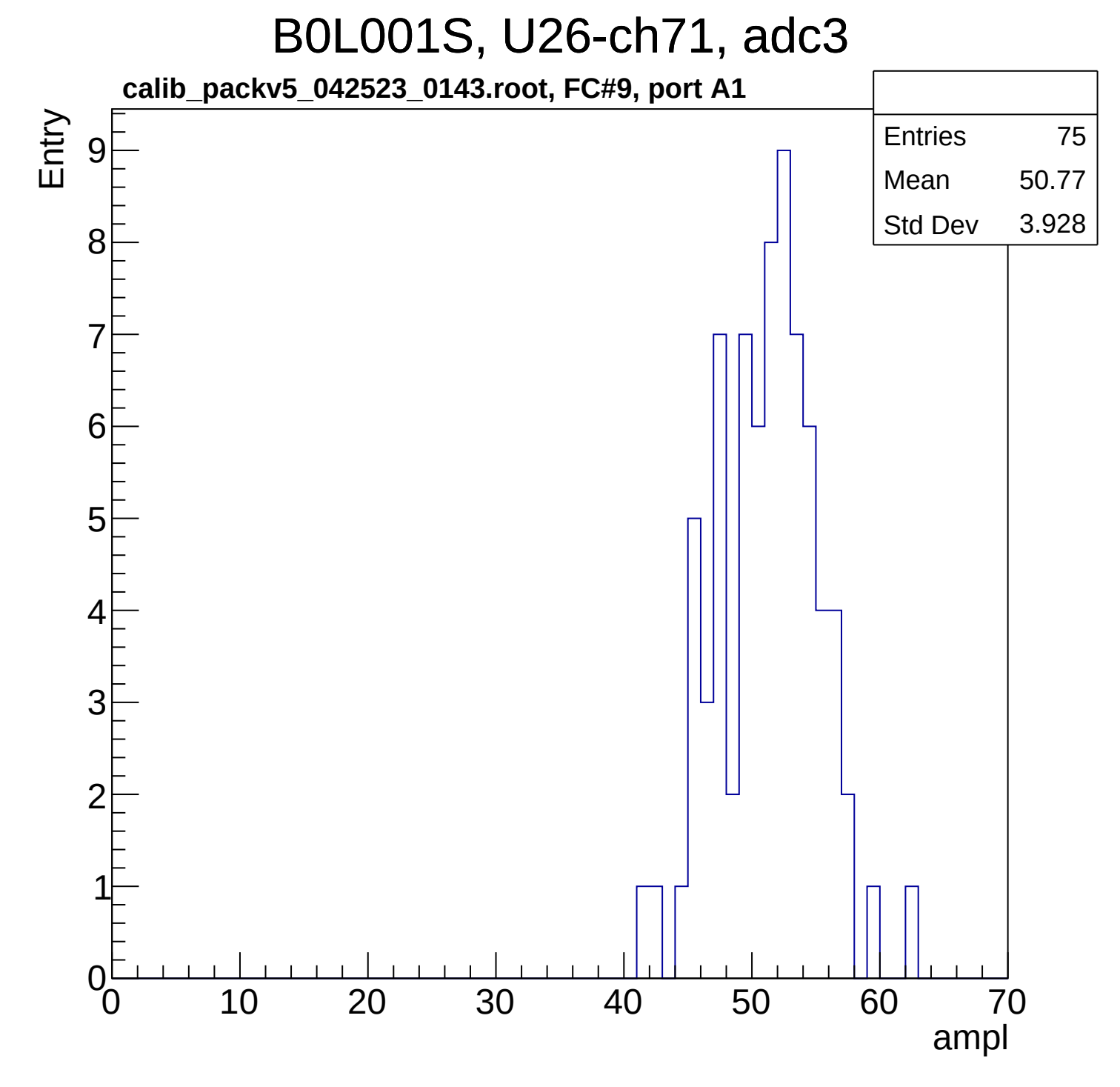
B0L001S, U26-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.77
Std Dev 3.928
Entry
ampl
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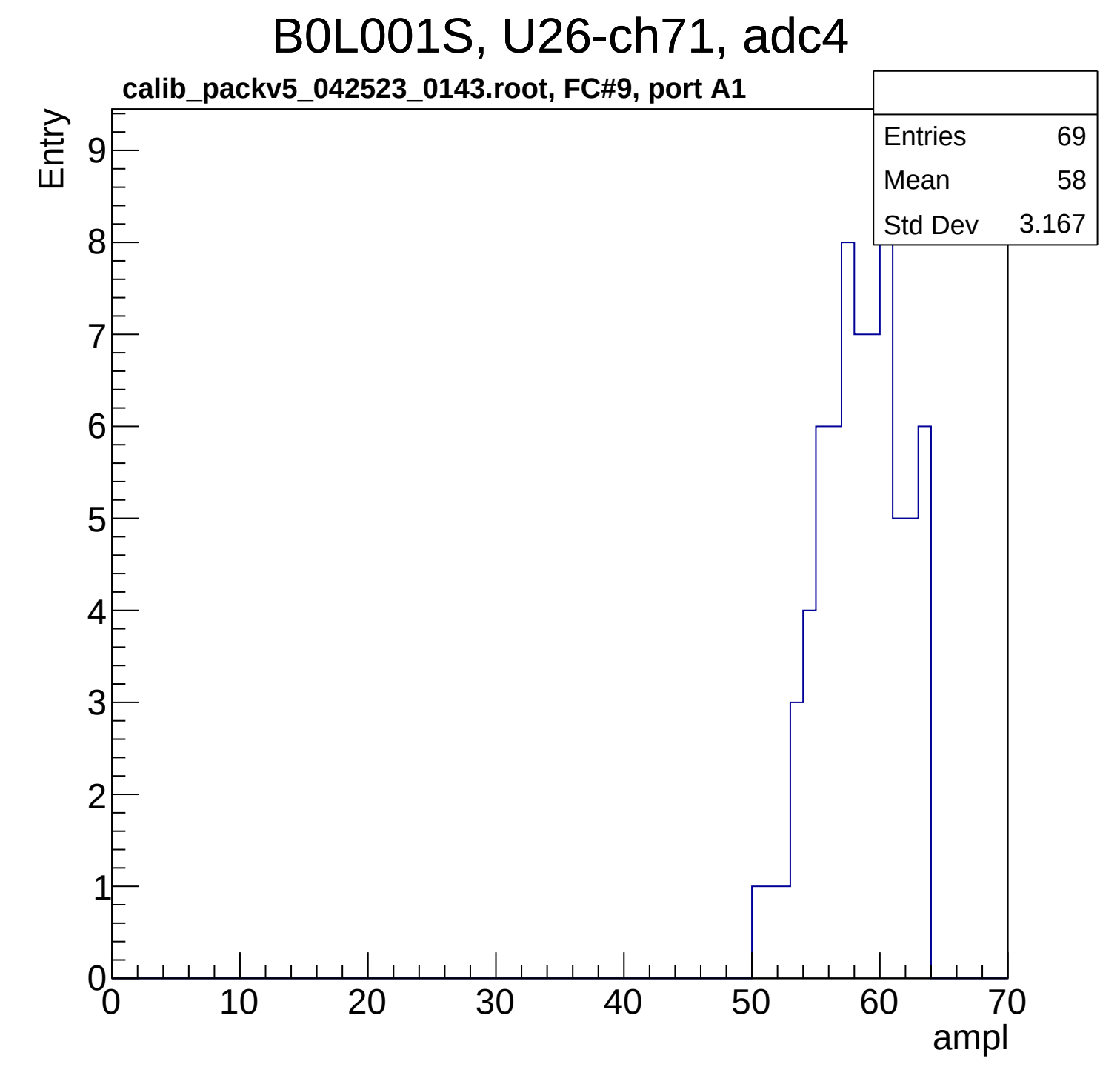
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 58
Std Dev 3.167
Entry
ampl
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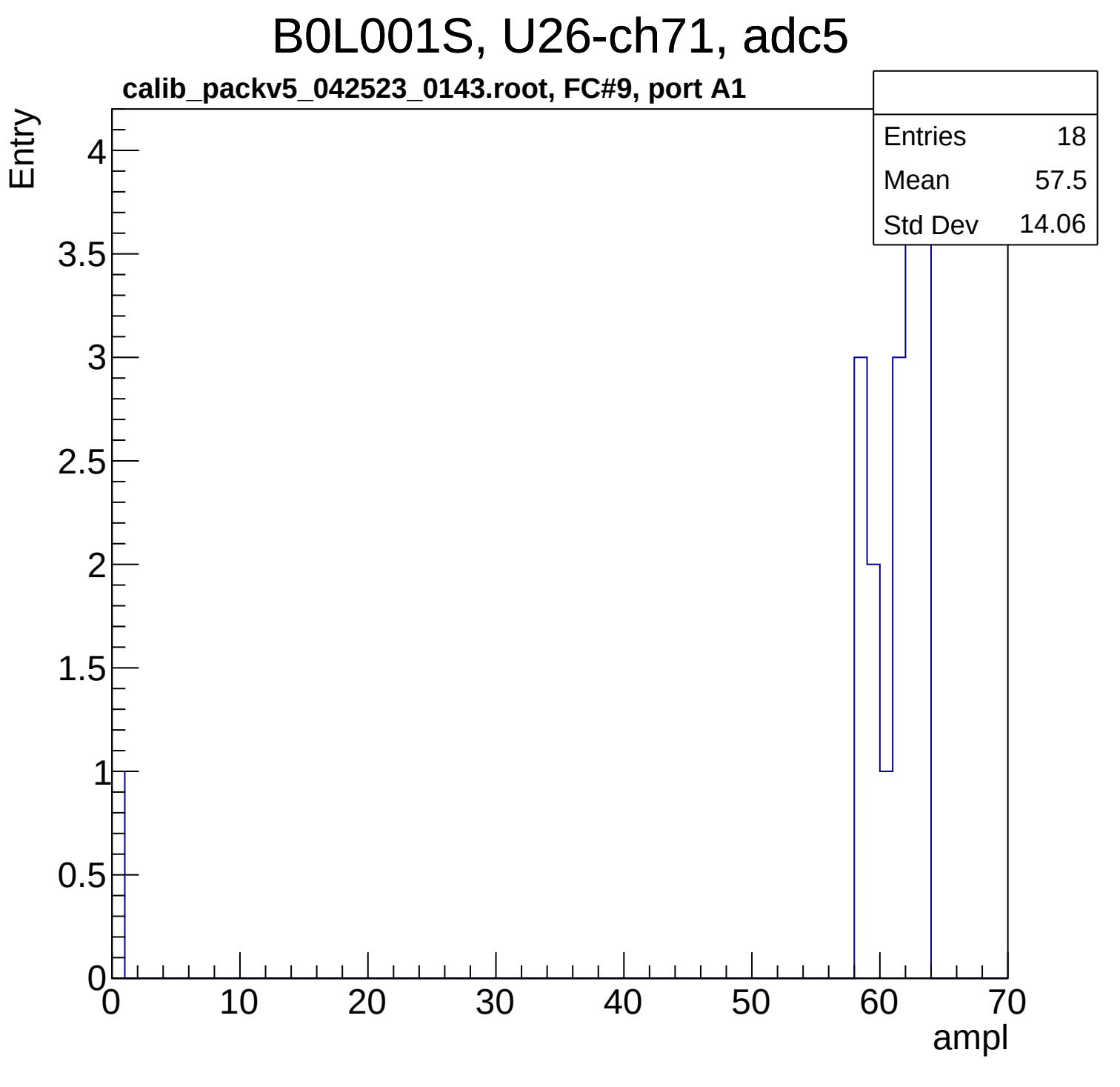
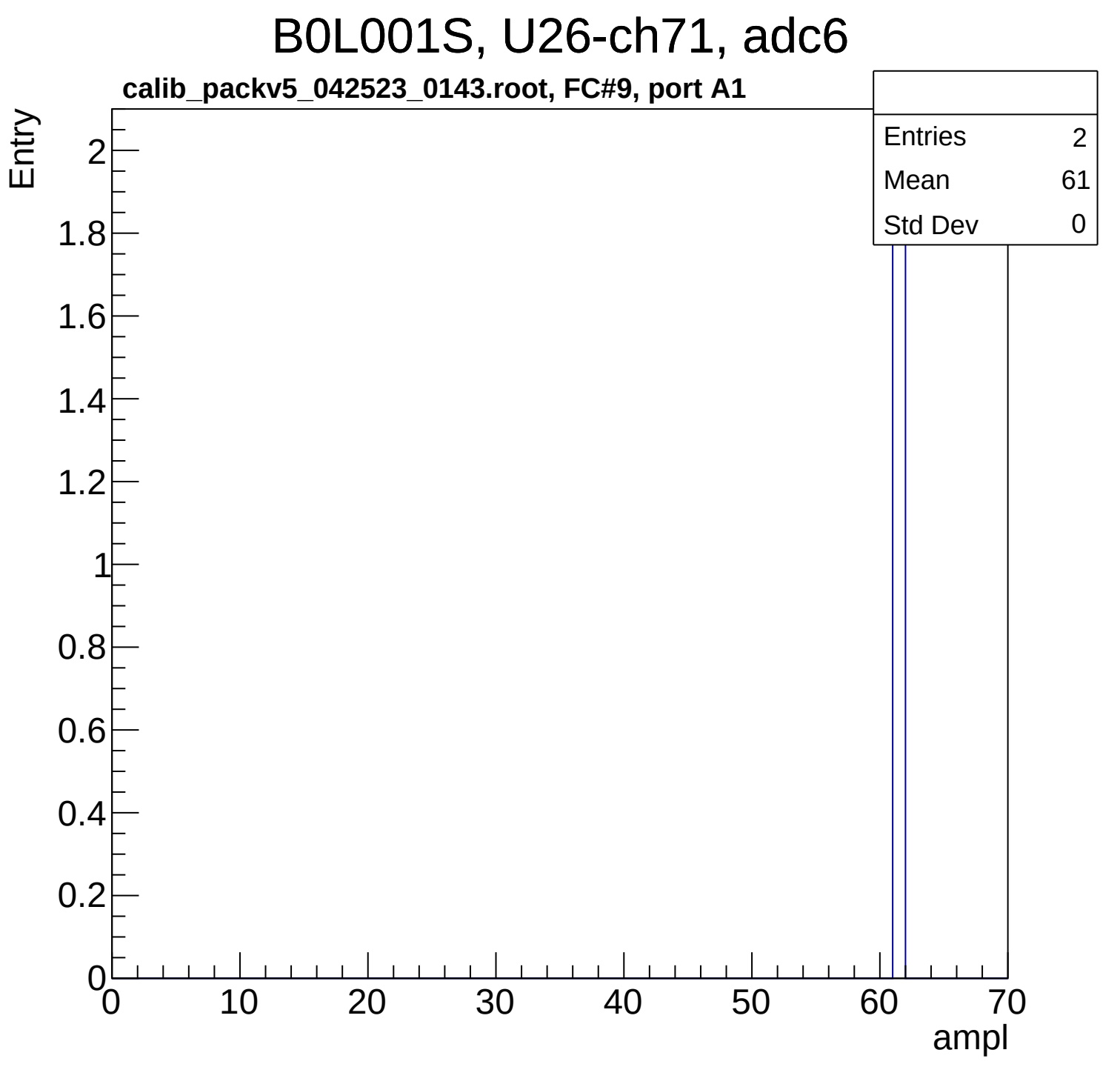
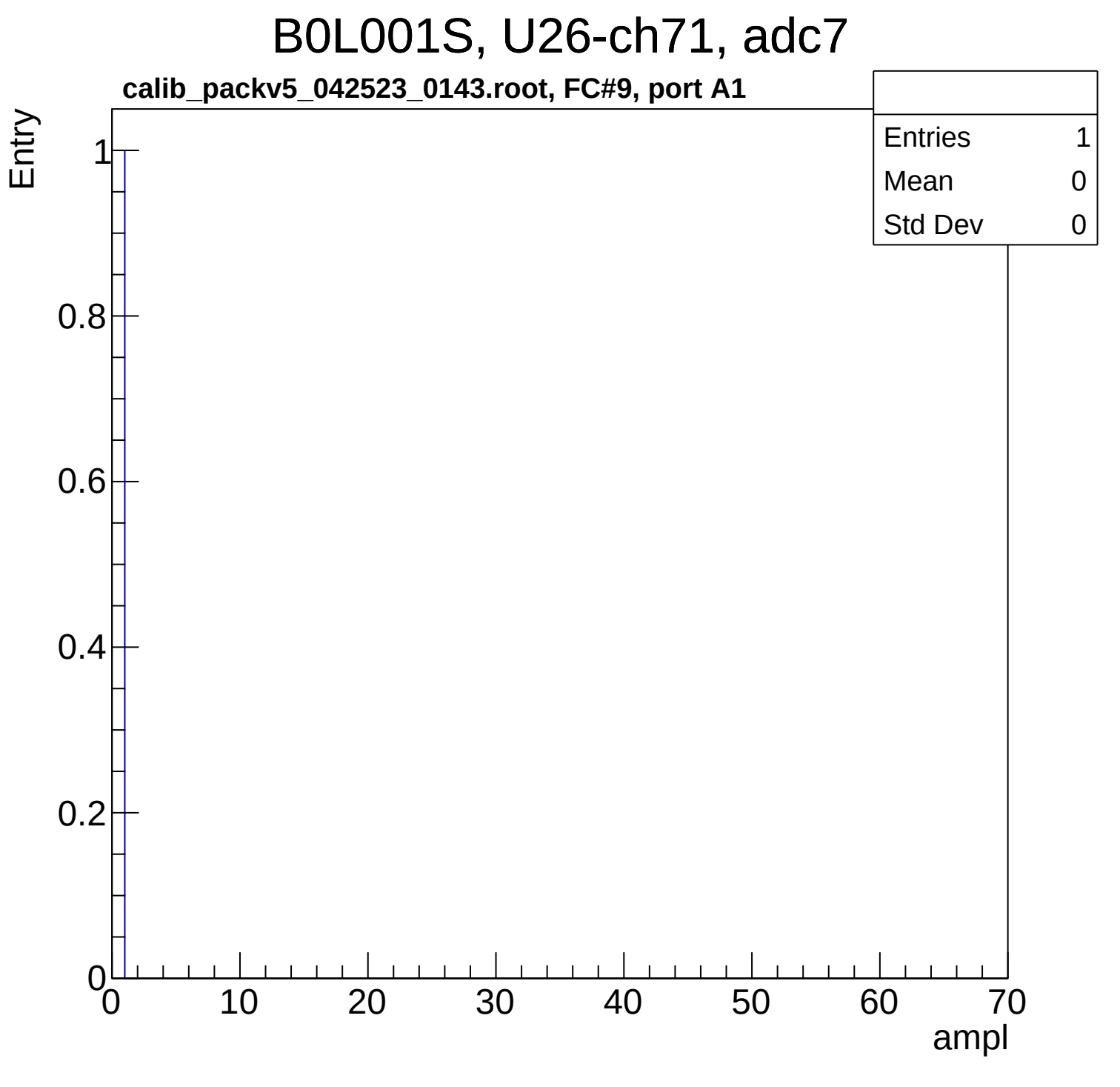
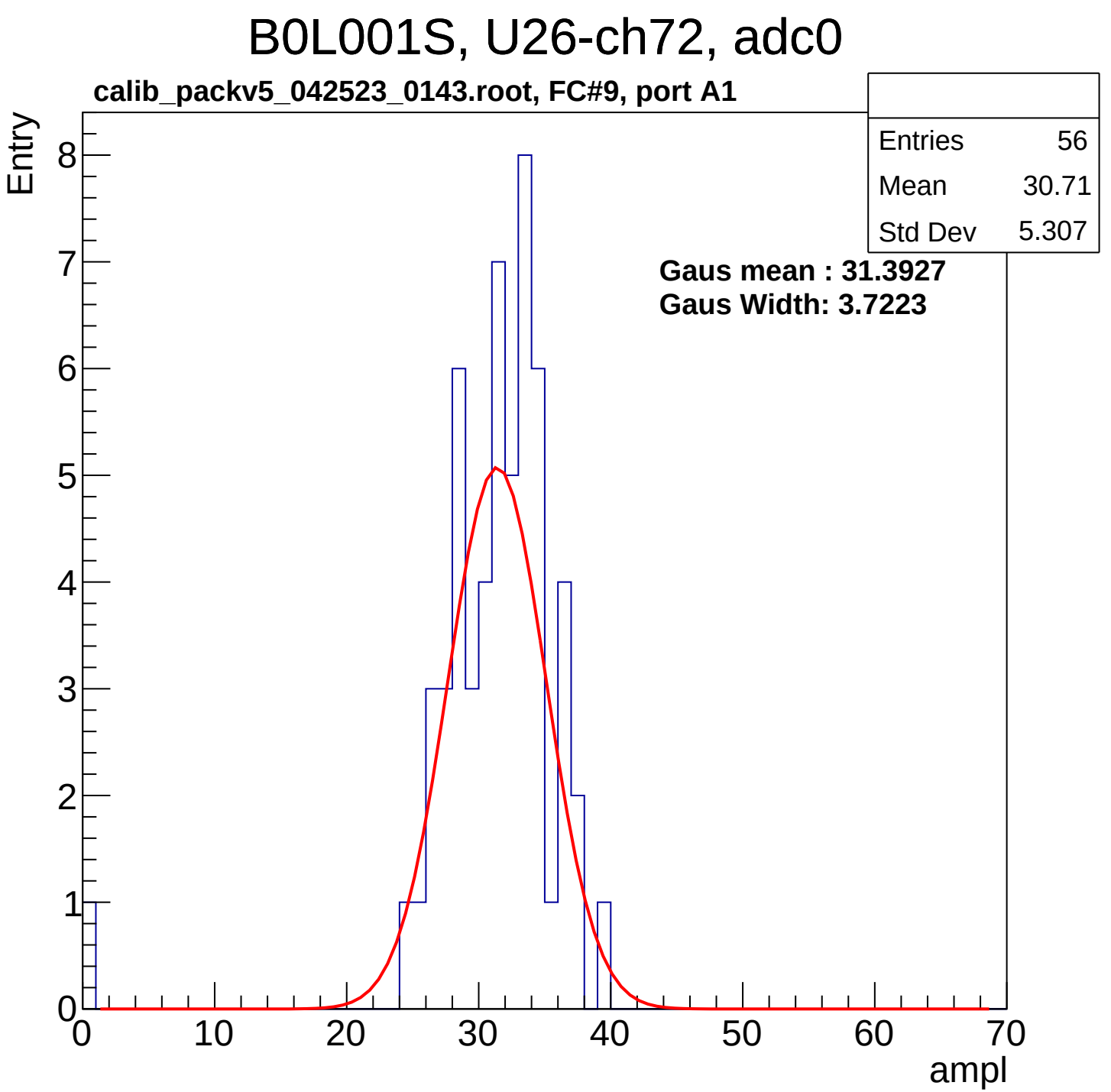
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 57.5
Std Dev 14.06
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.71
Std Dev 5.307
Gaus mean : 31.3927
Gaus Width: 3.7223
Entry
ampl
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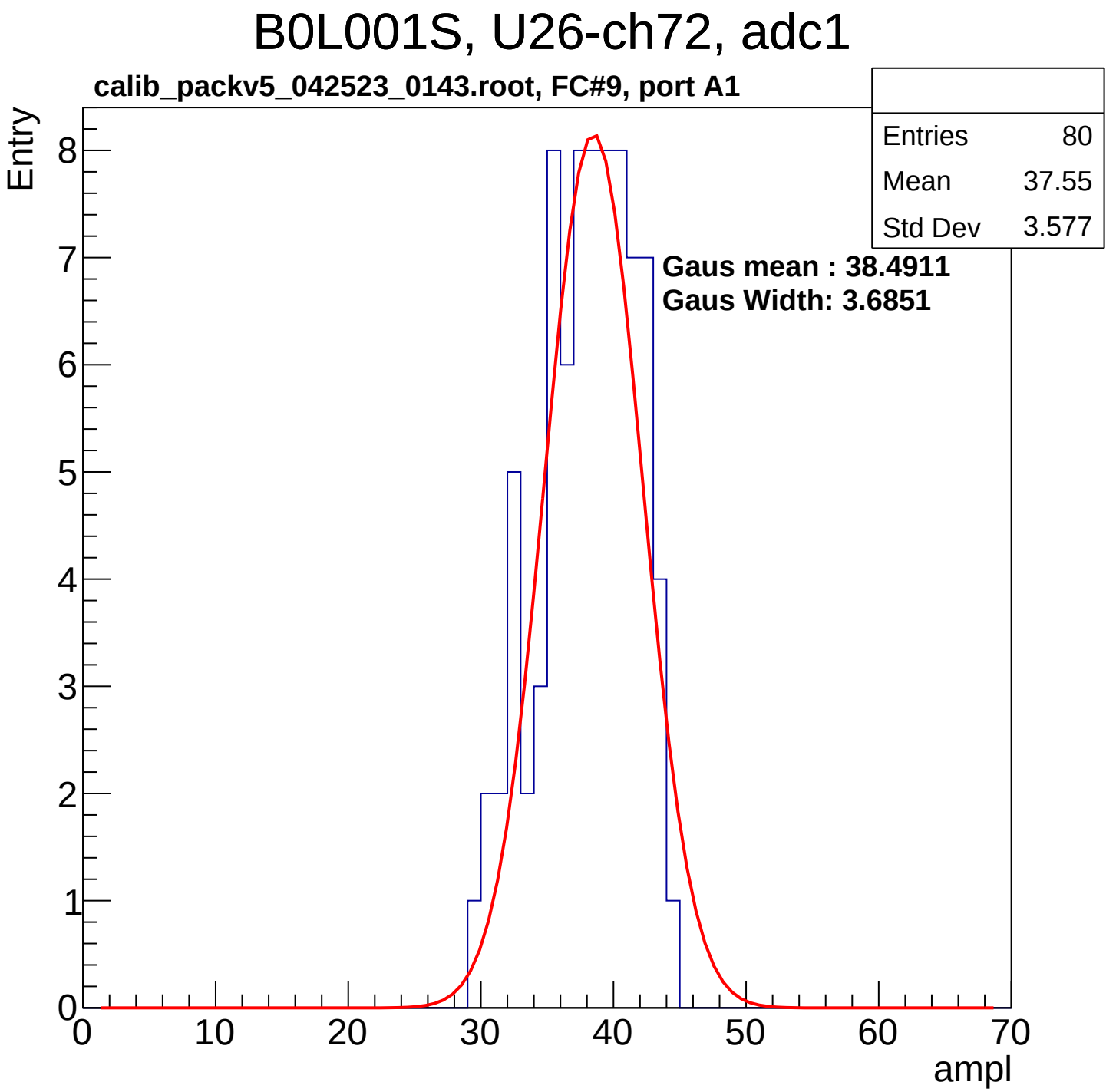
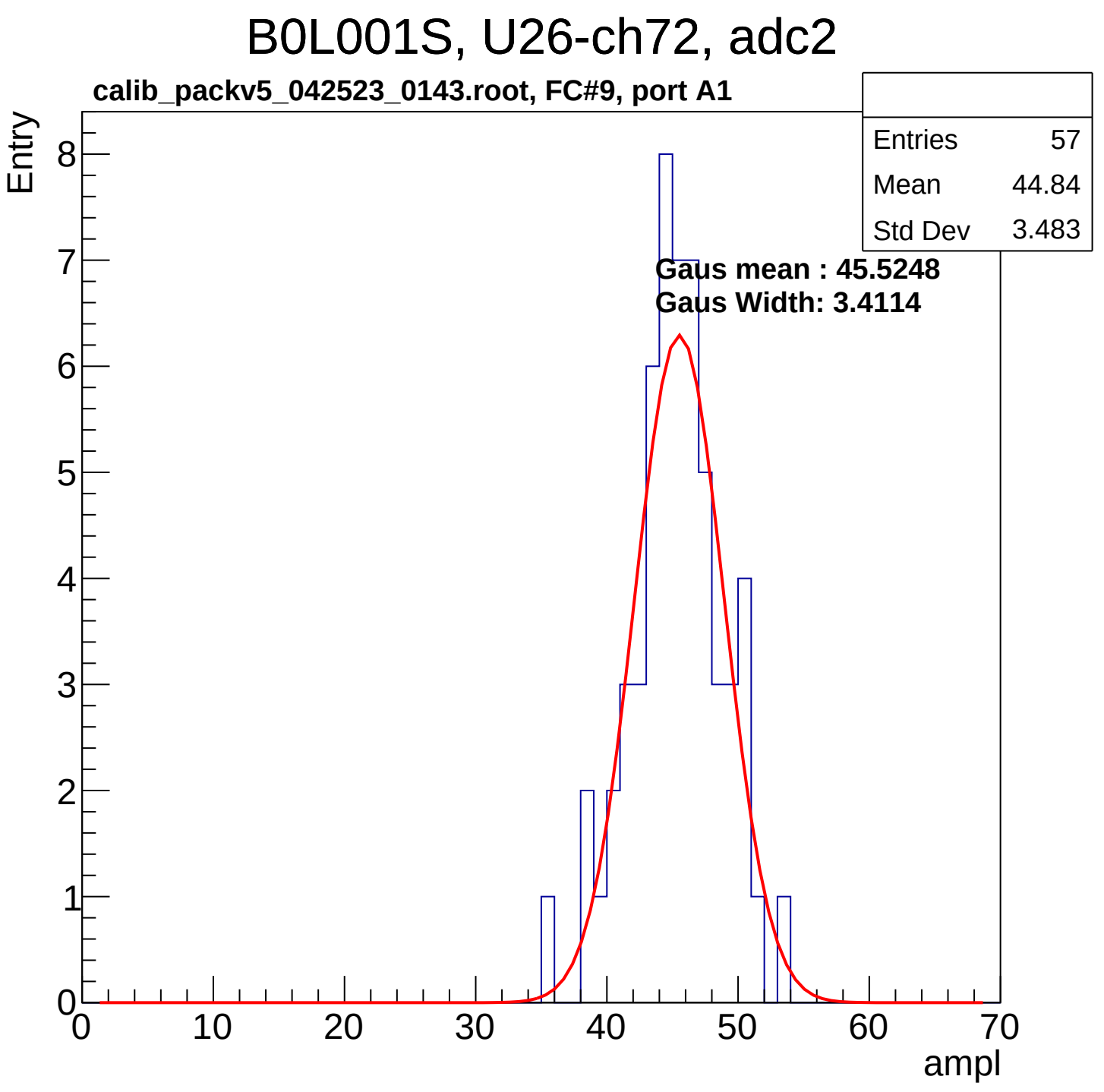
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.55
Std Dev 3.577
Gaus mean : 38.4911
Gaus Width: 3.6851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.84
Std Dev 3.483
Gaus mean : 45.5248
Gaus Width: 3.4114
Entry
ampl
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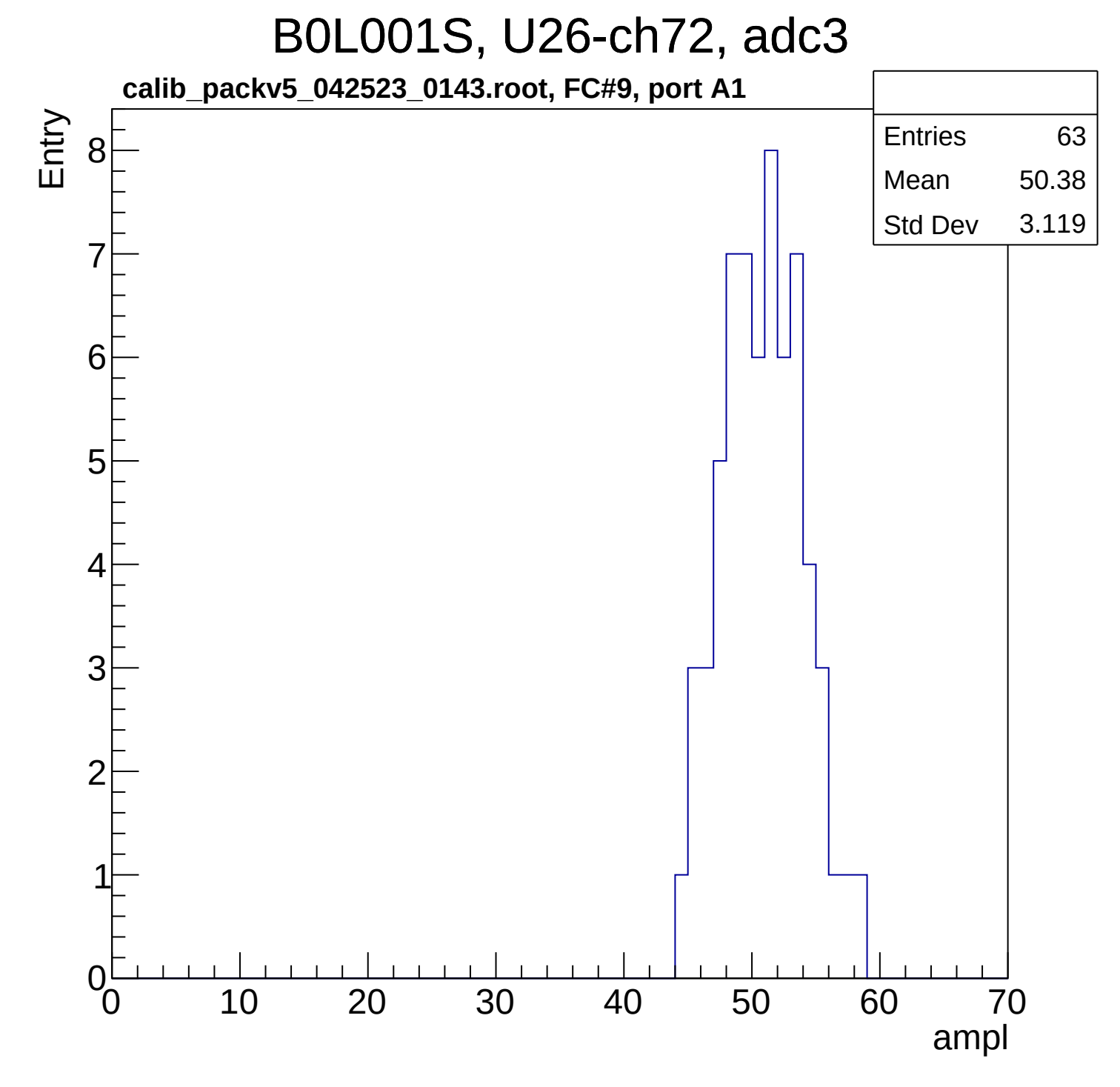
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.38
Std Dev 3.119
Entry
ampl
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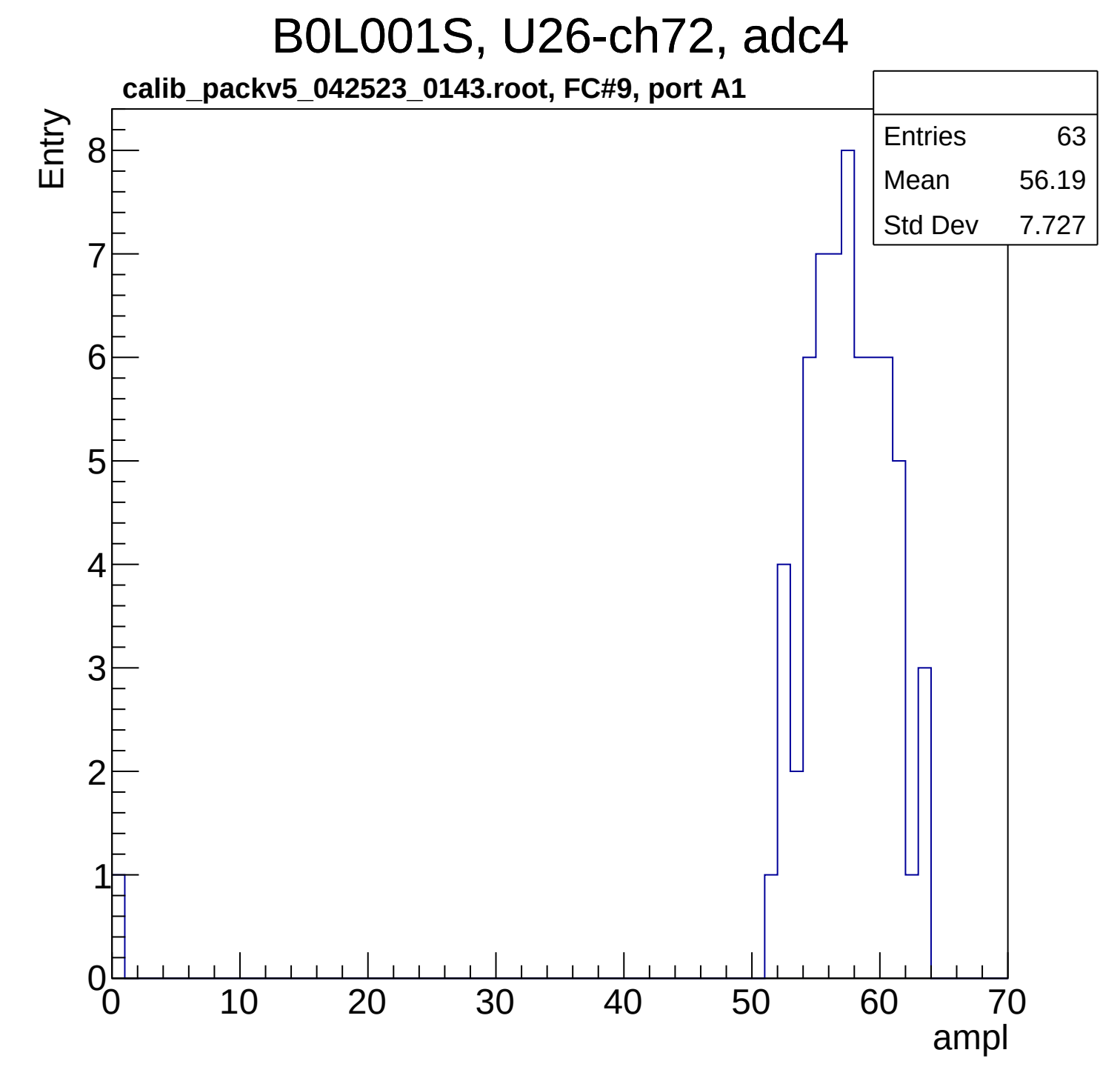
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.19
Std Dev 7.727
Entry
ampl
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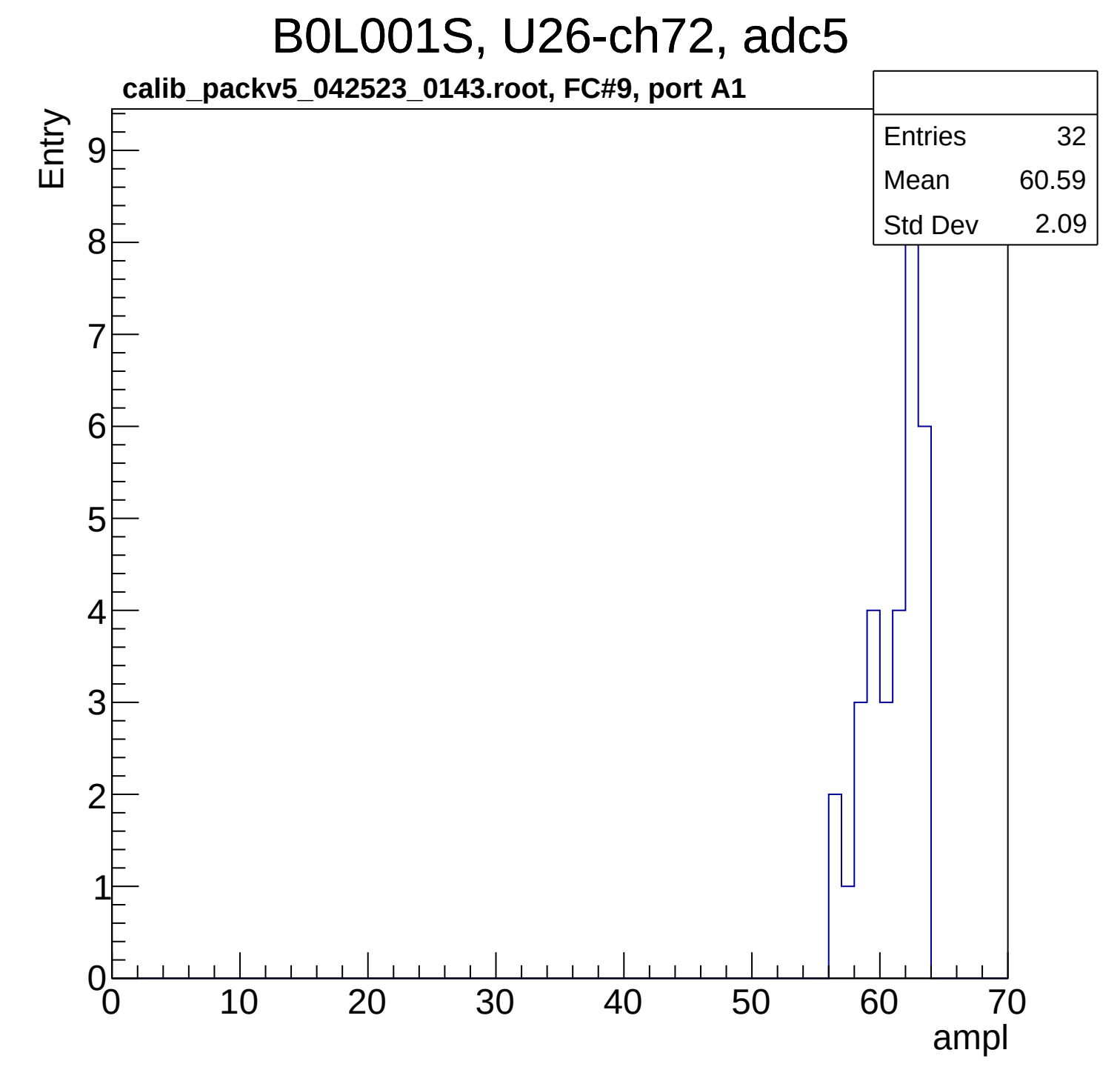
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.59
Std Dev 2.09
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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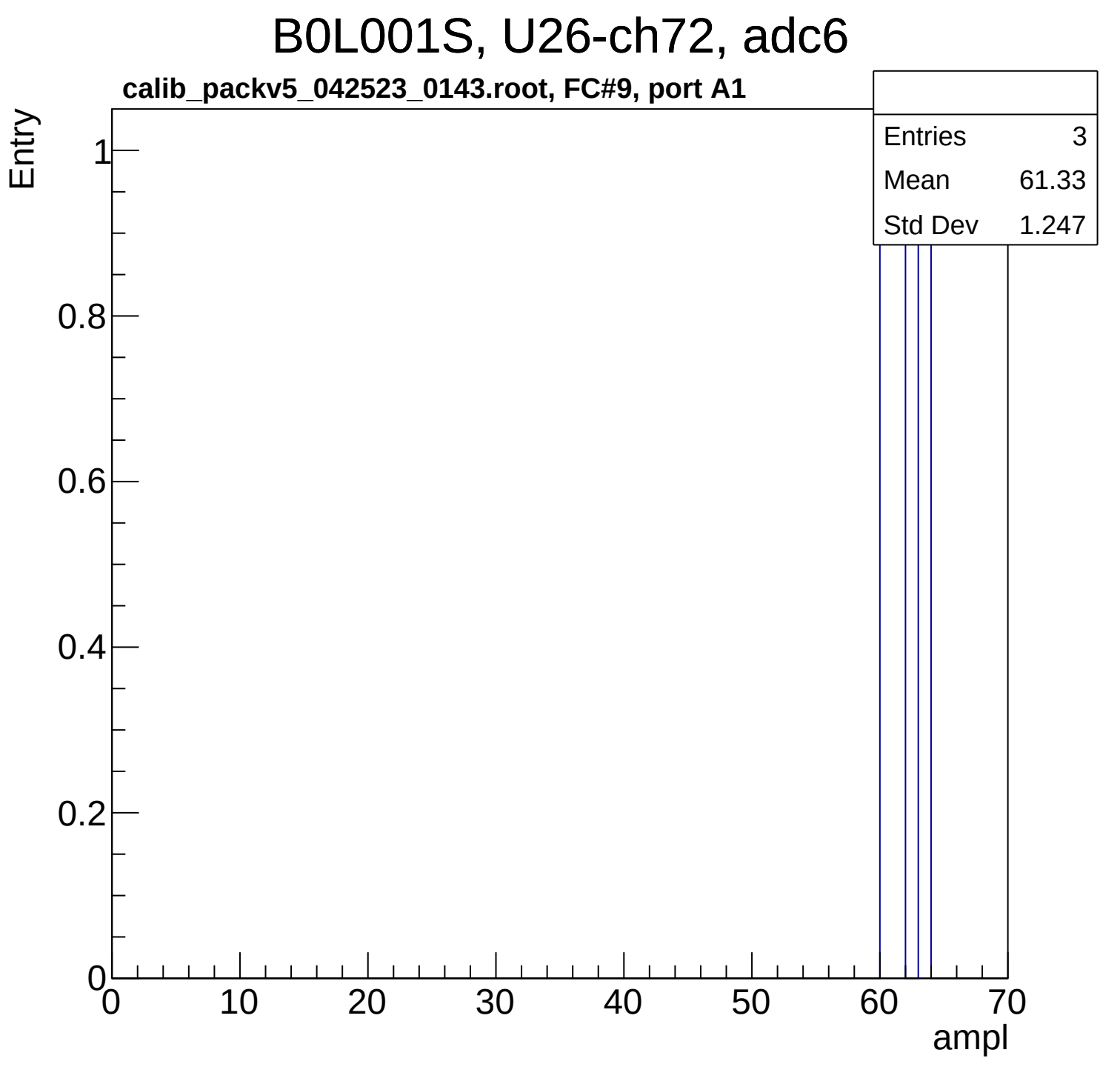
B0L001S, U26-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 1.247
Entry
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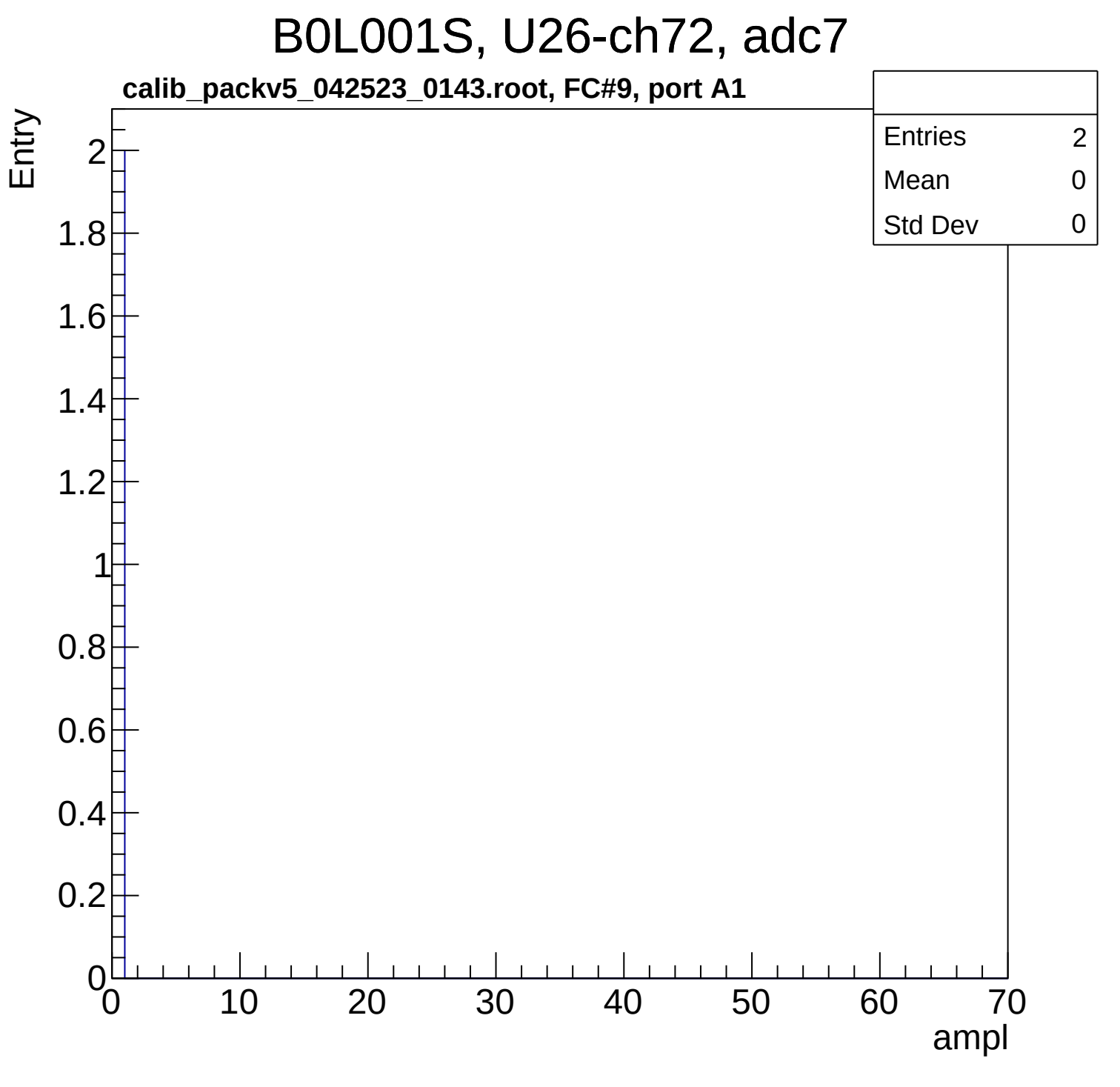
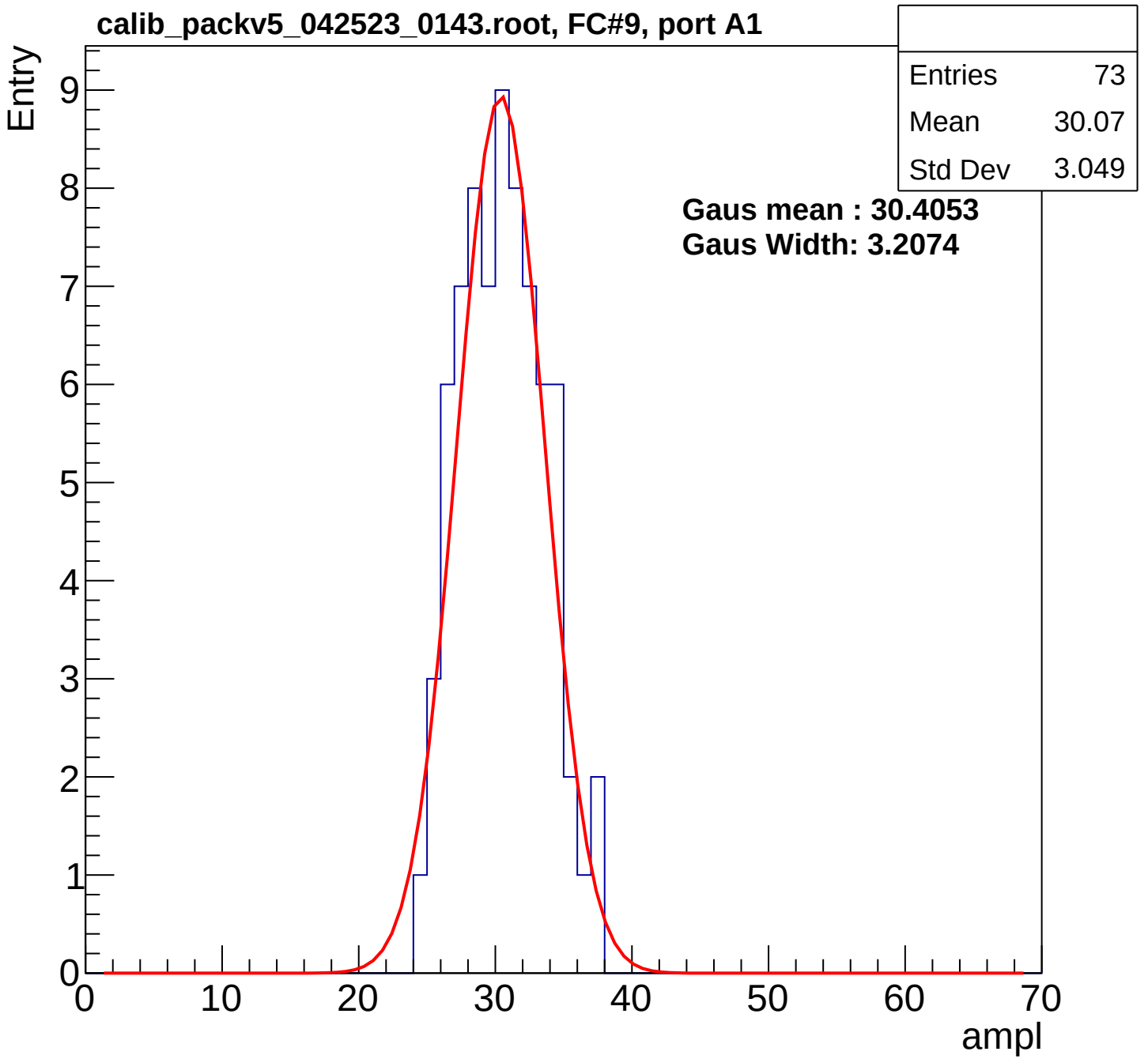
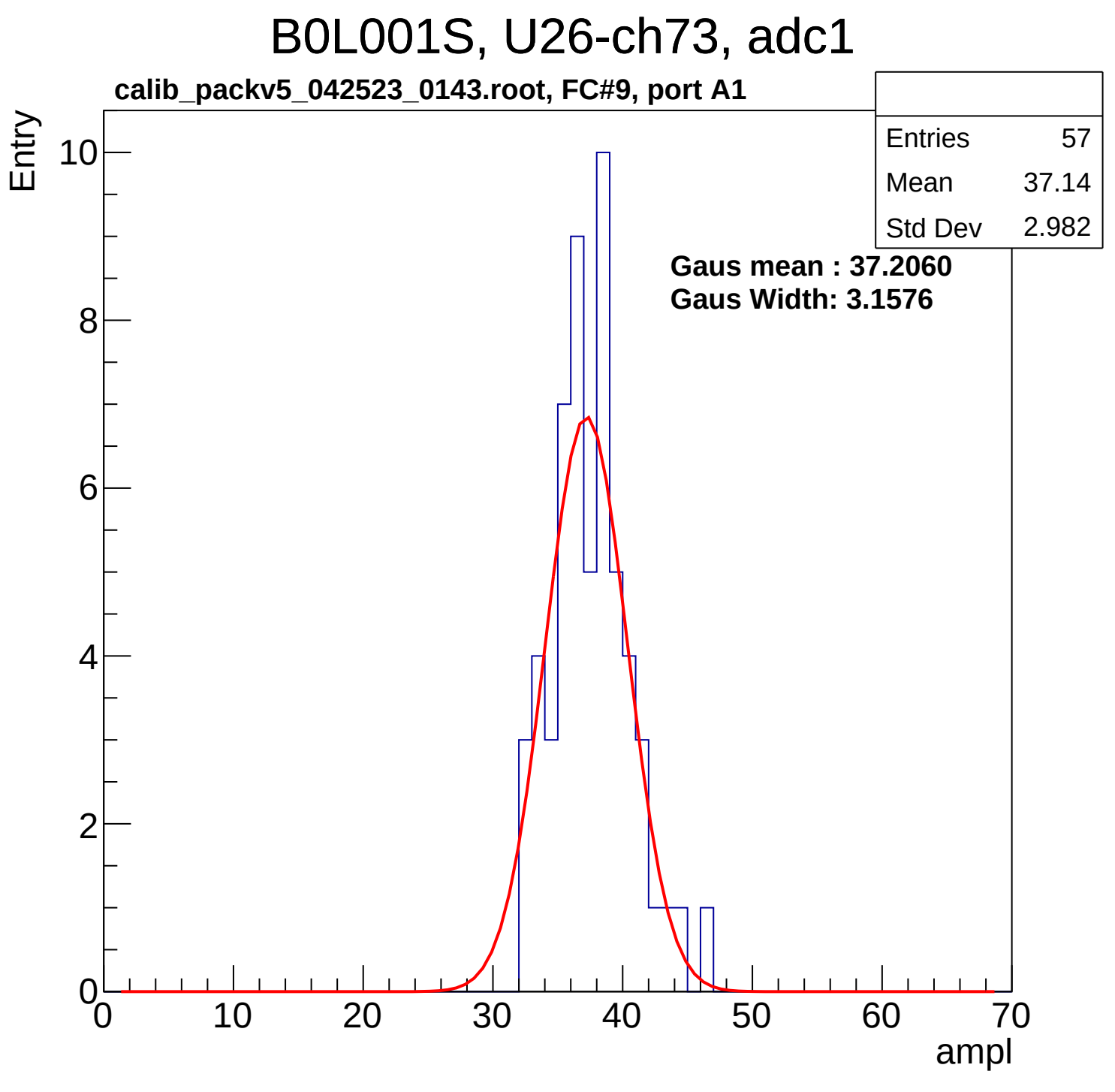
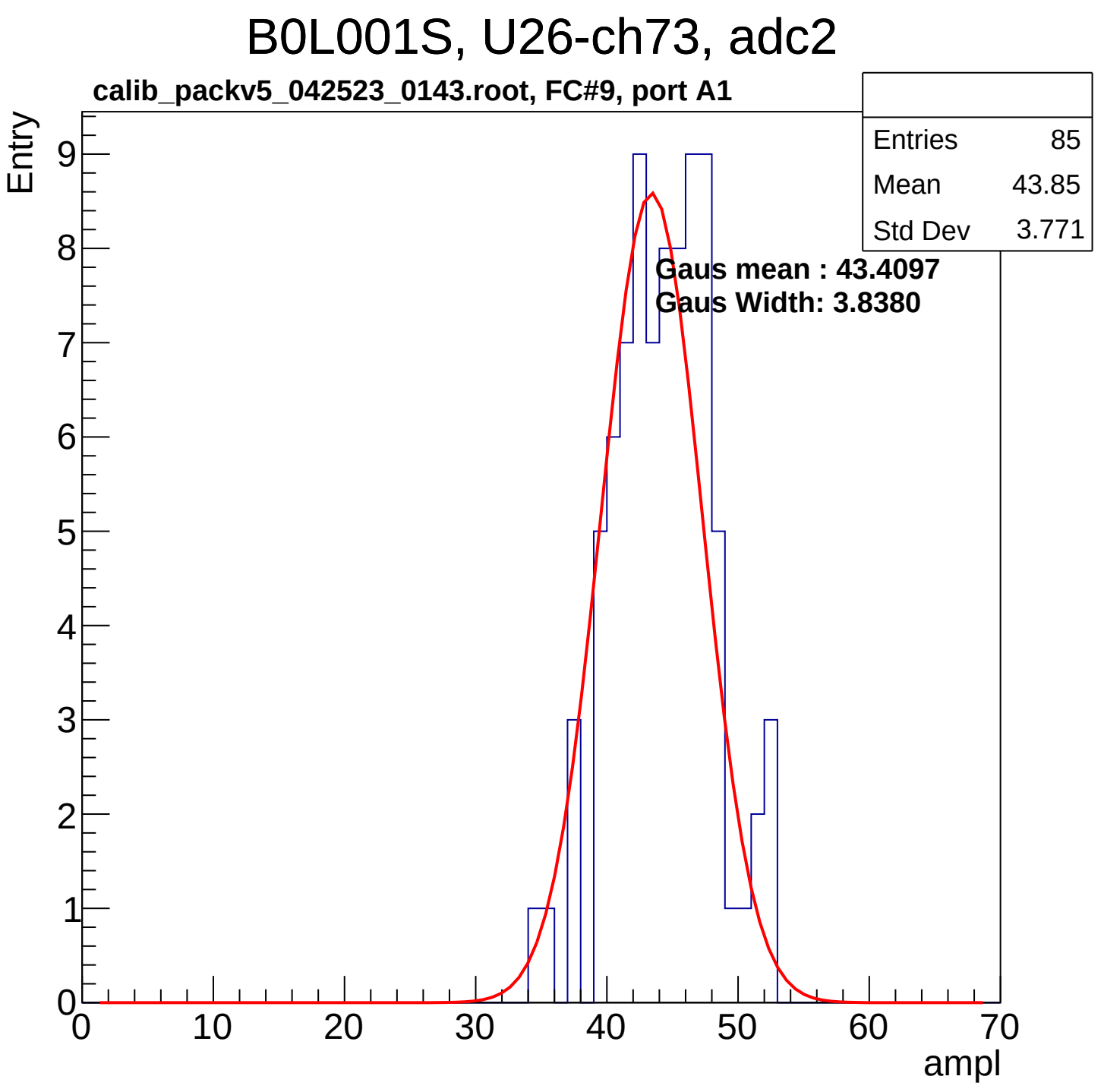
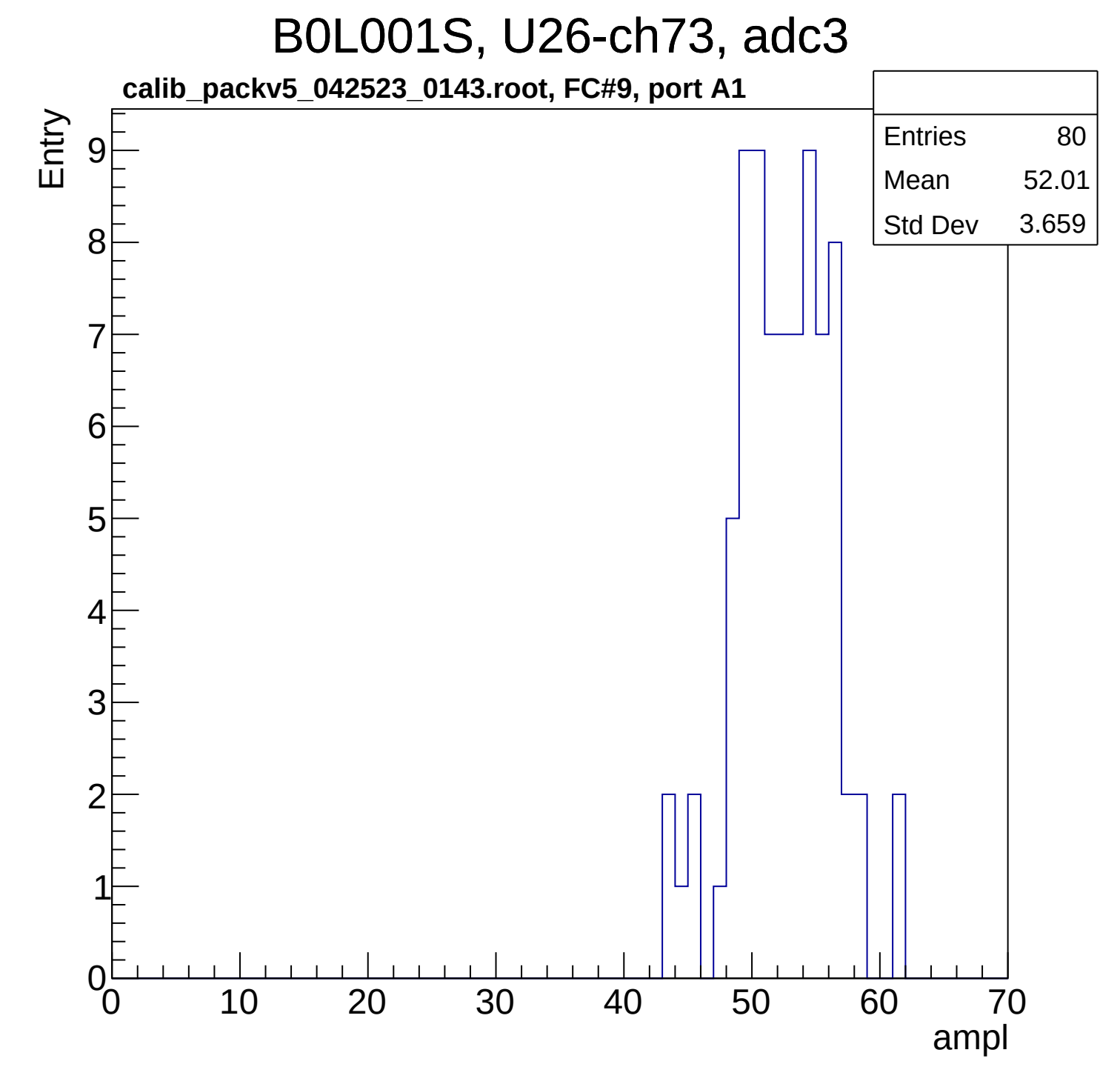
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

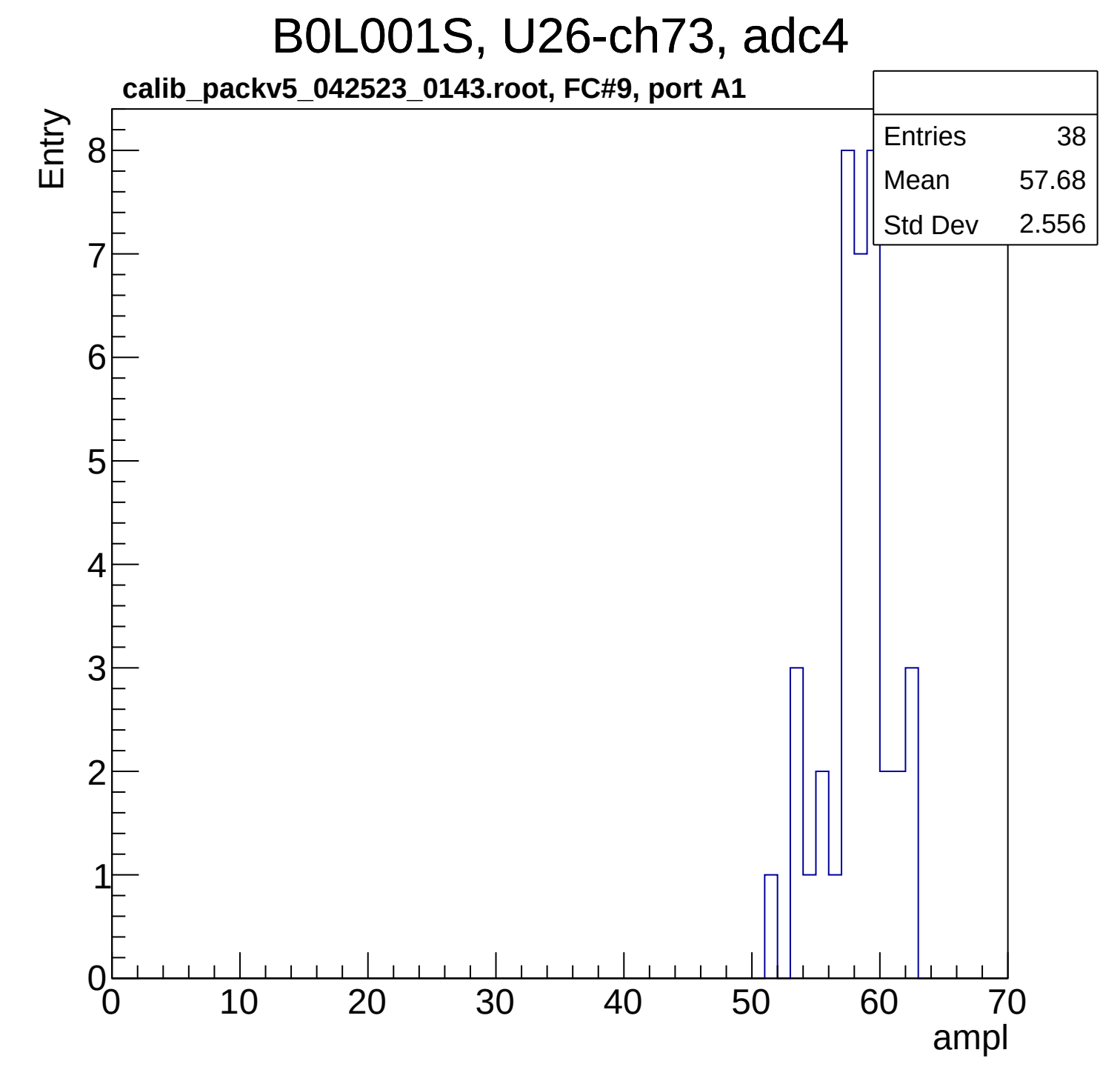

B0L001S, U26-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.07
Std Dev 3.049
Gaus mean : 30.4053
Gaus Width: 3.2074
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.14
Std Dev 2.982
Gaus mean : 37.2060
Gaus Width: 3.1576
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 43.85
Std Dev 3.771
Gaus mean : 43.4097
Gaus Width: 3.8380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 52.01
Std Dev 3.659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 57.68
Std Dev 2.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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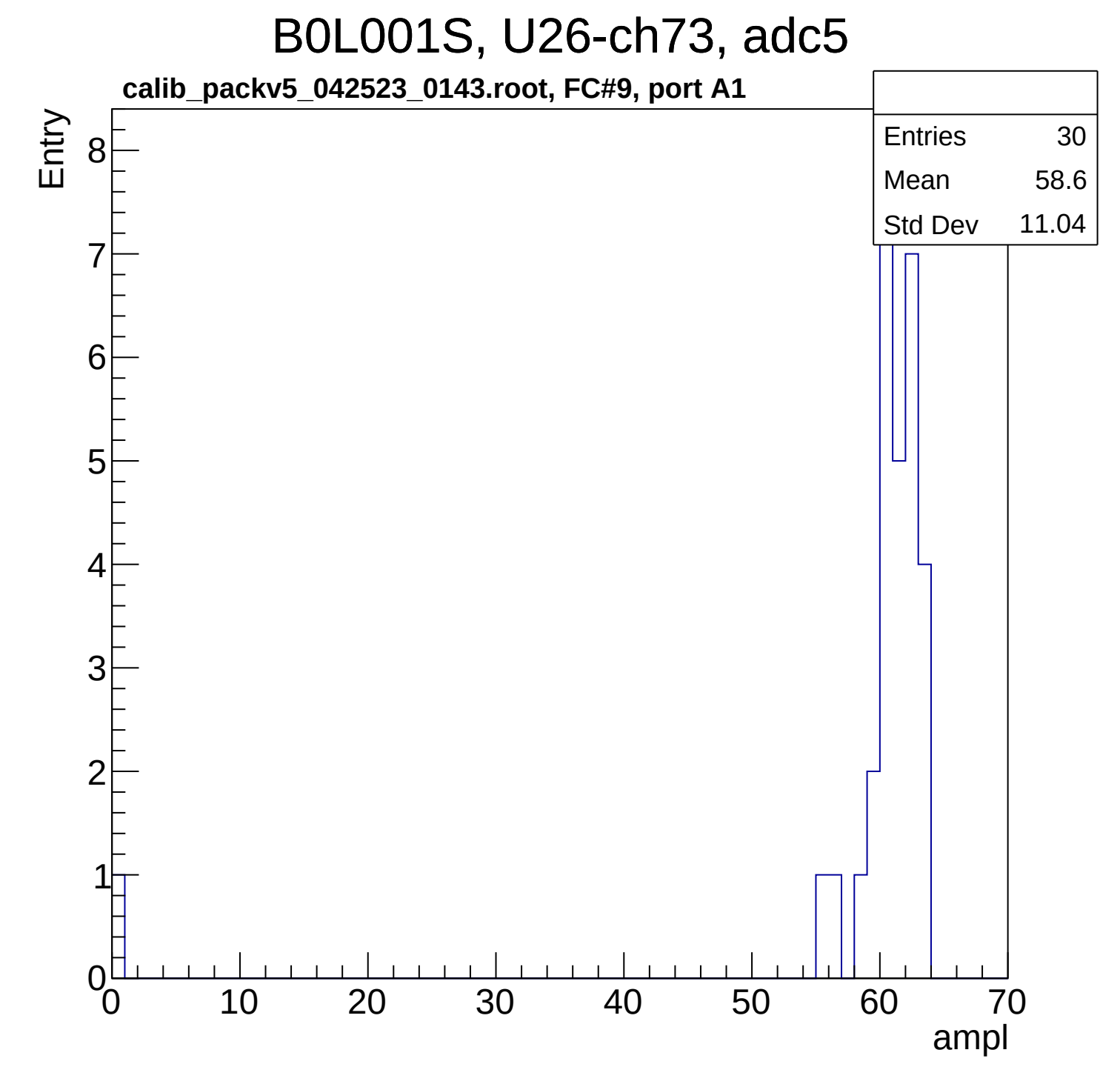
B0L001S, U26-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.6
Std Dev 11.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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30
40
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60
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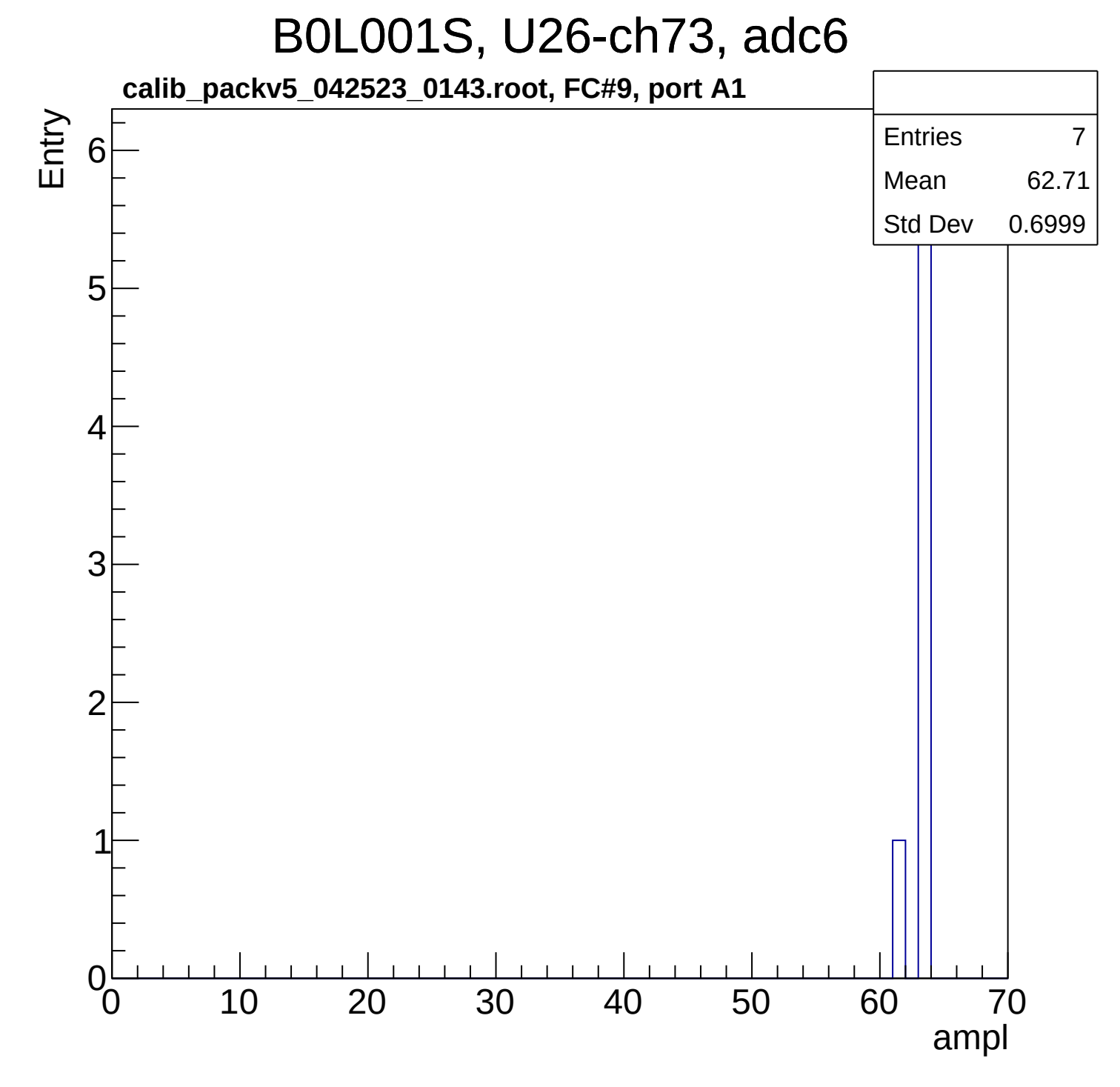
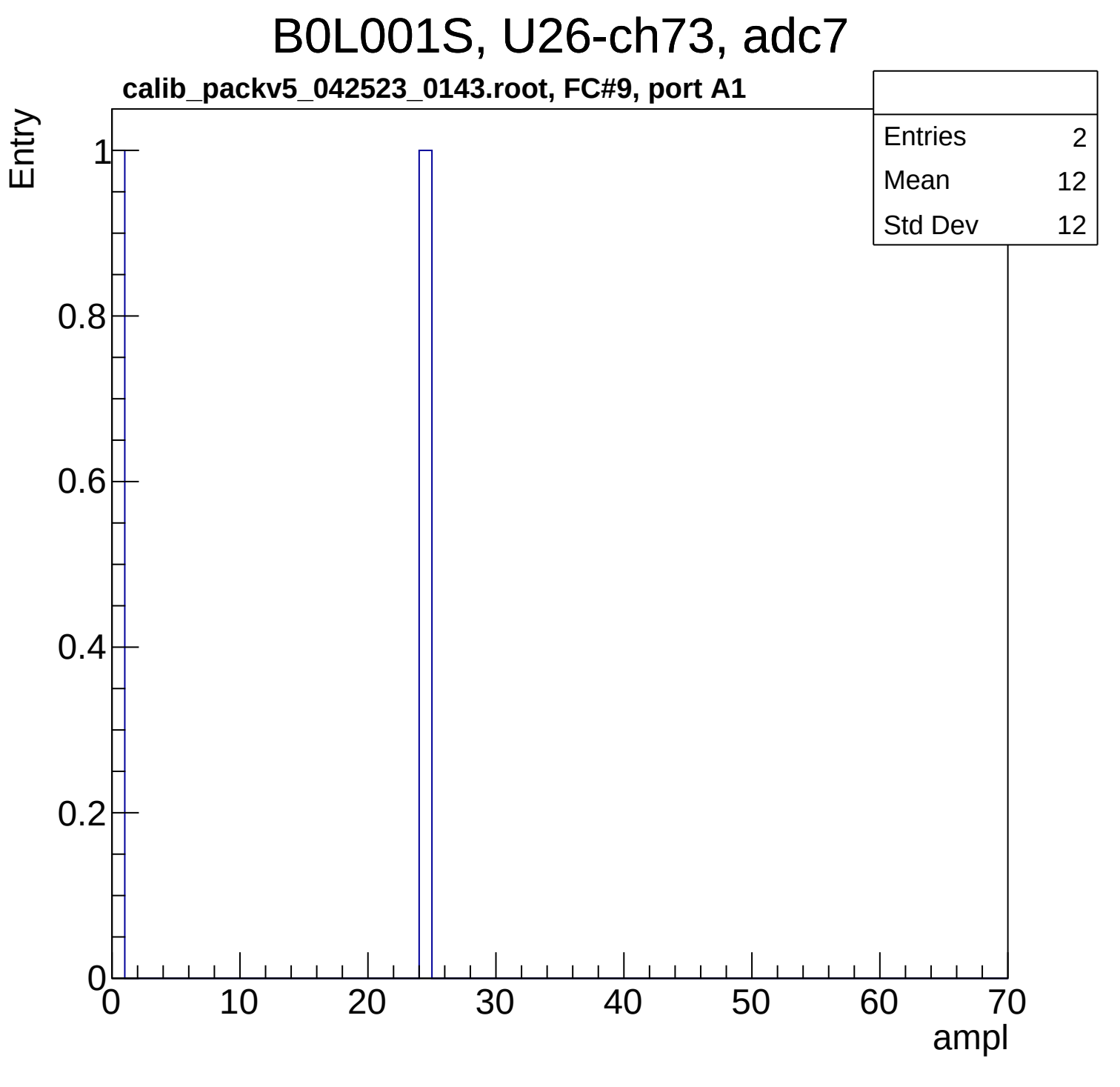
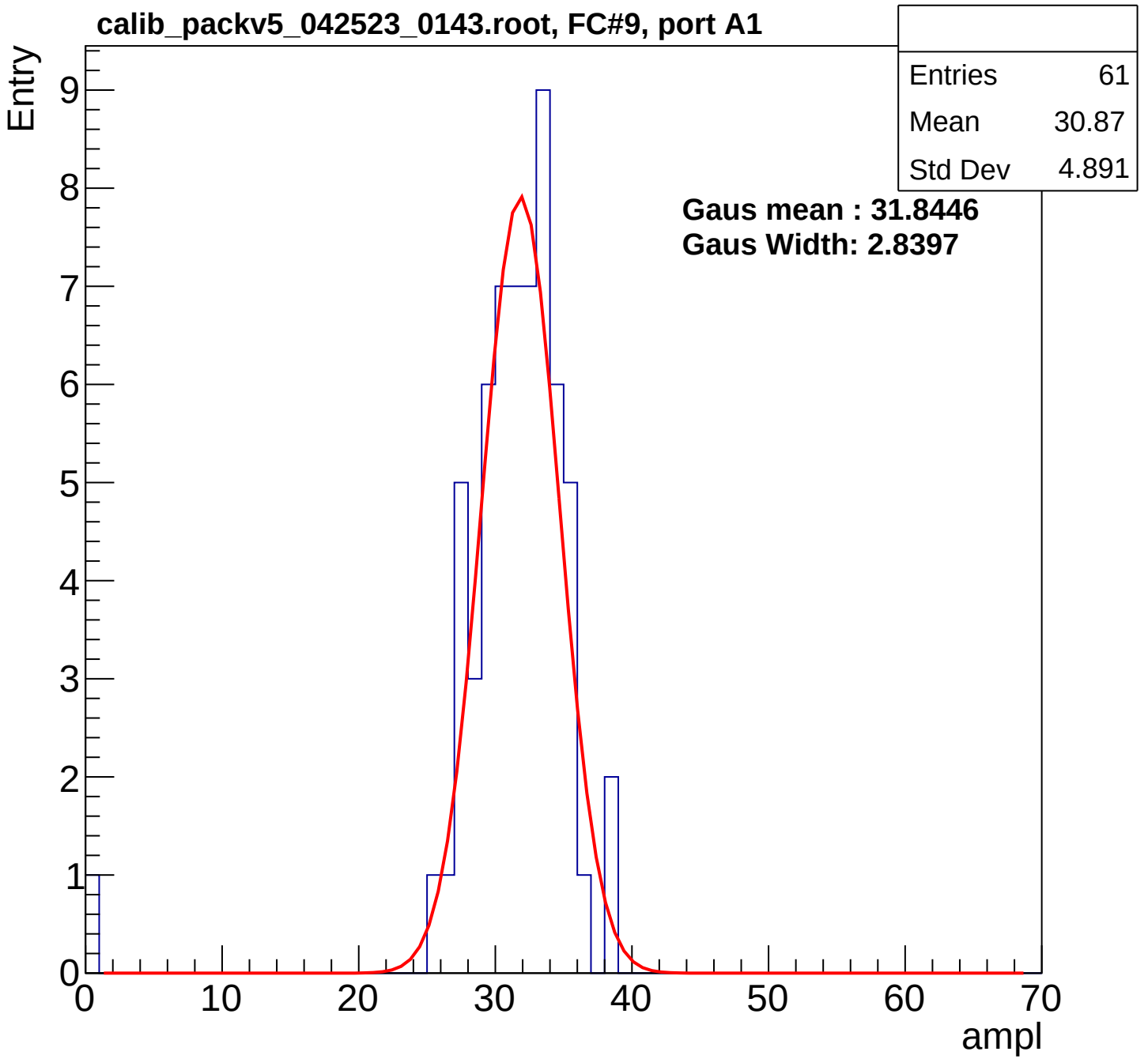

B0L001S, U26-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.71
Std Dev 0.6999
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.87
Std Dev 4.891
Gaus mean : 31.8446
Gaus Width: 2.8397
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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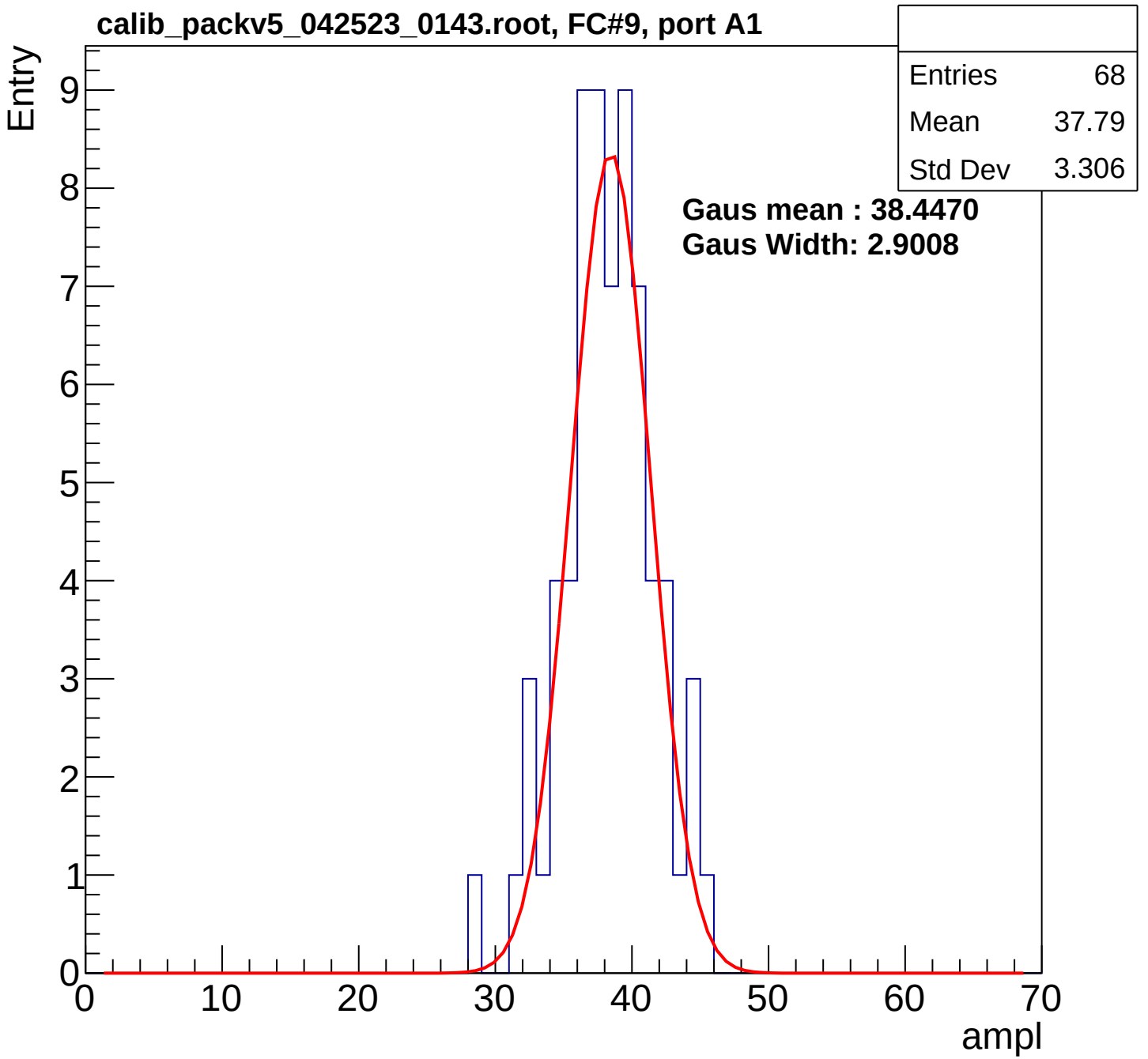
B0L001S, U26-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.79
Std Dev 3.306
Gaus mean : 38.4470
Gaus Width: 2.9008
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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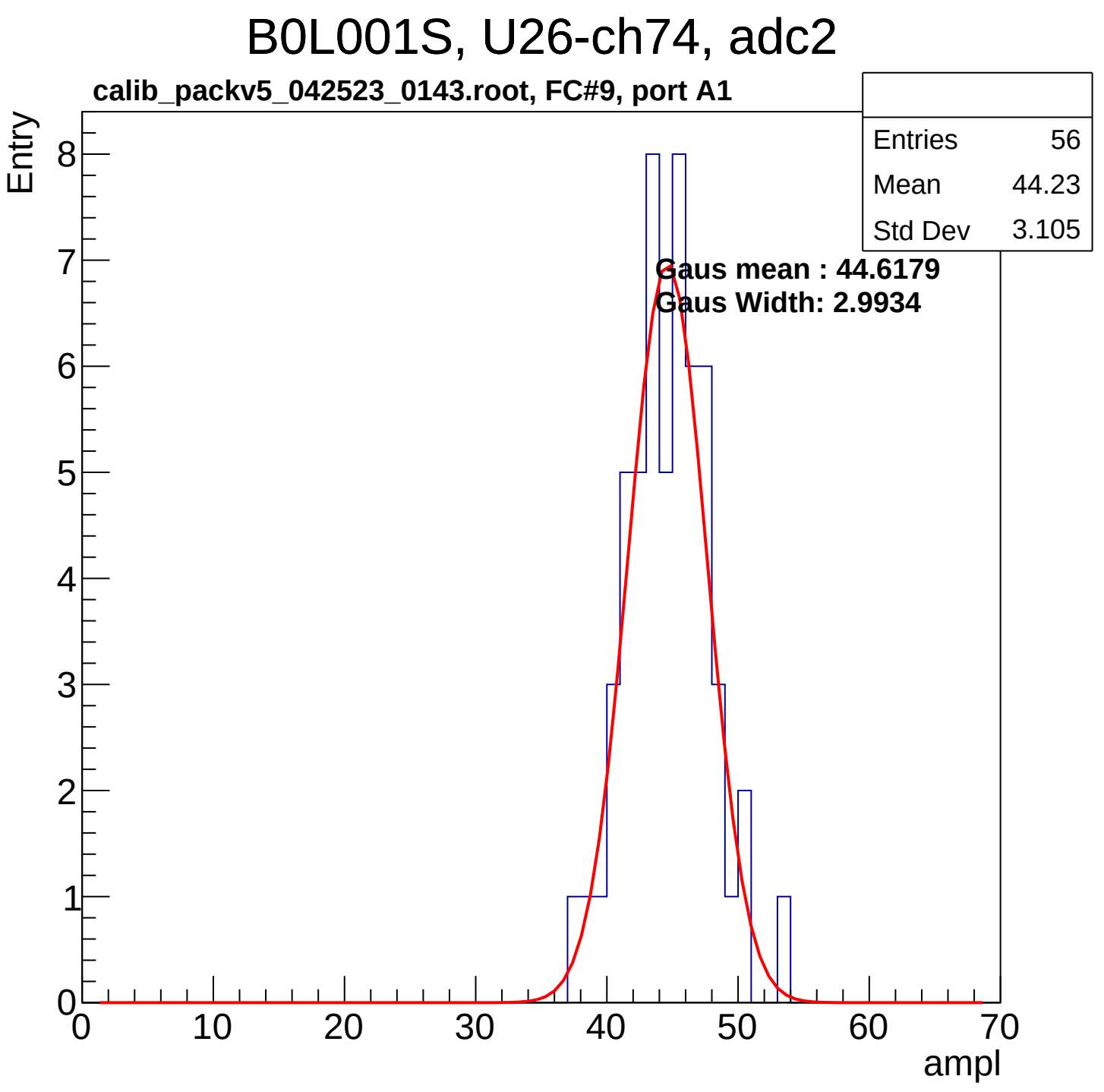
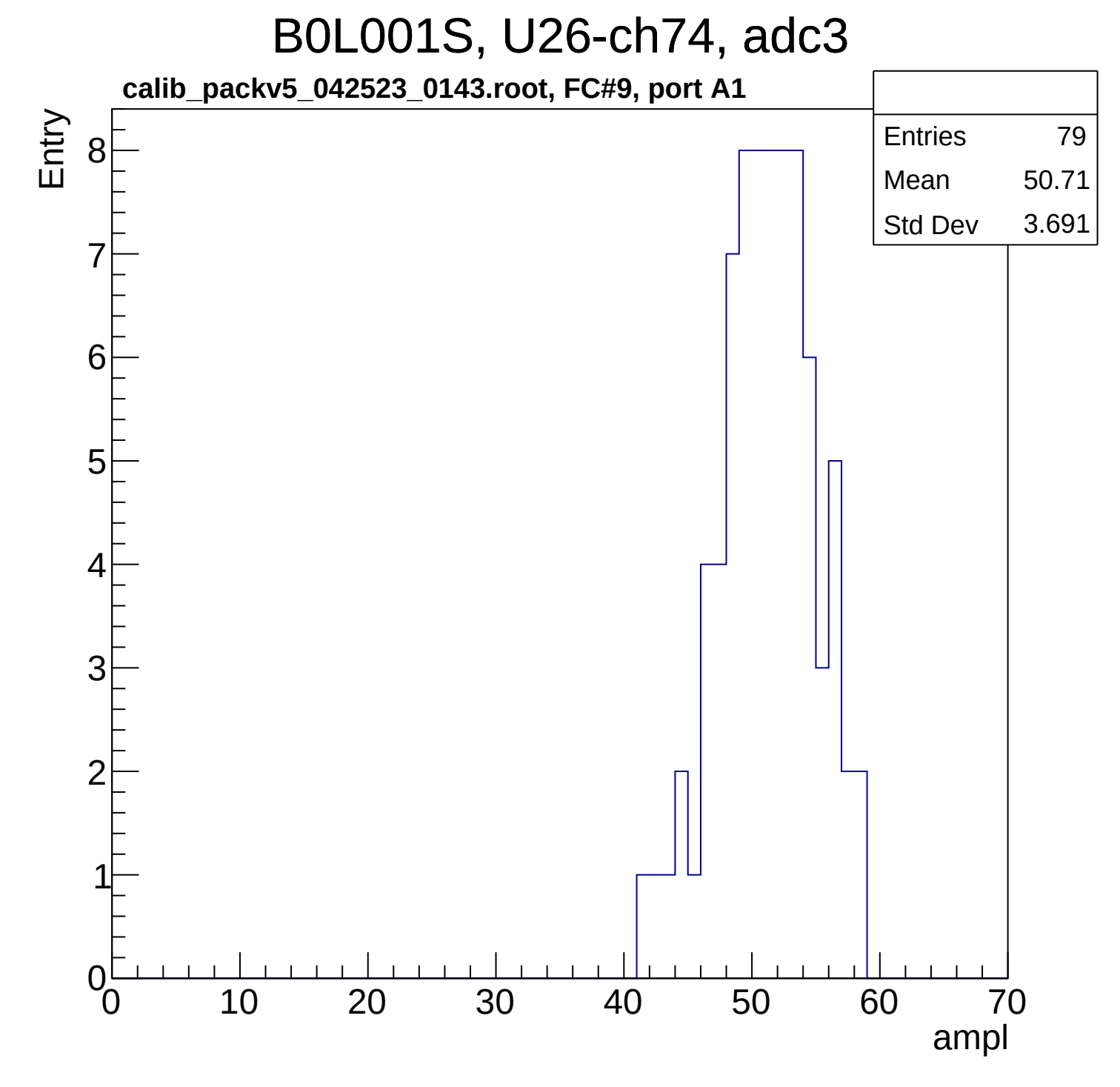
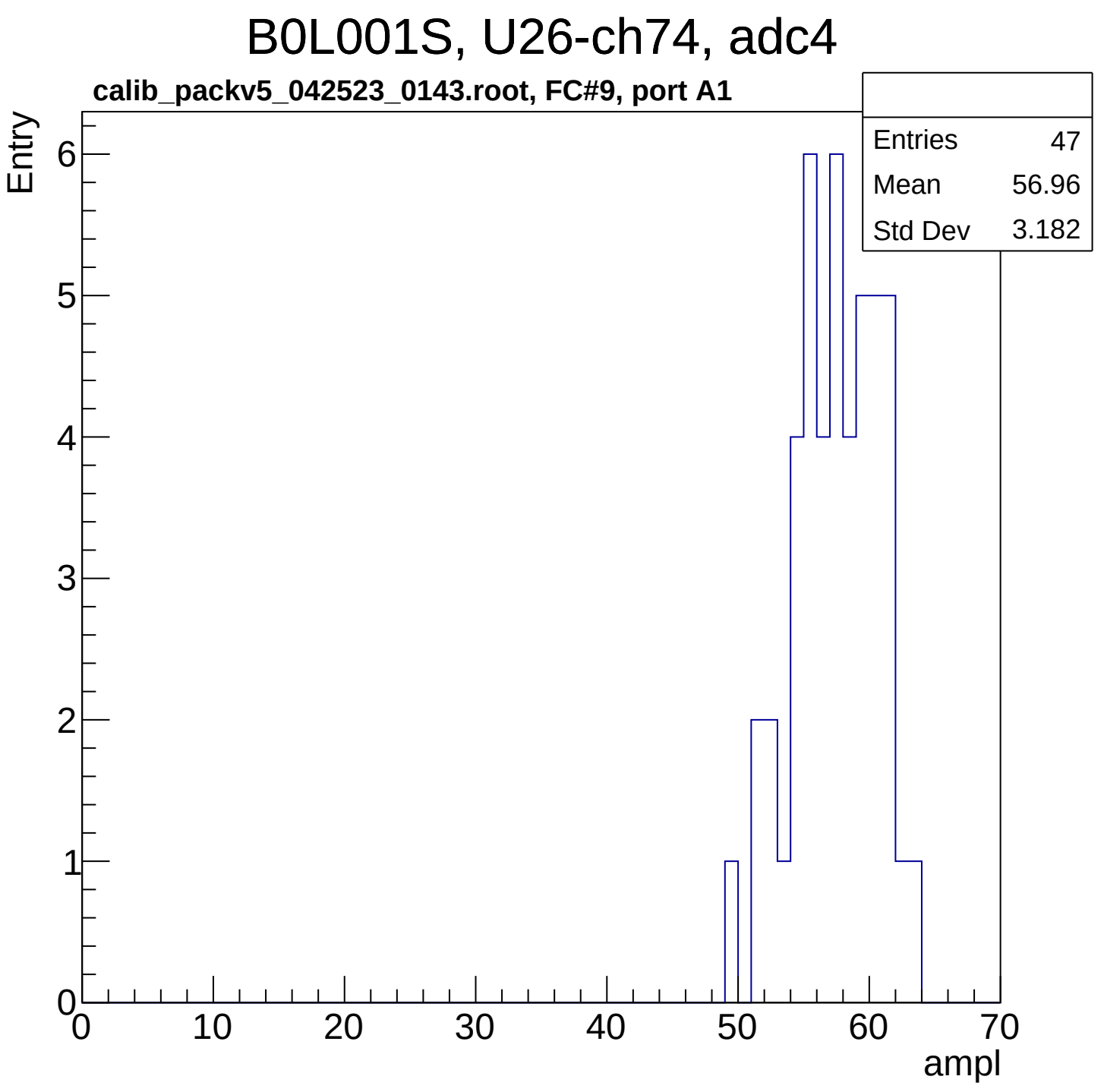
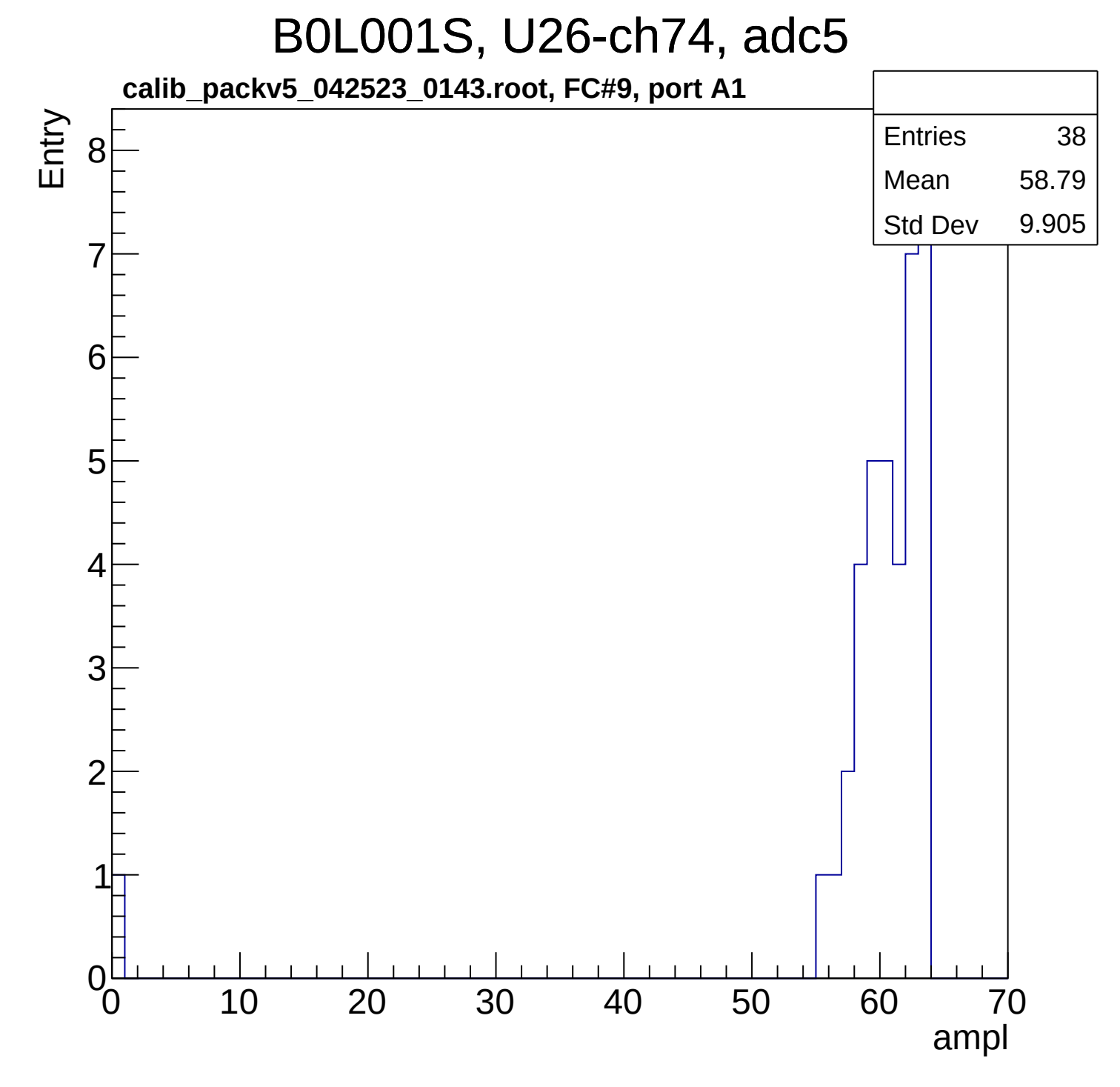
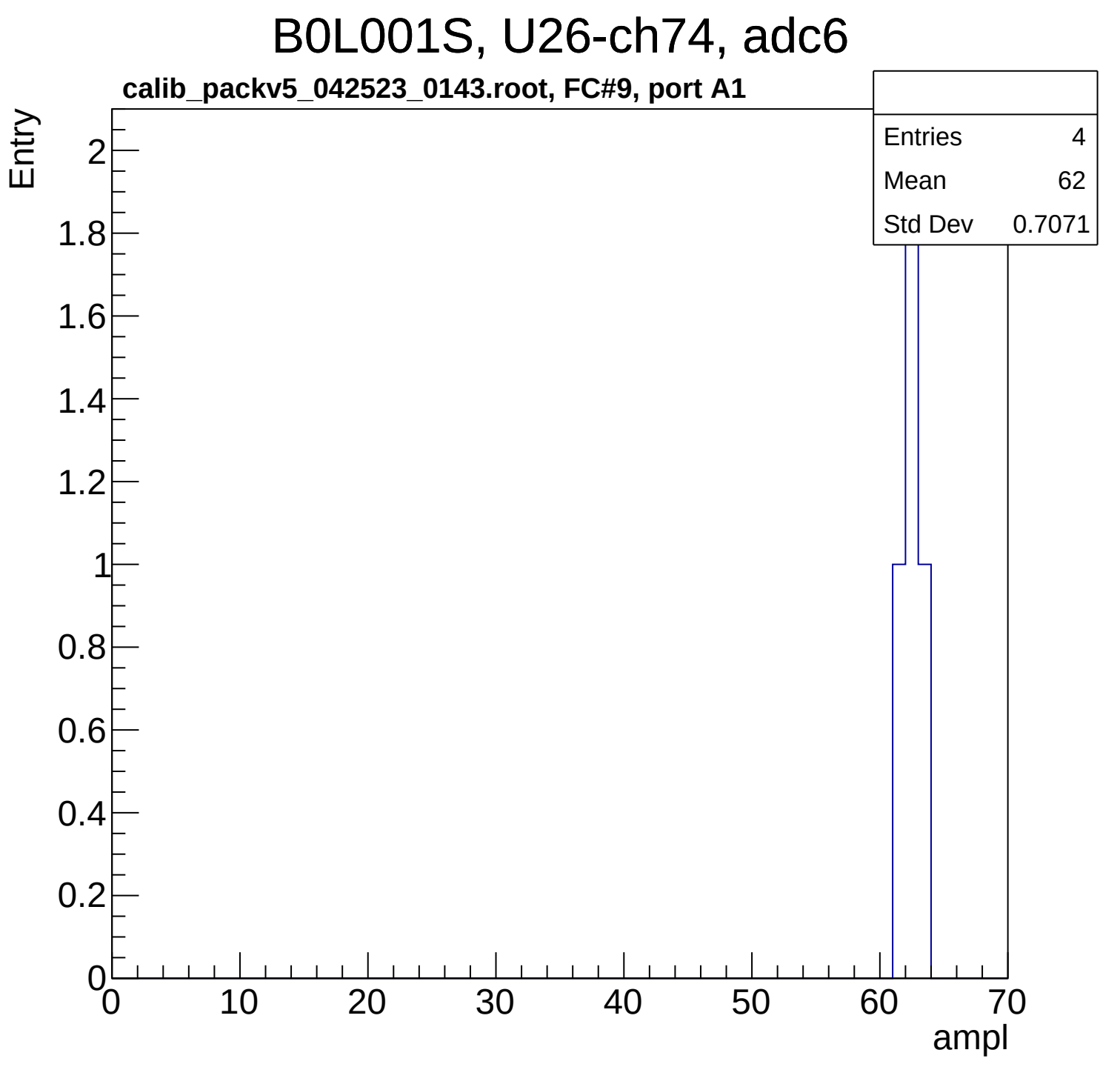
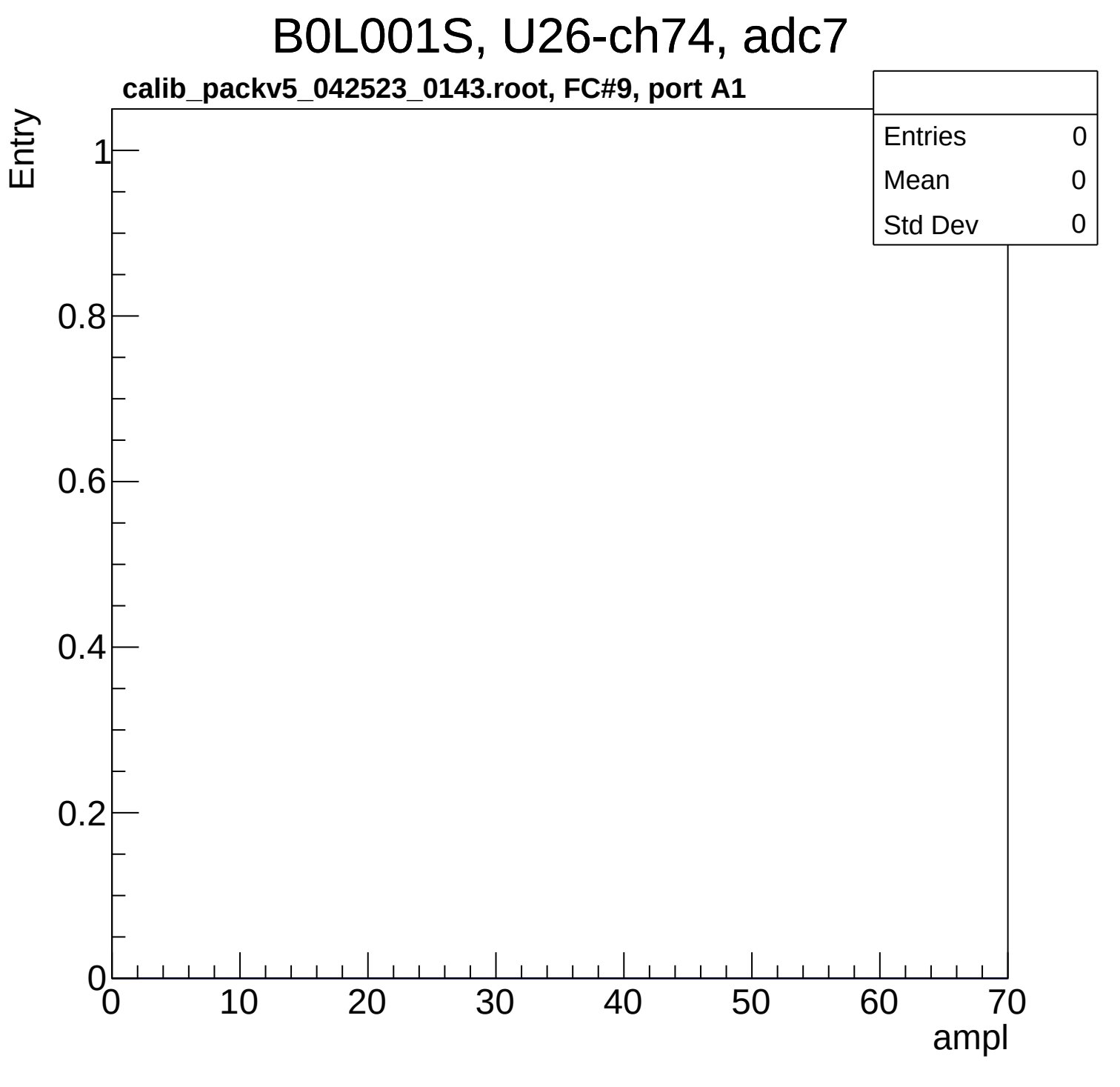

B0L001S, U26-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.23
Std Dev 3.105
Gaus mean : 44.6179
Gaus Width: 2.9934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

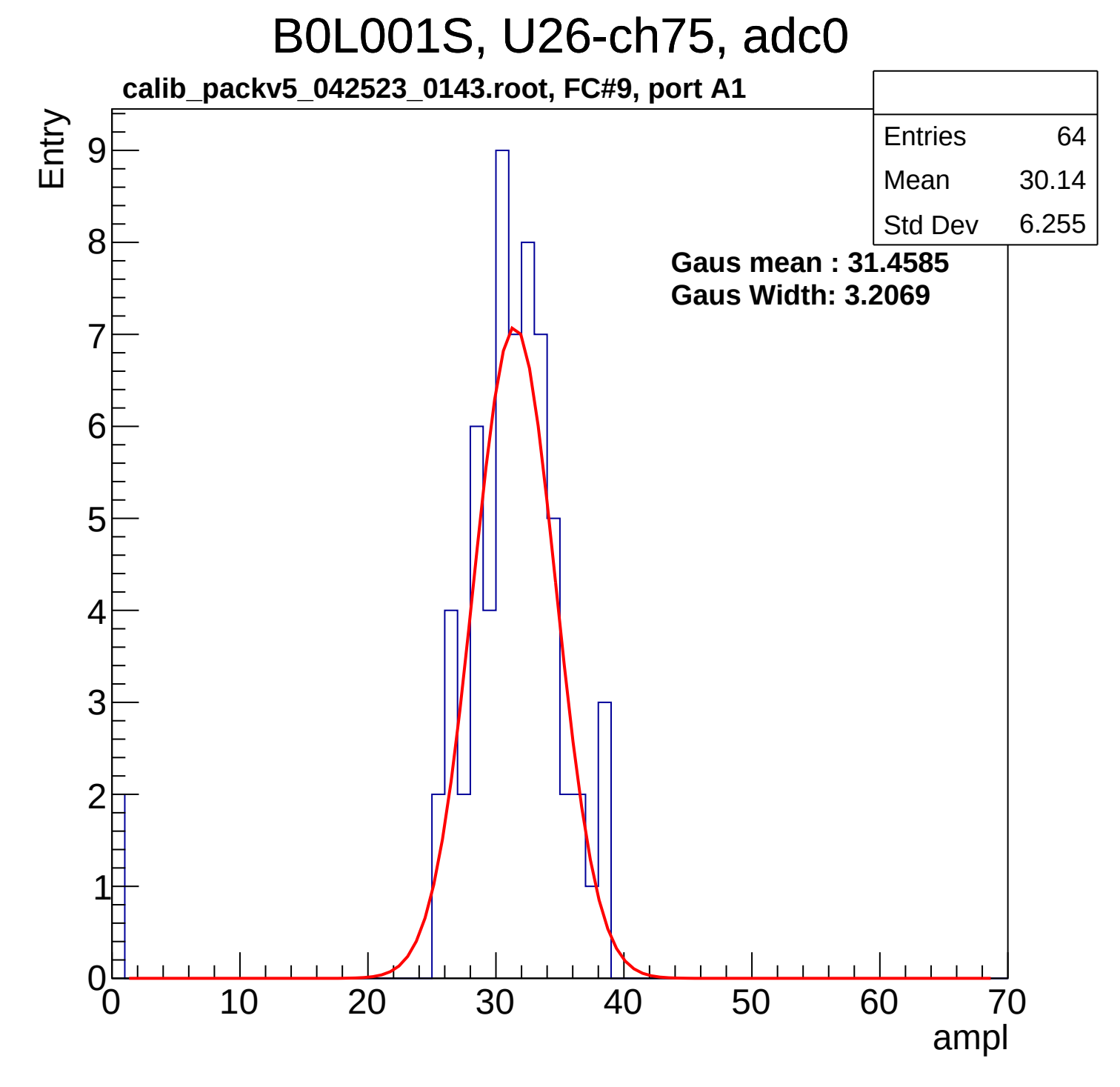

B0L001S, U26-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 50.71
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

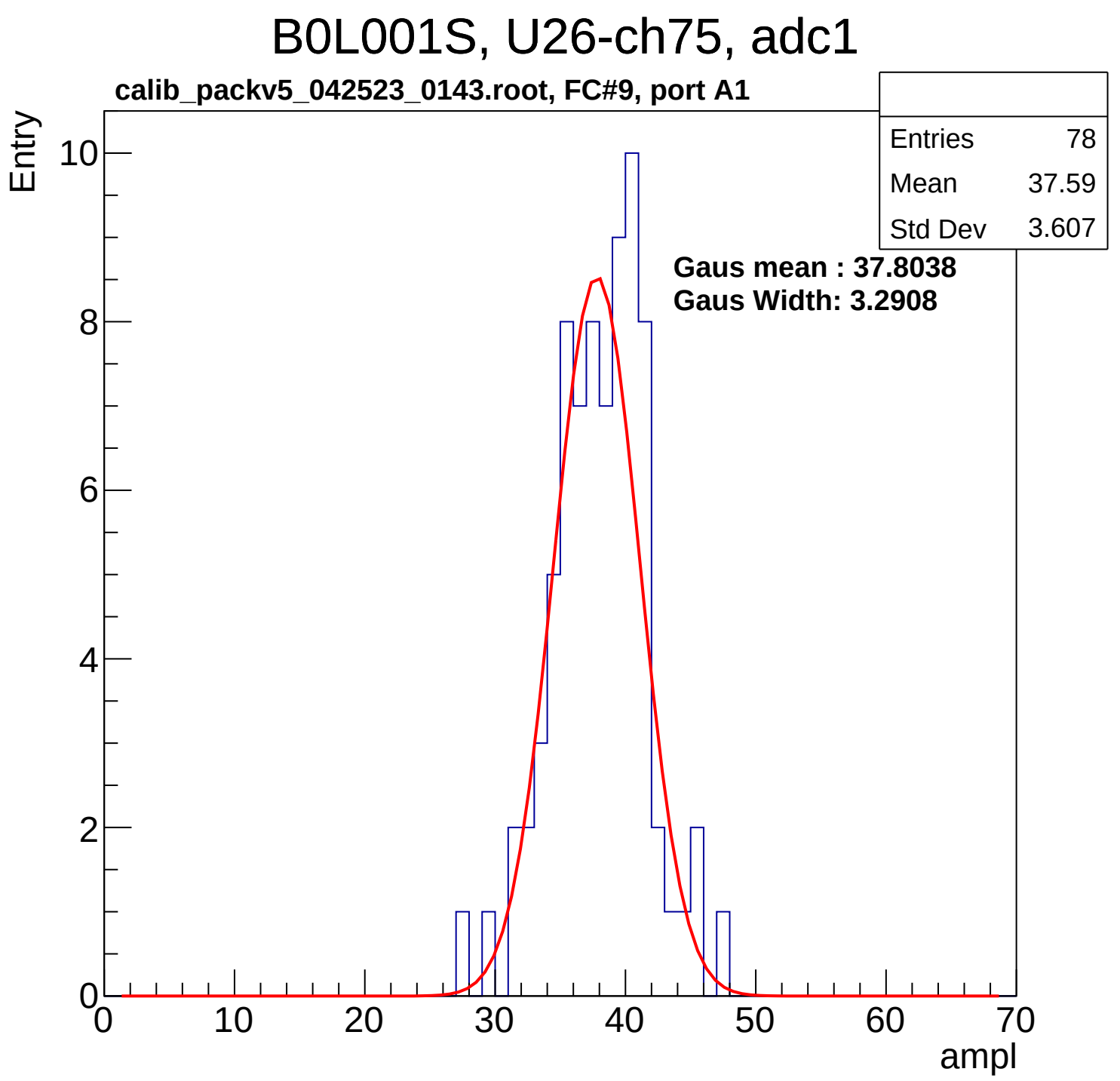

B0L001S, U26-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.96
Std Dev 3.182
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

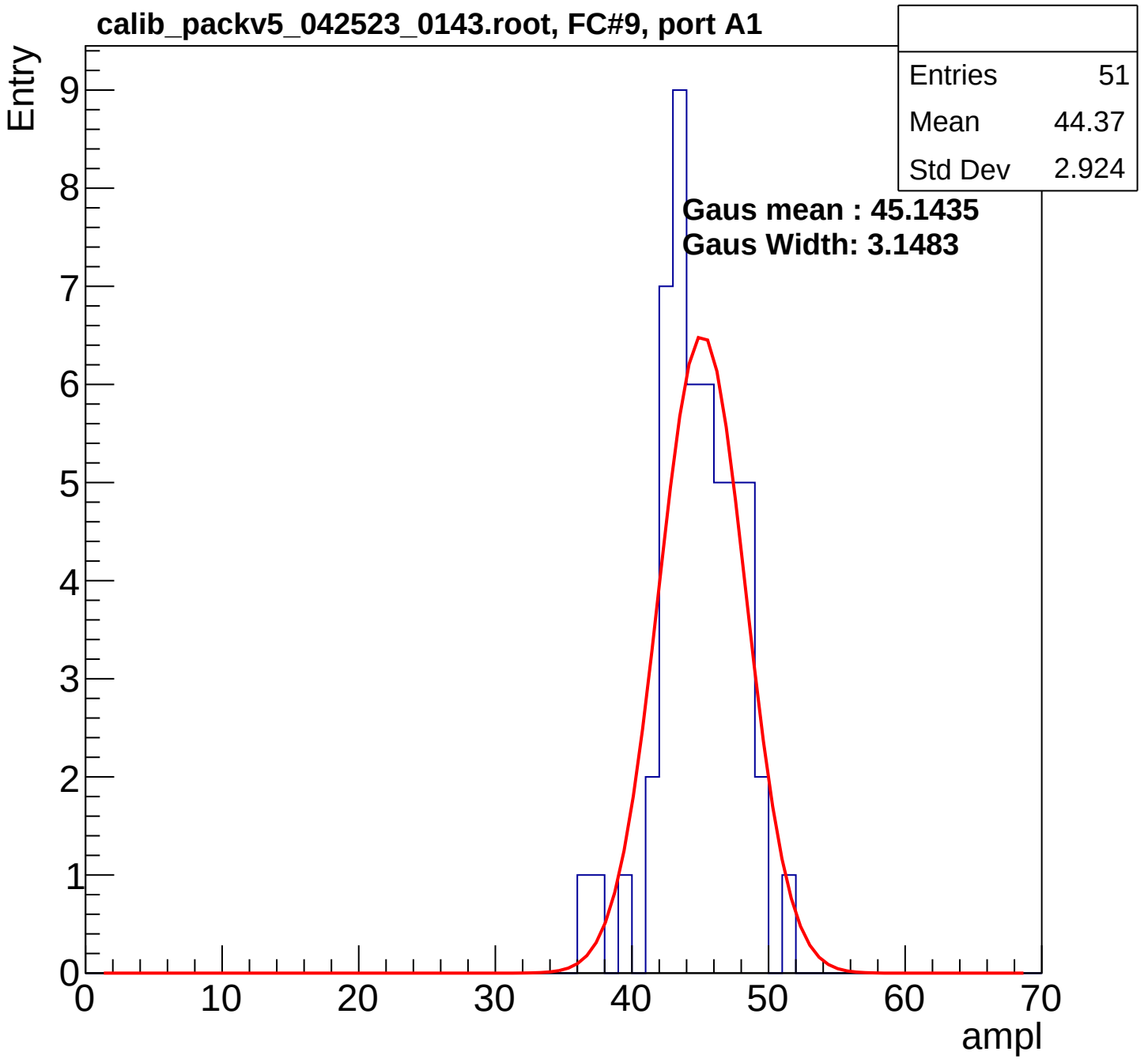

B0L001S, U26-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.79
Std Dev 9.905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

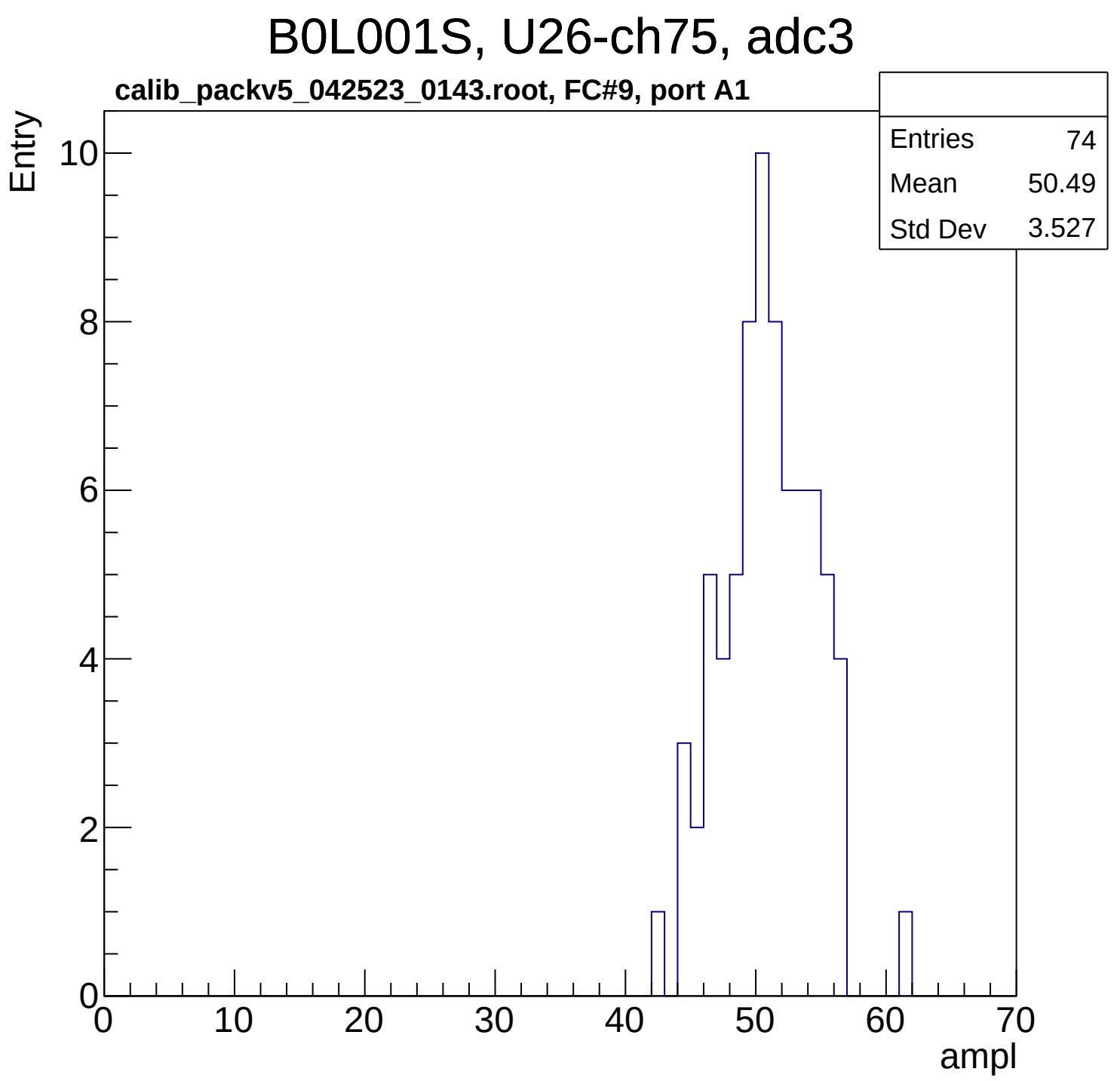

B0L001S, U26-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

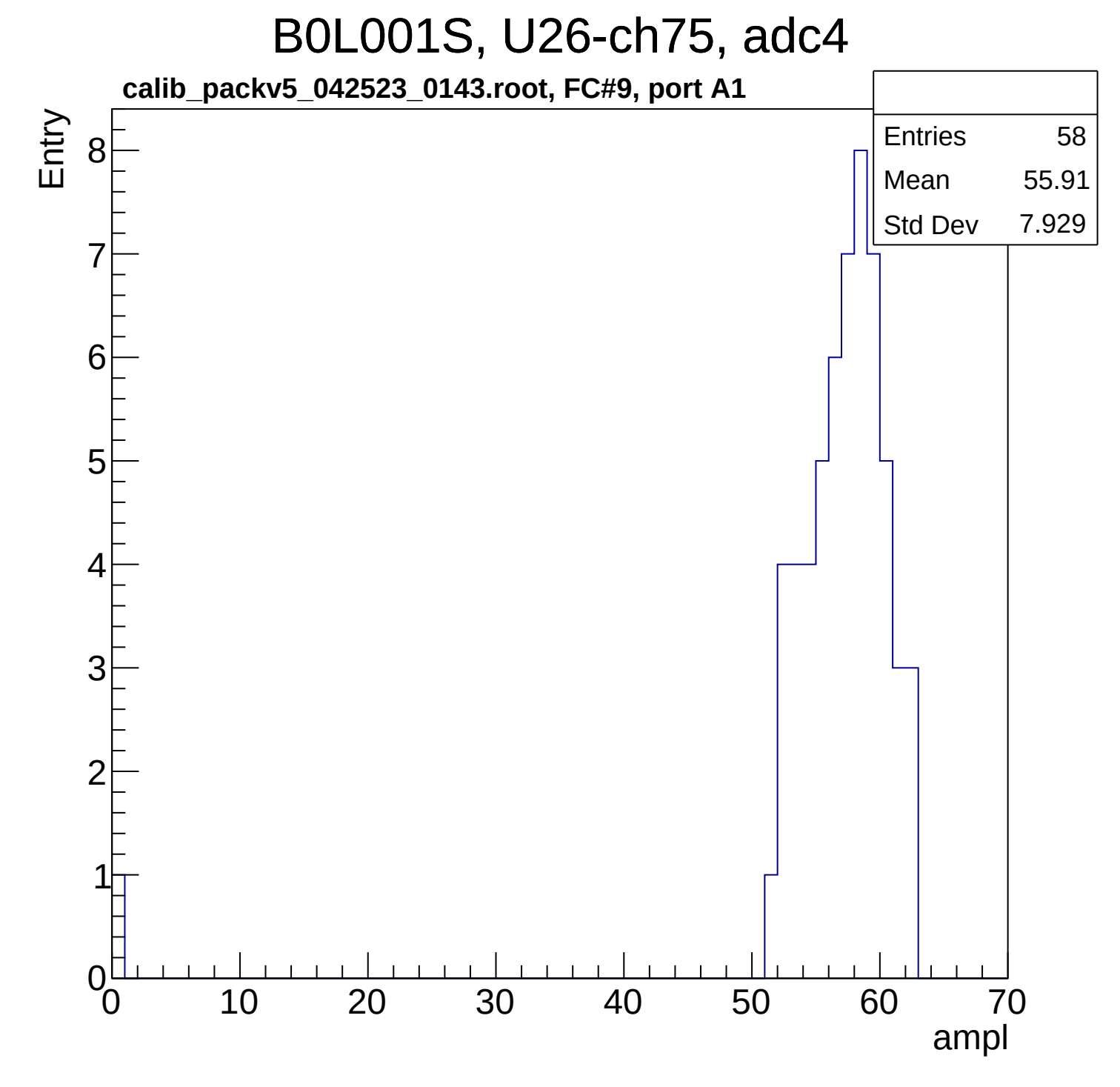

B0L001S, U26-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

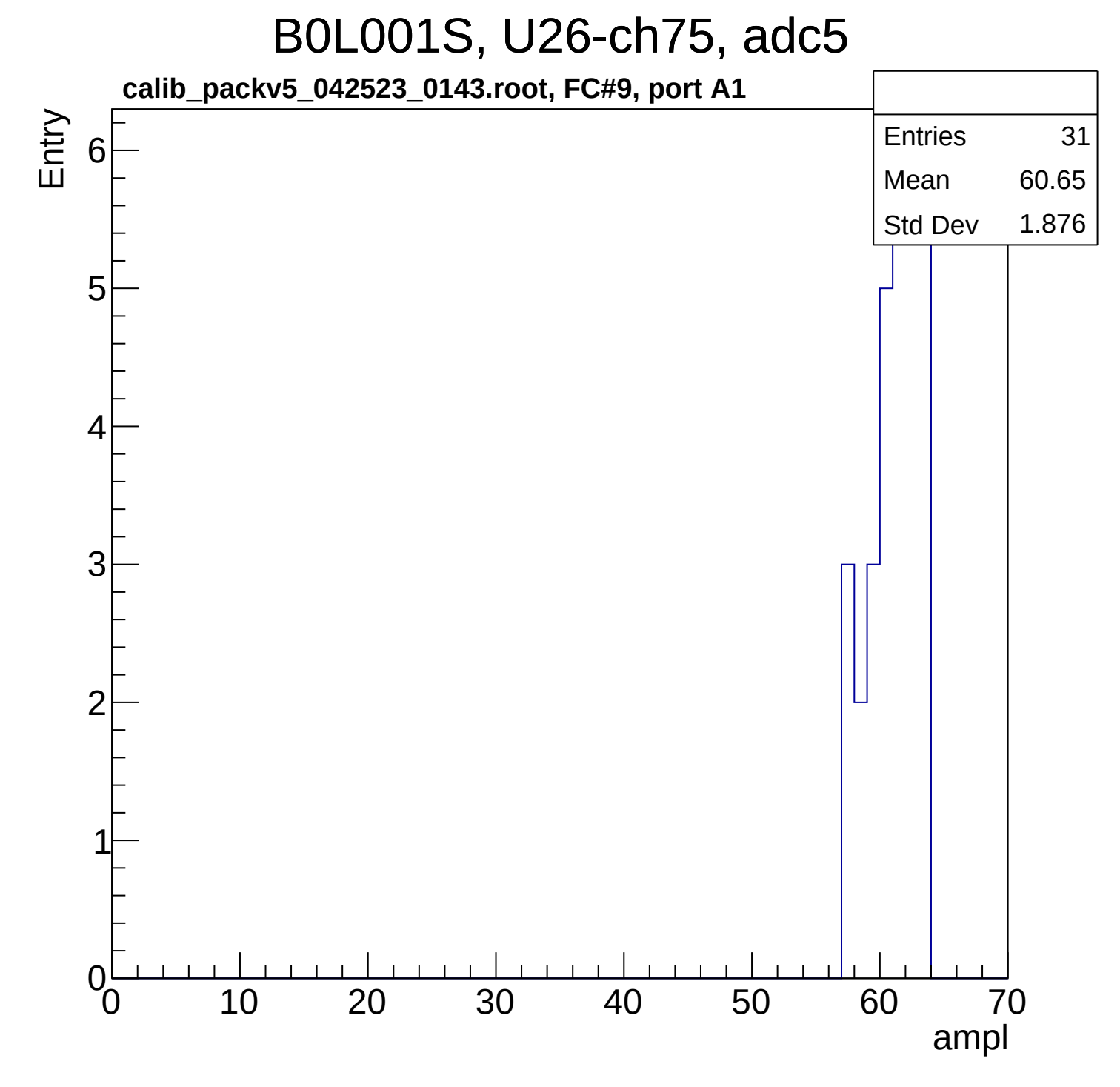

B0L001S, U26-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.14
Std Dev 6.255
Gaus mean : 31.4585
Gaus Width: 3.2069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

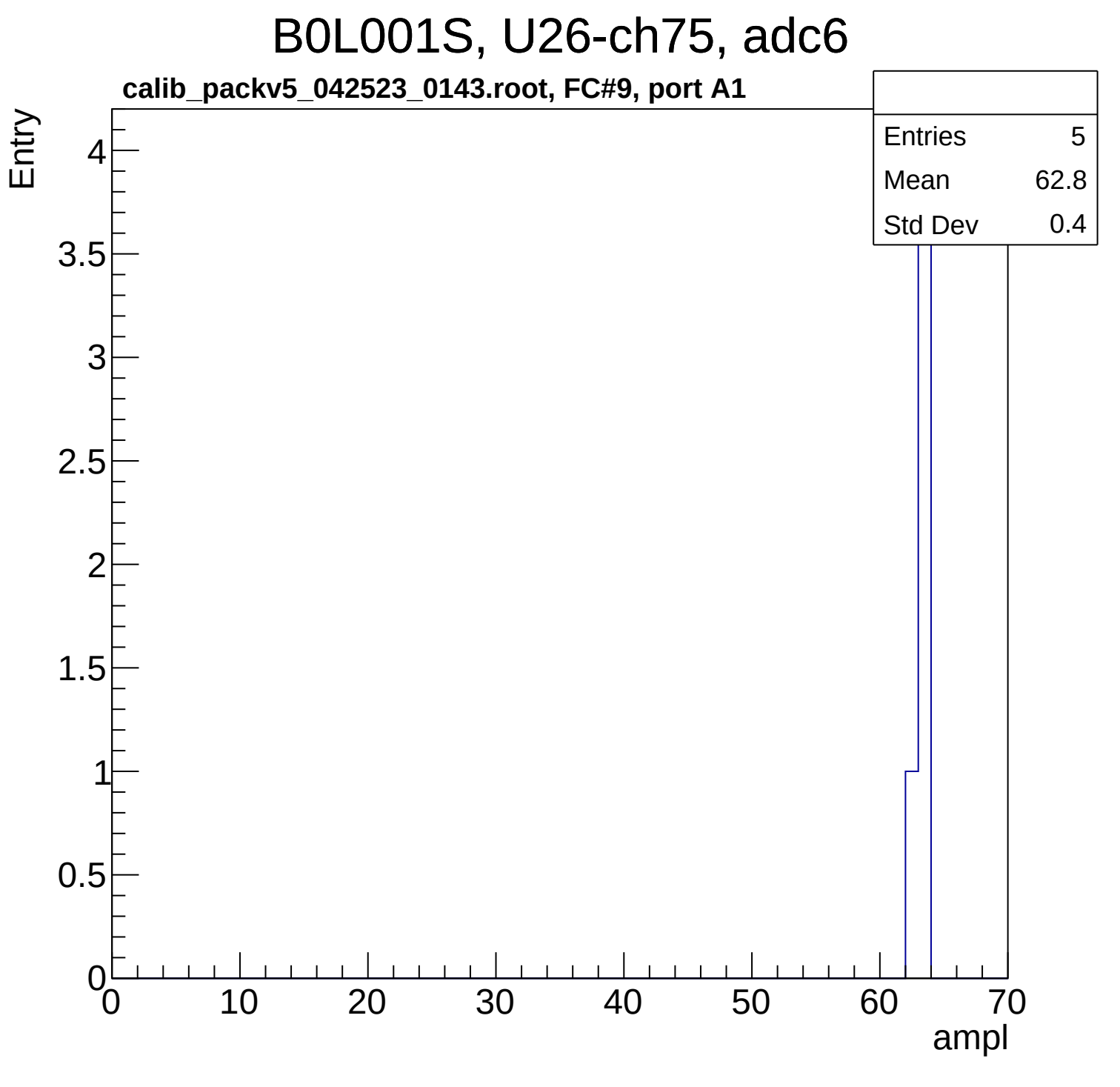

B0L001S, U26-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.59
Std Dev 3.607
Gaus mean : 37.8038
Gaus Width: 3.2908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

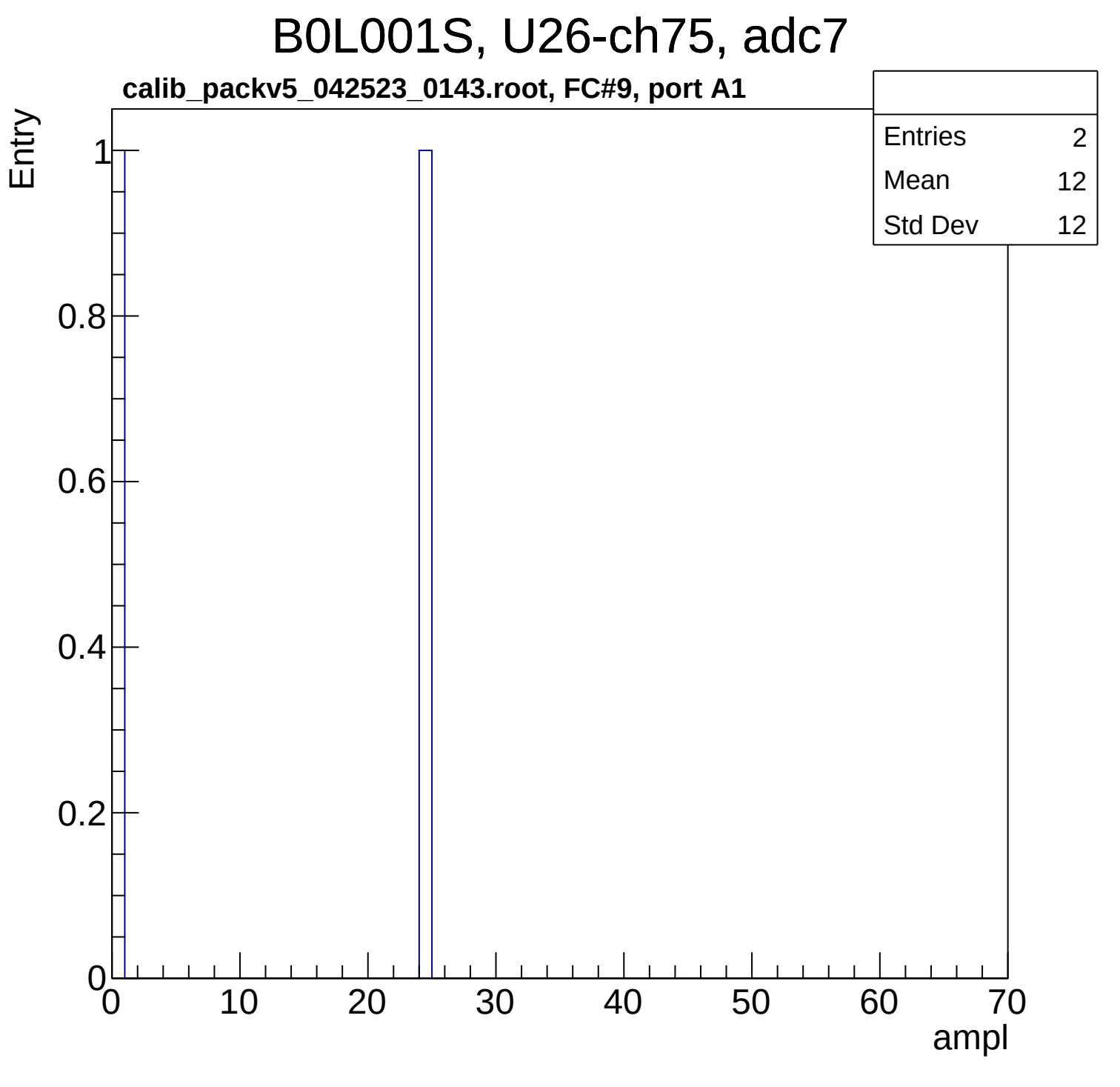

B0L001S, U26-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 44.37
Std Dev 2.924
Gaus mean : 45.1435
Gaus Width: 3.1483
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

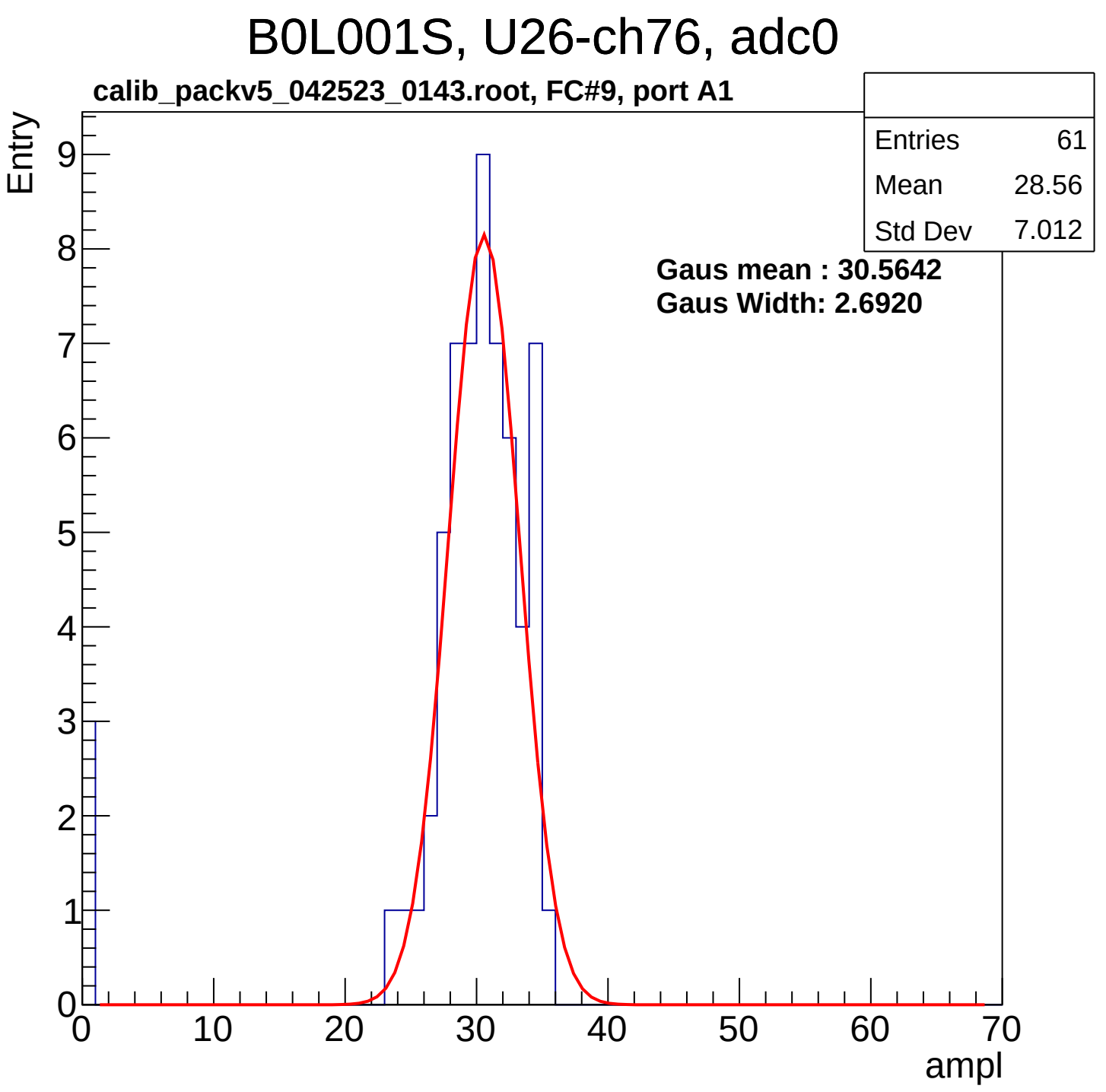

B0L001S, U26-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.49
Std Dev 3.527
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

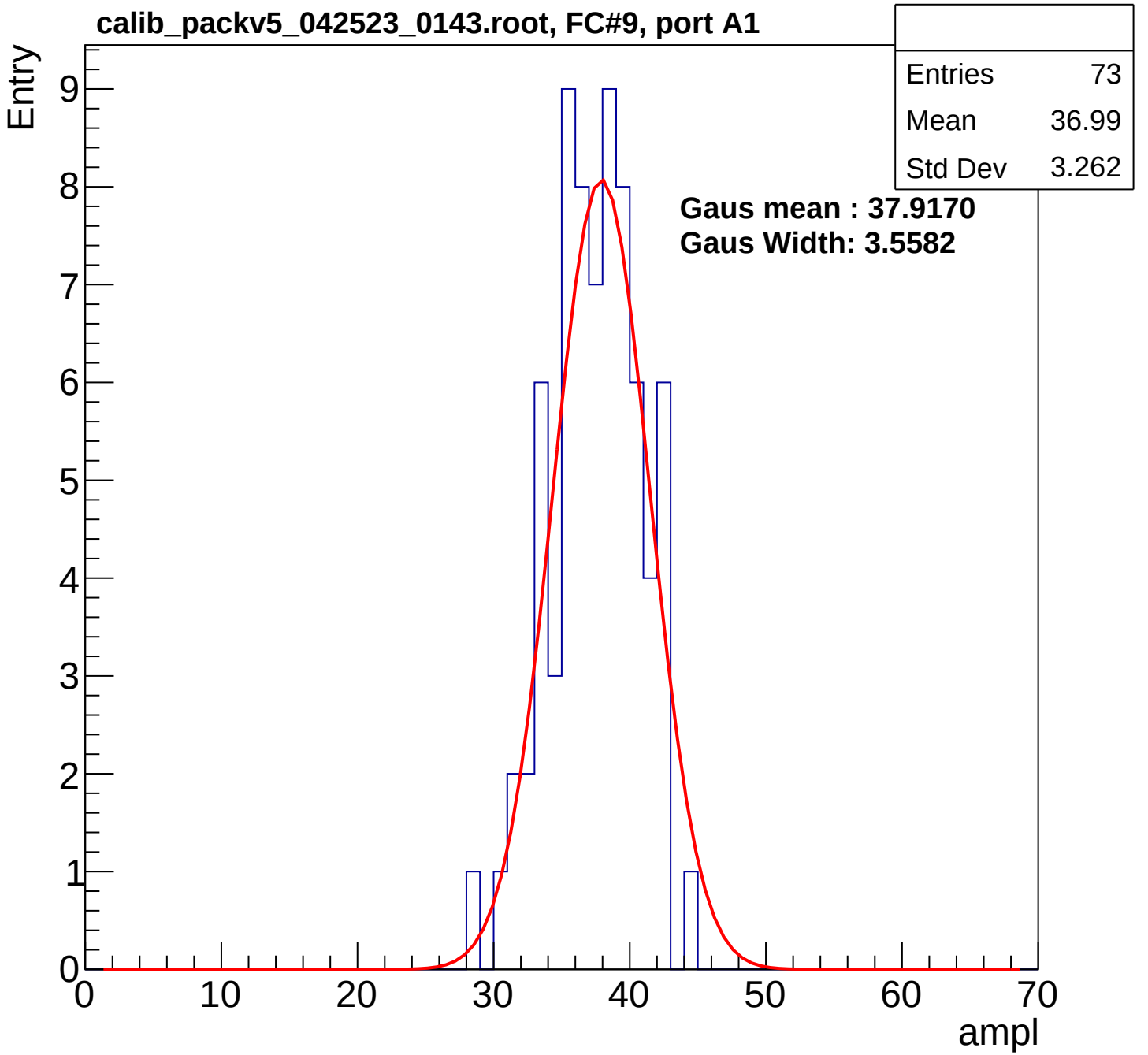

B0L001S, U26-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.91
Std Dev 7.929
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

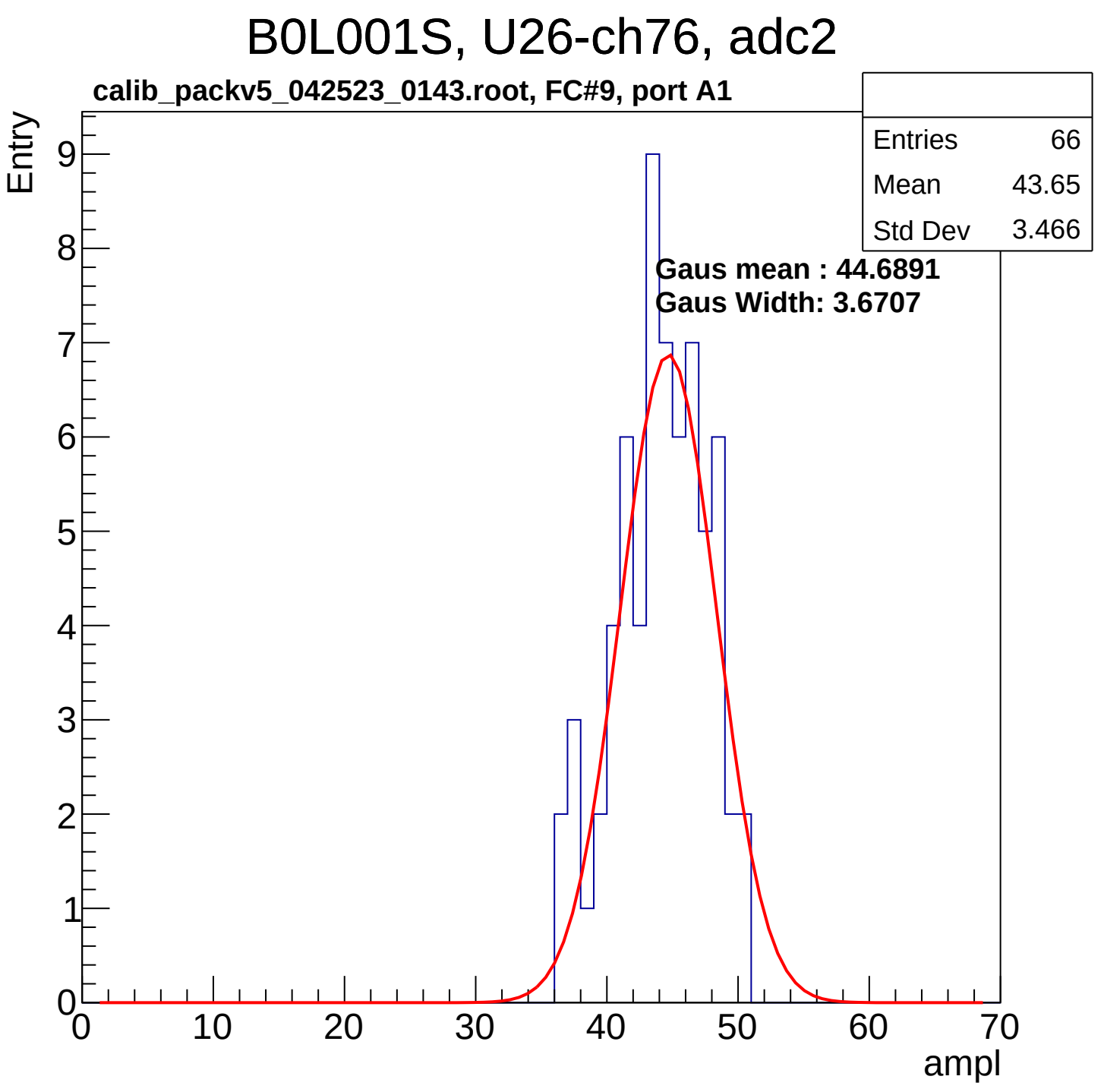

B0L001S, U26-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.65
Std Dev 1.876
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

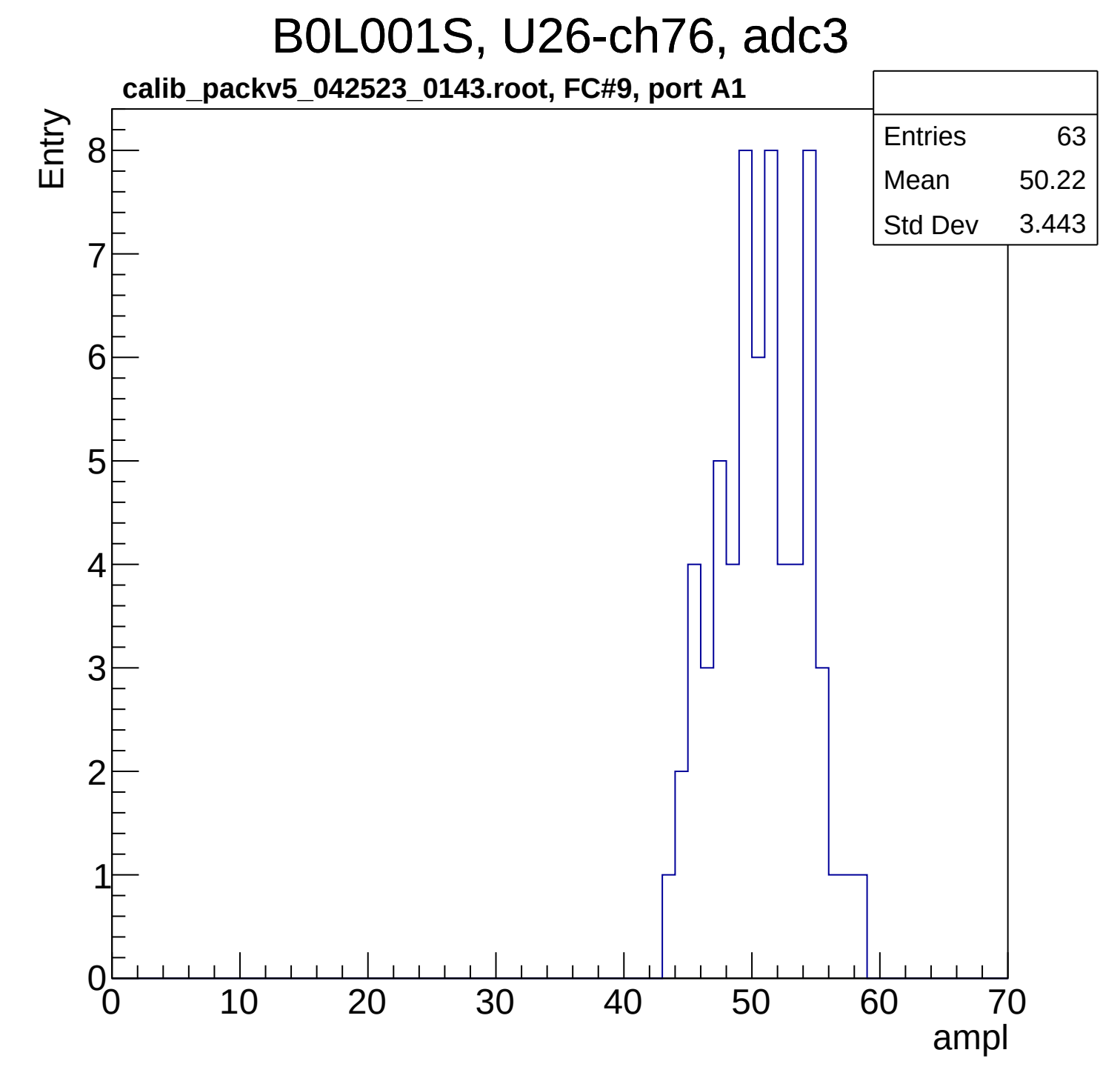

B0L001S, U26-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 28.56
Std Dev 7.012
Gaus mean : 30.5642
Gaus Width: 2.6920
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.99
Std Dev 3.262
Gaus mean : 37.9170
Gaus Width: 3.5582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.65
Std Dev 3.466
Gaus mean : 44.6891
Gaus Width: 3.6707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.22
Std Dev 3.443
Entry
ampl
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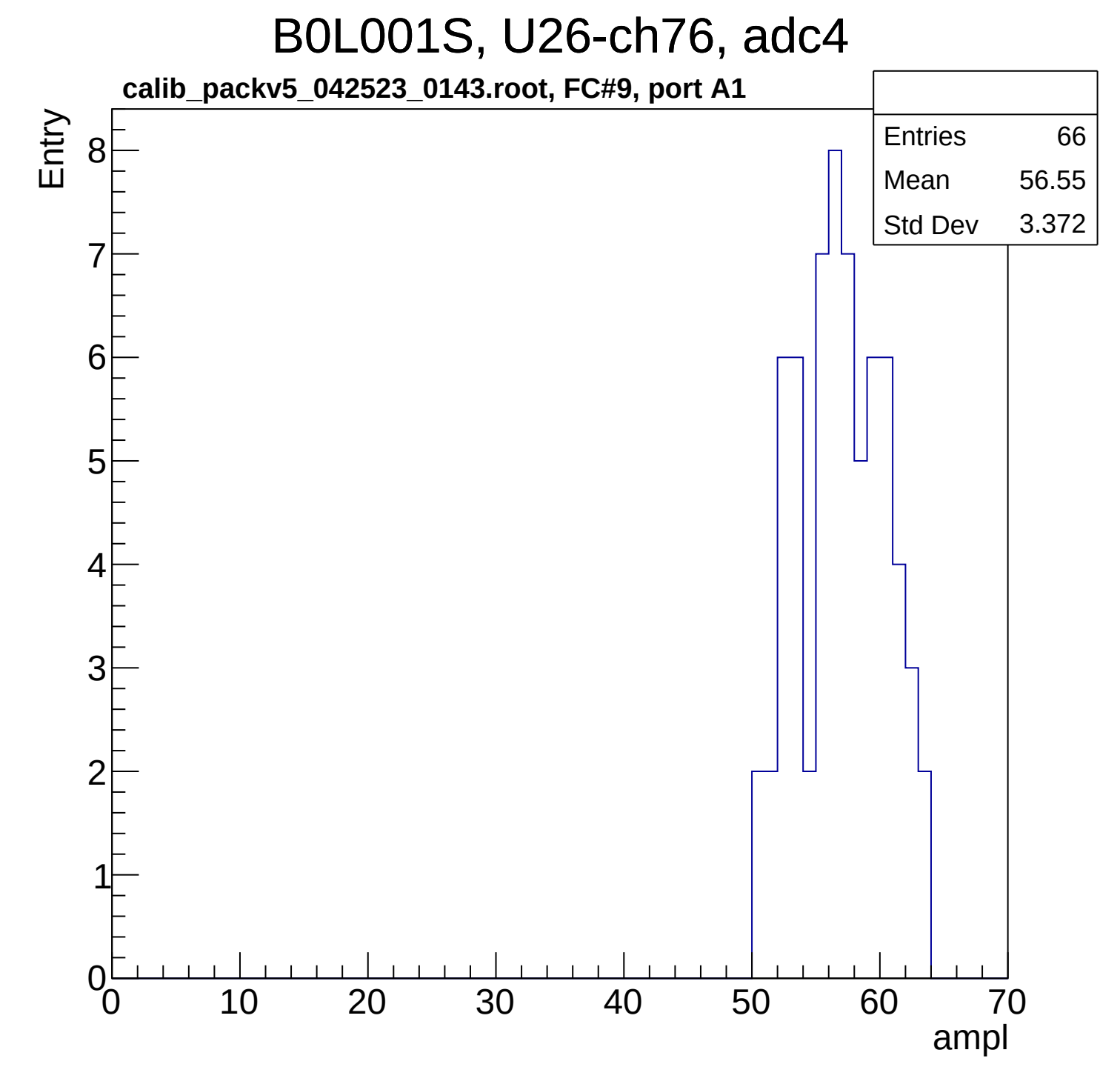
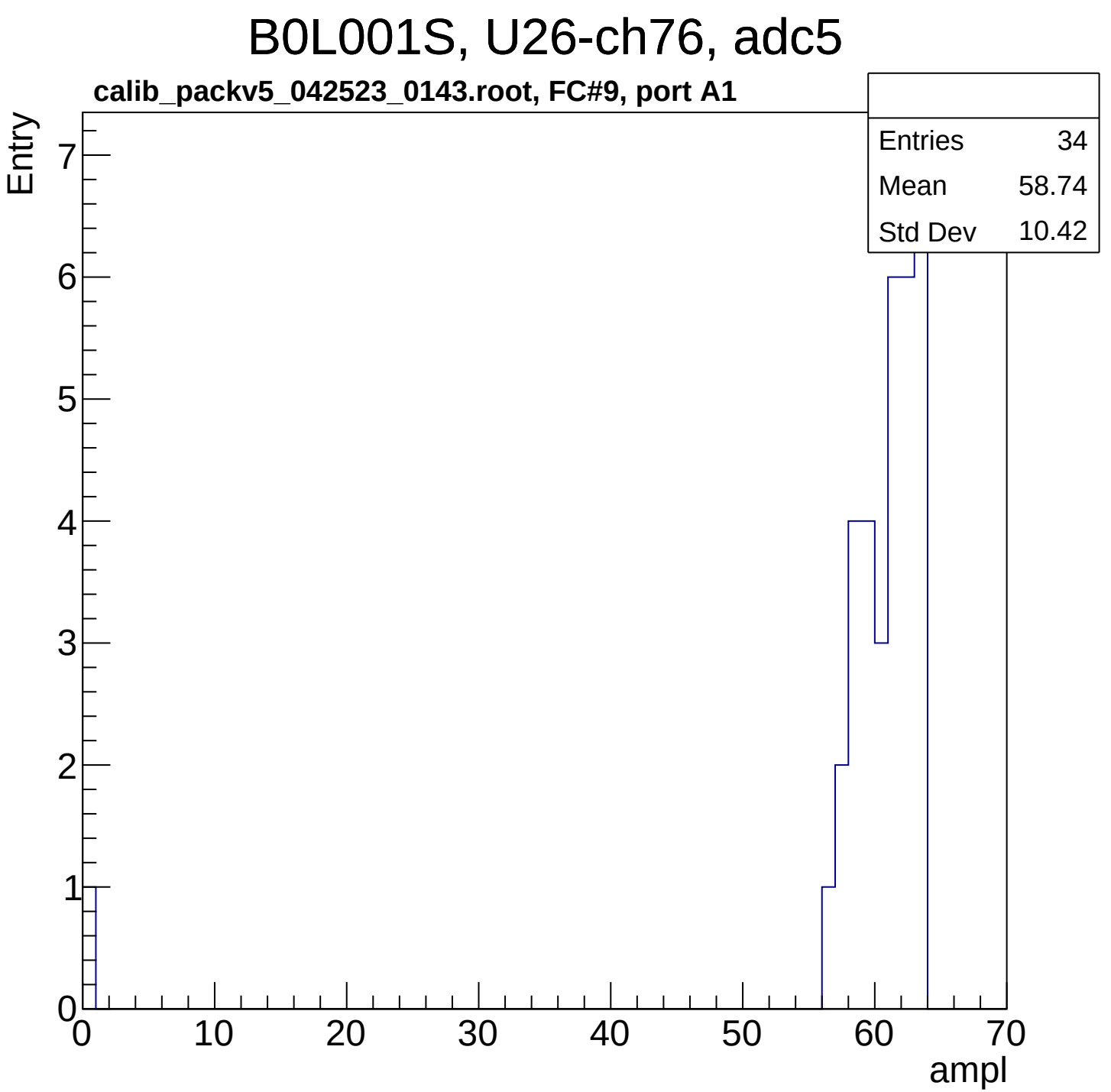
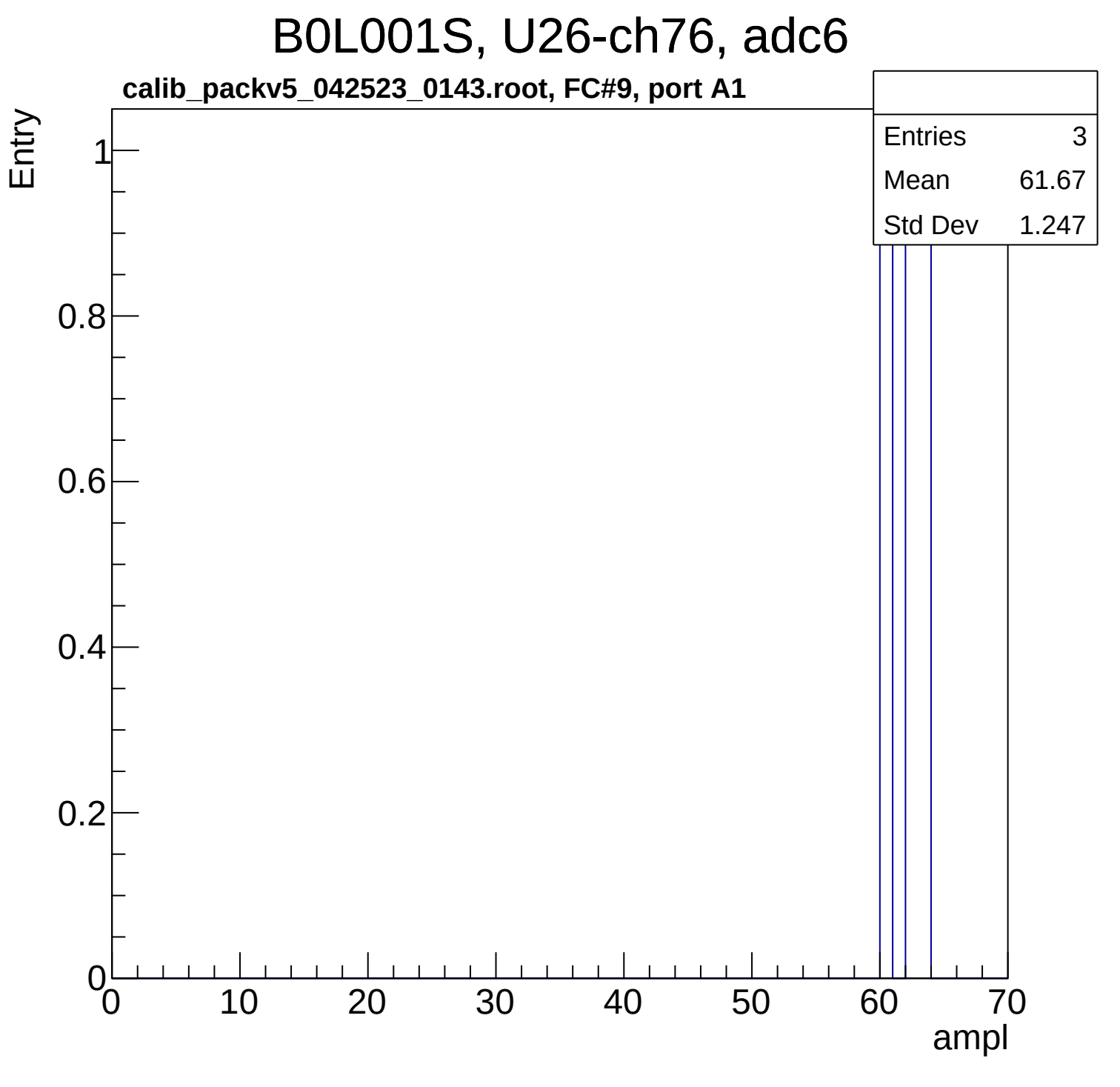
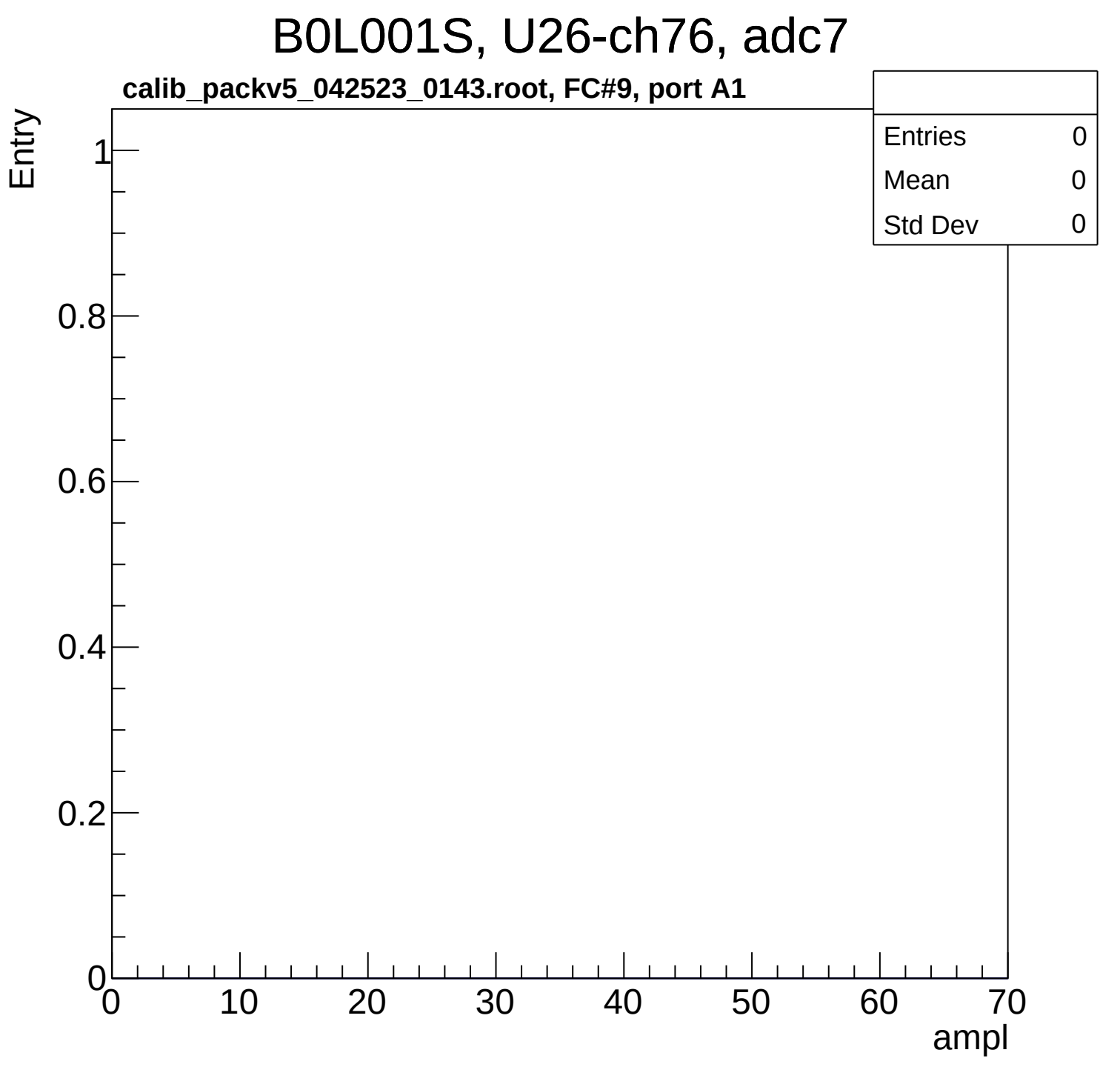
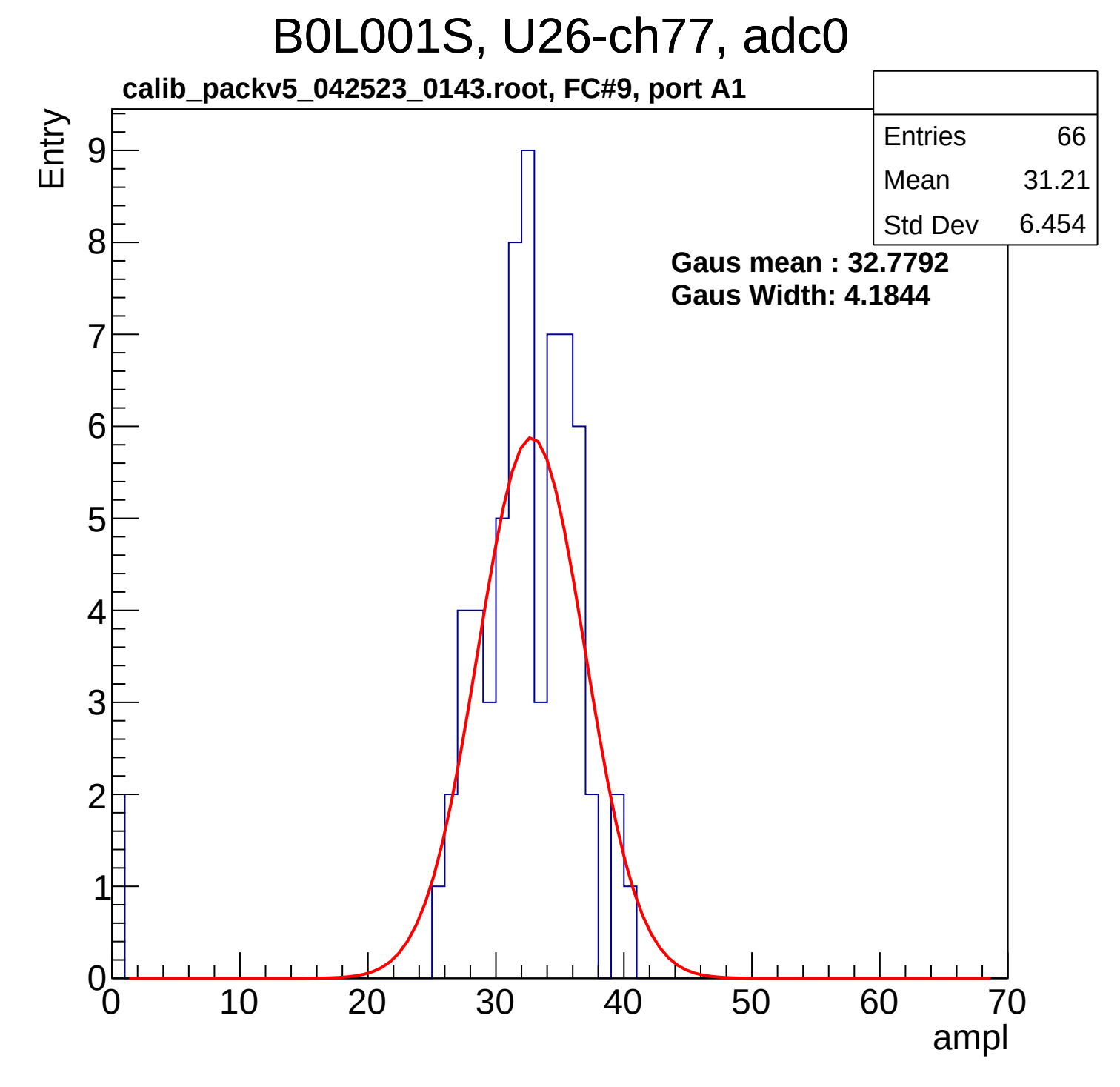
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.55
Std Dev 3.372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.74
Std Dev 10.42
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.21
Std Dev 6.454
Gaus mean : 32.7792
Gaus Width: 4.1844
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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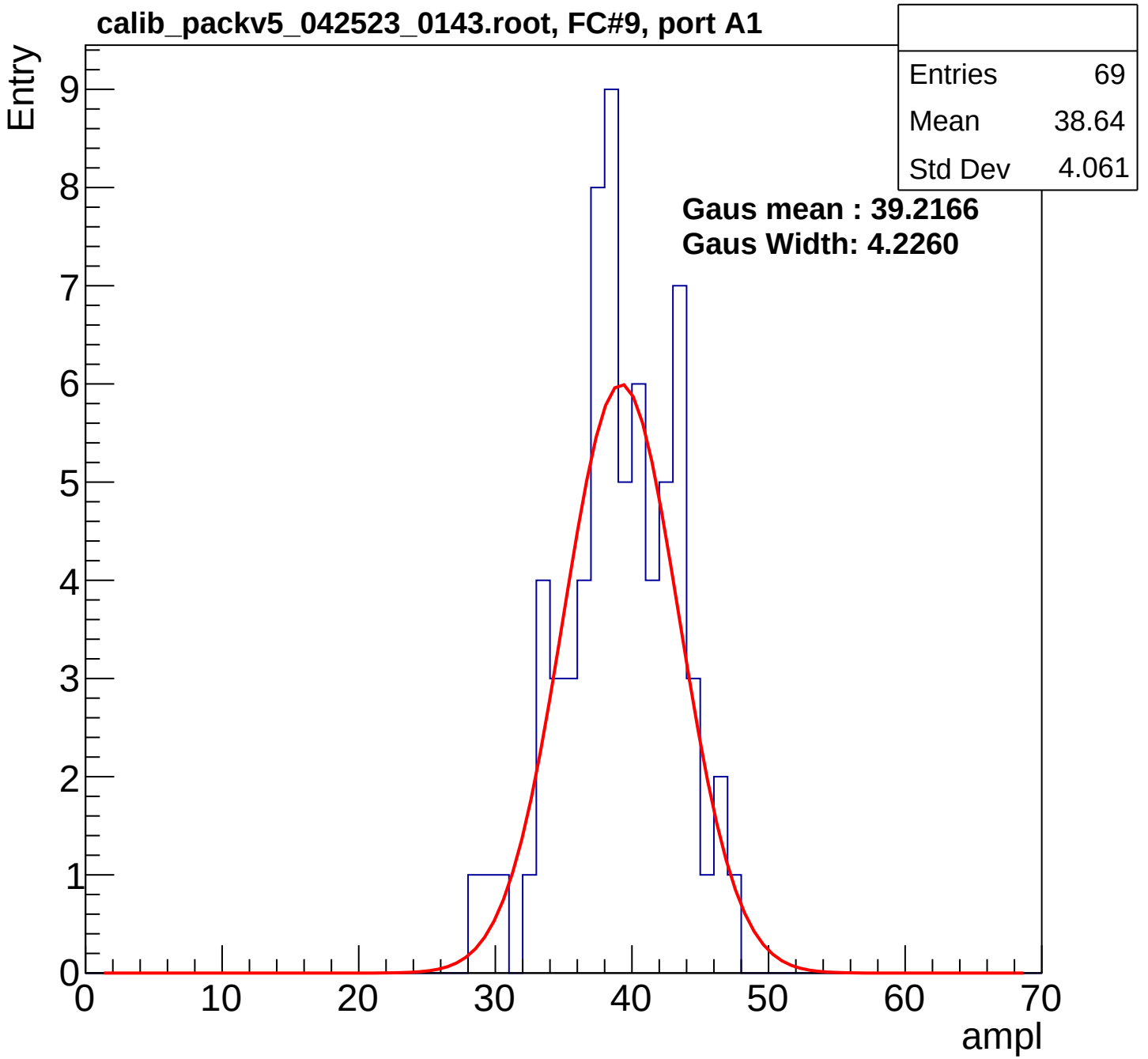
B0L001S, U26-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.64
Std Dev 4.061
Gaus mean : 39.2166
Gaus Width: 4.2260
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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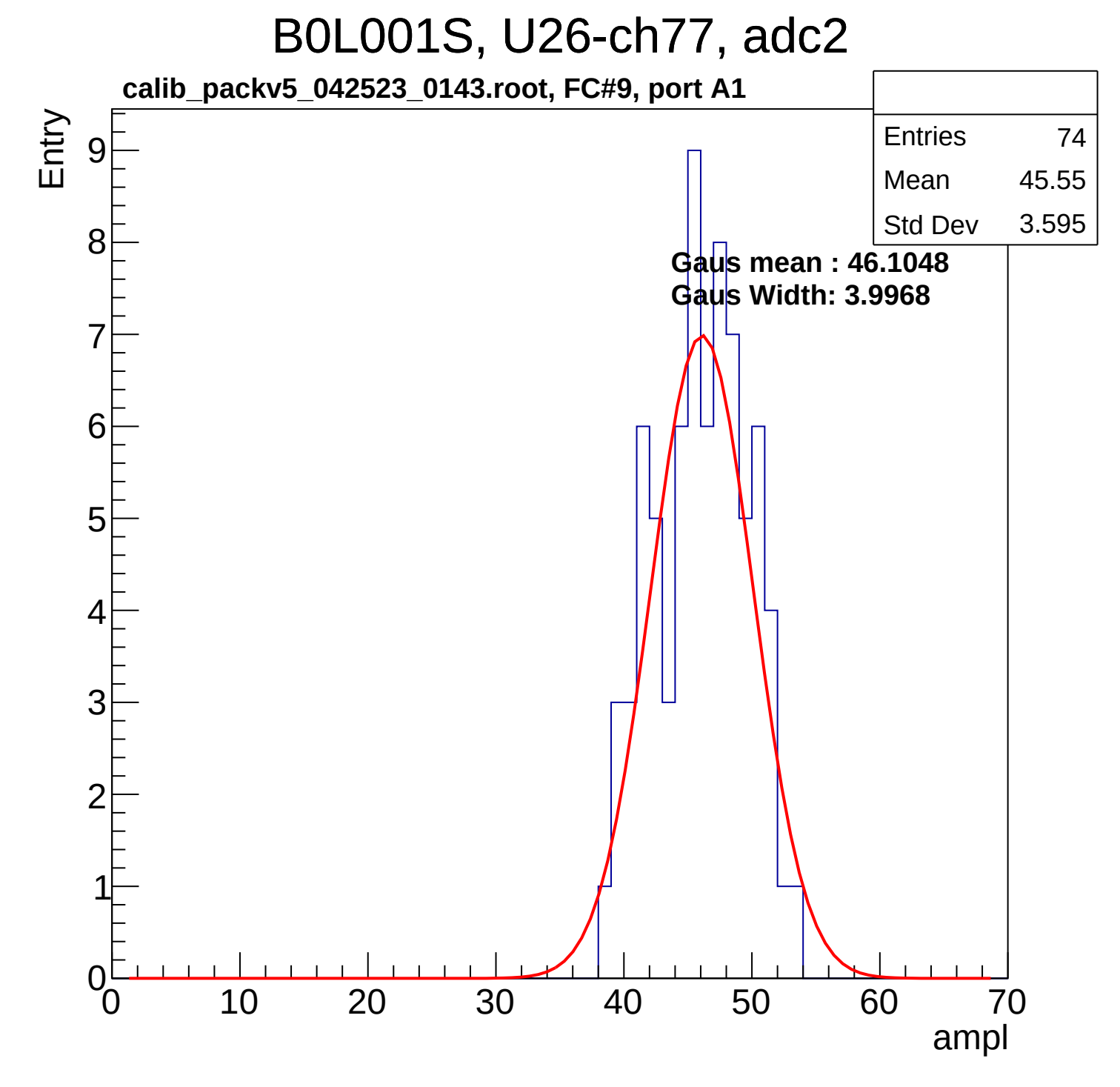
B0L001S, U26-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 45.55
Std Dev 3.595
Gaus mean : 46.1048
Gaus Width: 3.9968
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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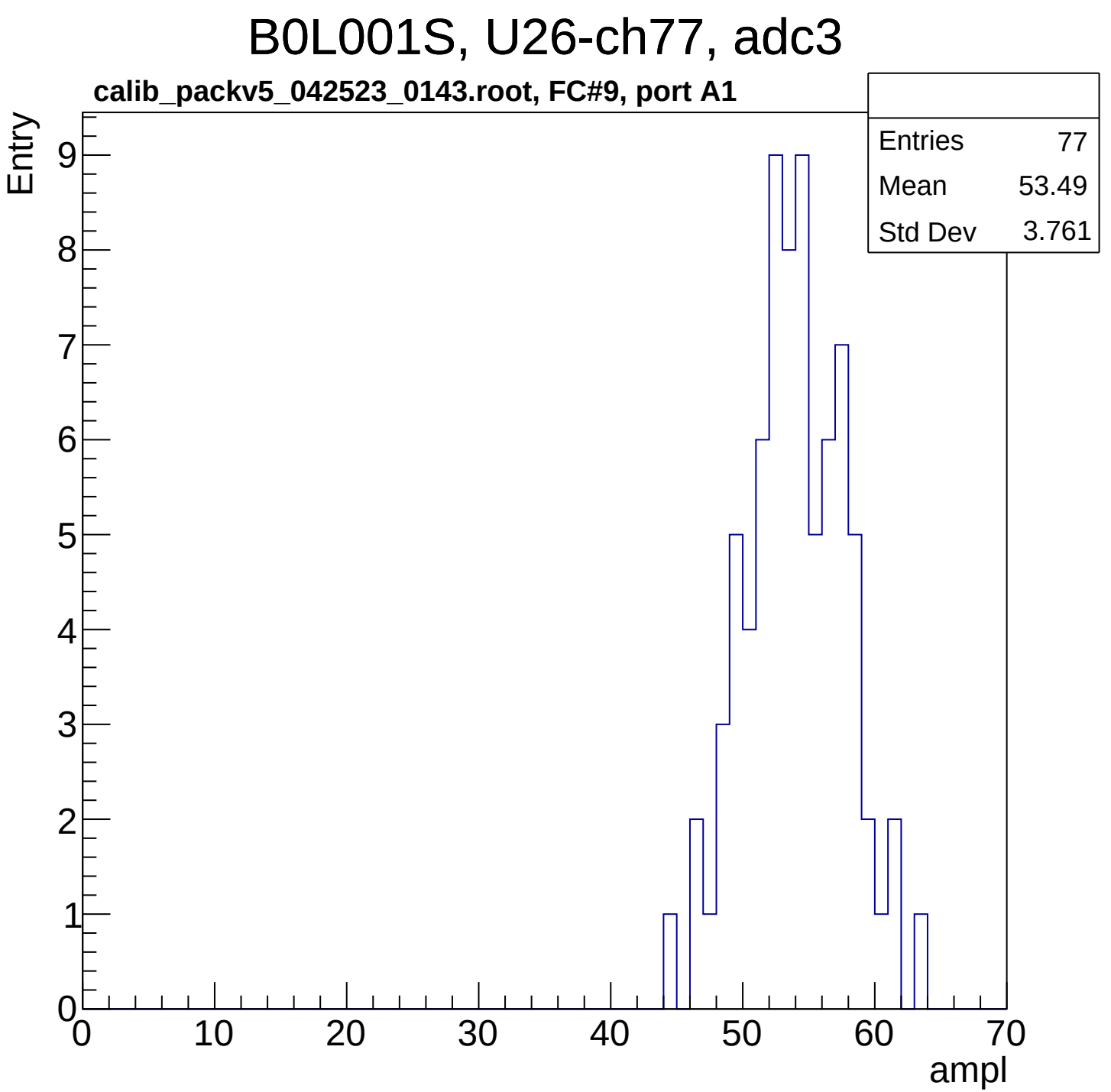
40
50
60
70


B0L001S, U26-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 53.49
Std Dev 3.761
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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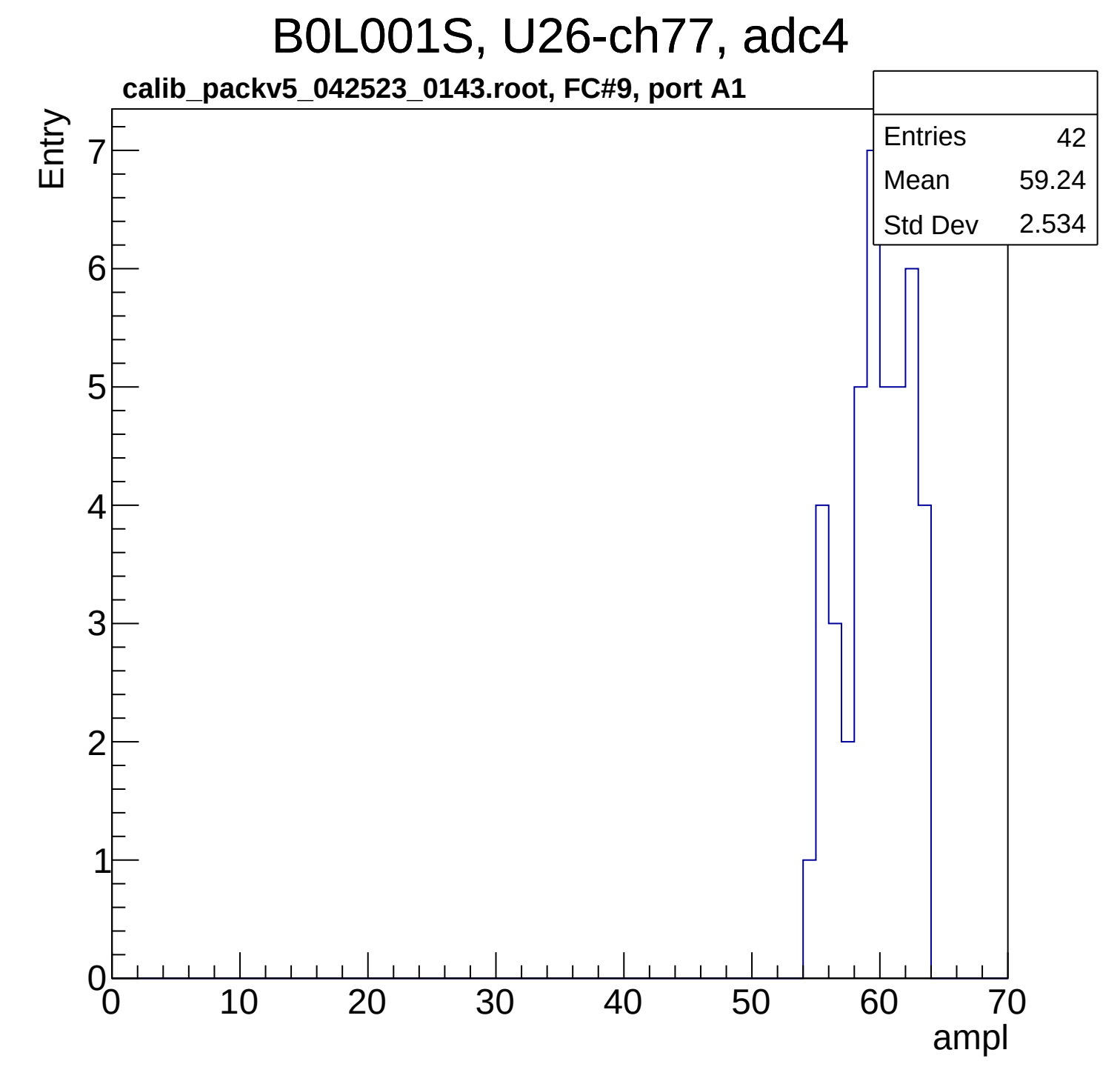
B0L001S, U26-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.24
Std Dev 2.534
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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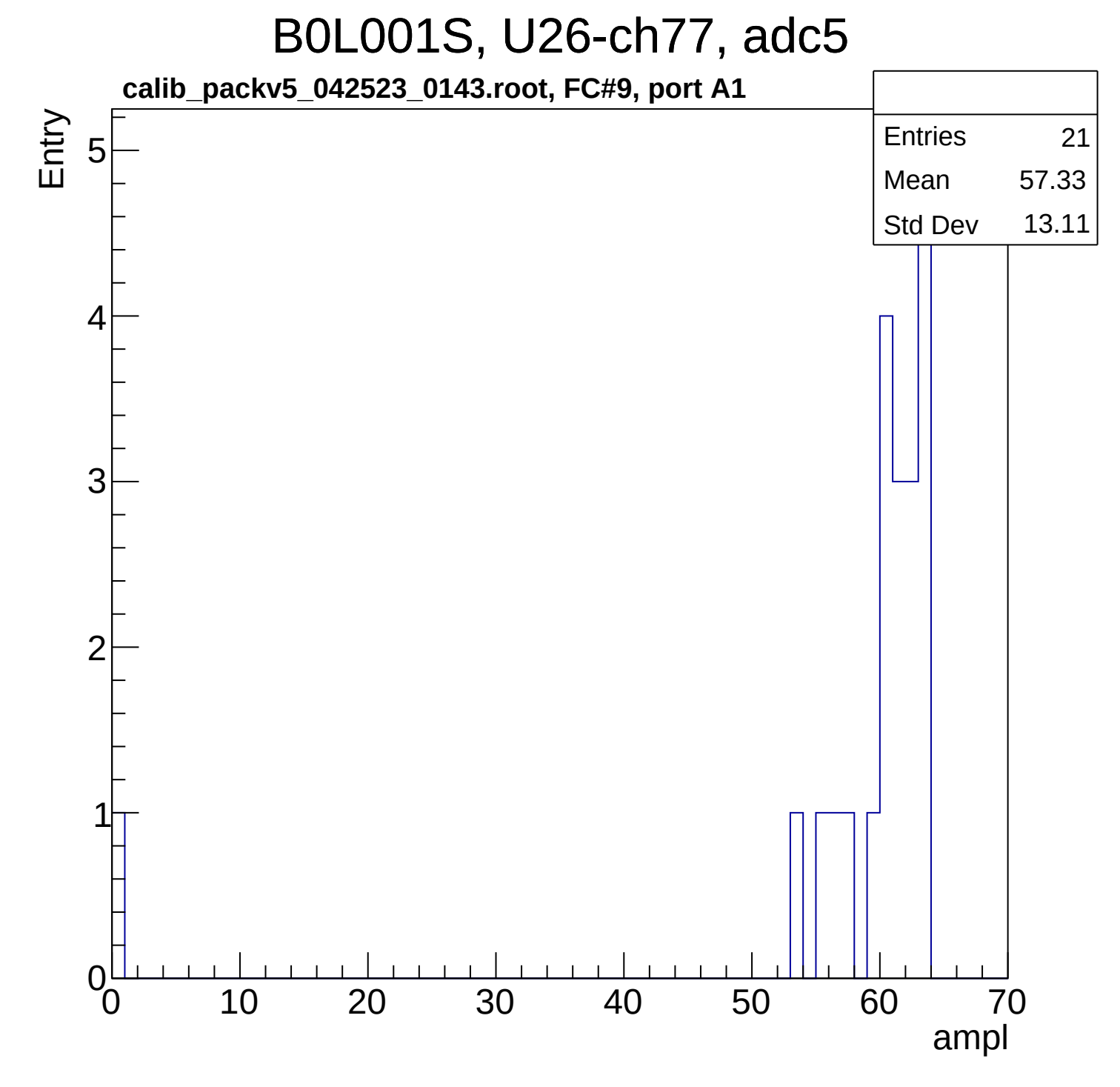
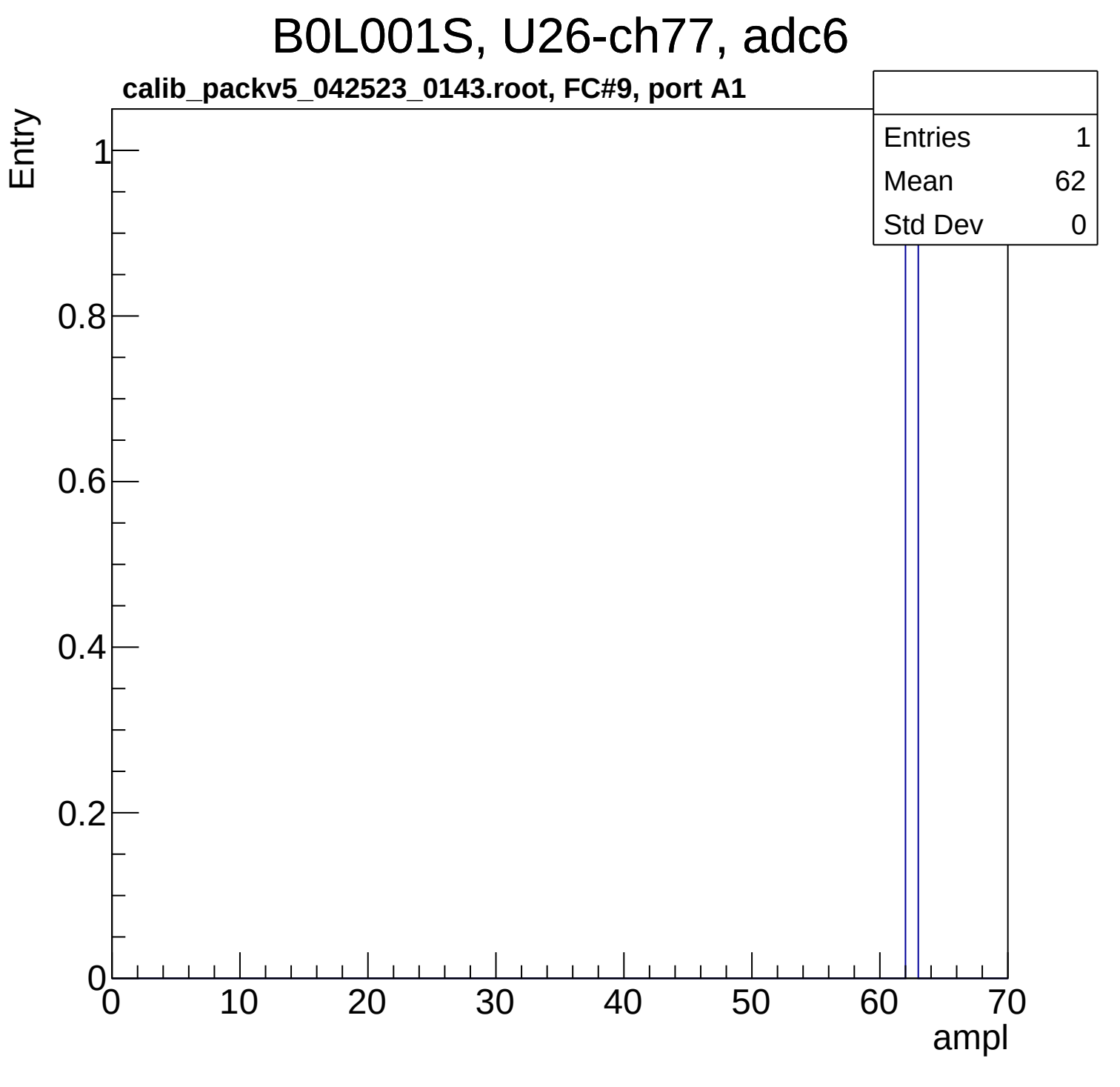
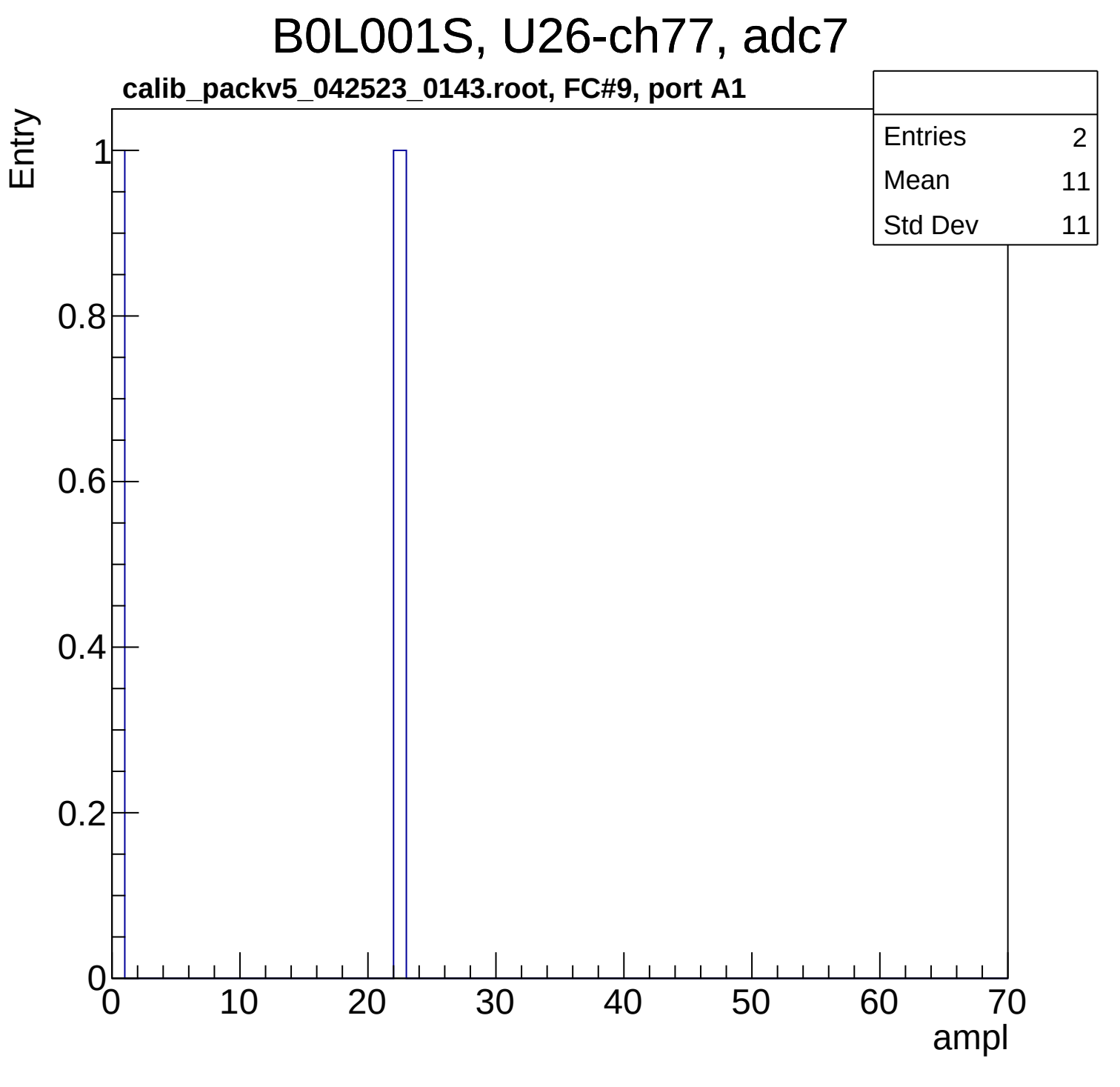
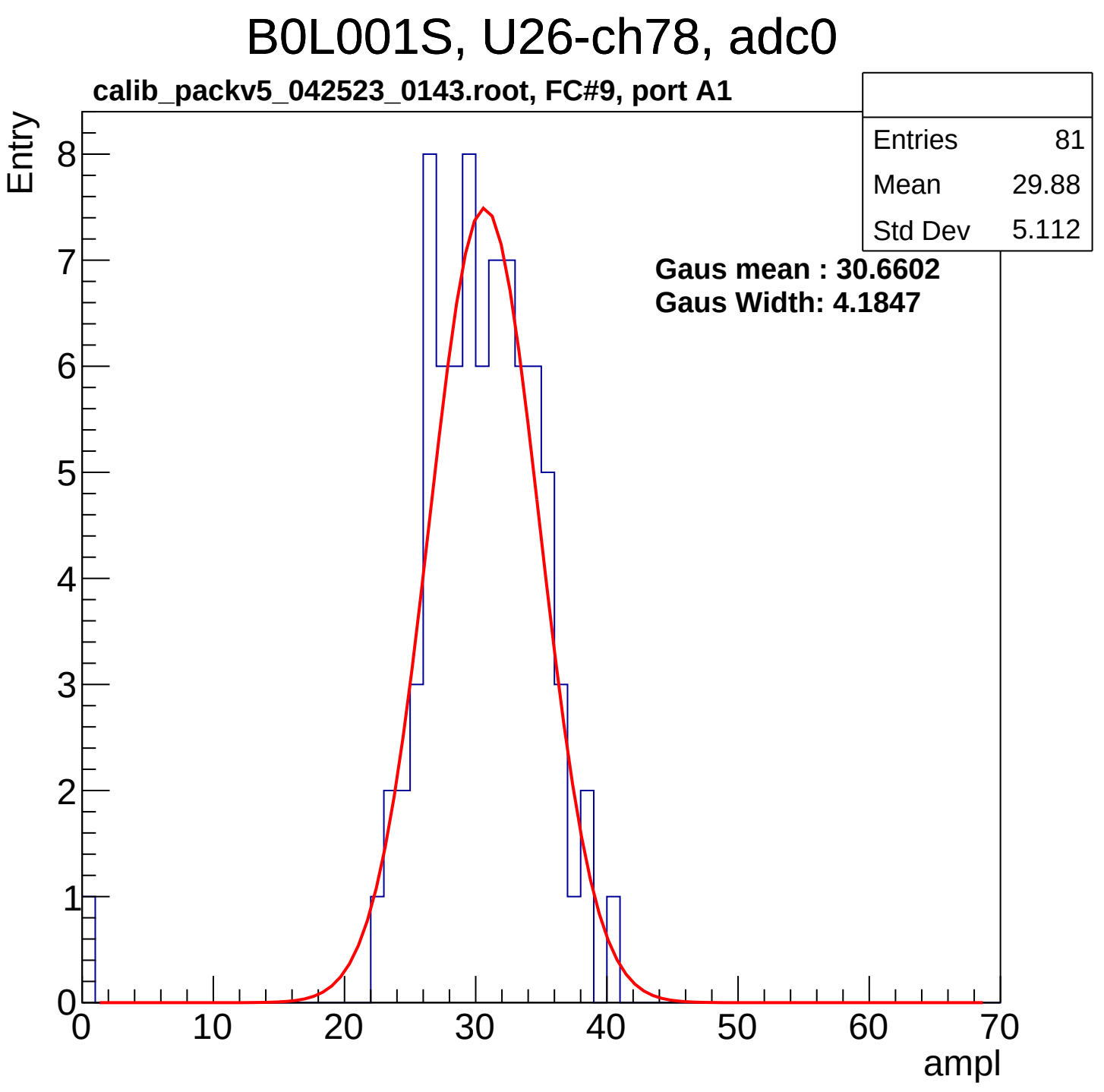
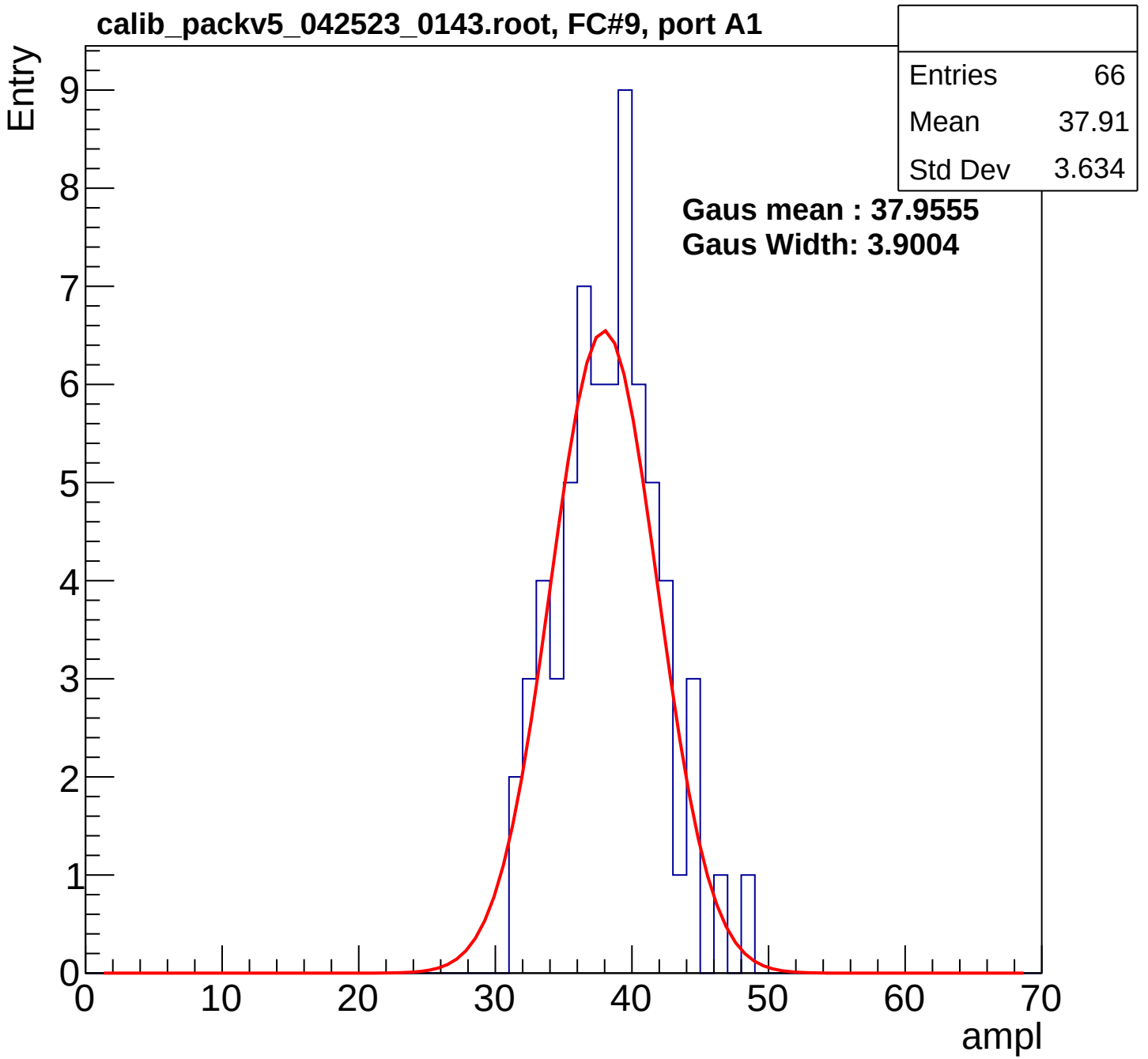

B0L001S, U26-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 57.33
Std Dev 13.11
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 29.88
Std Dev 5.112
Gaus mean : 30.6602
Gaus Width: 4.1847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.91
Std Dev 3.634
Gaus mean : 37.9555
Gaus Width: 3.9004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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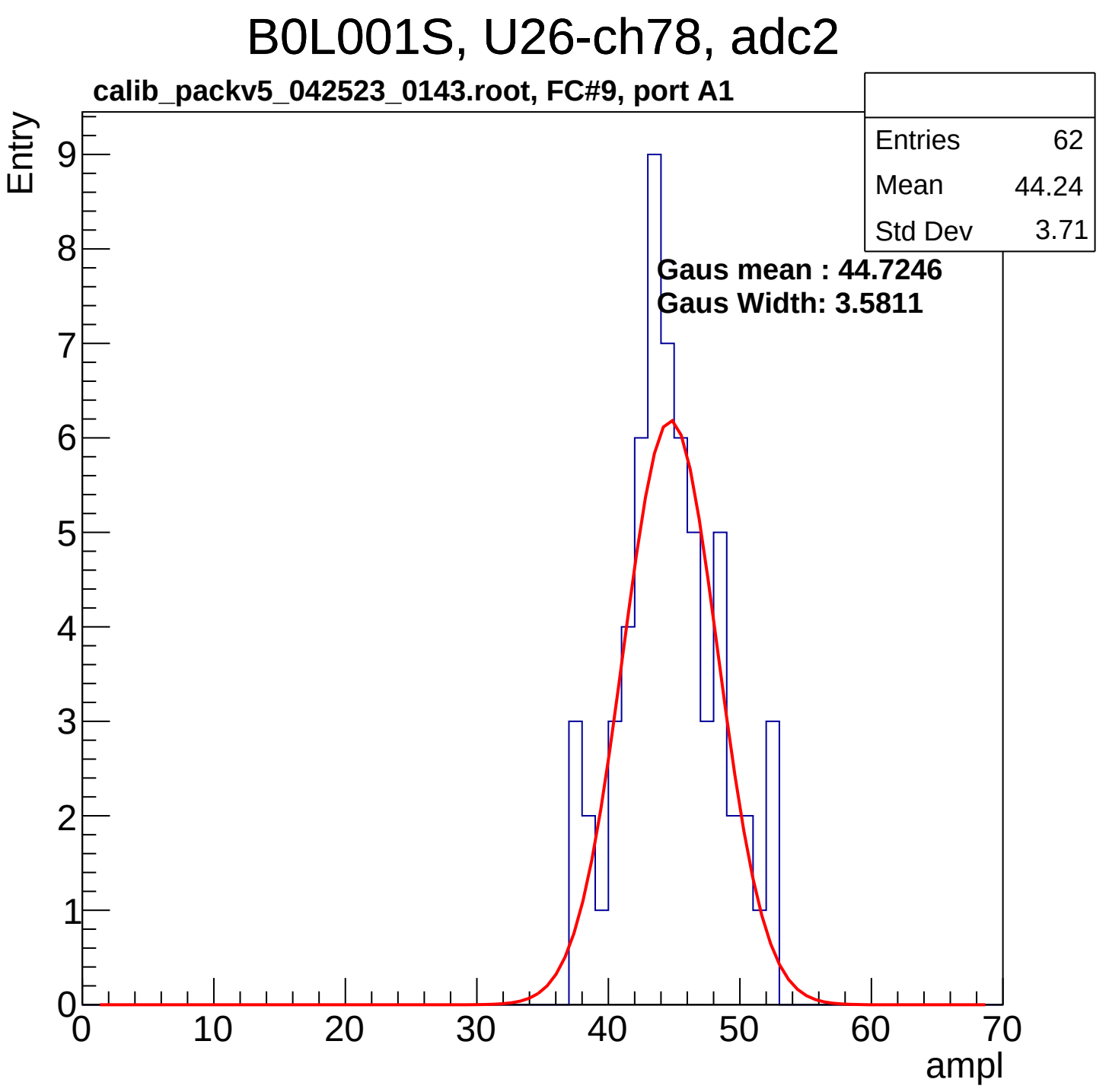
B0L001S, U26-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.24
Std Dev 3.71
Gaus mean : 44.7246
Gaus Width: 3.5811
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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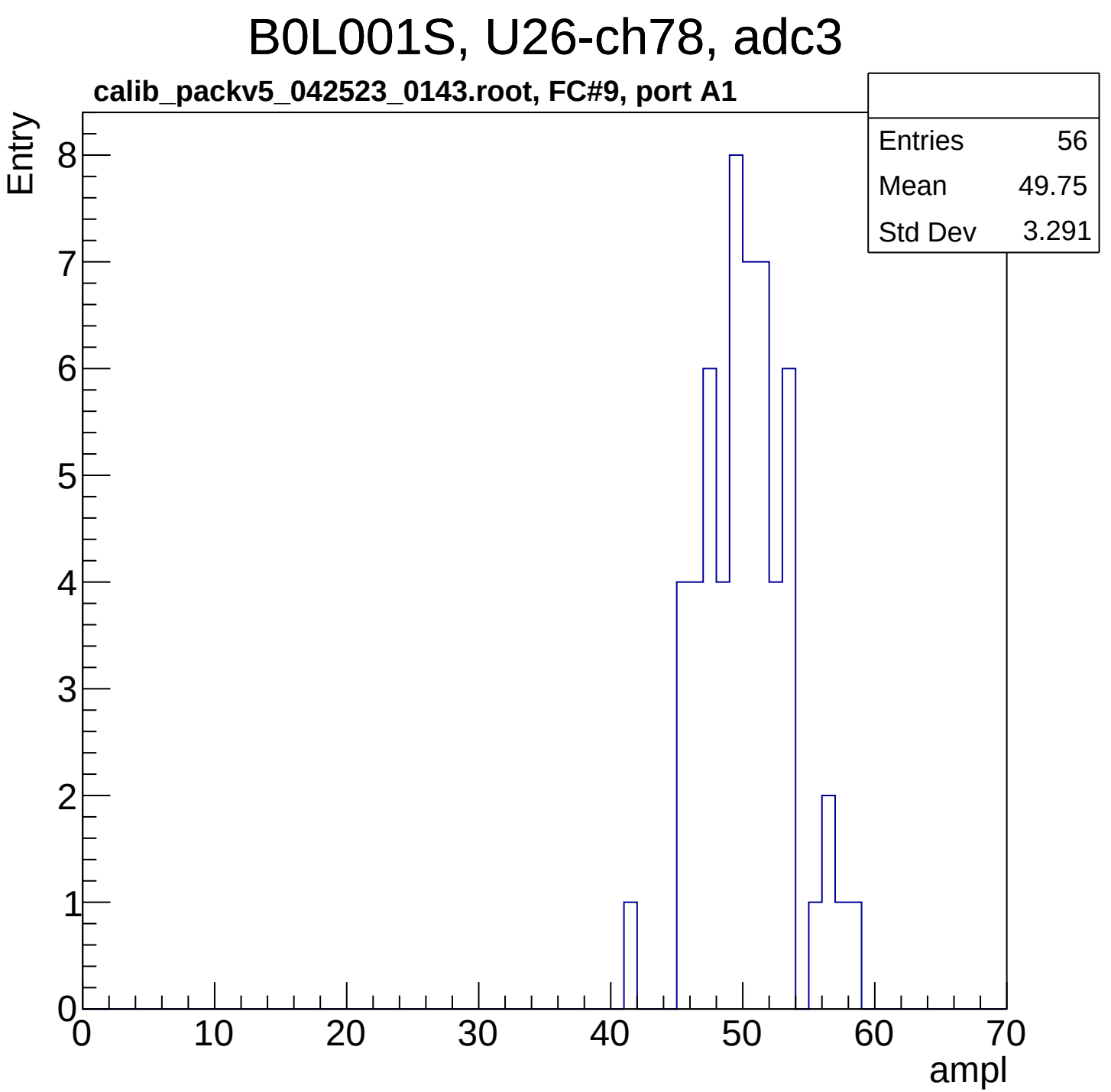
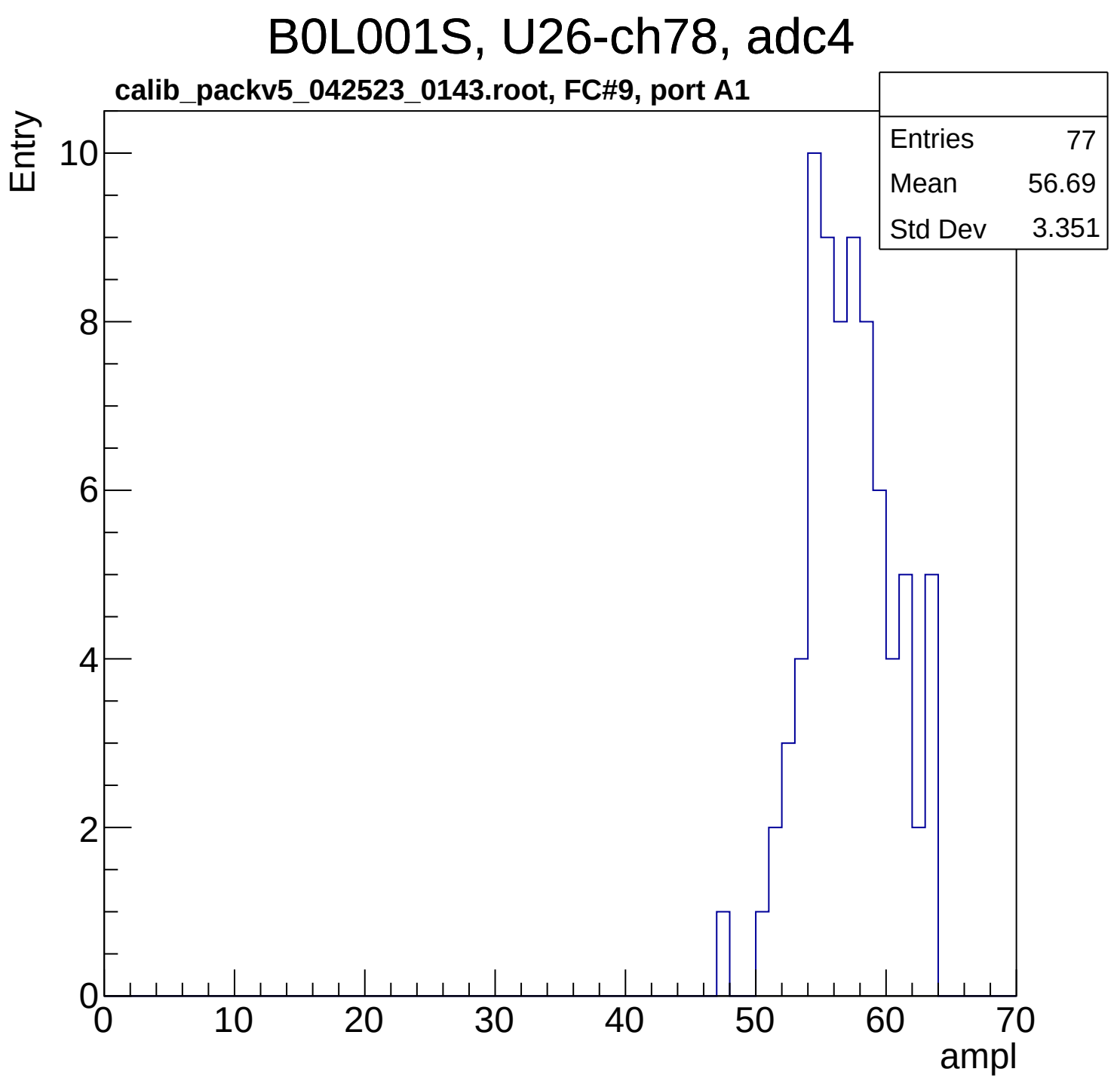
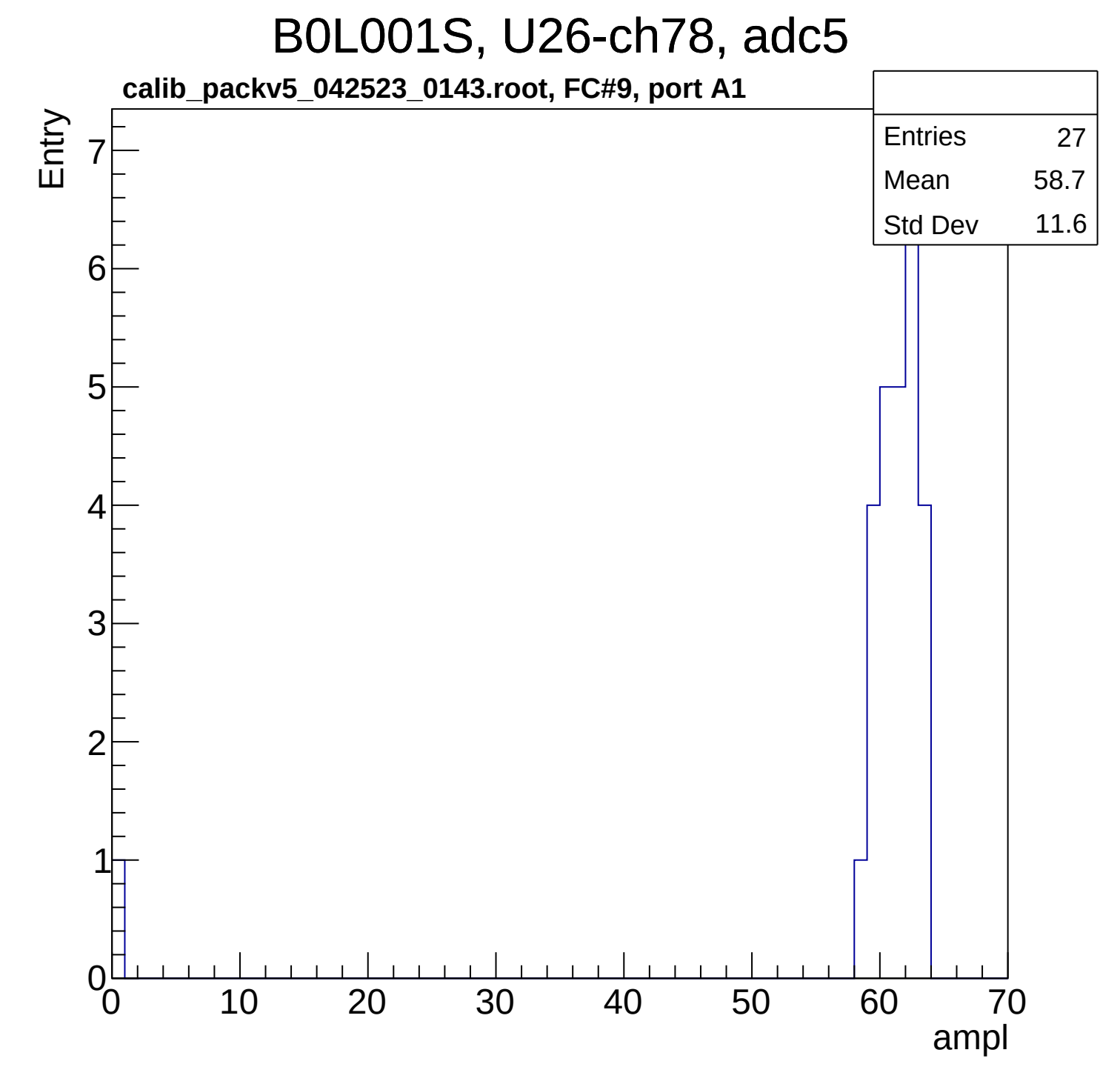
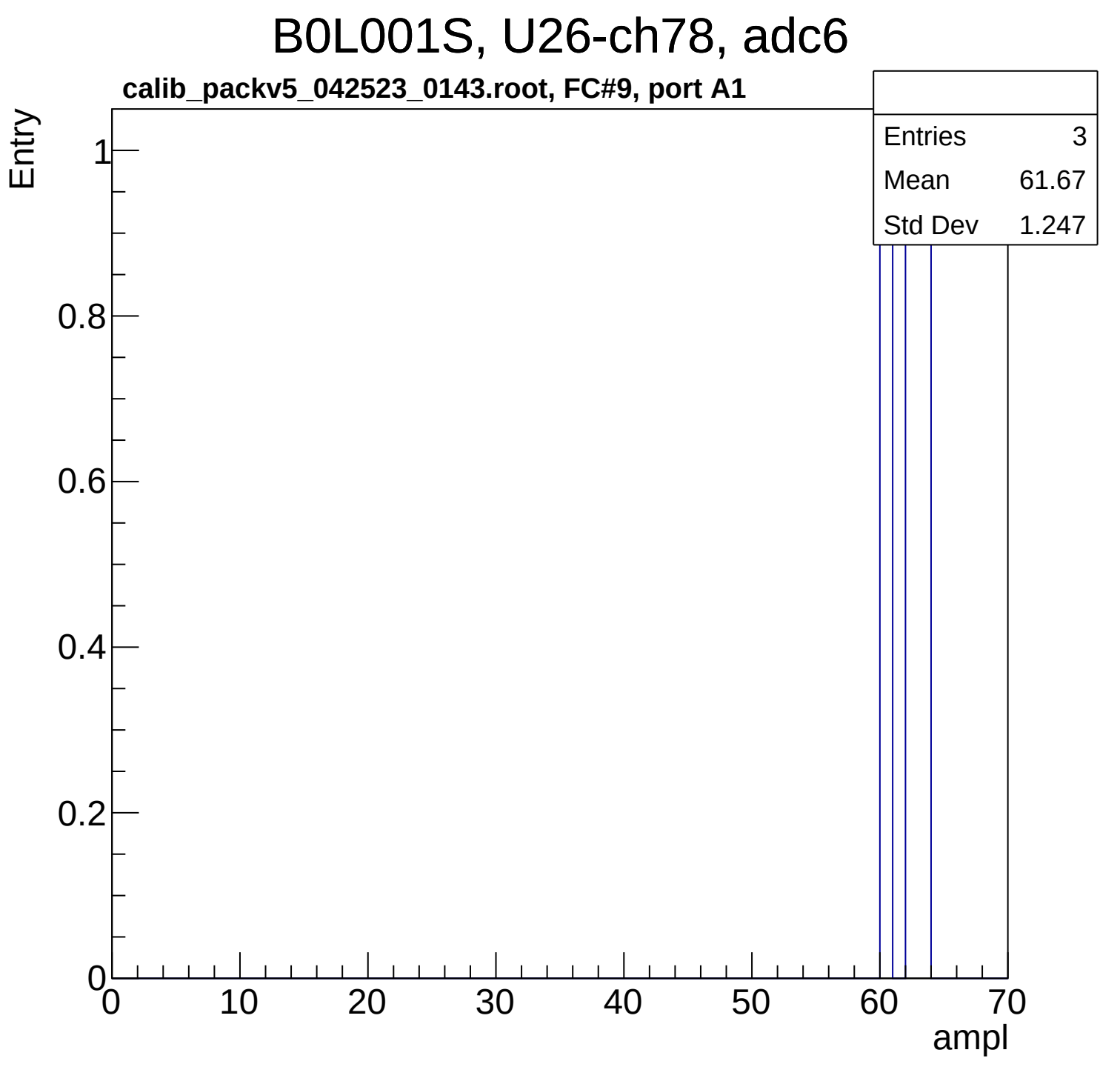
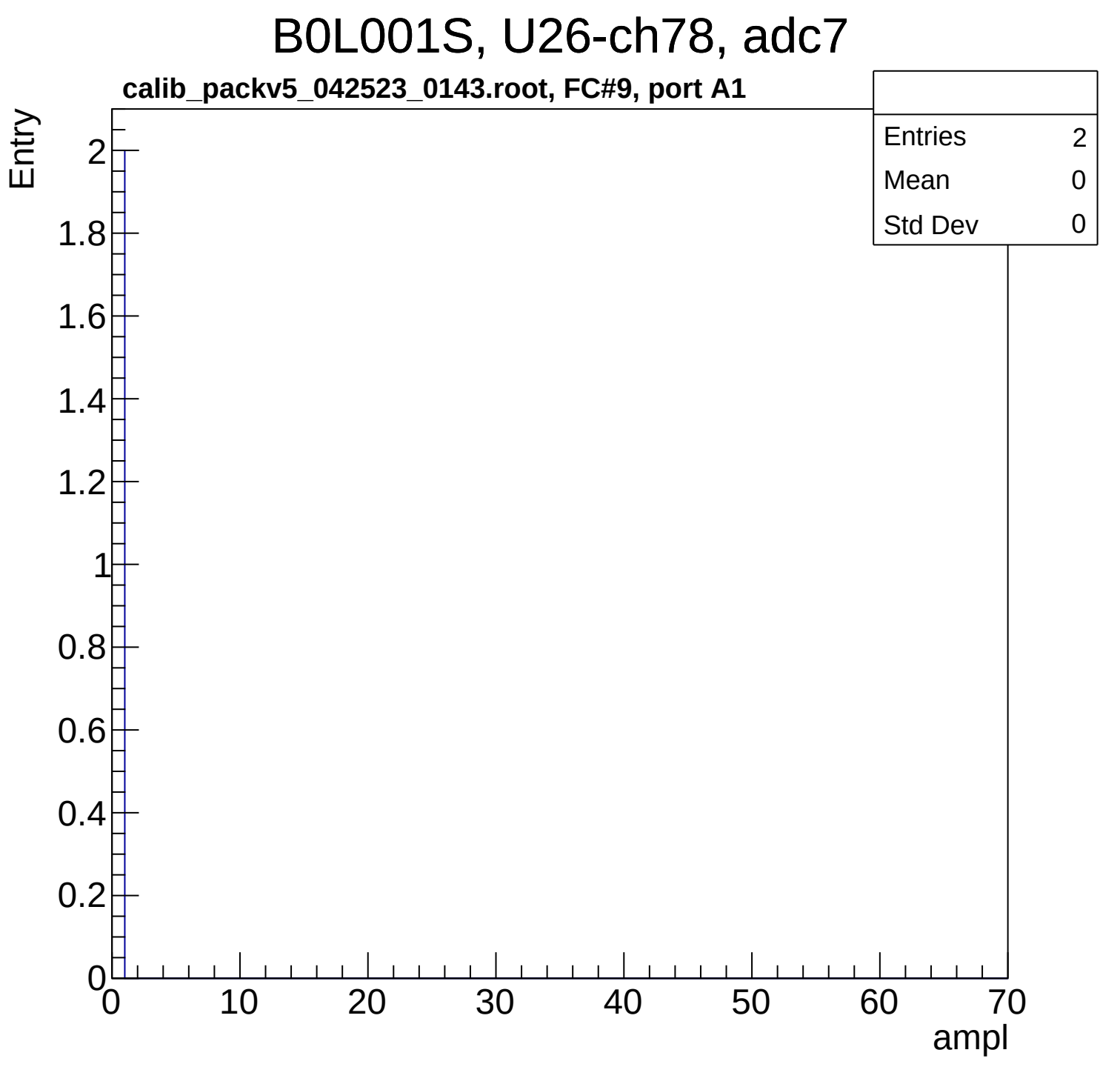
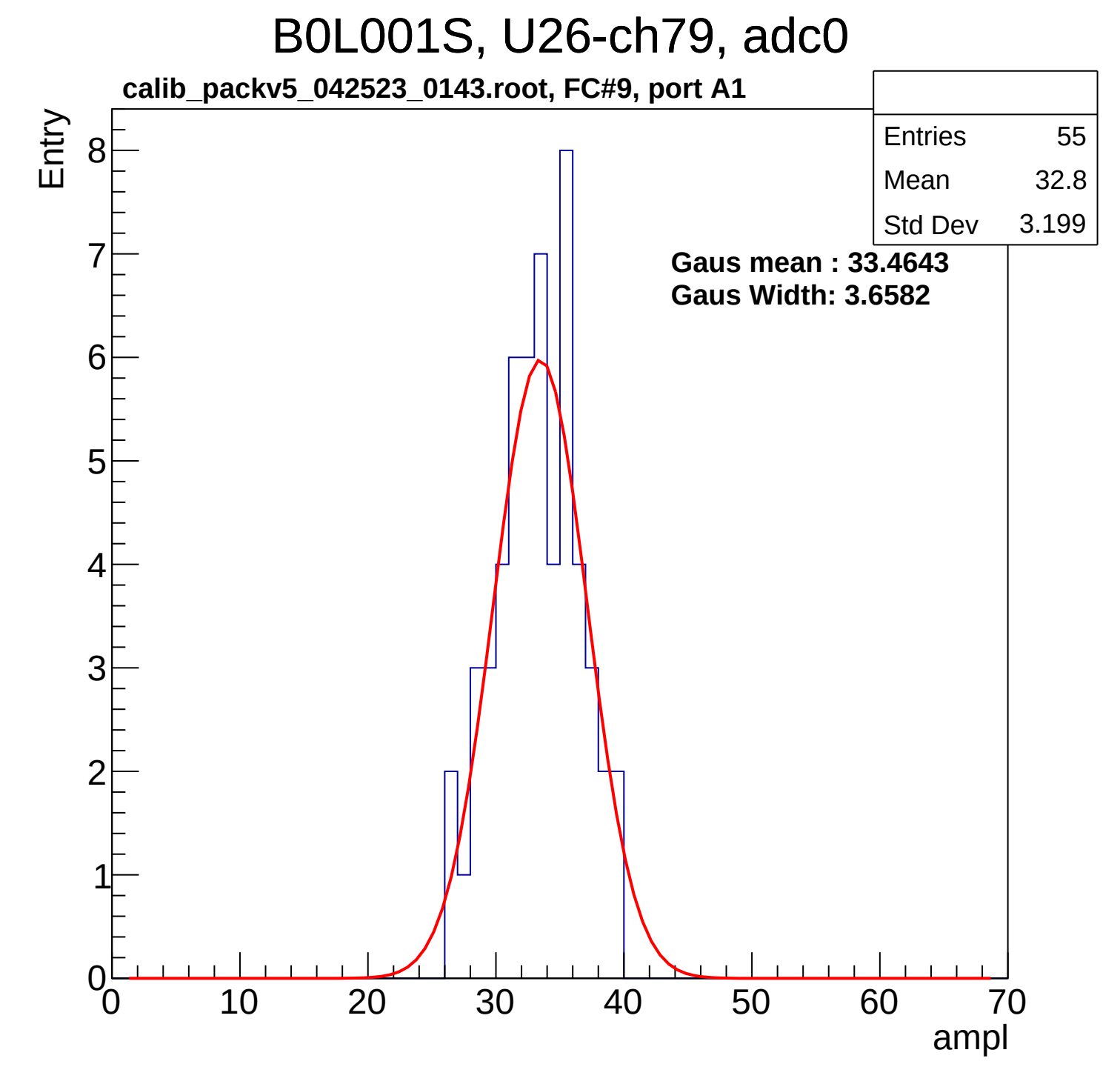

B0L001S, U26-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.75
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

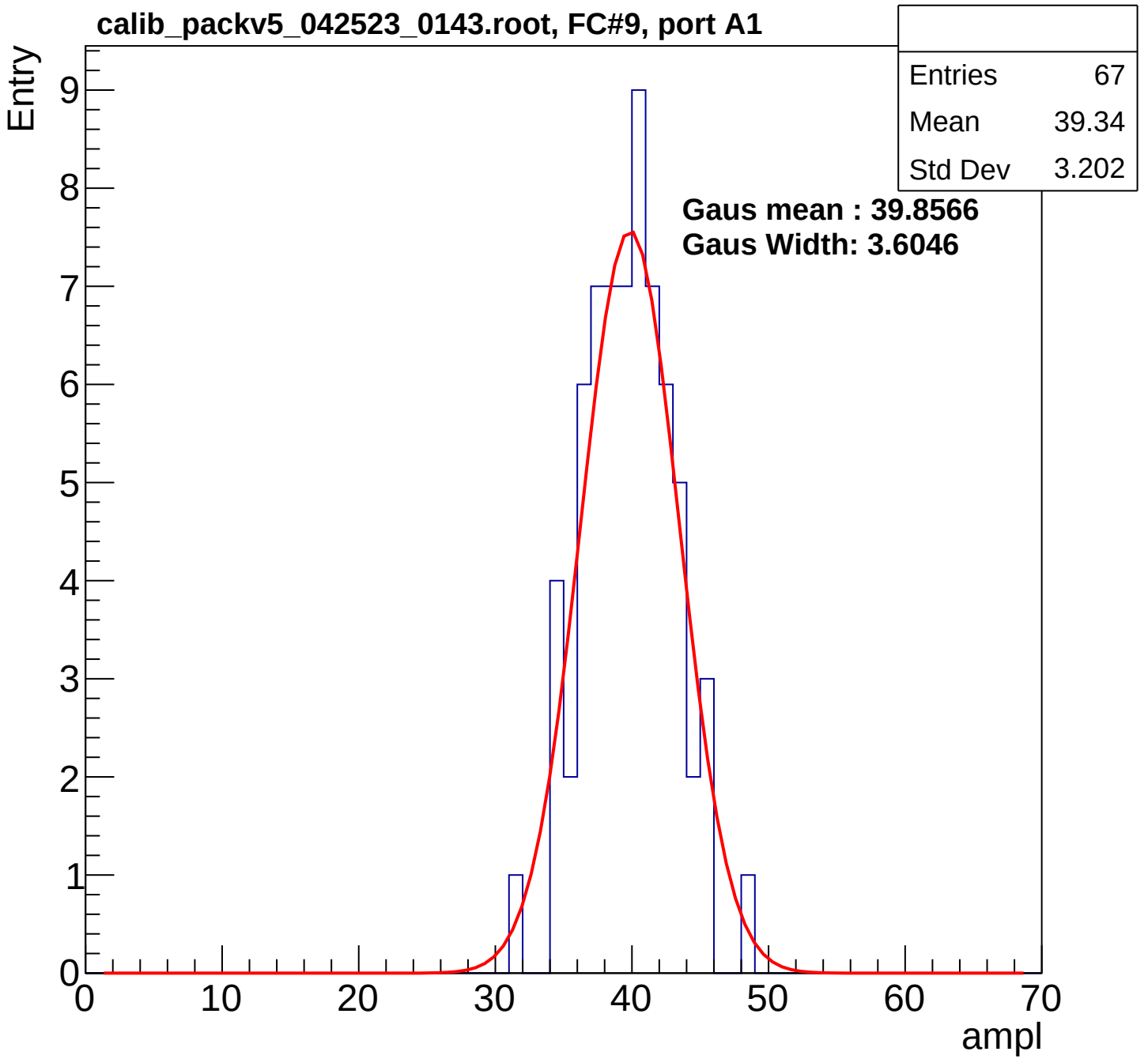

B0L001S, U26-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 56.69
Std Dev 3.351
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

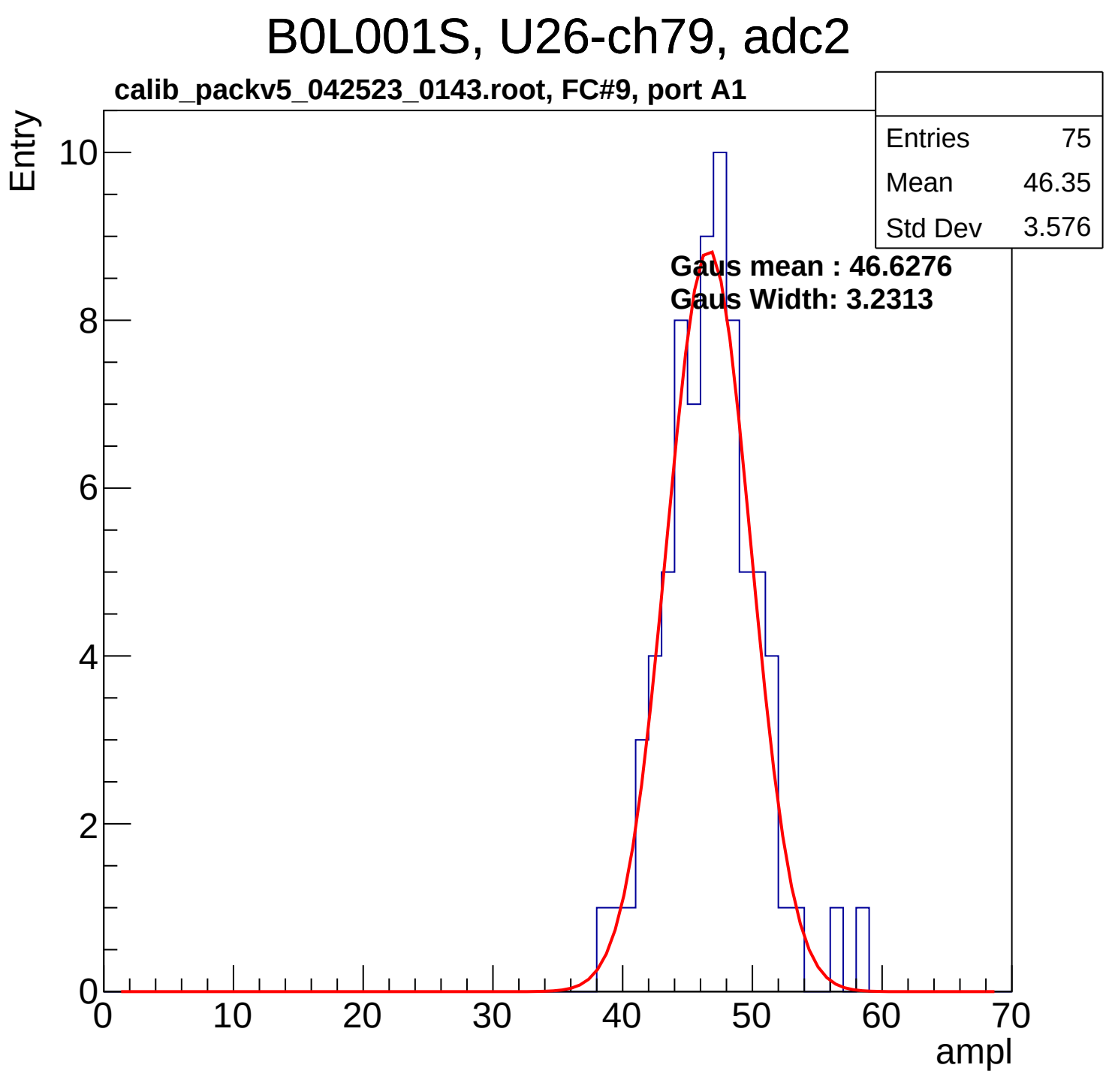

B0L001S, U26-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.7
Std Dev 11.6
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

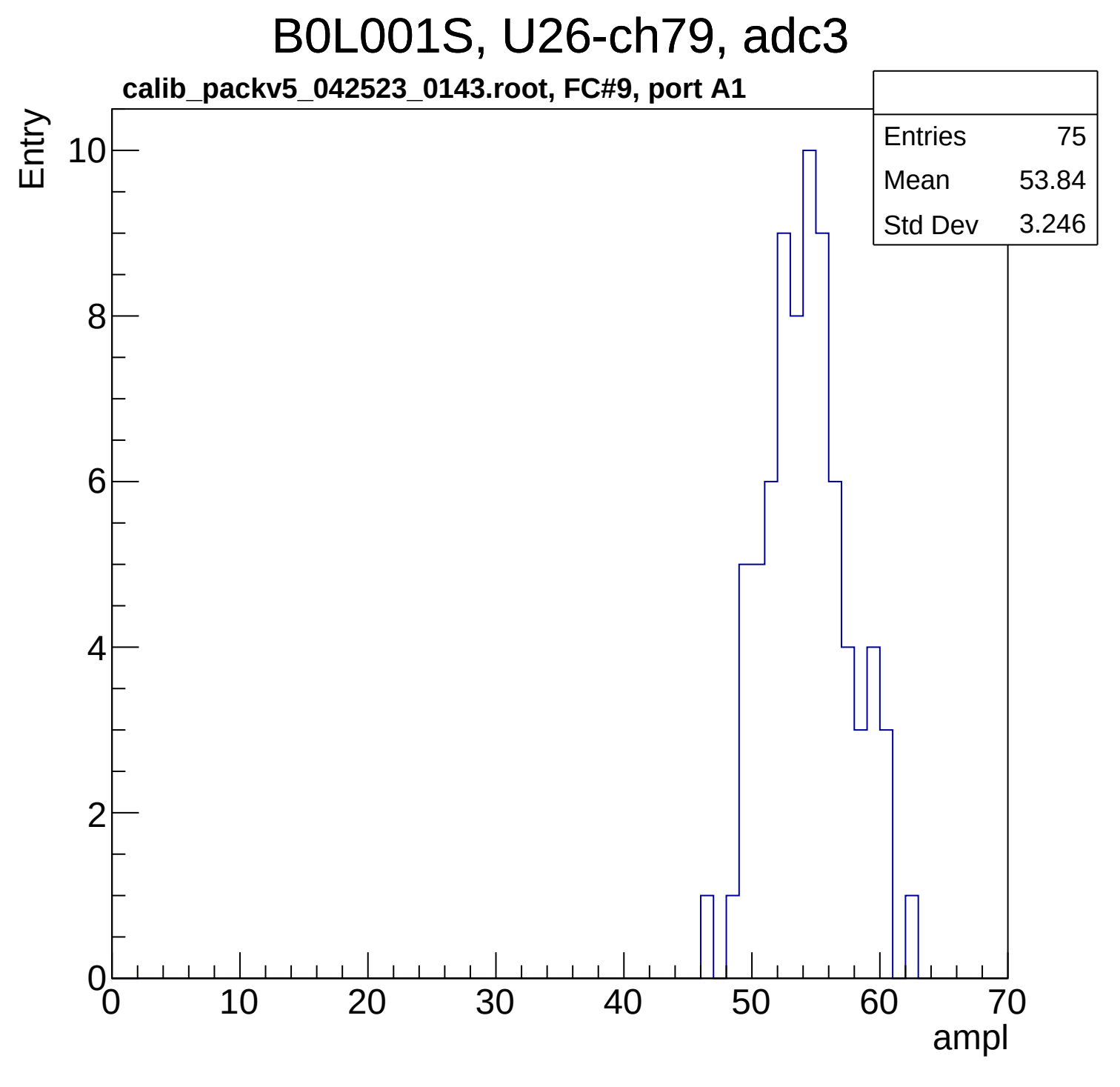

B0L001S, U26-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

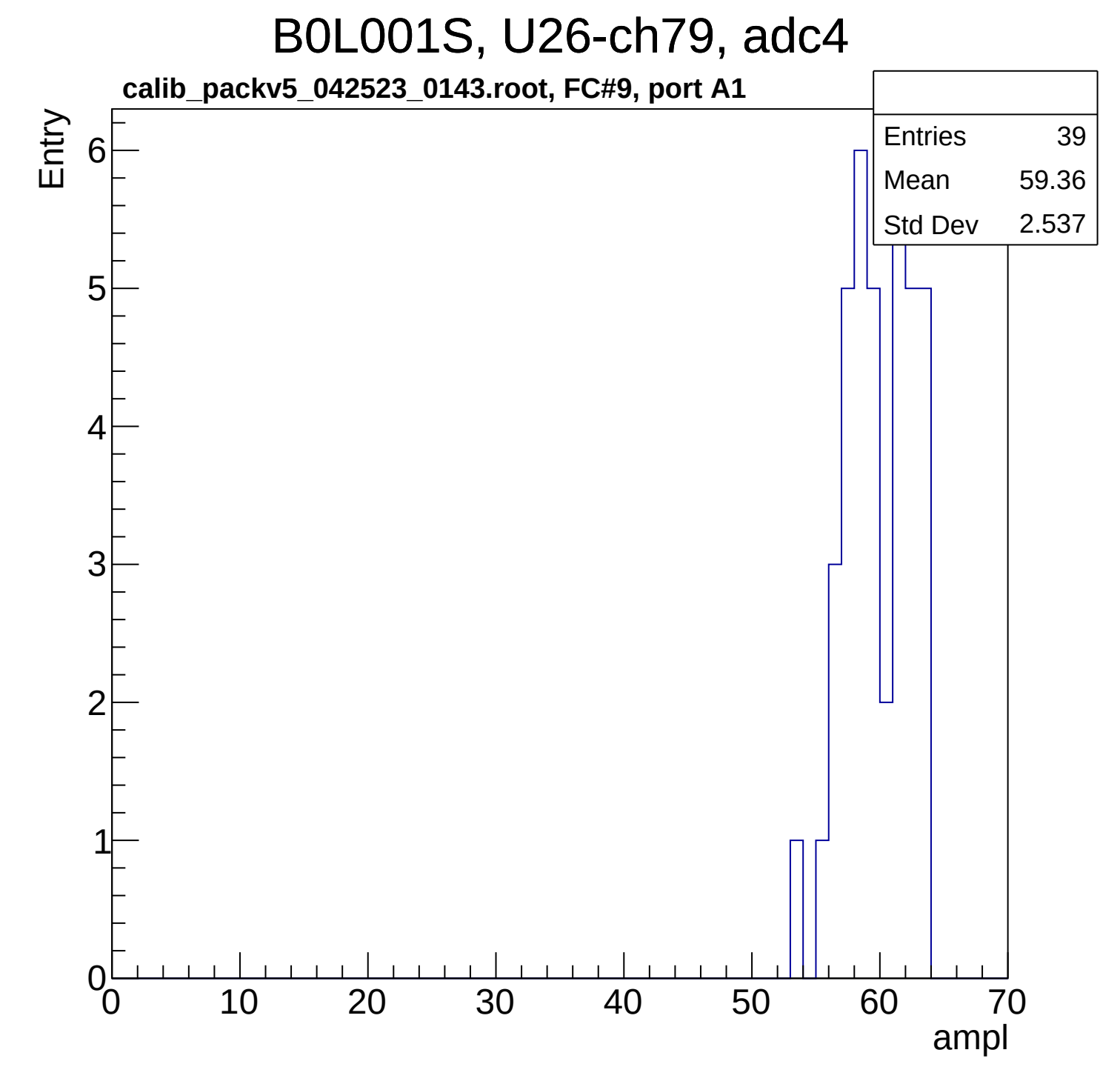

B0L001S, U26-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

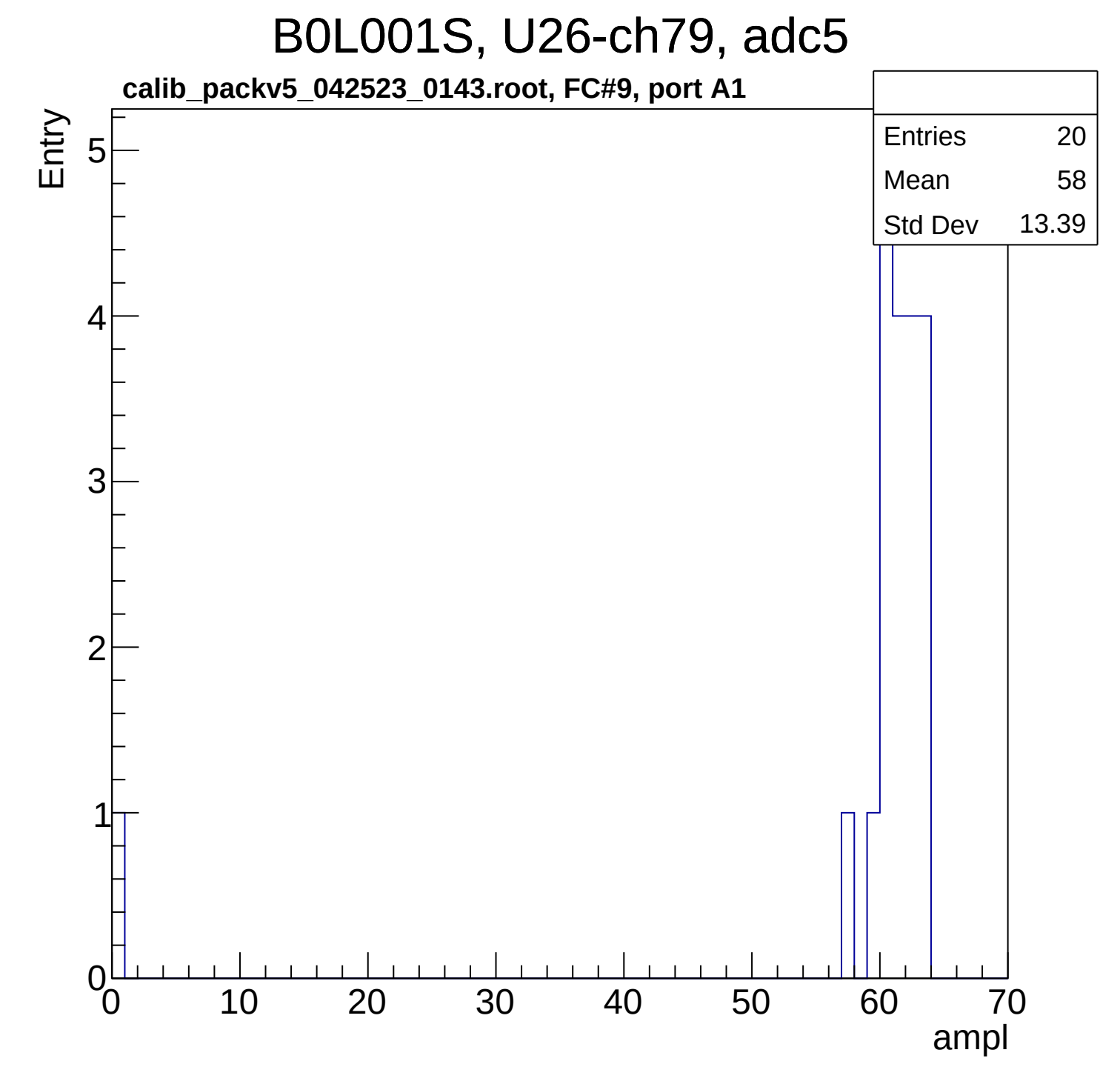

B0L001S, U26-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 32.8
Std Dev 3.199
Gaus mean : 33.4643
Gaus Width: 3.6582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

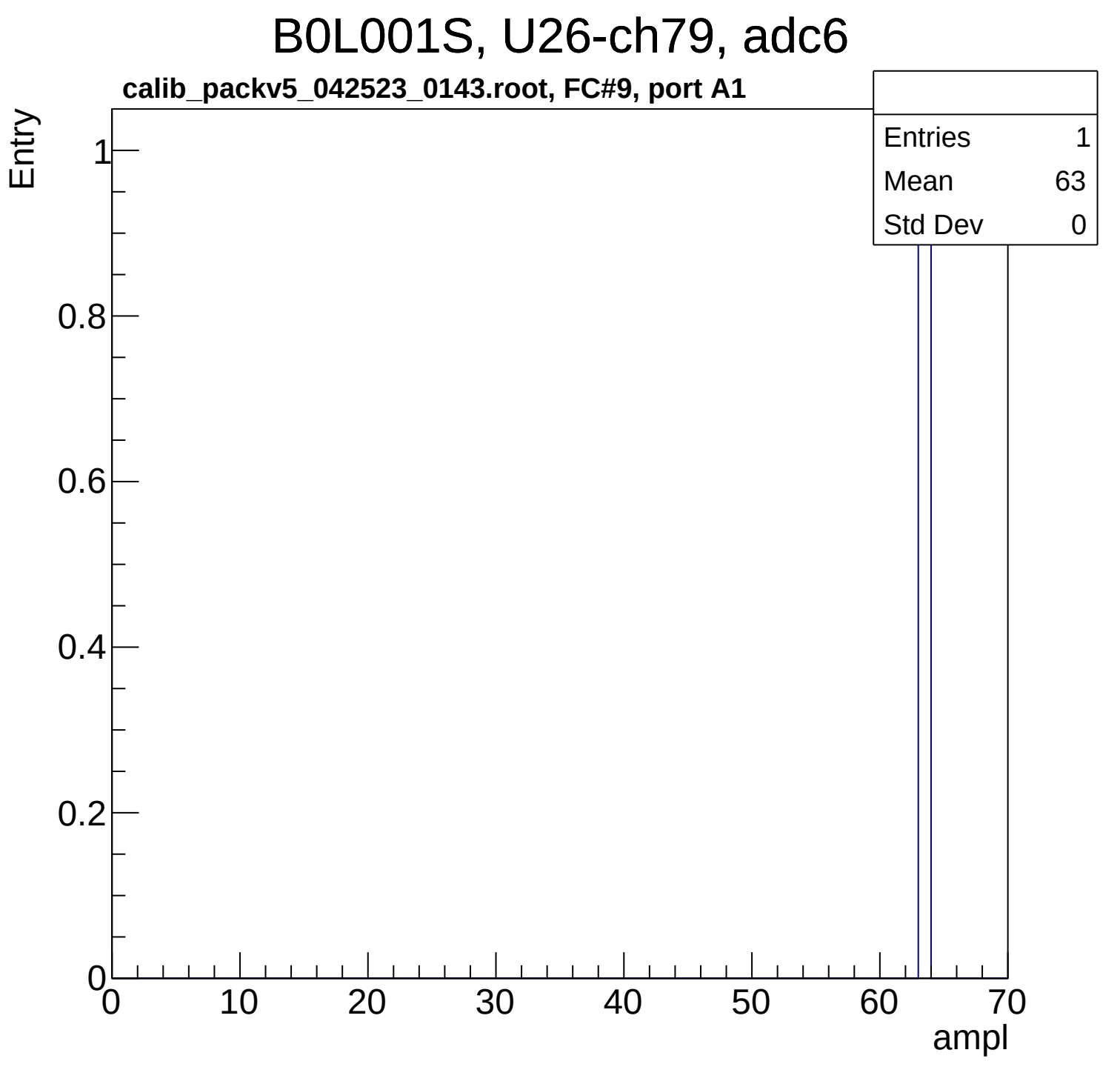

B0L001S, U26-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 39.34
Std Dev 3.202
Gaus mean : 39.8566
Gaus Width: 3.6046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 46.35
Std Dev 3.576
Gaus mean : 46.6276
Gaus Width: 3.2313
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 53.84
Std Dev 3.246
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.36
Std Dev 2.537
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 58
Std Dev 13.39
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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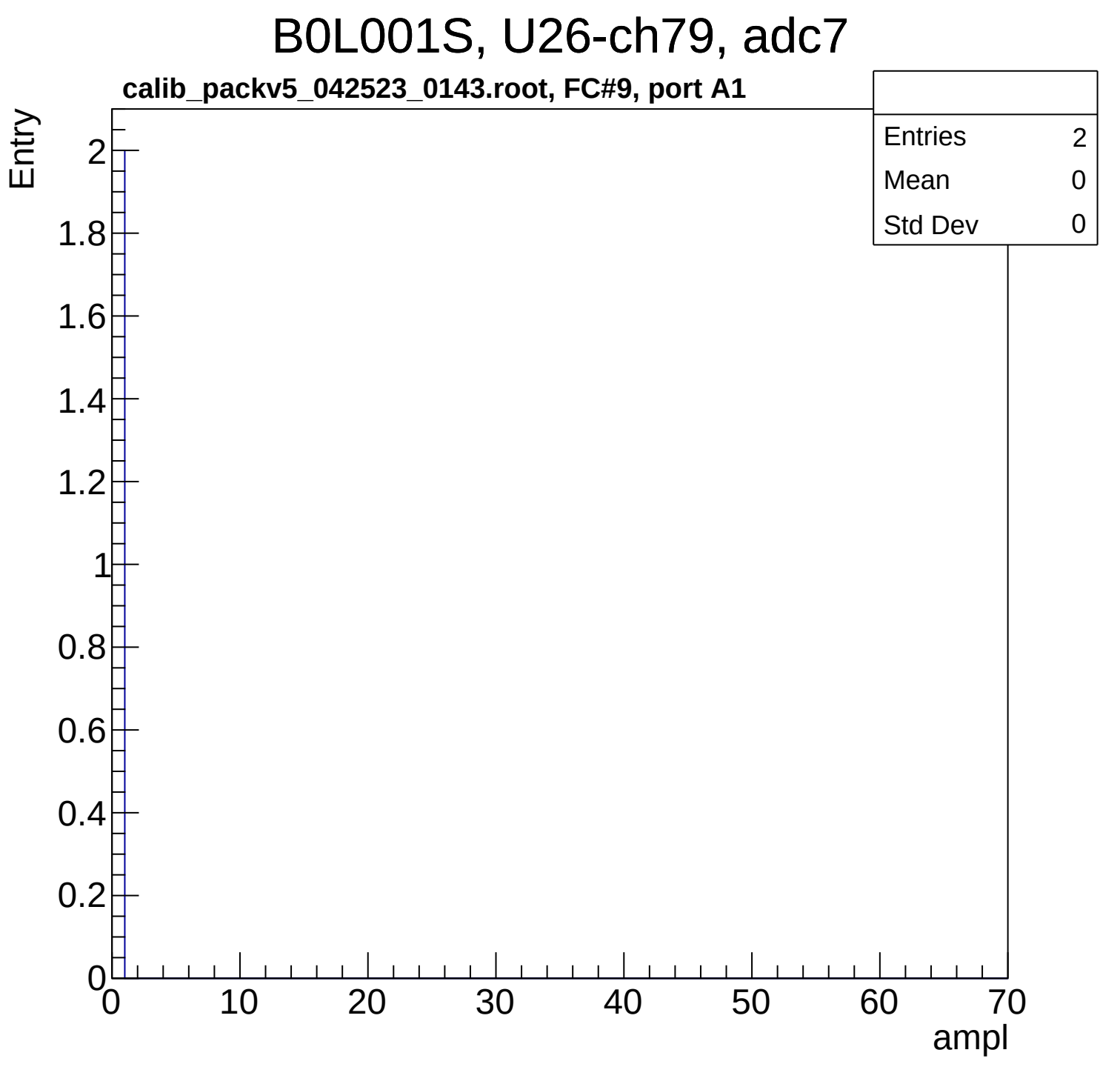
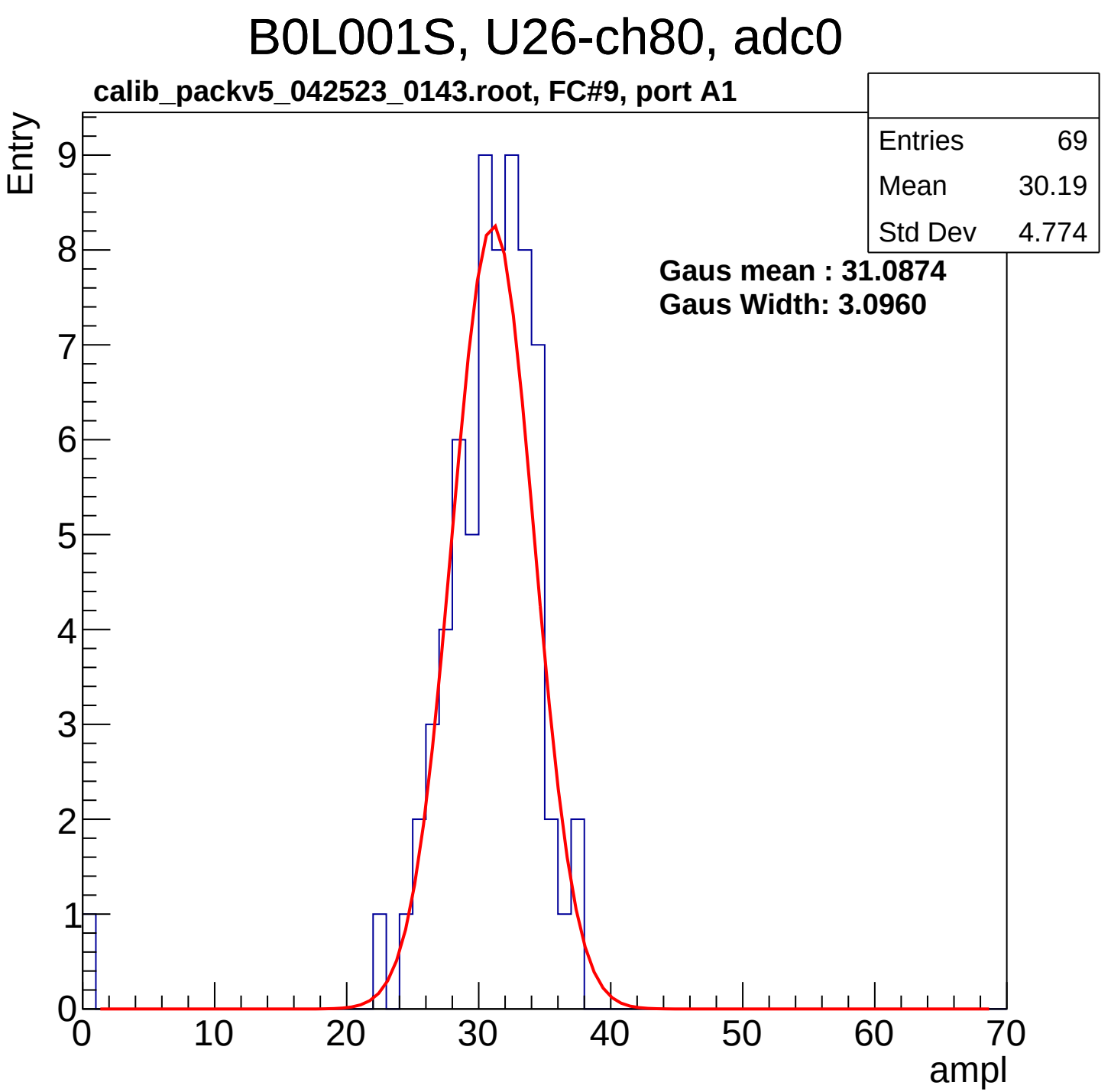
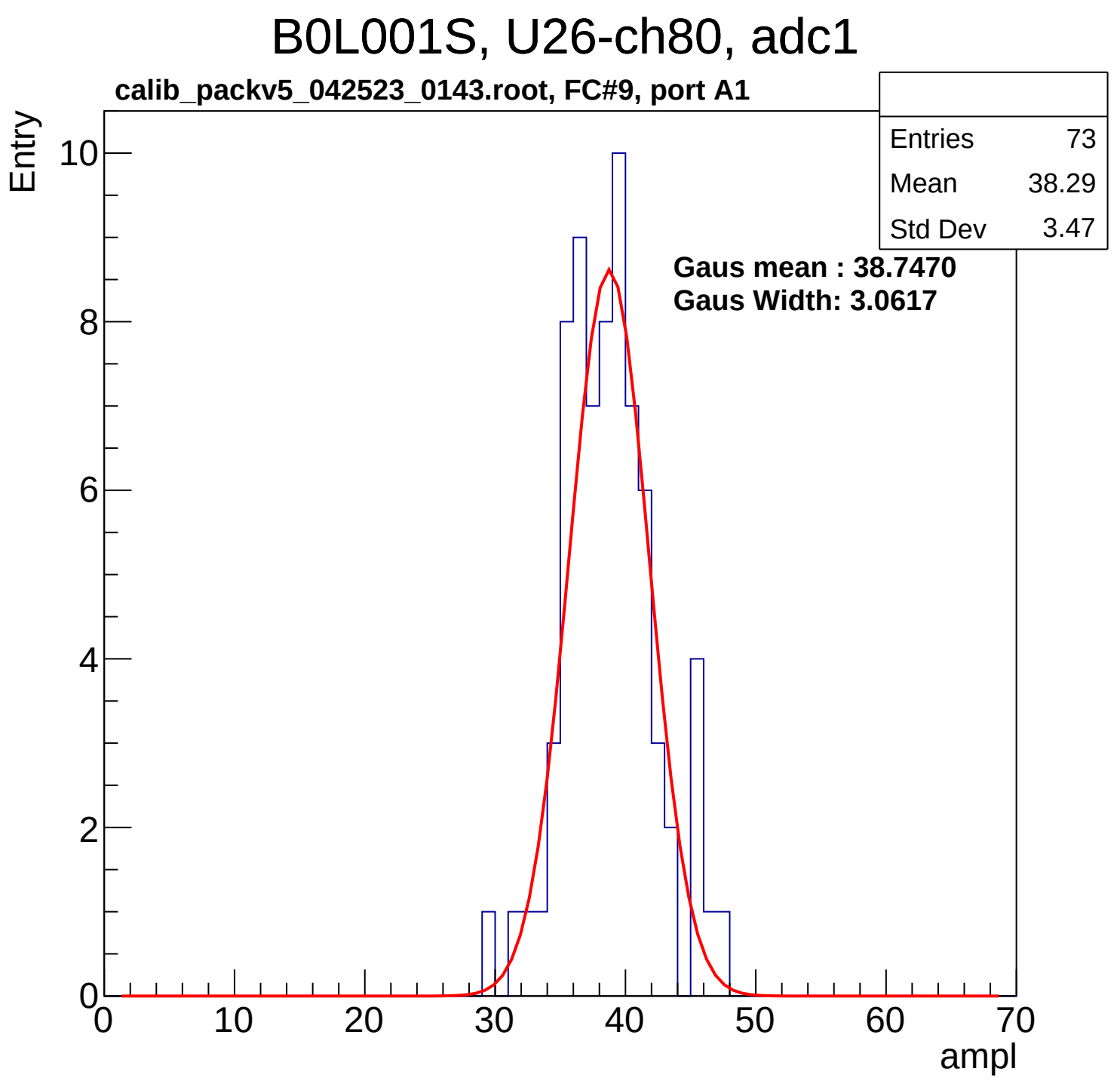
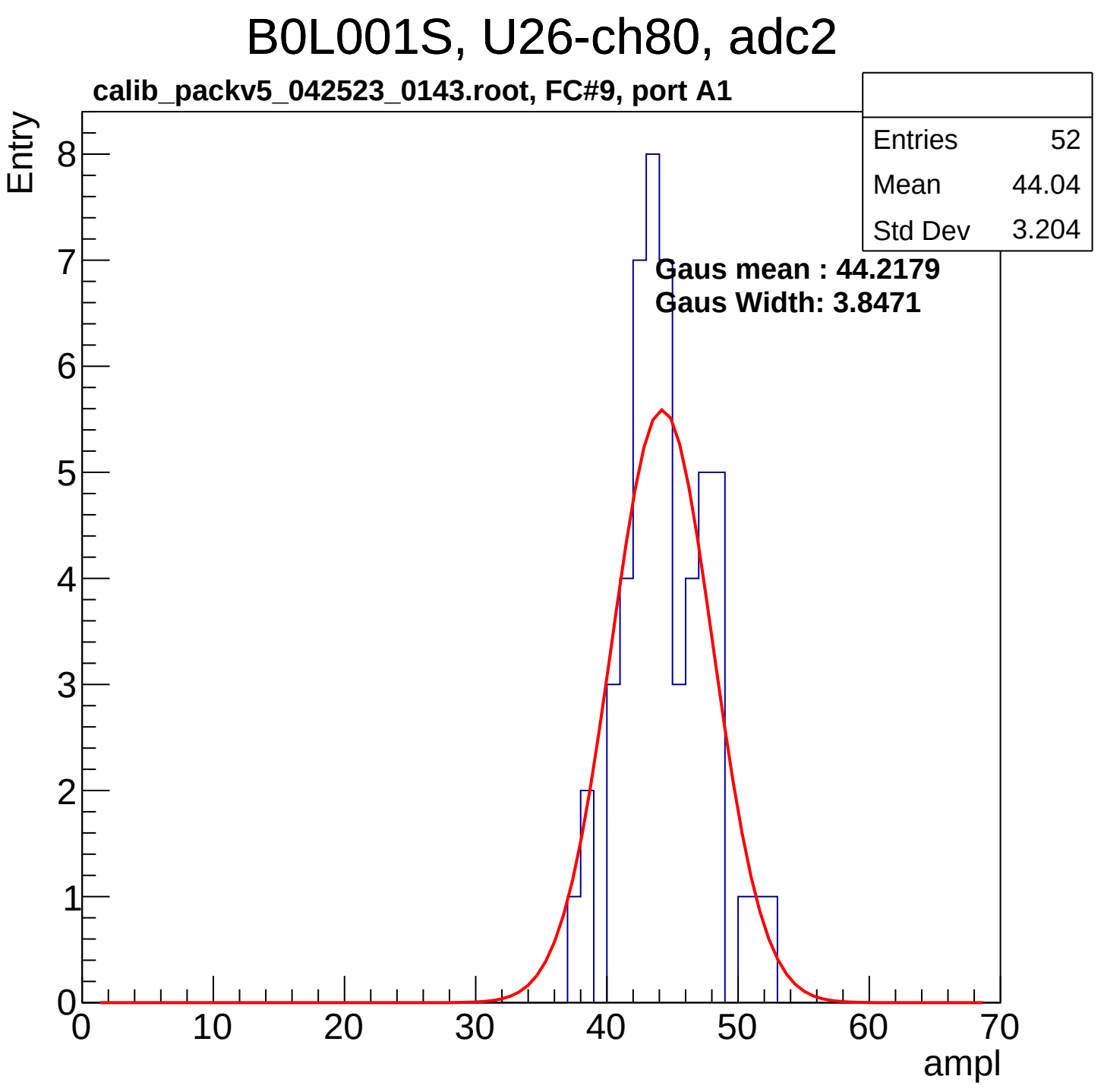
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.19
Std Dev 4.774
Gaus mean : 31.0874
Gaus Width: 3.0960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.29
Std Dev 3.47
Gaus mean : 38.7470
Gaus Width: 3.0617
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 44.04
Std Dev 3.204
Gaus mean : 44.2179
Gaus Width: 3.8471
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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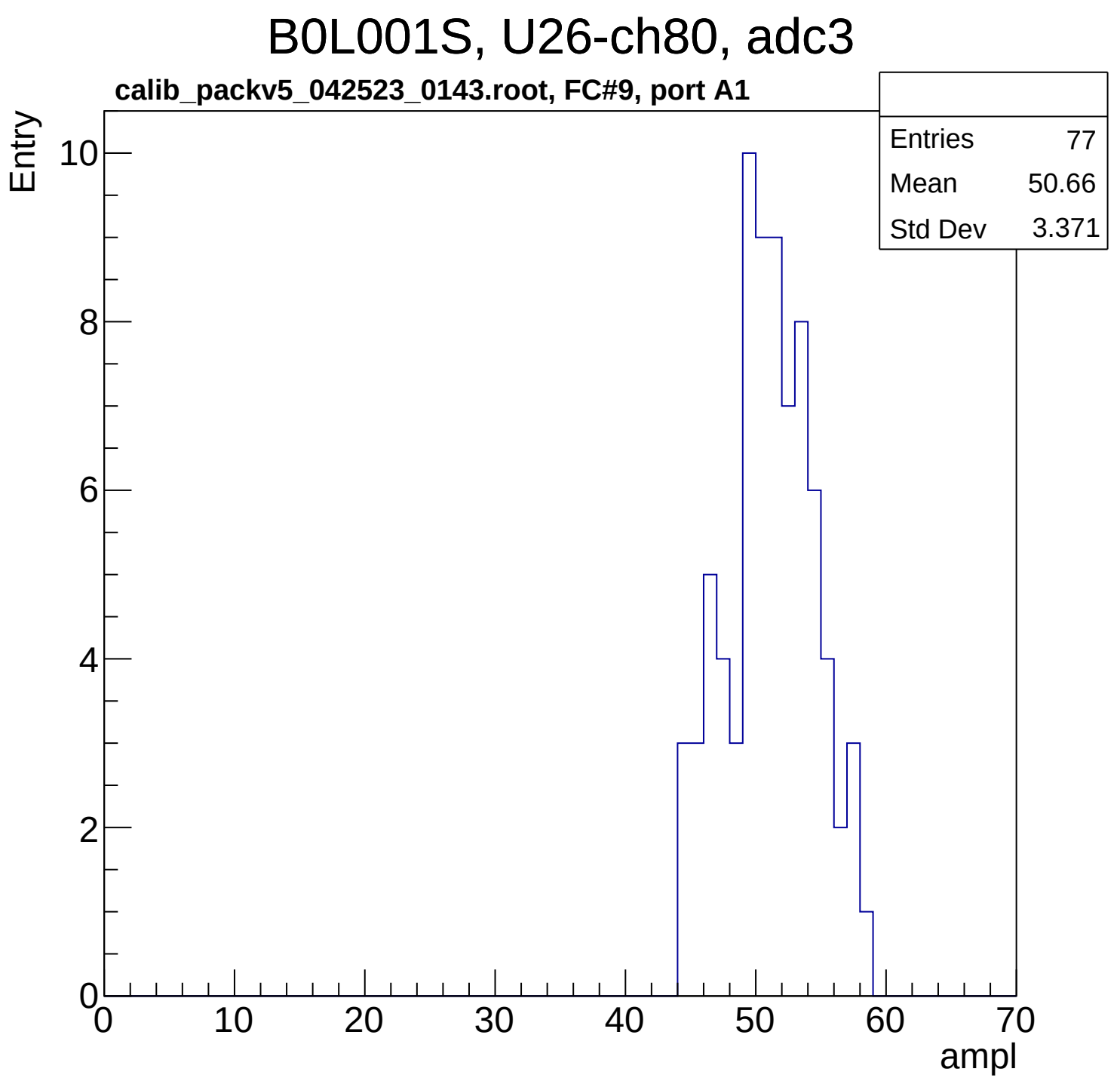
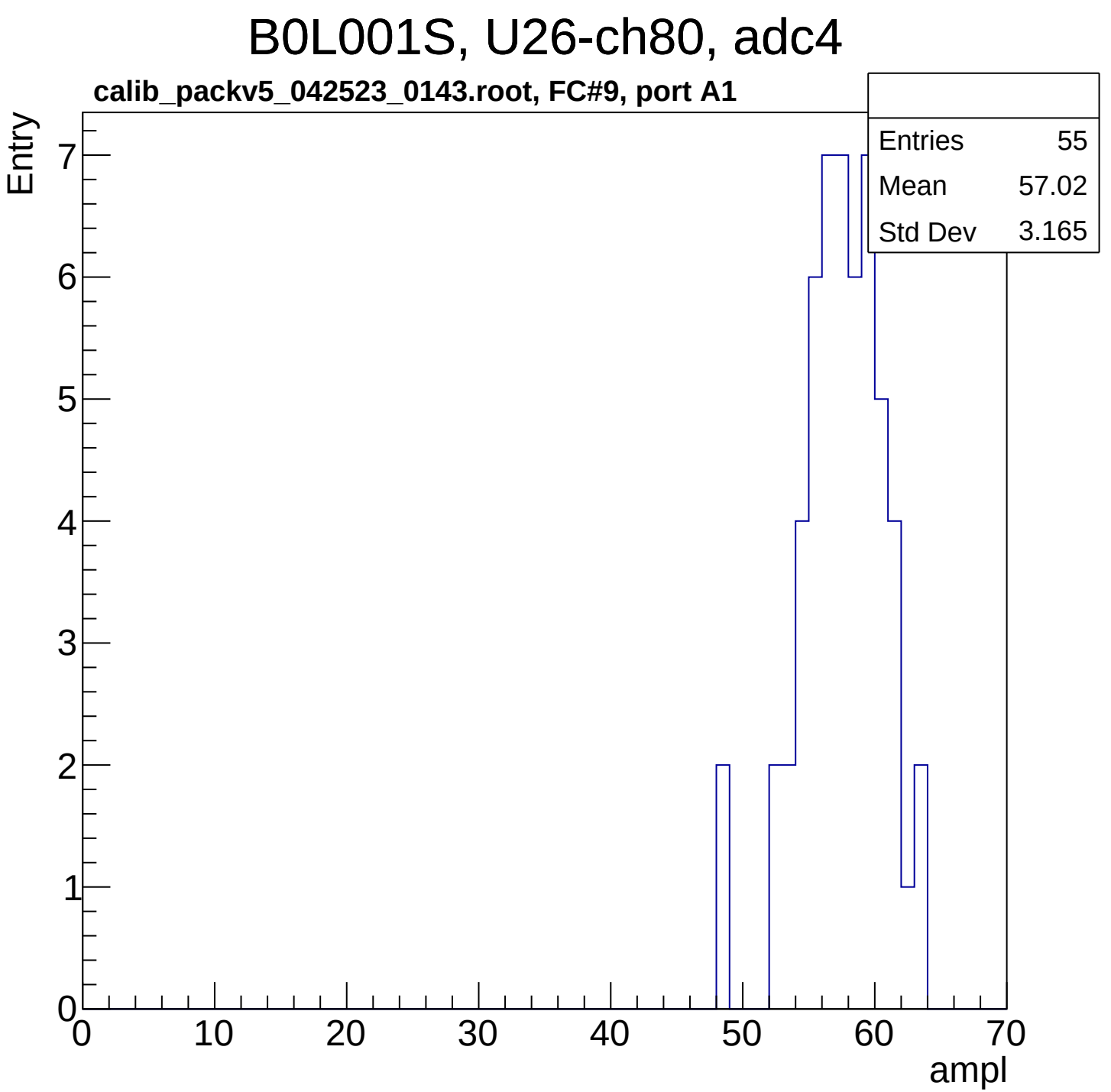

B0L001S, U26-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 50.66
Std Dev 3.371
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.02
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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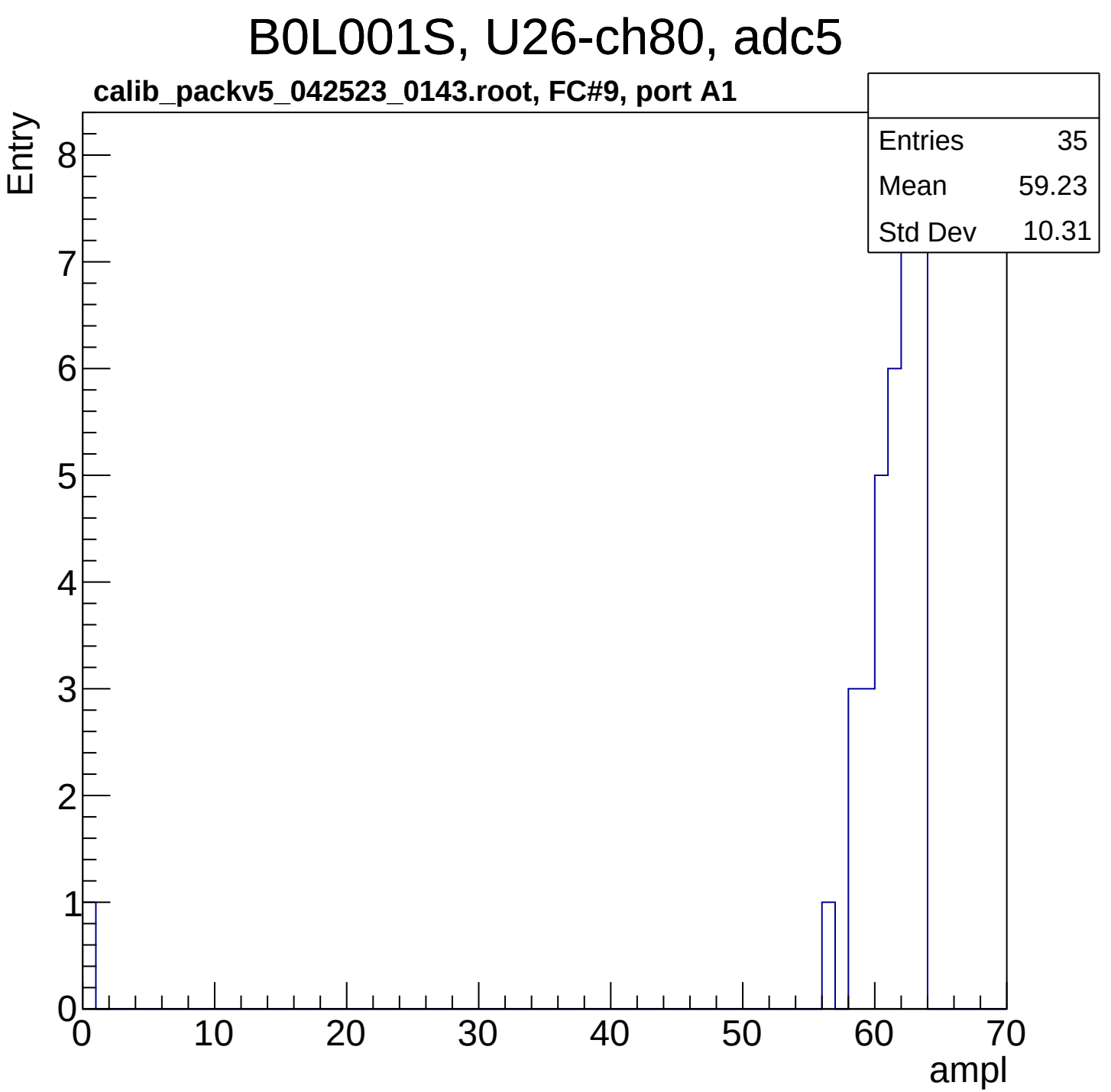
B0L001S, U26-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.23
Std Dev 10.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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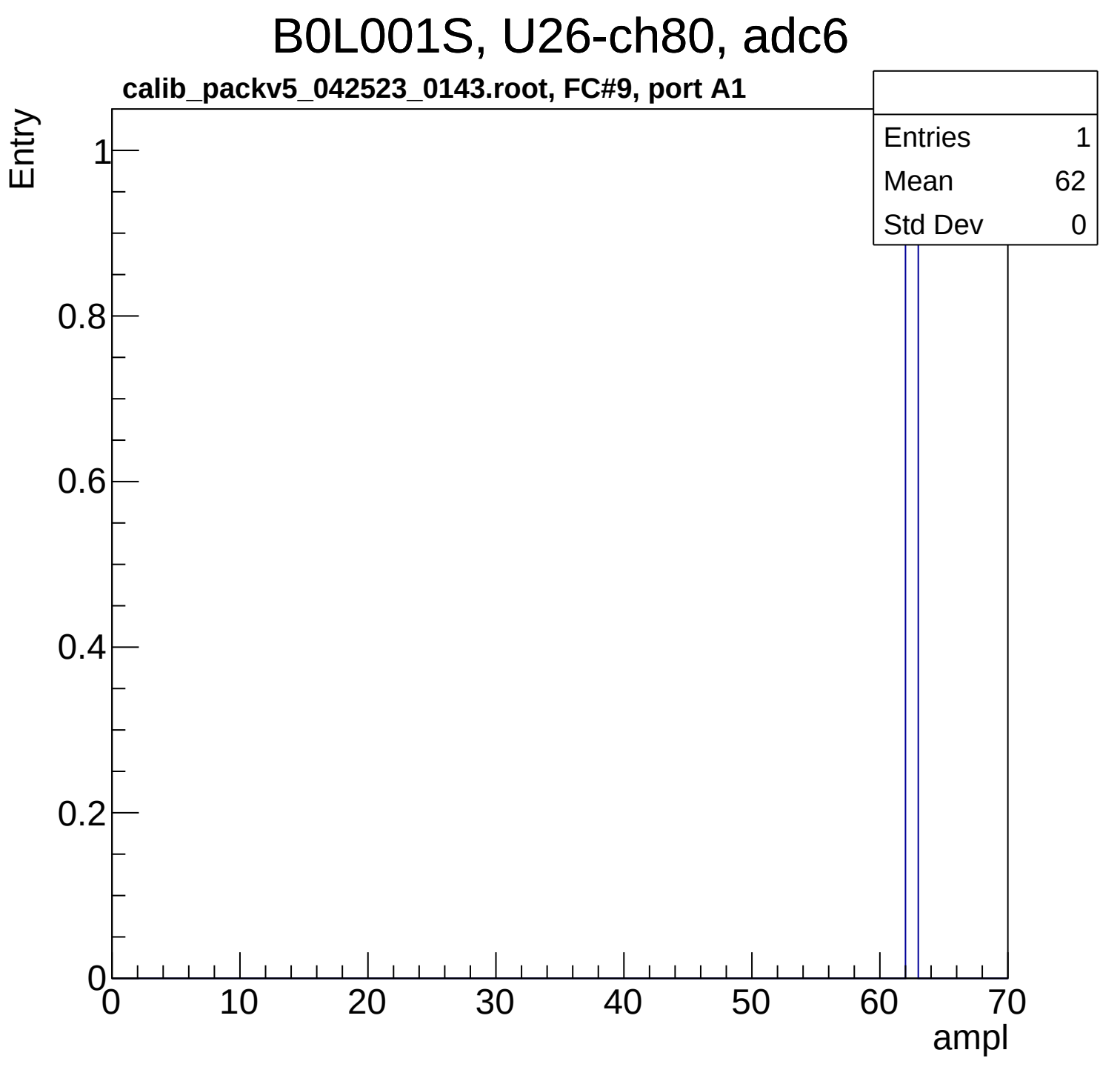
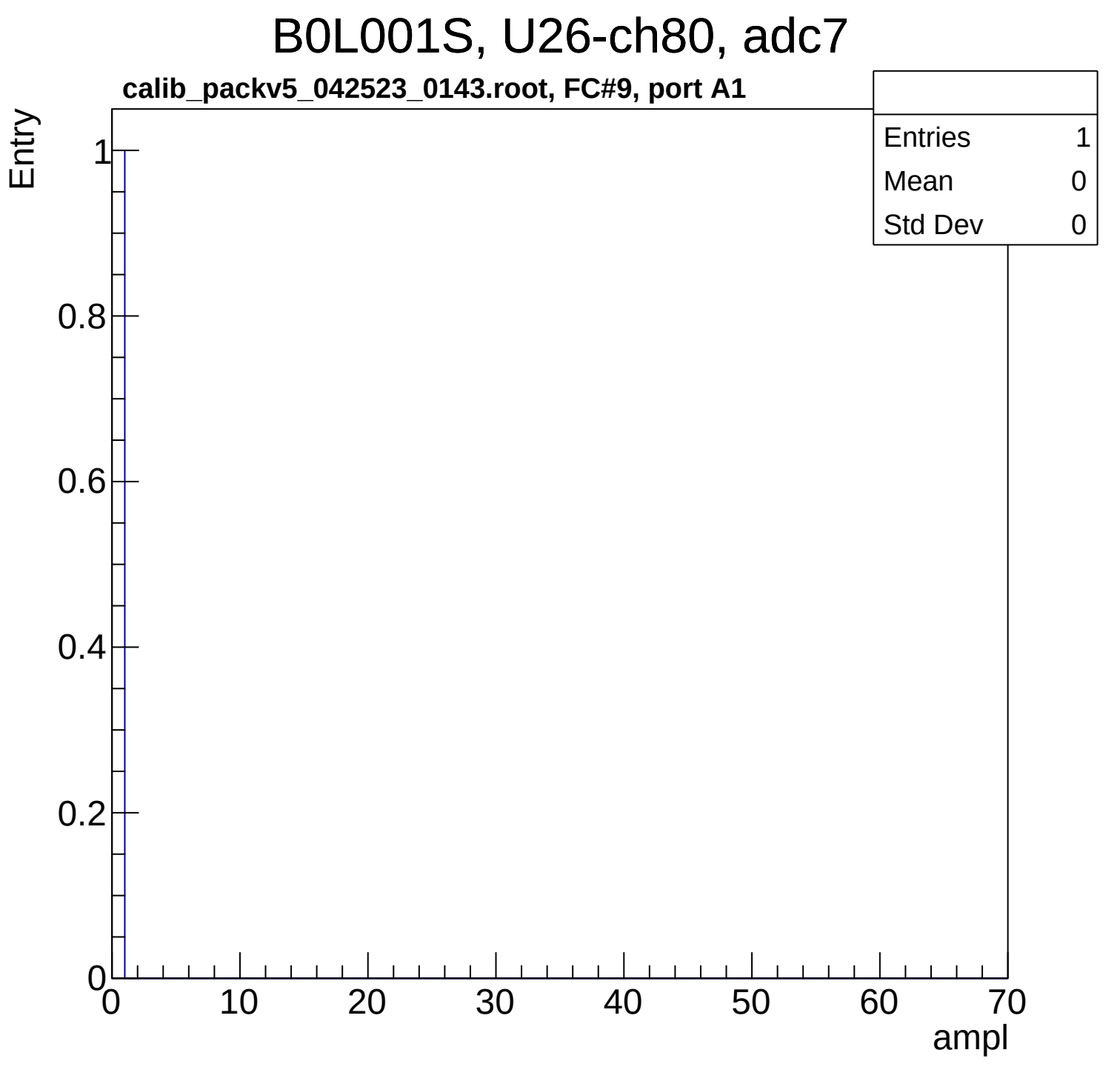

B0L001S, U26-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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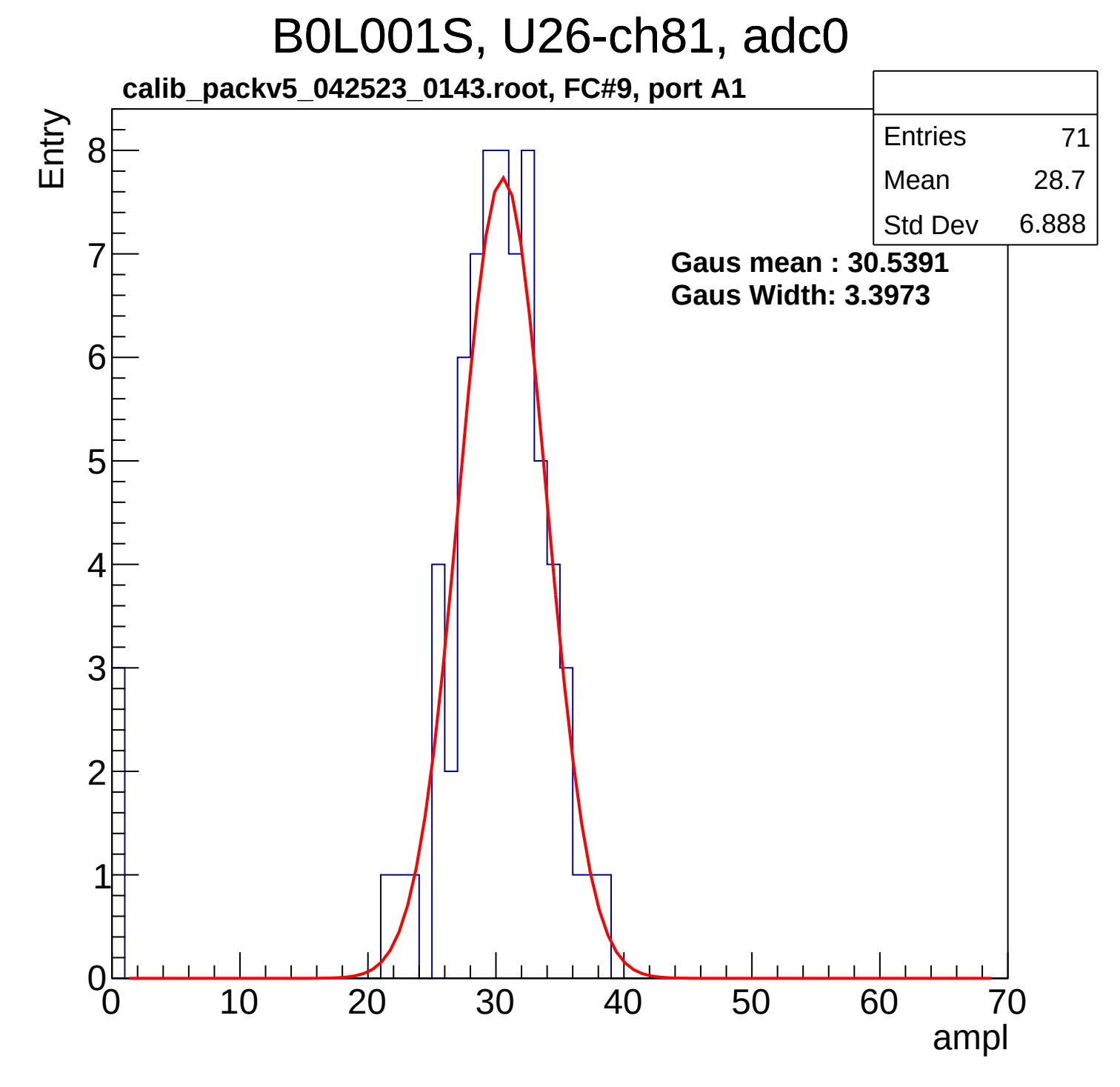
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.7
Std Dev 6.888
Gaus mean : 30.5391
Gaus Width: 3.3973
Entry
ampl
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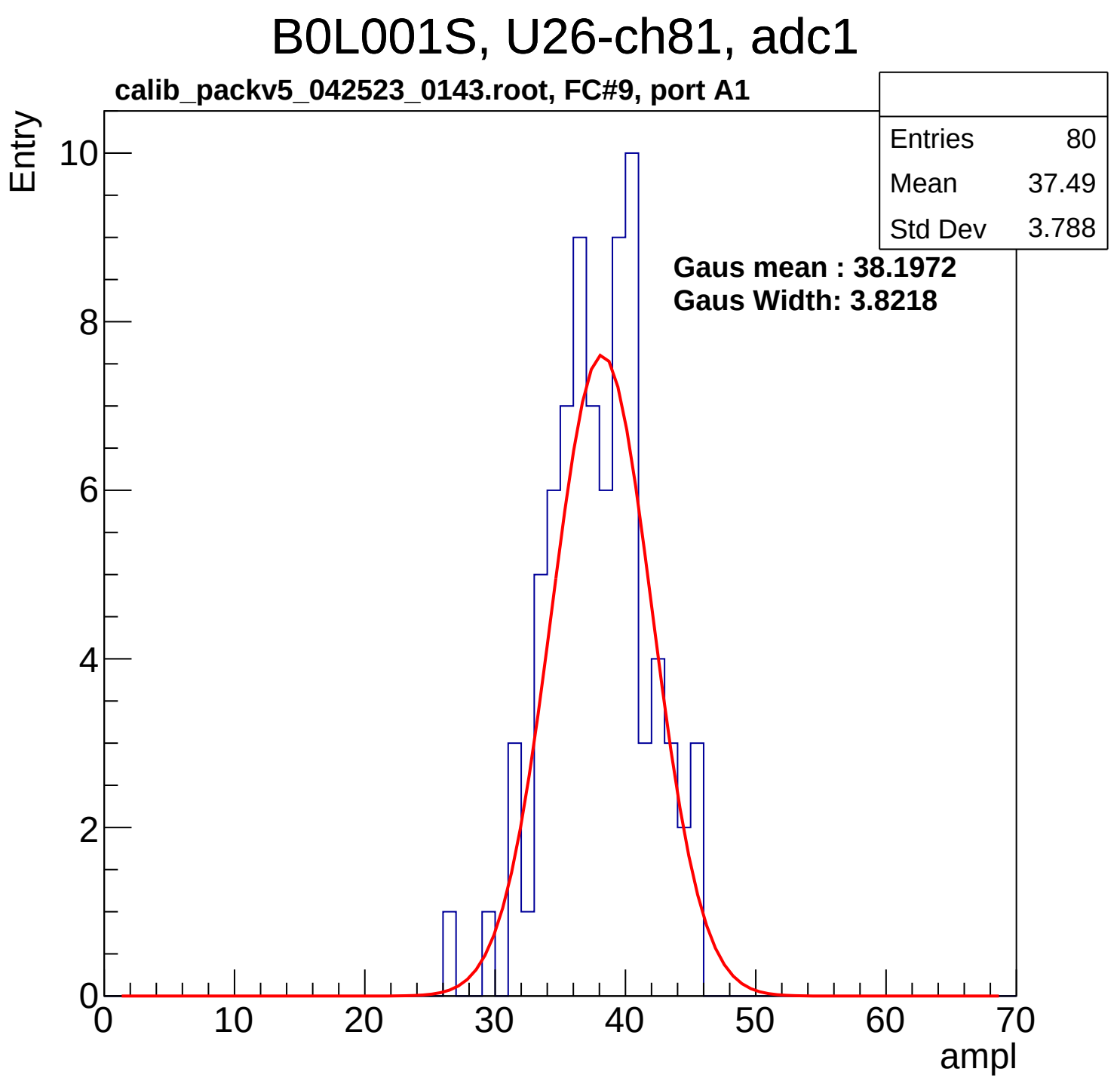
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.49
Std Dev 3.788
Gaus mean : 38.1972
Gaus Width: 3.8218
Entry
ampl
0
2
4
6
8
10
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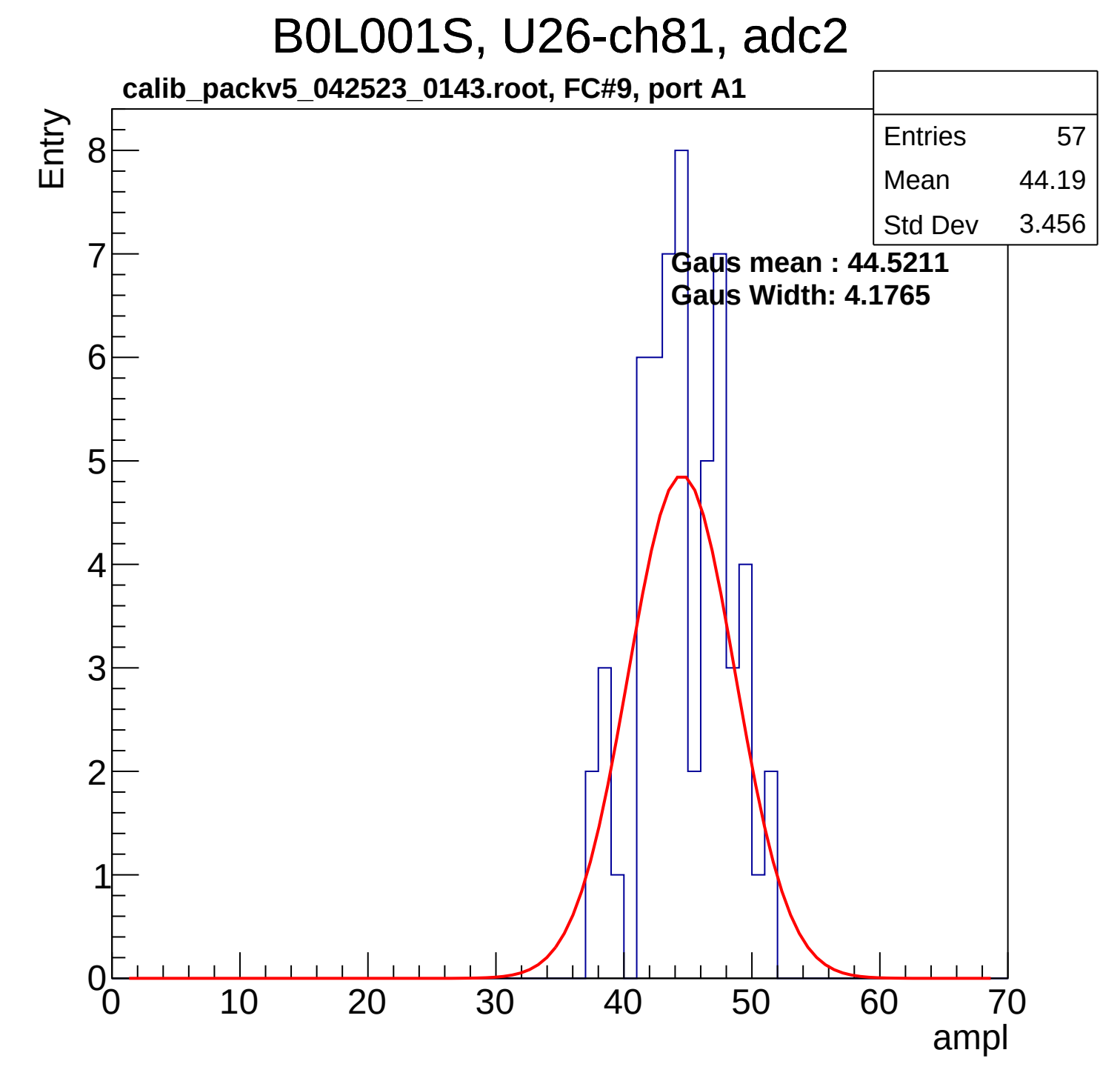
B0L001S, U26-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.19
Std Dev 3.456
Gaus mean : 44.5211
Gaus Width: 4.1765
Entry
ampl
0
1
2
3
4
5
6
7
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0
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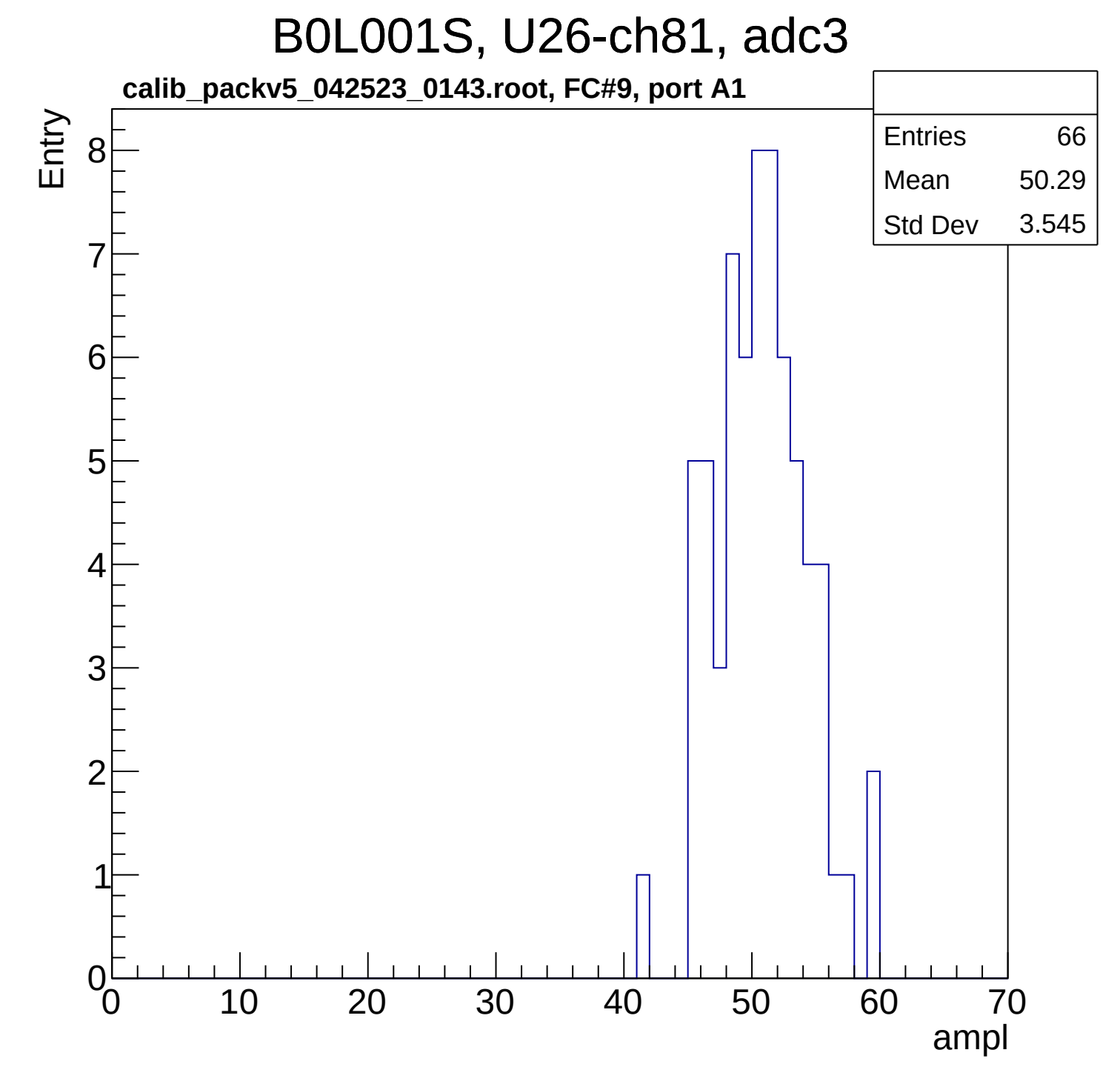
B0L001S, U26-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.29
Std Dev 3.545
Entry
ampl
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1
2
3
4
5
6
7
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0
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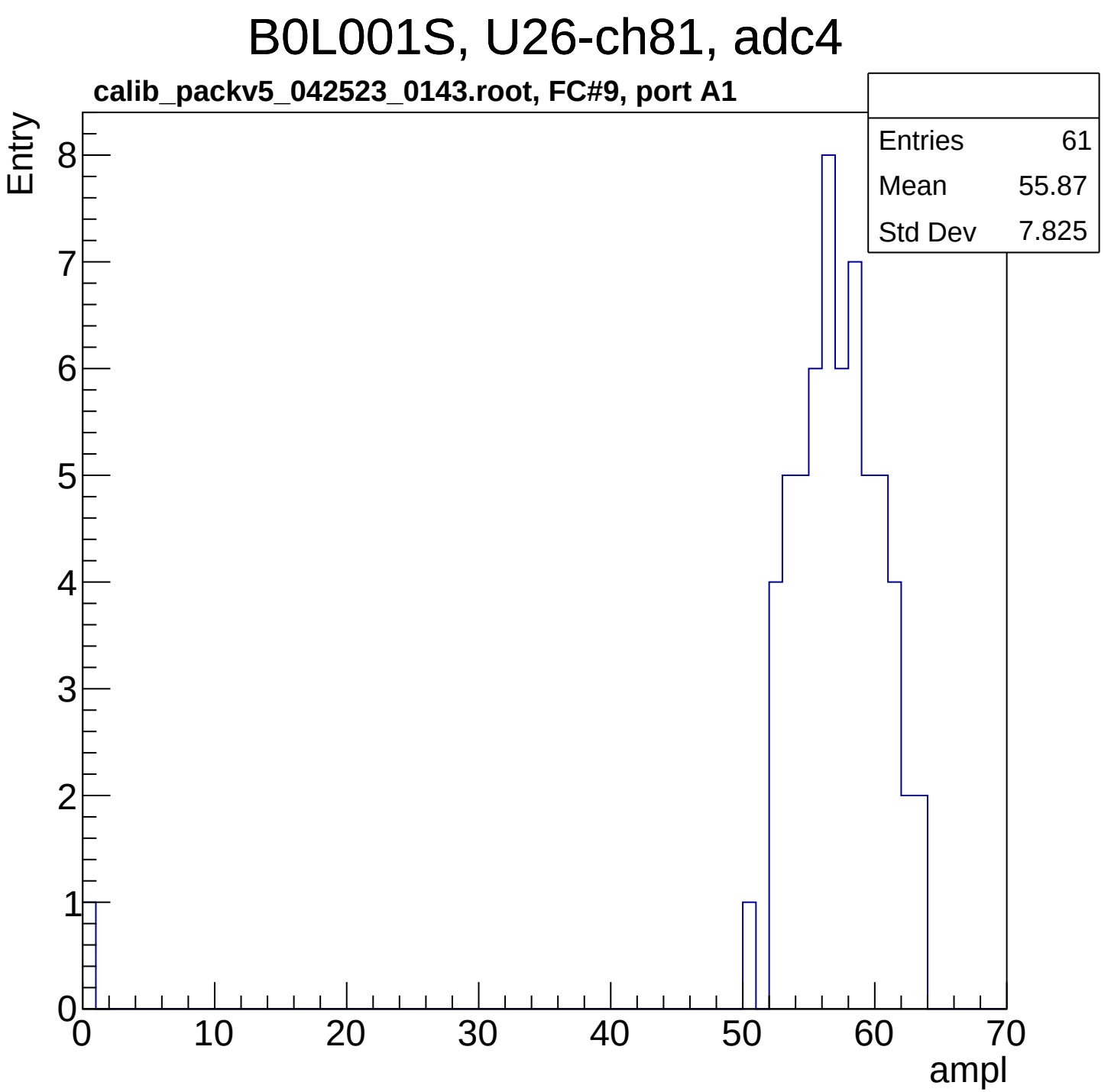
B0L001S, U26-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 55.87
Std Dev 7.825
Entry
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1
2
3
4
5
6
7
8
0
10
20
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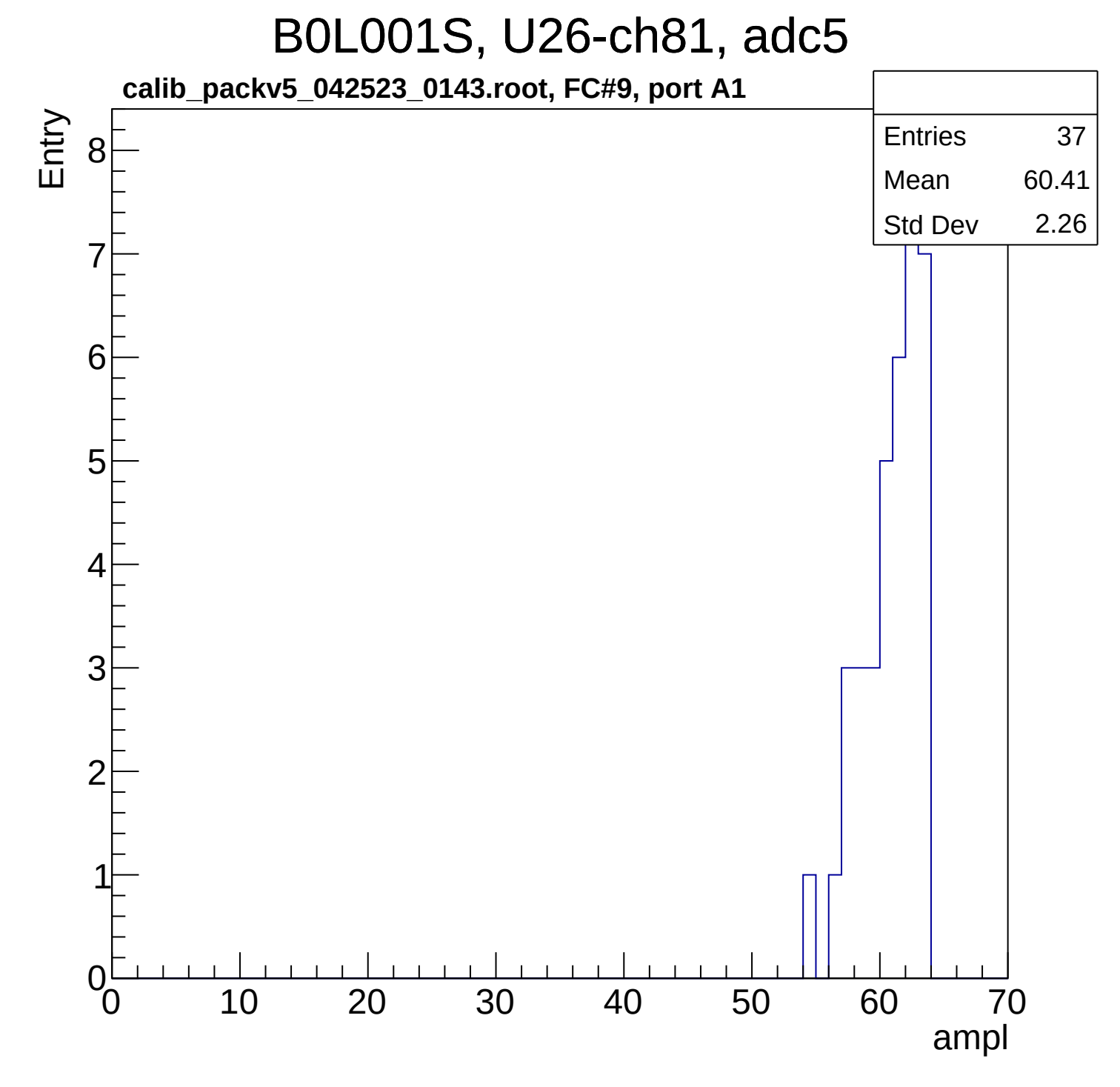
50
60
70
ampl


B0L001S, U26-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.41
Std Dev 2.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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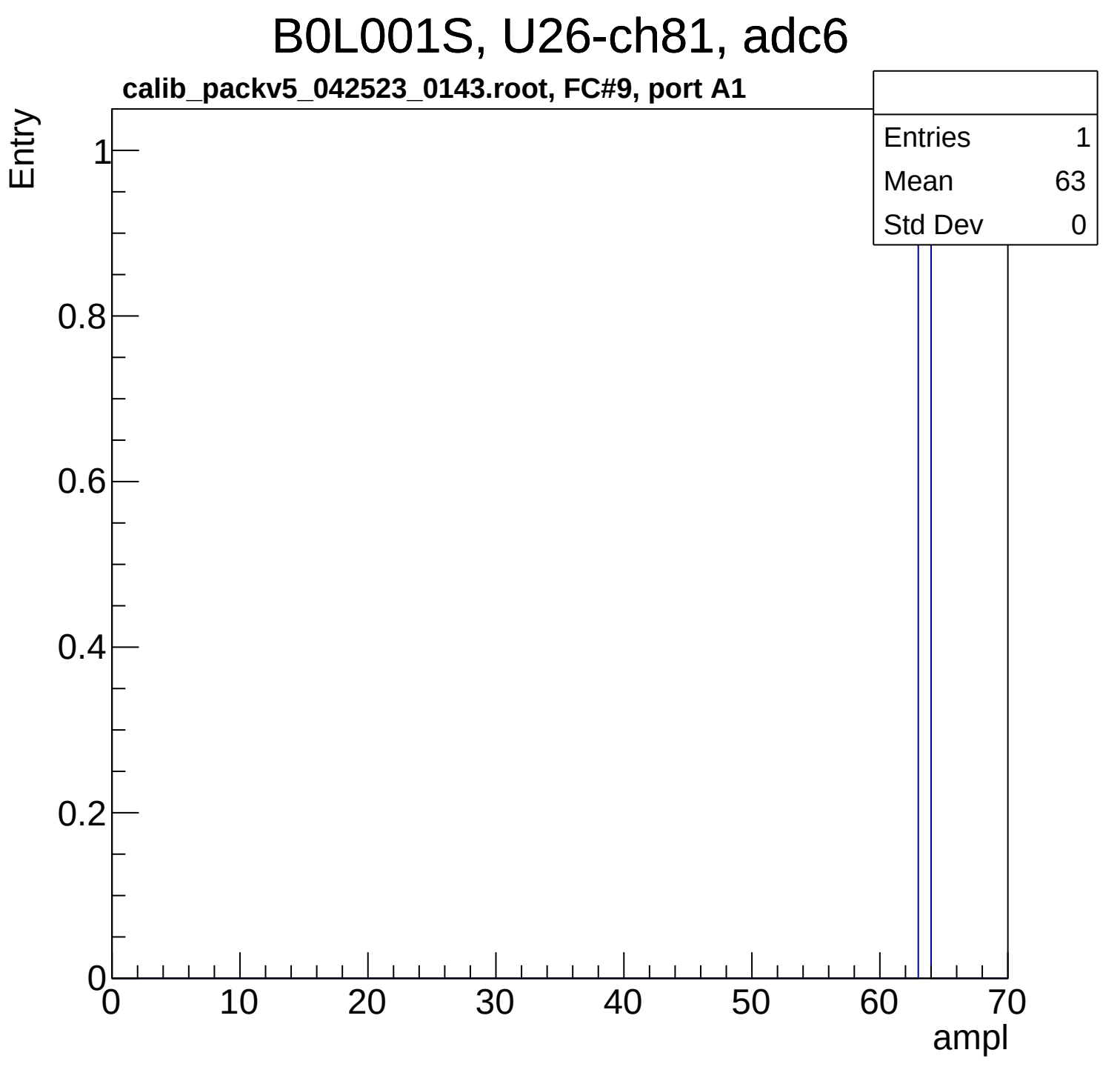
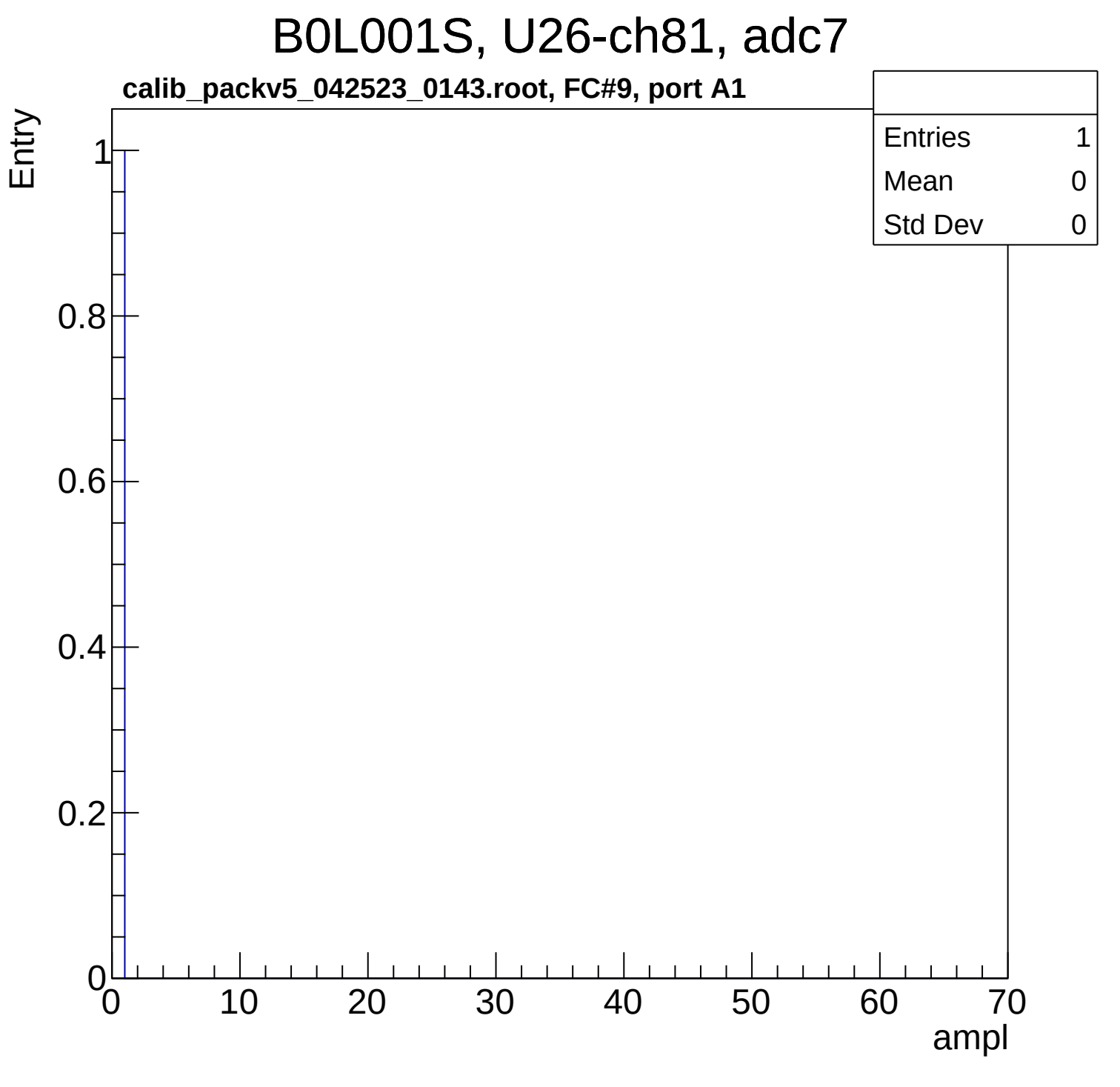
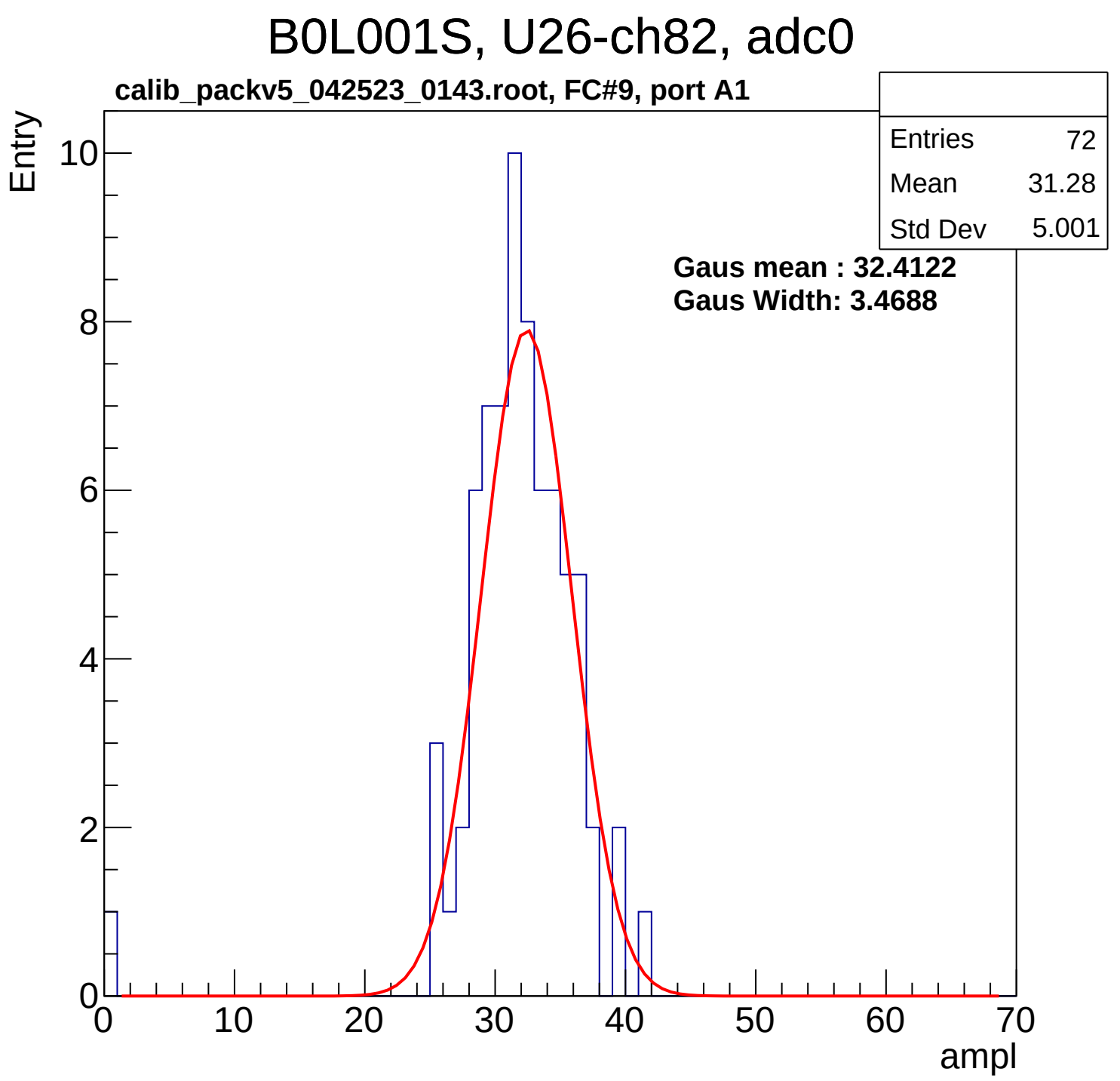
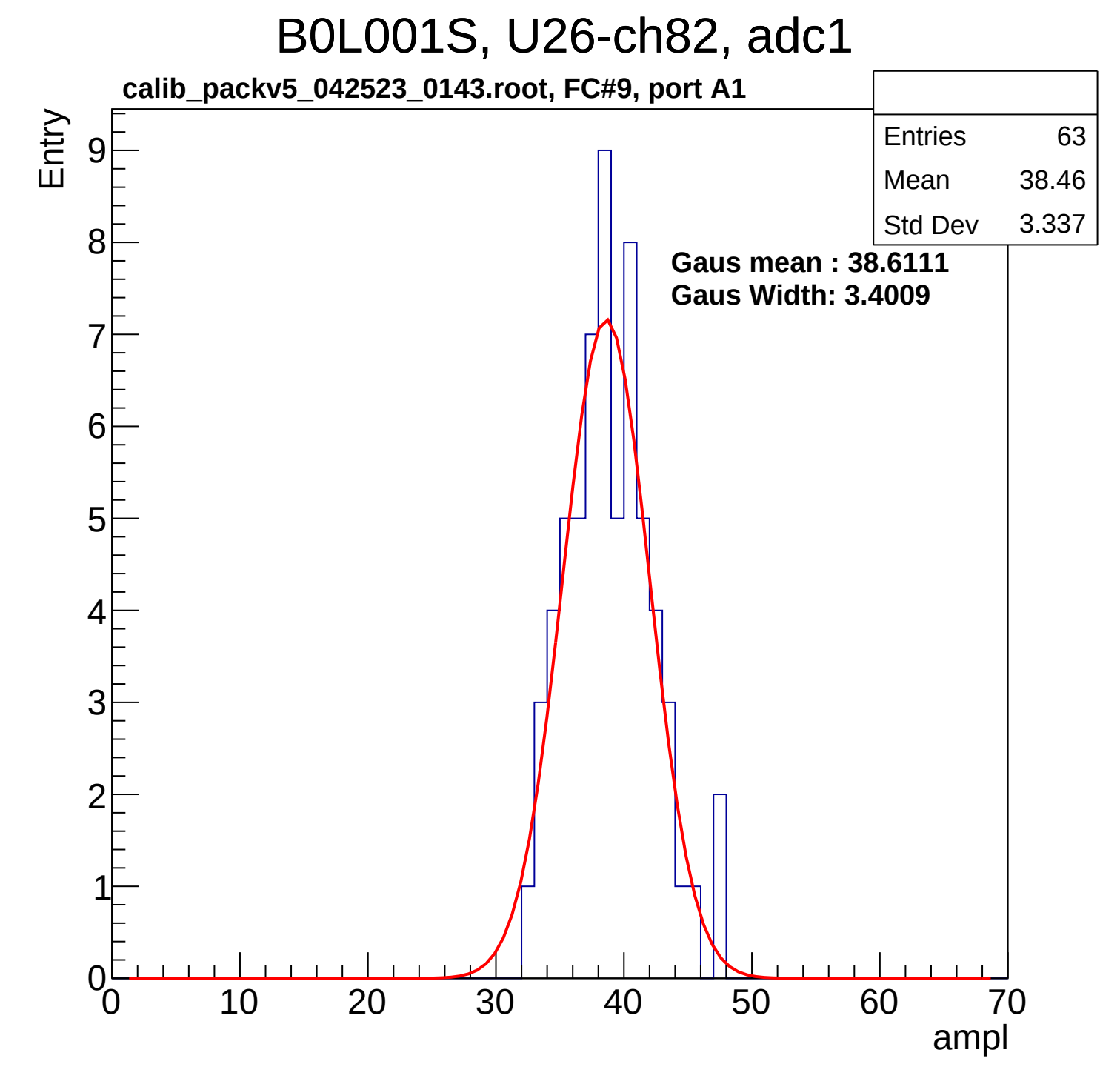

B0L001S, U26-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 31.28
Std Dev 5.001
Gaus mean : 32.4122
Gaus Width: 3.4688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.46
Std Dev 3.337
Gaus mean : 38.6111
Gaus Width: 3.4009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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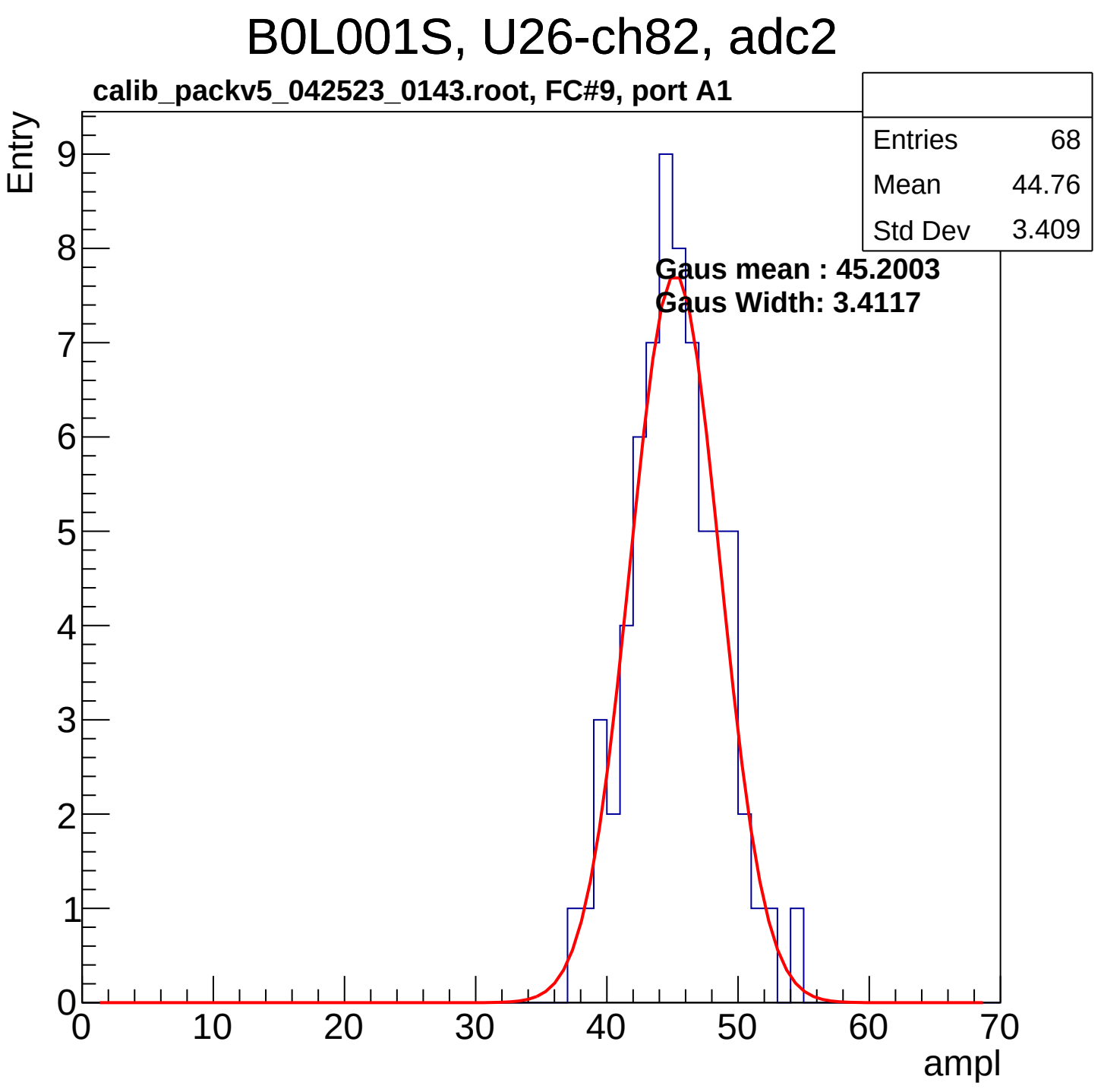
B0L001S, U26-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.76
Std Dev 3.409
Gaus mean : 45.2003
Gaus Width: 3.4117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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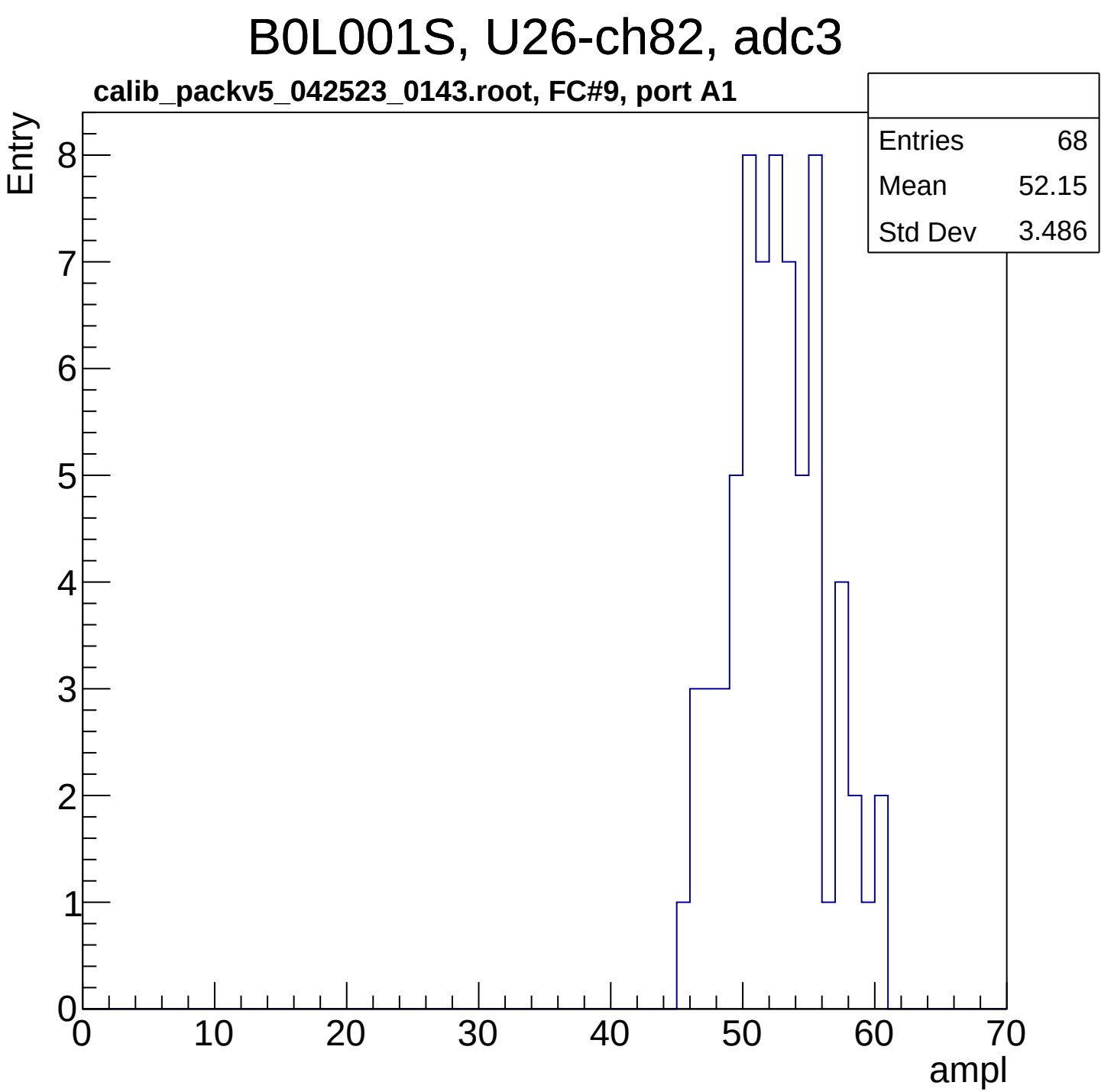
B0L001S, U26-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.15
Std Dev 3.486
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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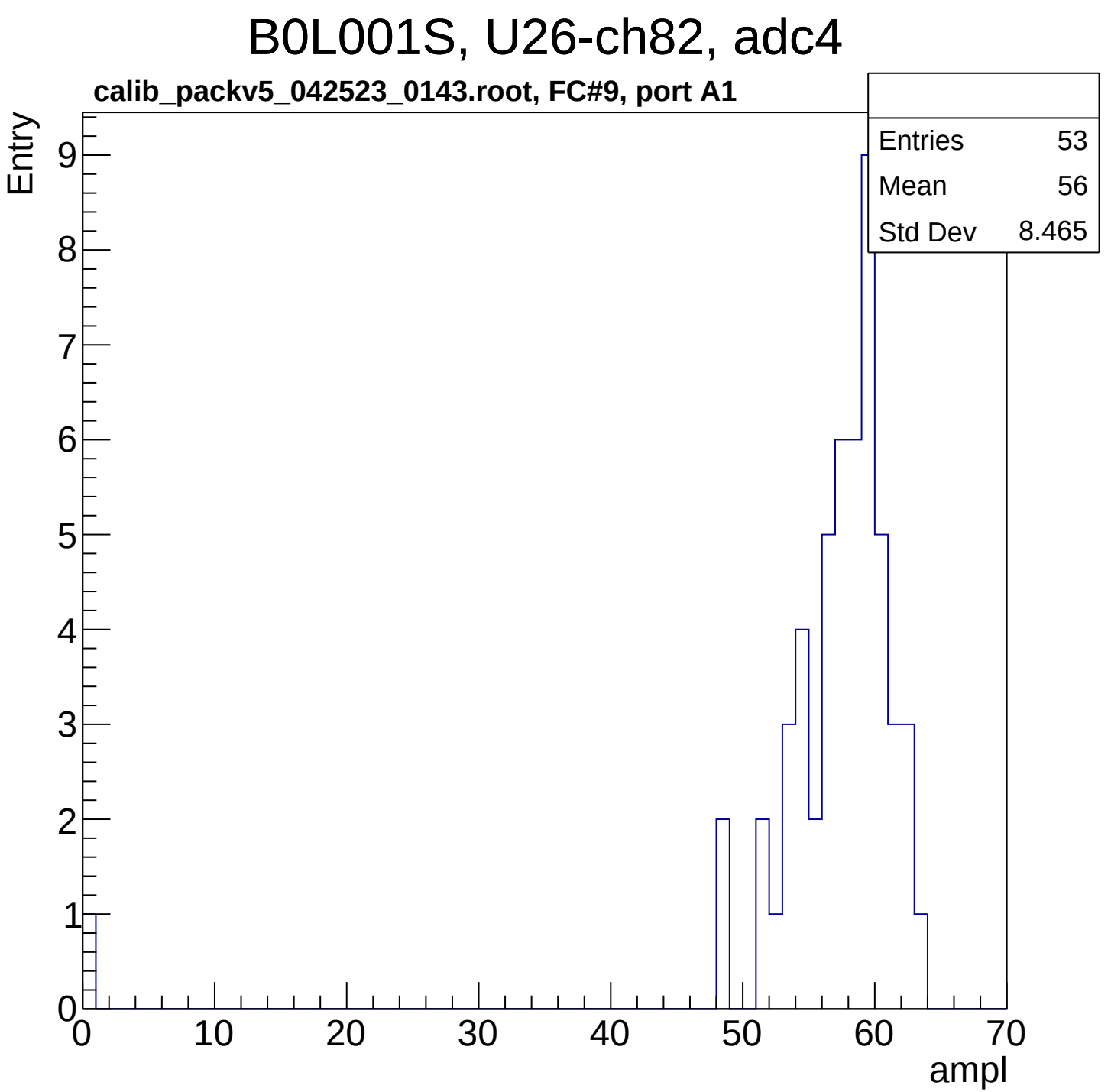
B0L001S, U26-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56
Std Dev 8.465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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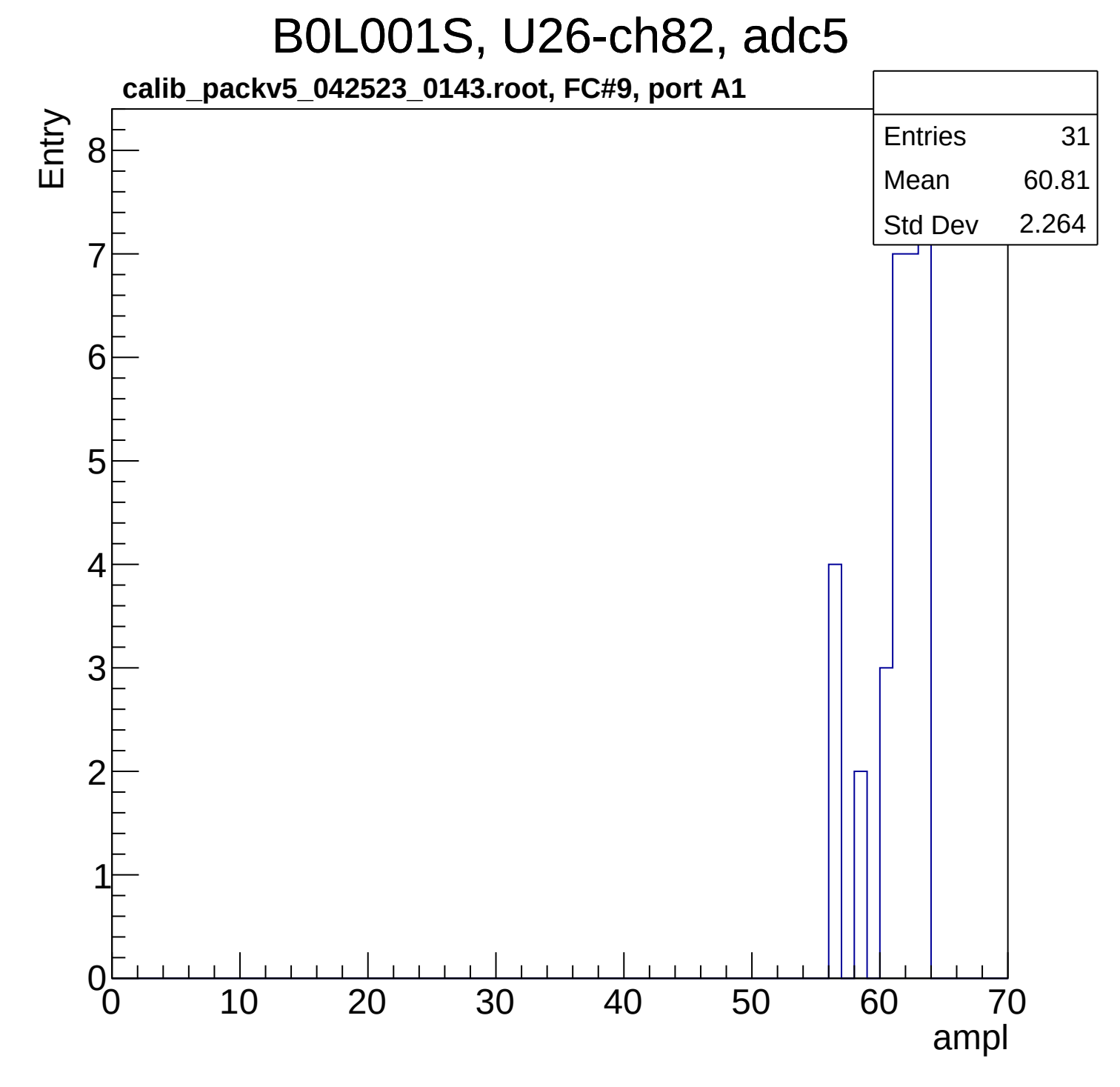
B0L001S, U26-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.81
Std Dev 2.264
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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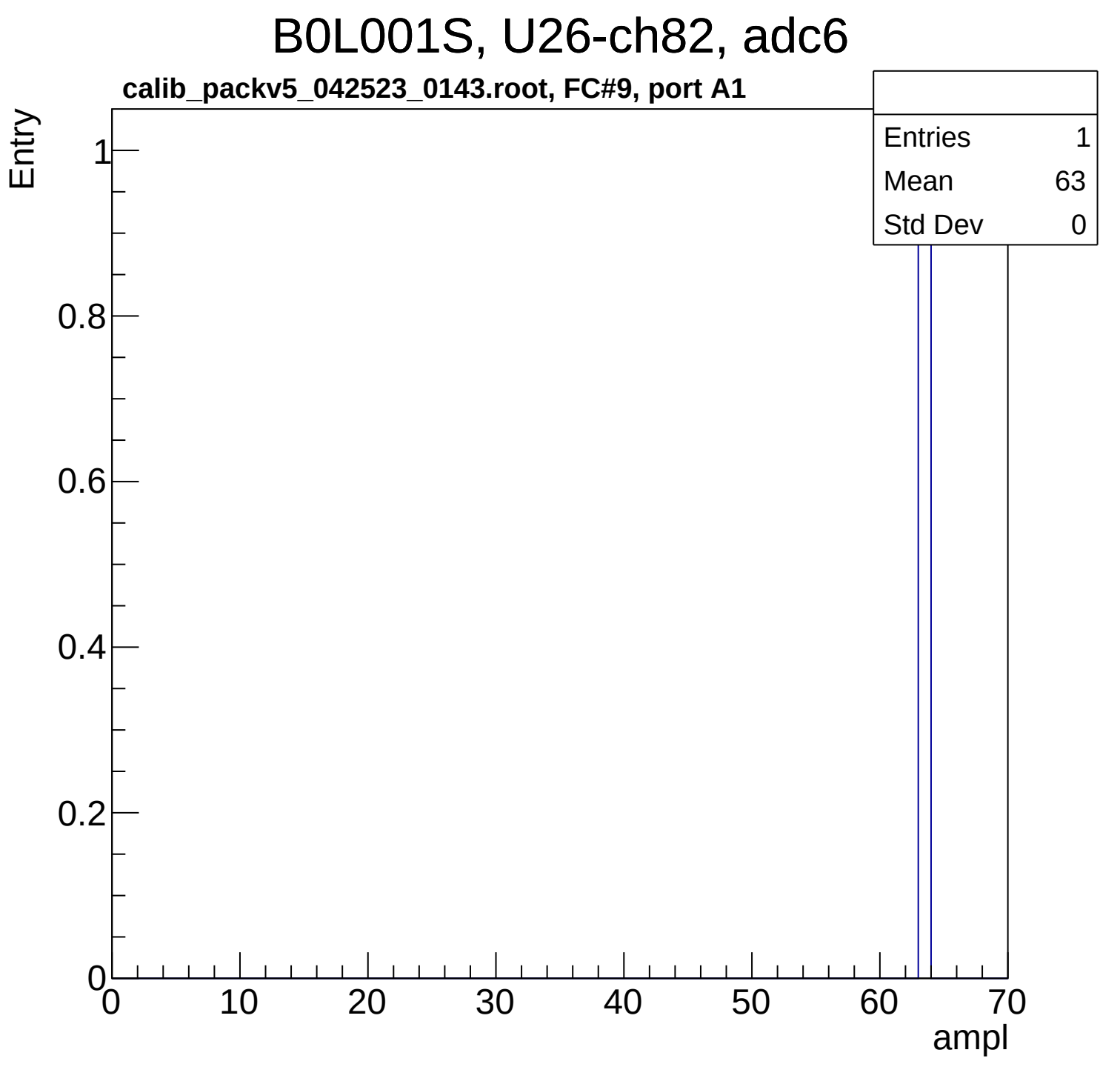
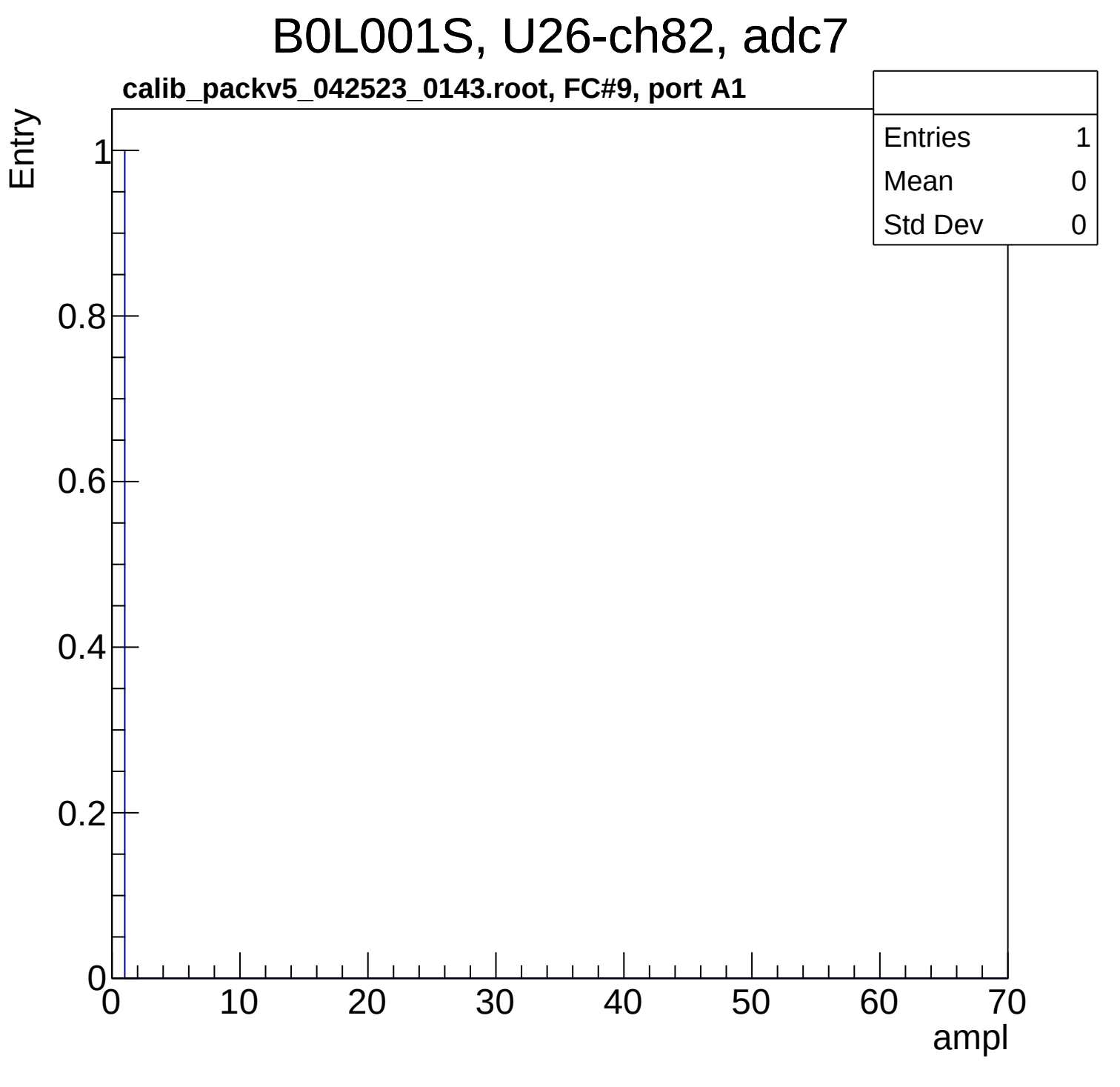
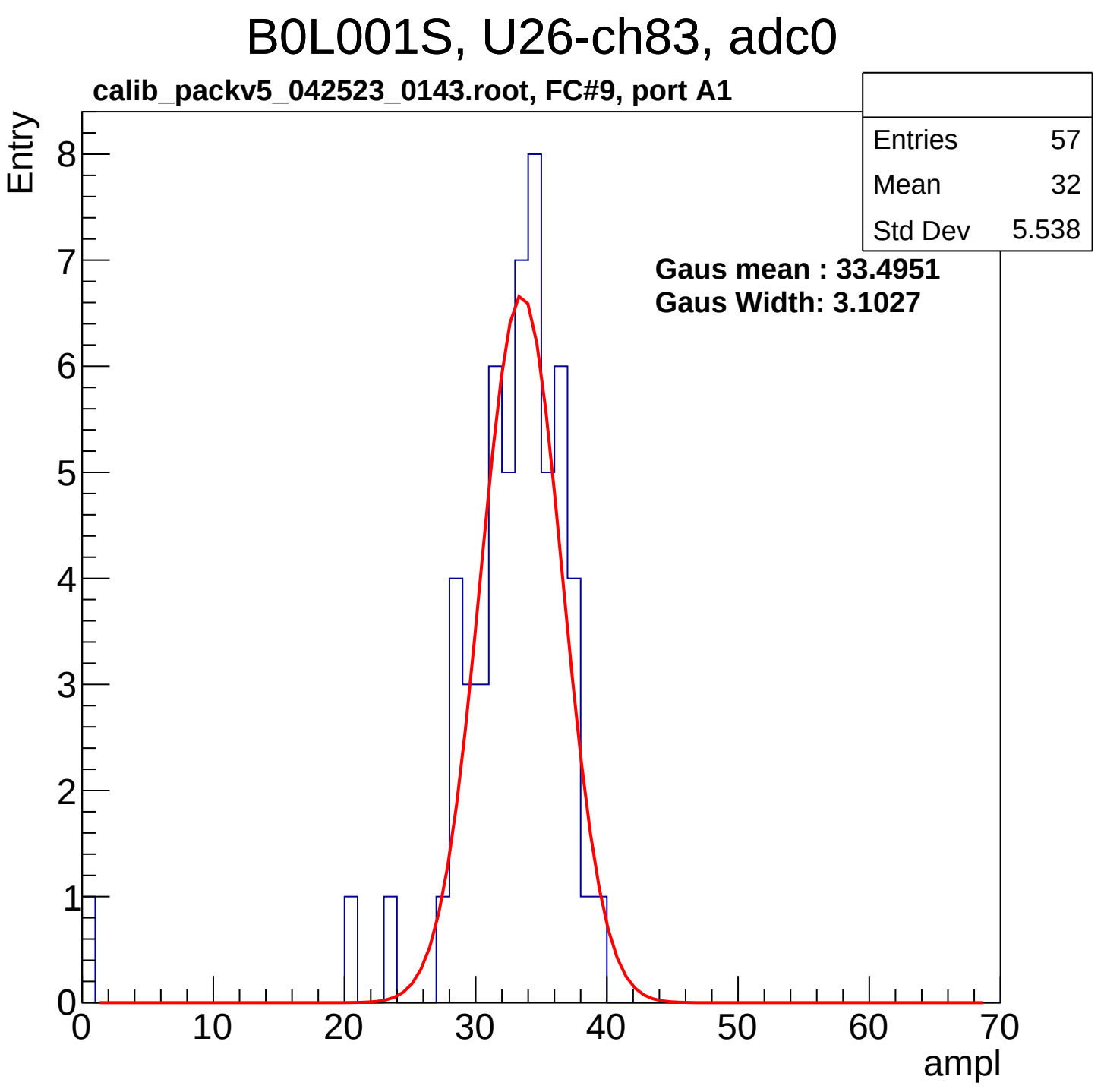

B0L001S, U26-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 32
Std Dev 5.538
Gaus mean : 33.4951
Gaus Width: 3.1027
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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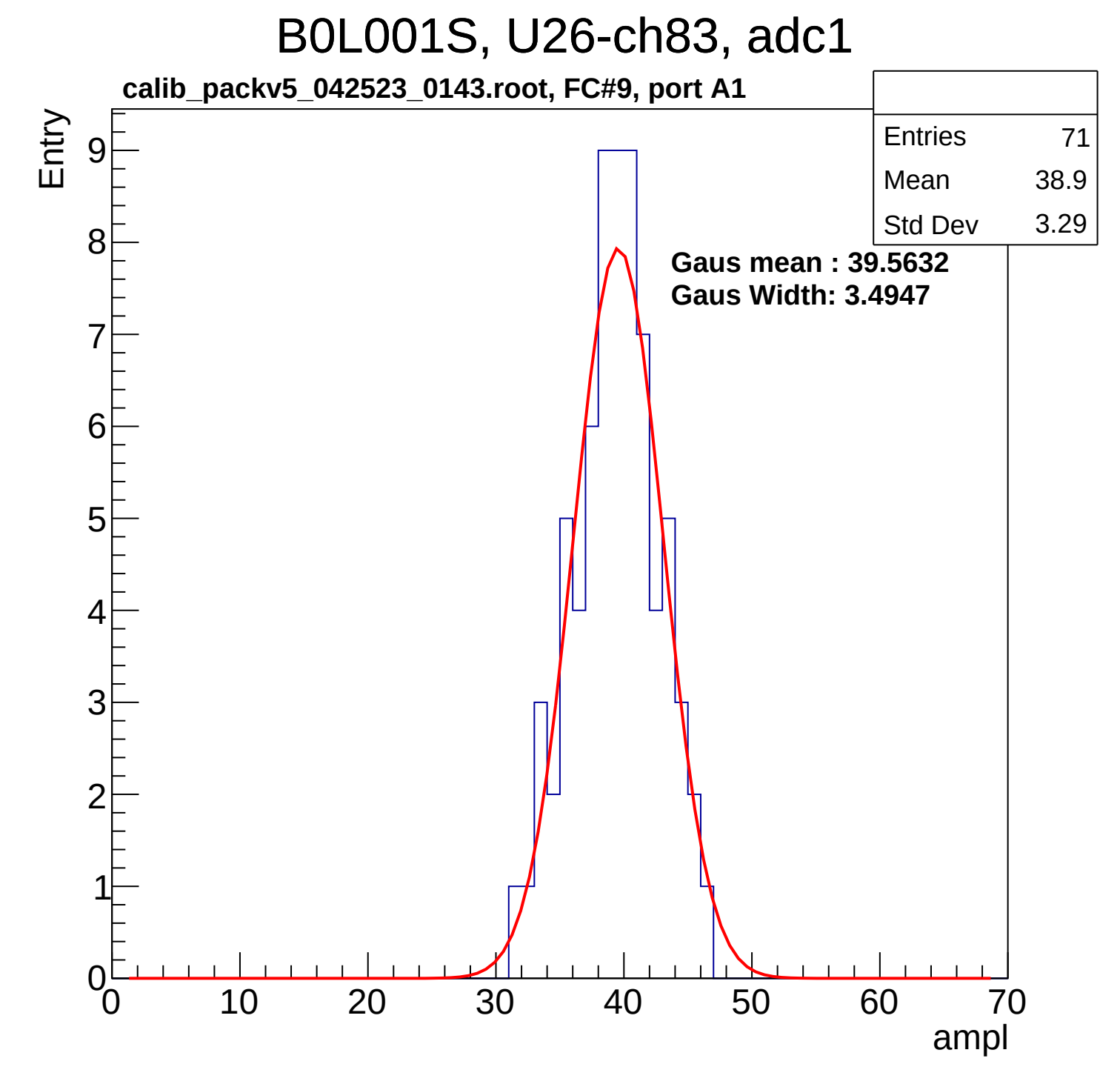
40
50
60
70


B0L001S, U26-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.9
Std Dev 3.29
Gaus mean : 39.5632
Gaus Width: 3.4947
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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40
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60
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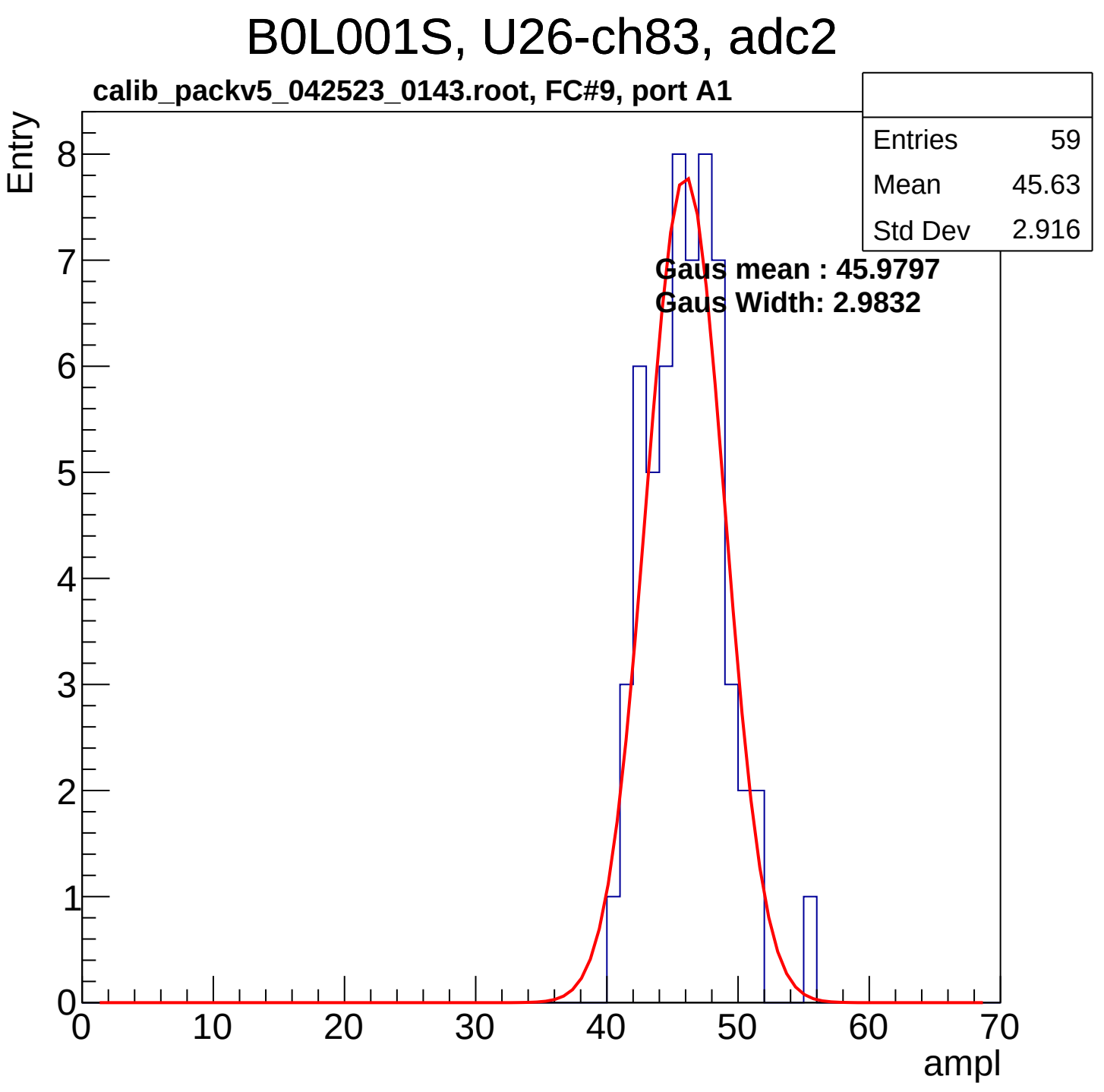
B0L001S, U26-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.63
Std Dev 2.916
Gaus mean : 45.9797
Gaus Width: 2.9832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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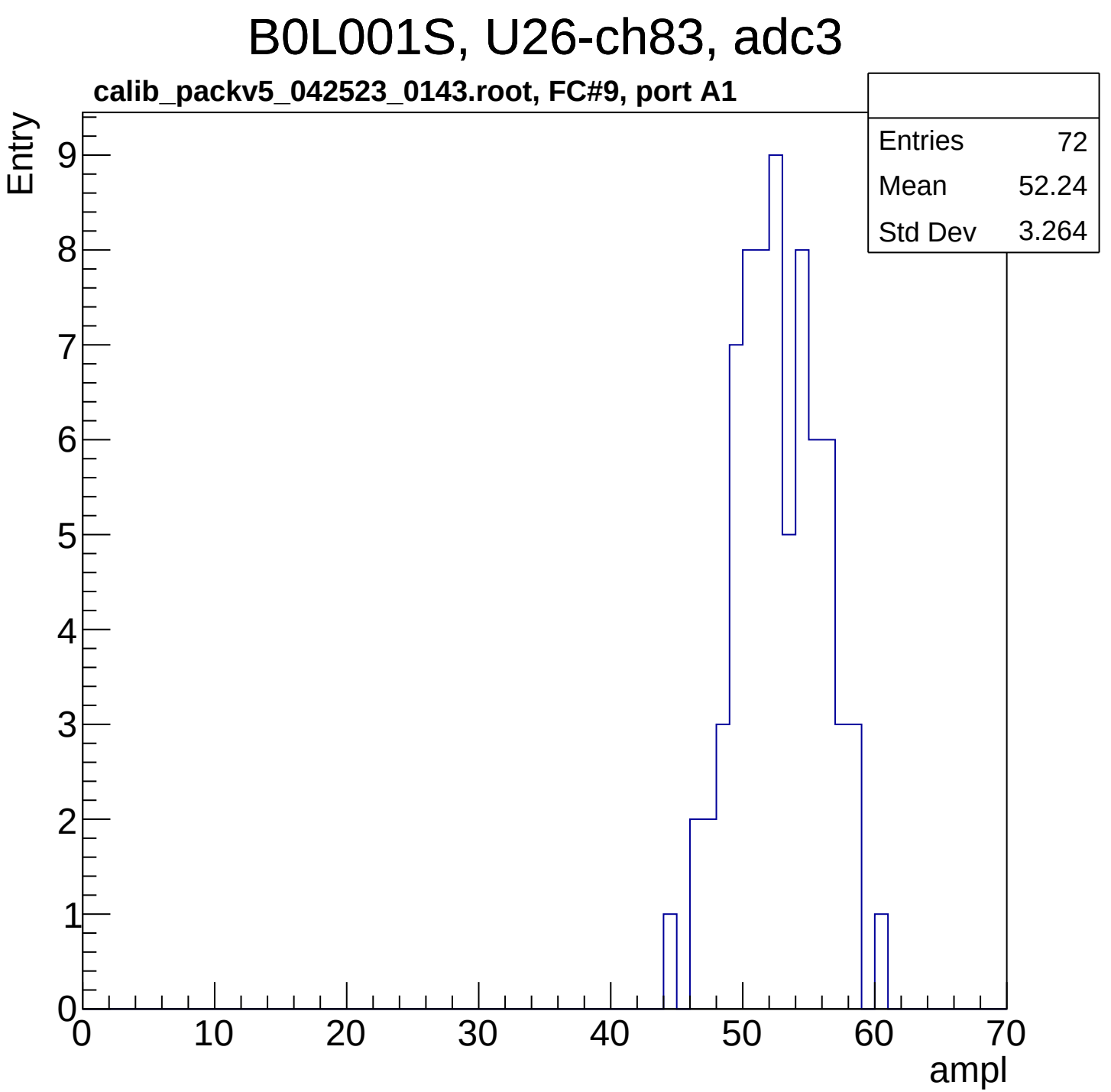
B0L001S, U26-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.24
Std Dev 3.264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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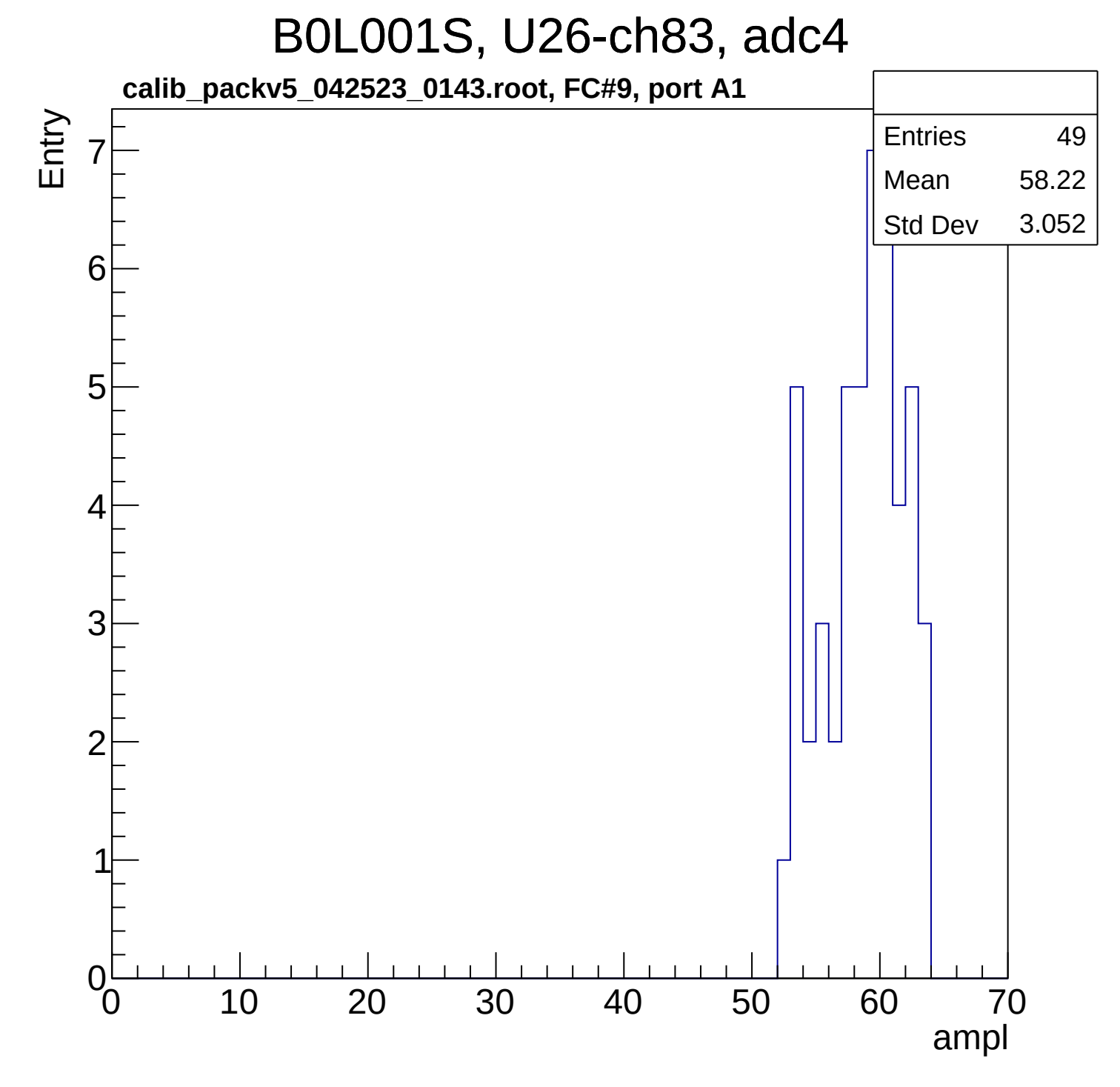
B0L001S, U26-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.22
Std Dev 3.052
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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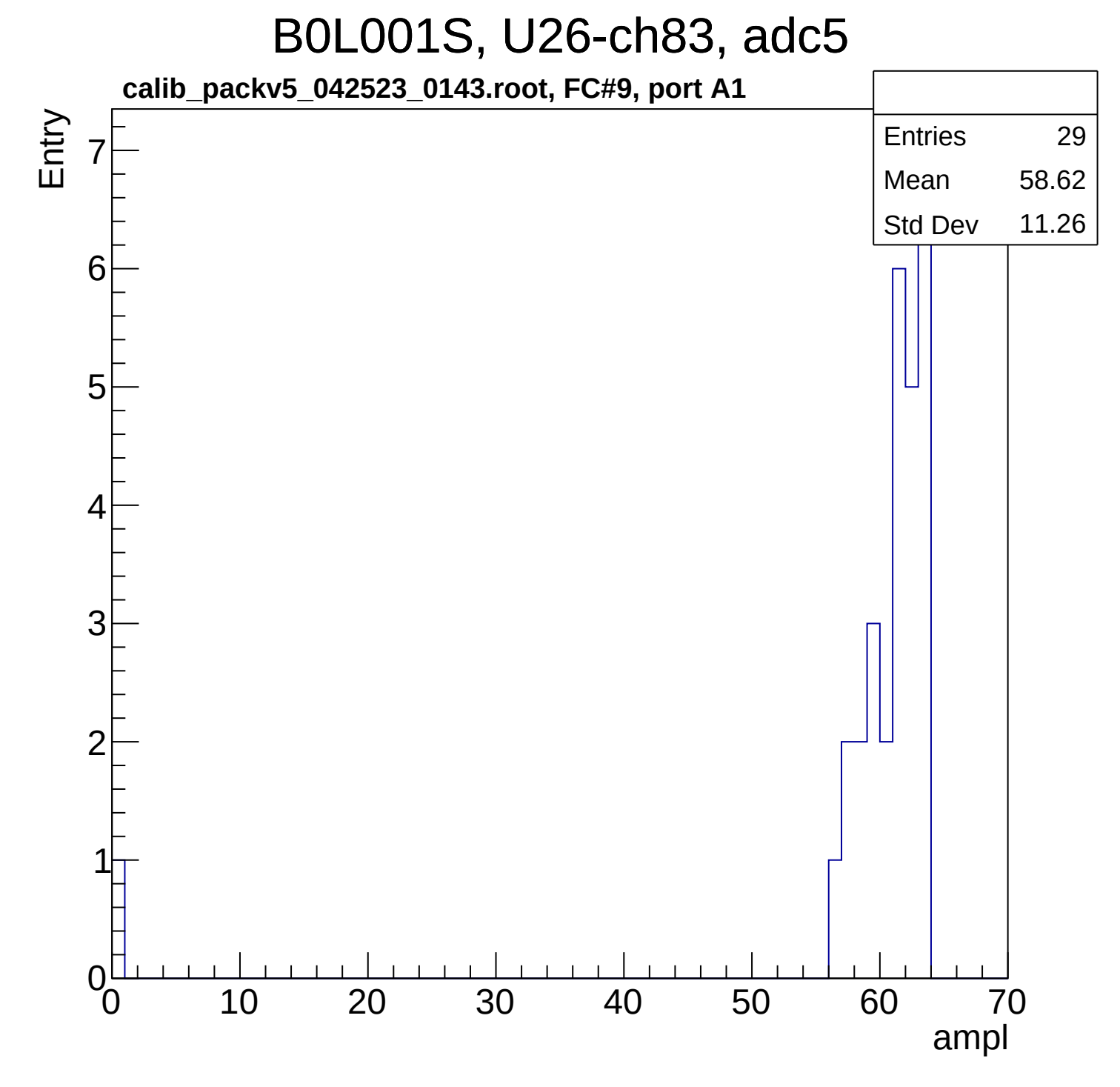
B0L001S, U26-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.62
Std Dev 11.26
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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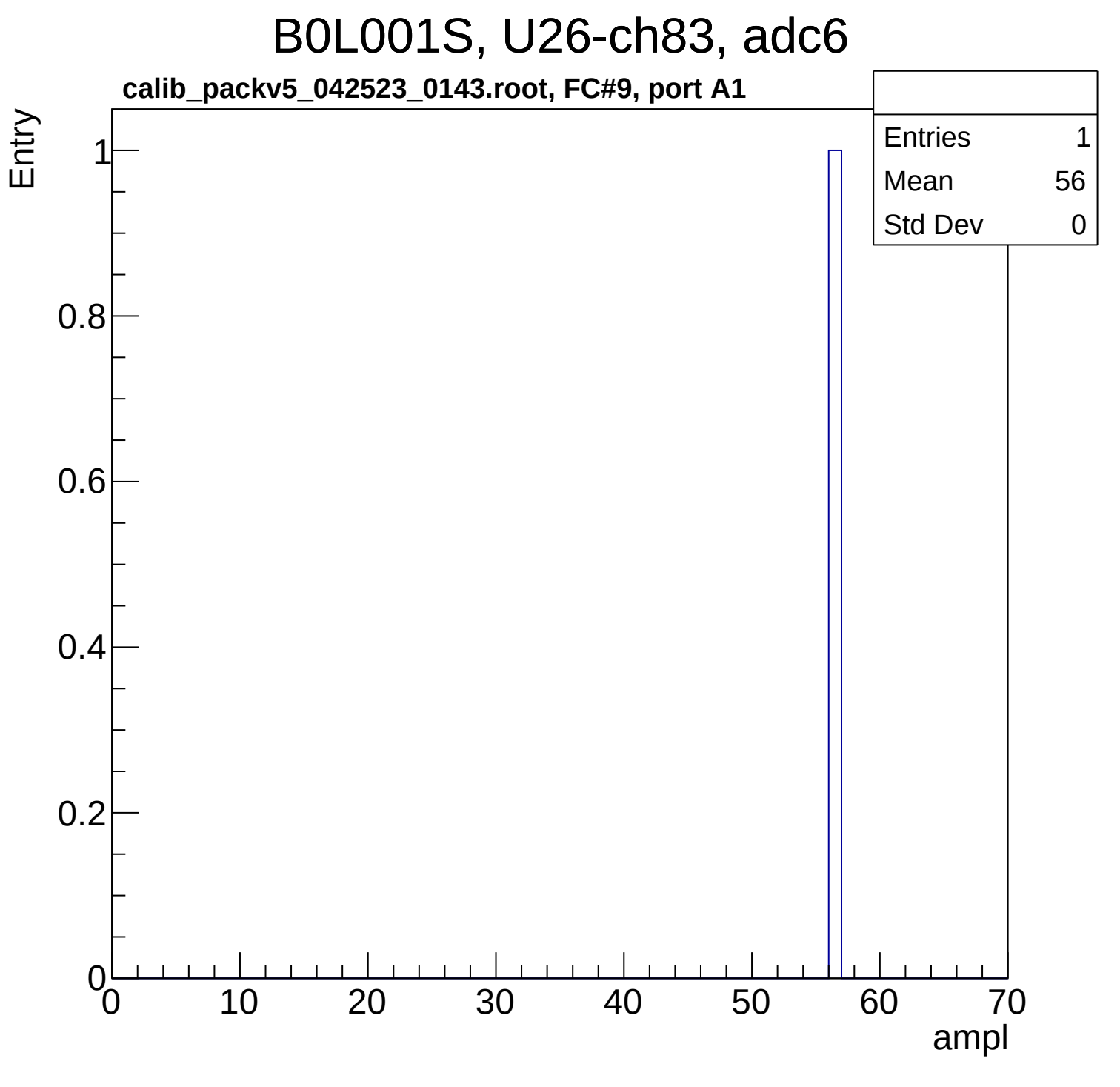
B0L001S, U26-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 56
Std Dev 0
Entry
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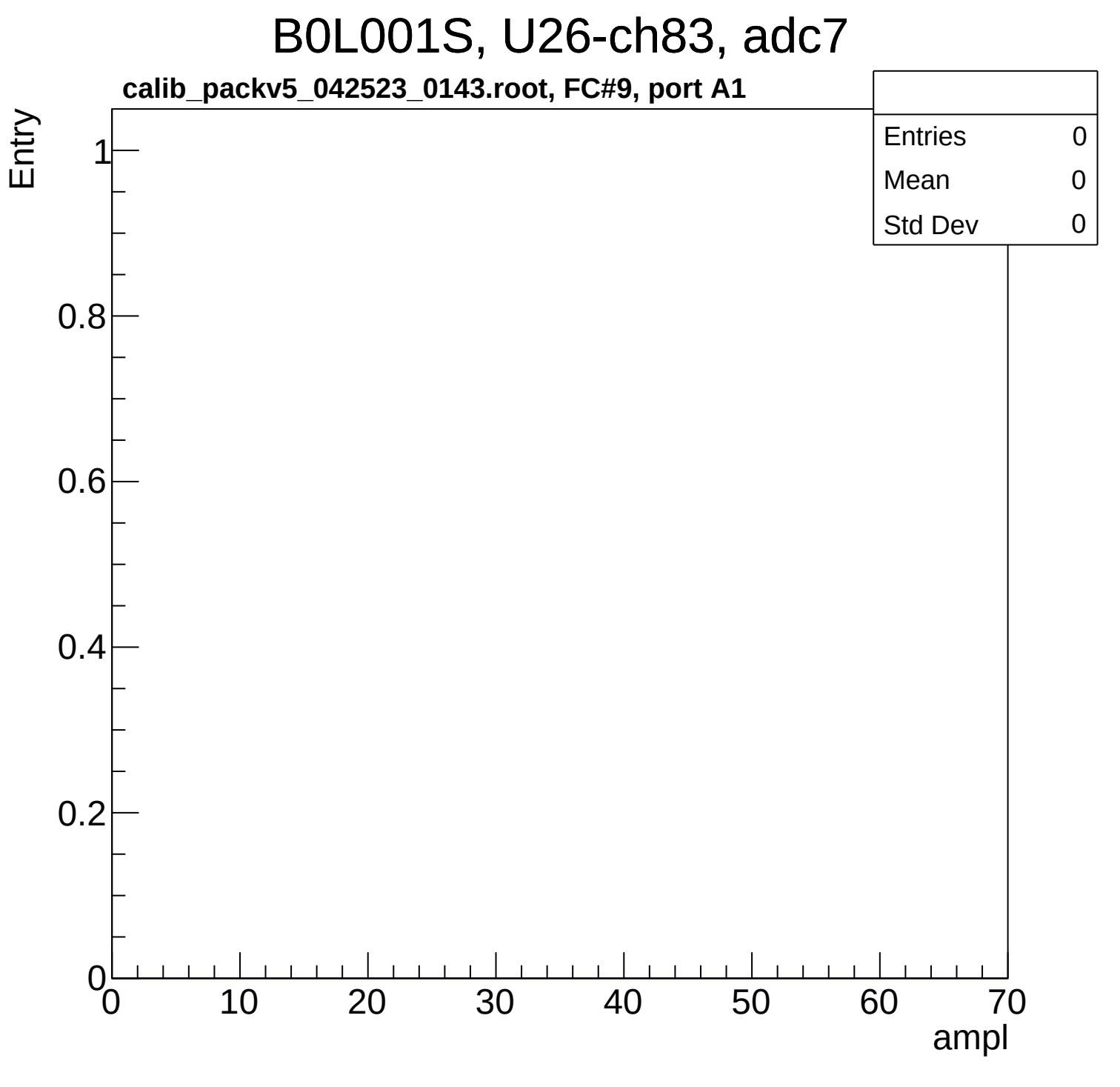
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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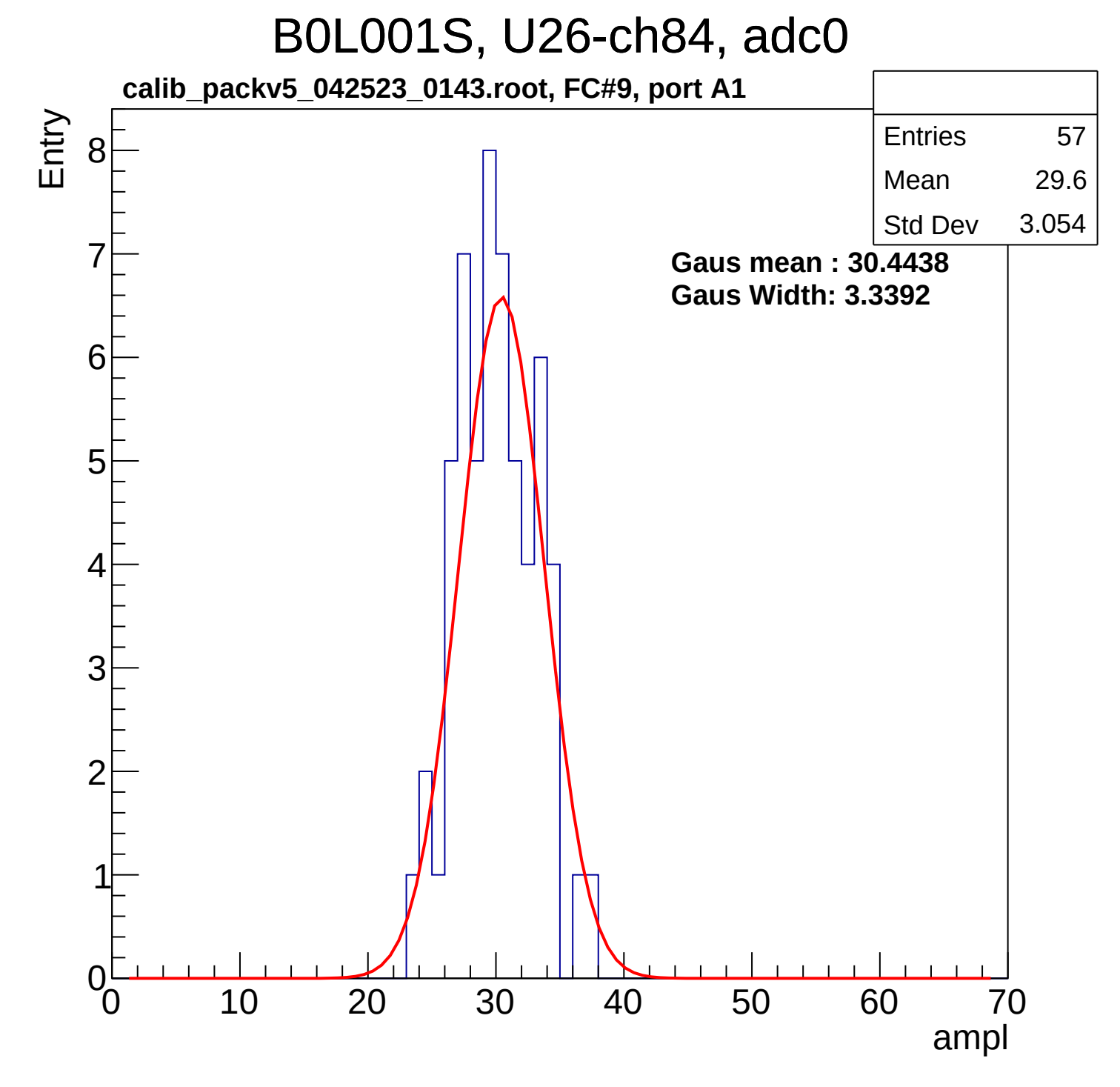
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 29.6
Std Dev 3.054
Gaus mean : 30.4438
Gaus Width: 3.3392
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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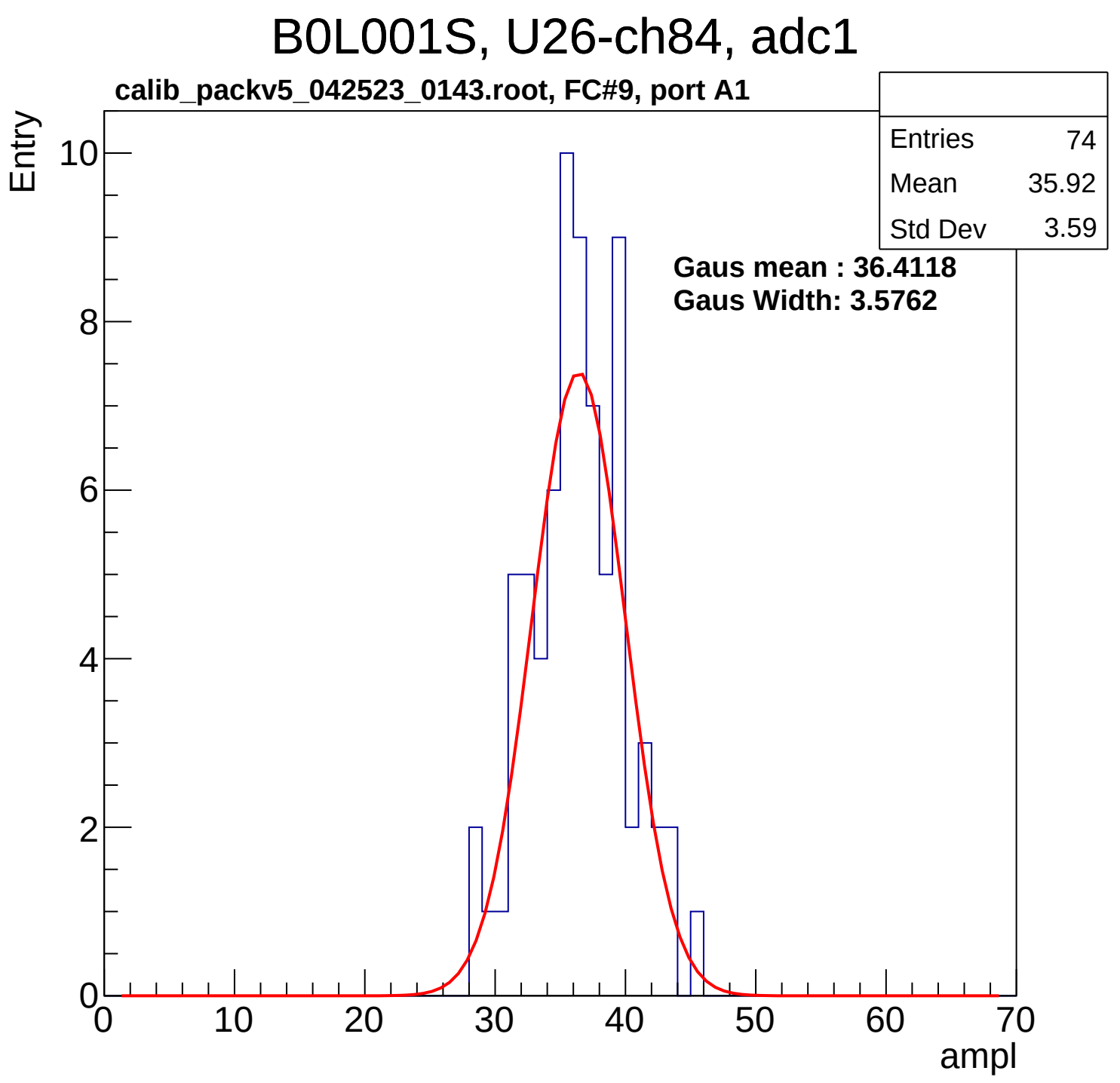
B0L001S, U26-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 35.92
Std Dev 3.59
Gaus mean : 36.4118
Gaus Width: 3.5762
Entry
ampl
0
2
4
6
8
10
0
10
20
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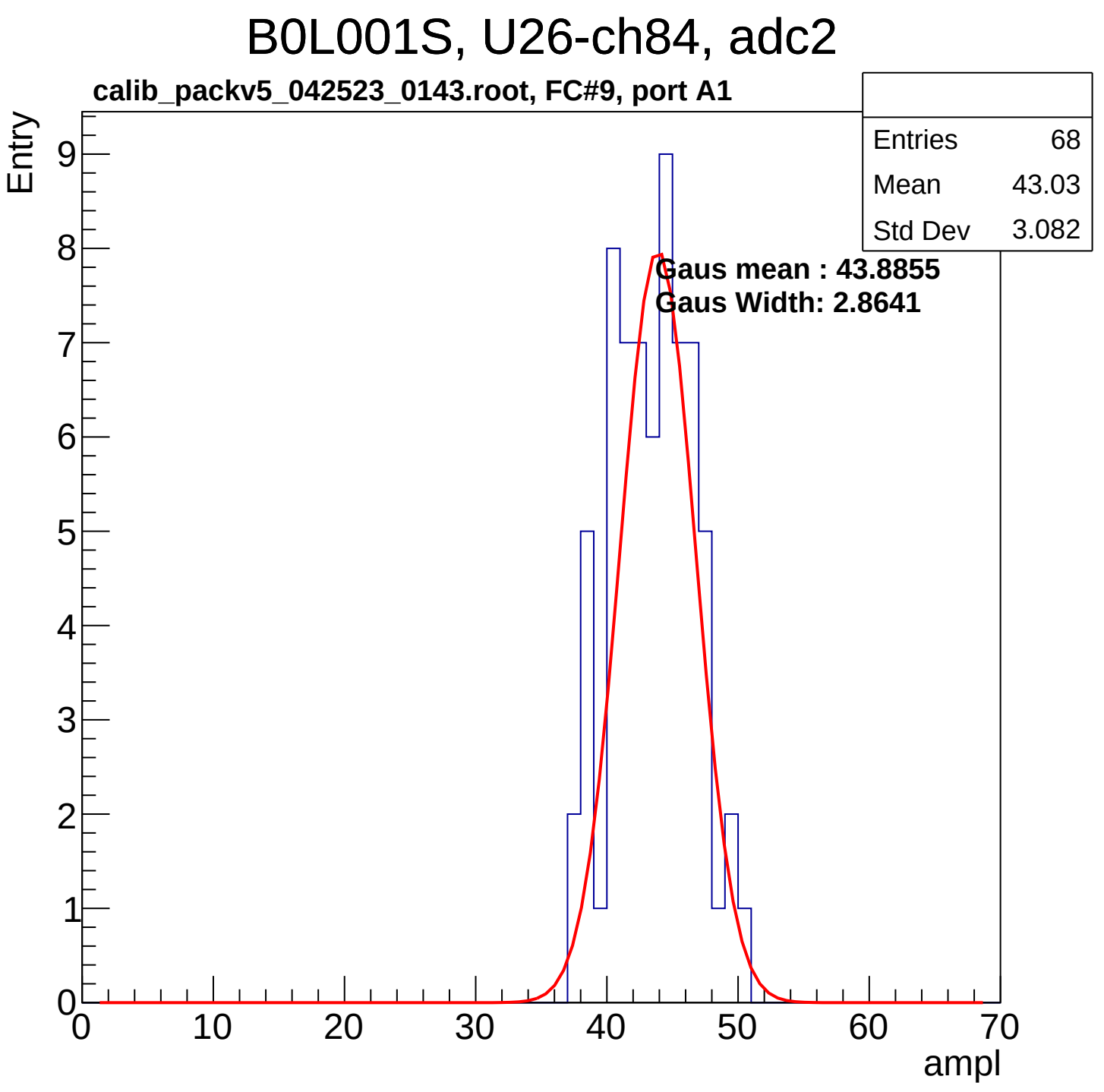
40
50
60
70


B0L001S, U26-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.03
Std Dev 3.082
Gaus mean : 43.8855
Gaus Width: 2.8641
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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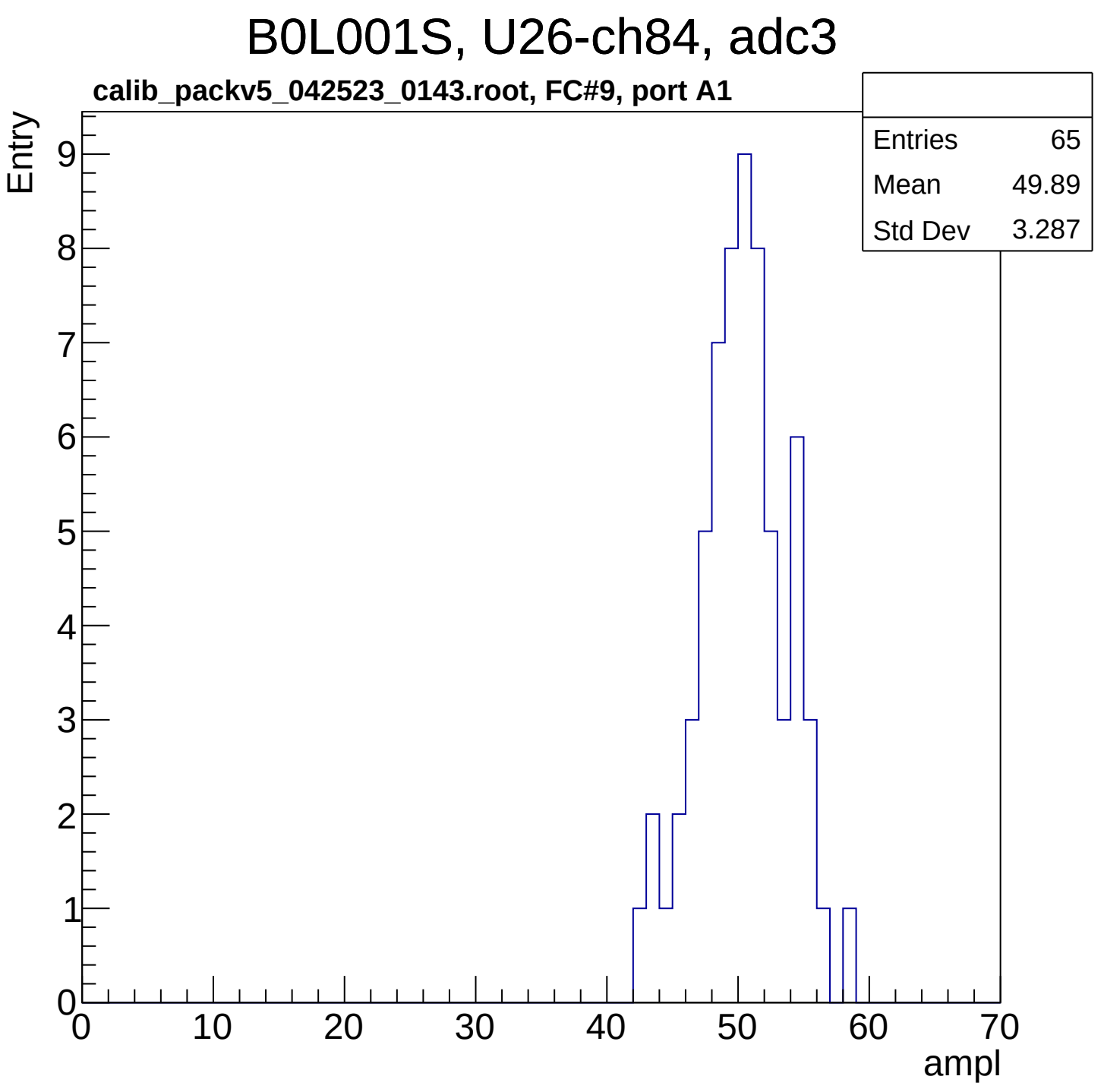
B0L001S, U26-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.89
Std Dev 3.287
Entry
ampl
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3
4
5
6
7
8
9
0
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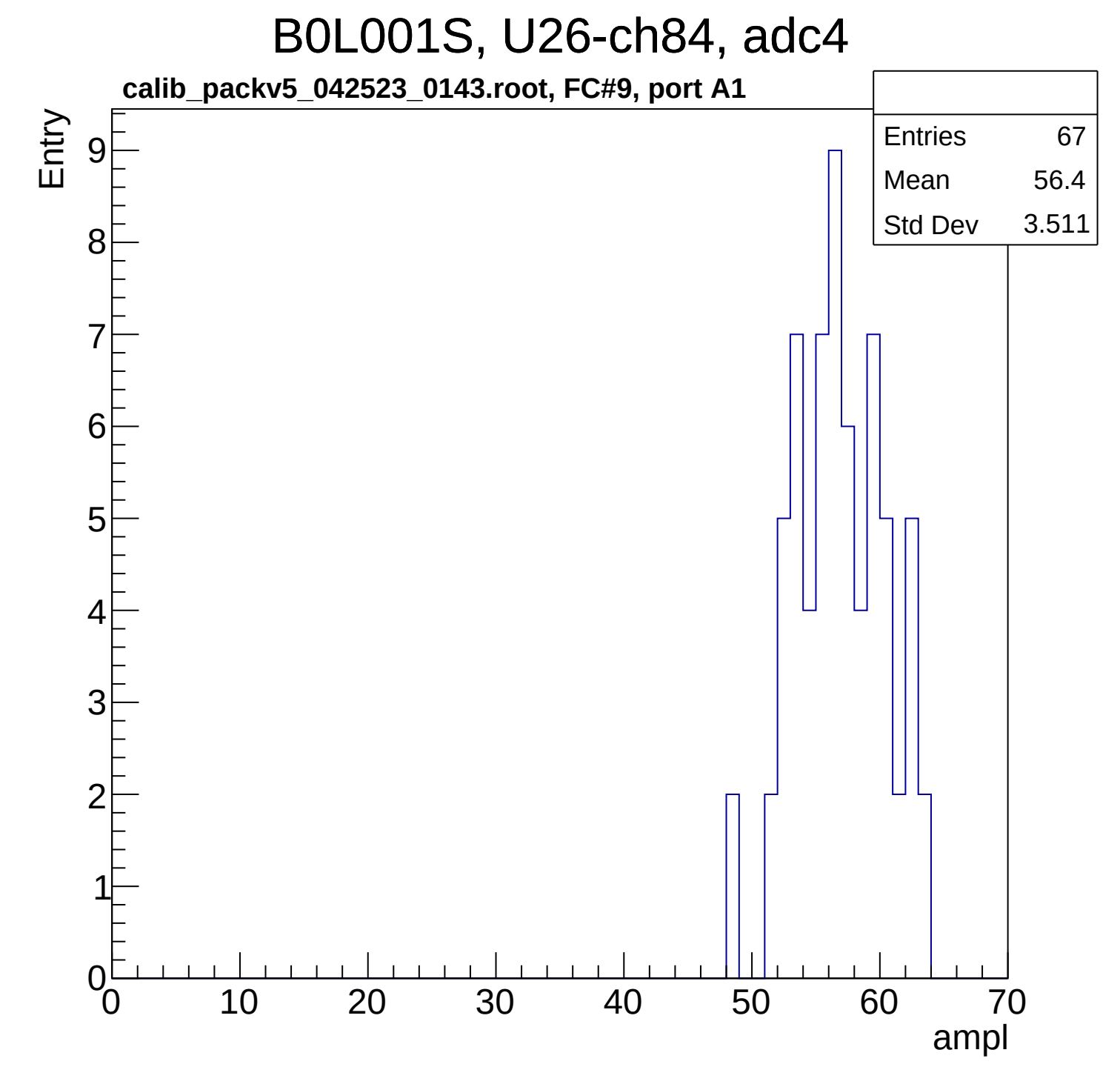
B0L001S, U26-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.4
Std Dev 3.511
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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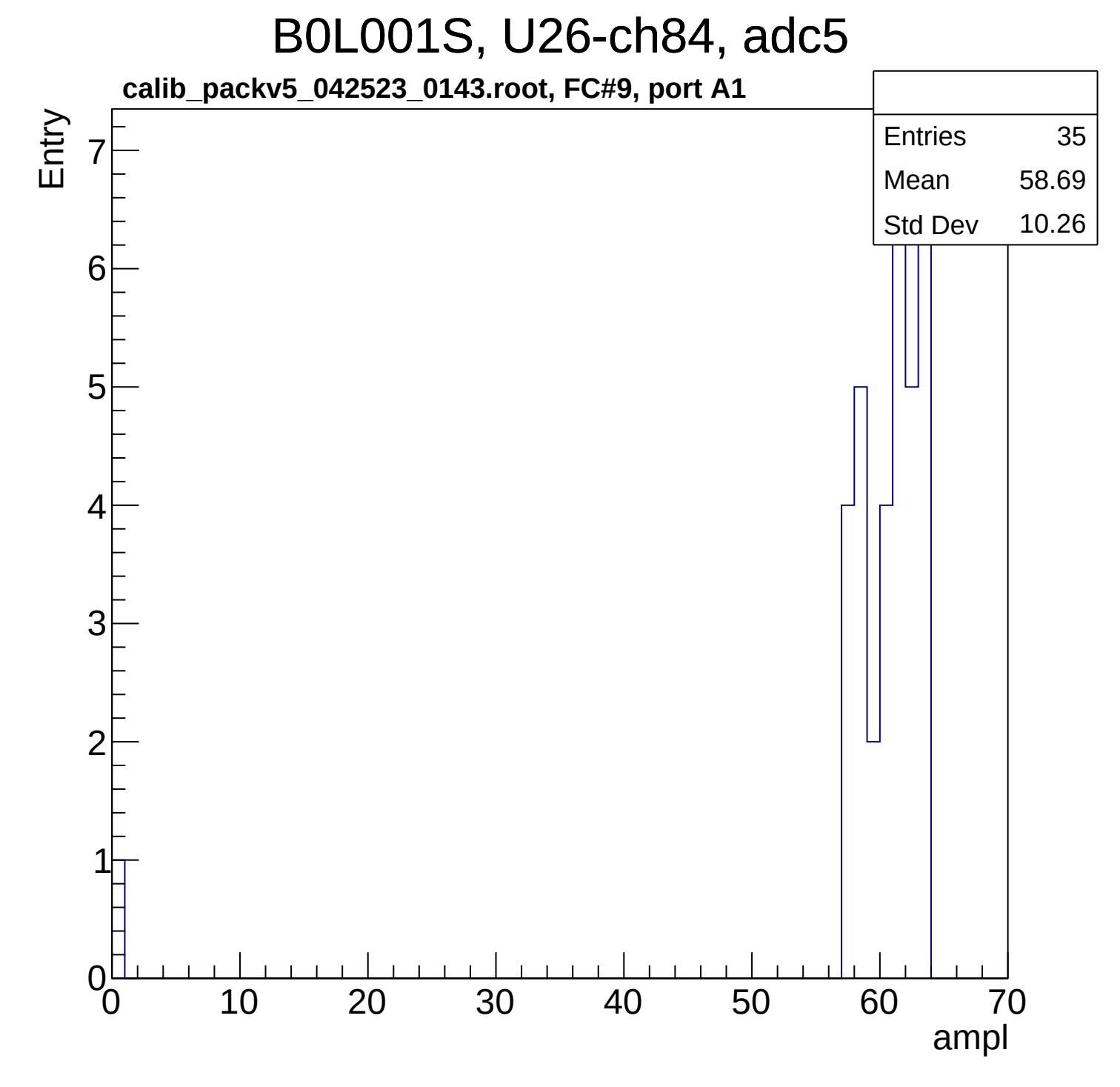
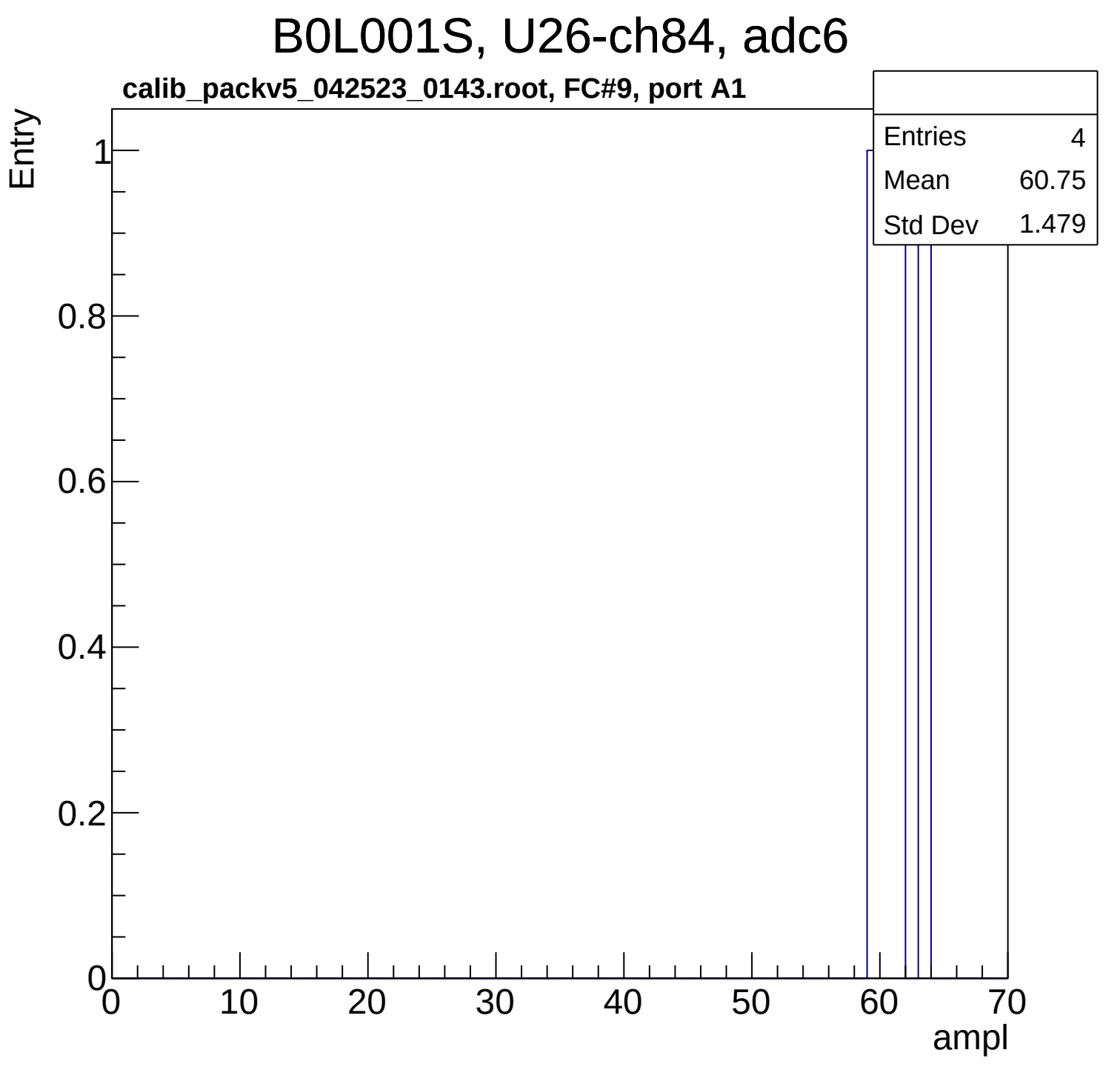

B0L001S, U26-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.69
Std Dev 10.26
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 60.75
Std Dev 1.479
Entry
ampl
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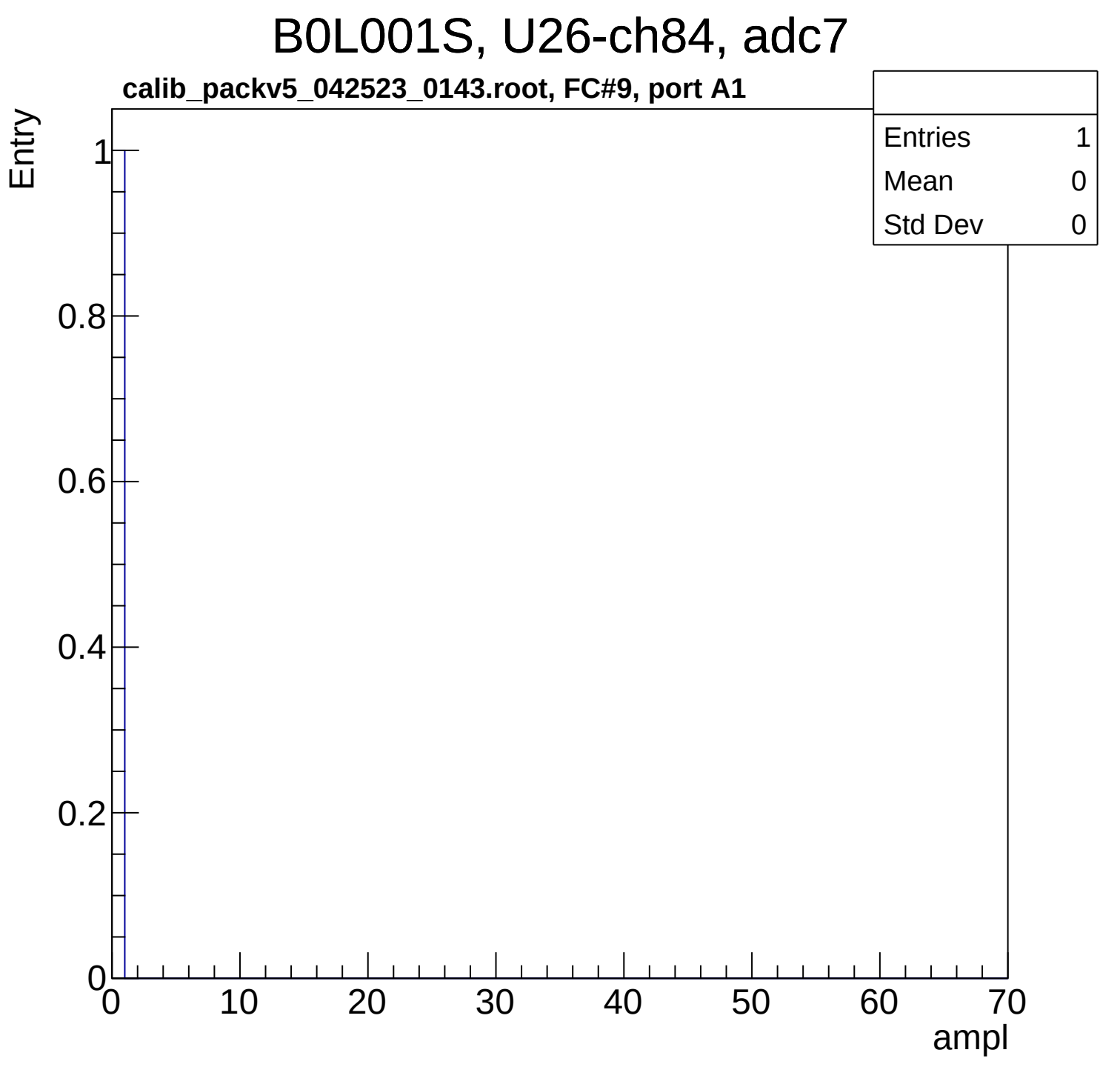
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L001S, U26-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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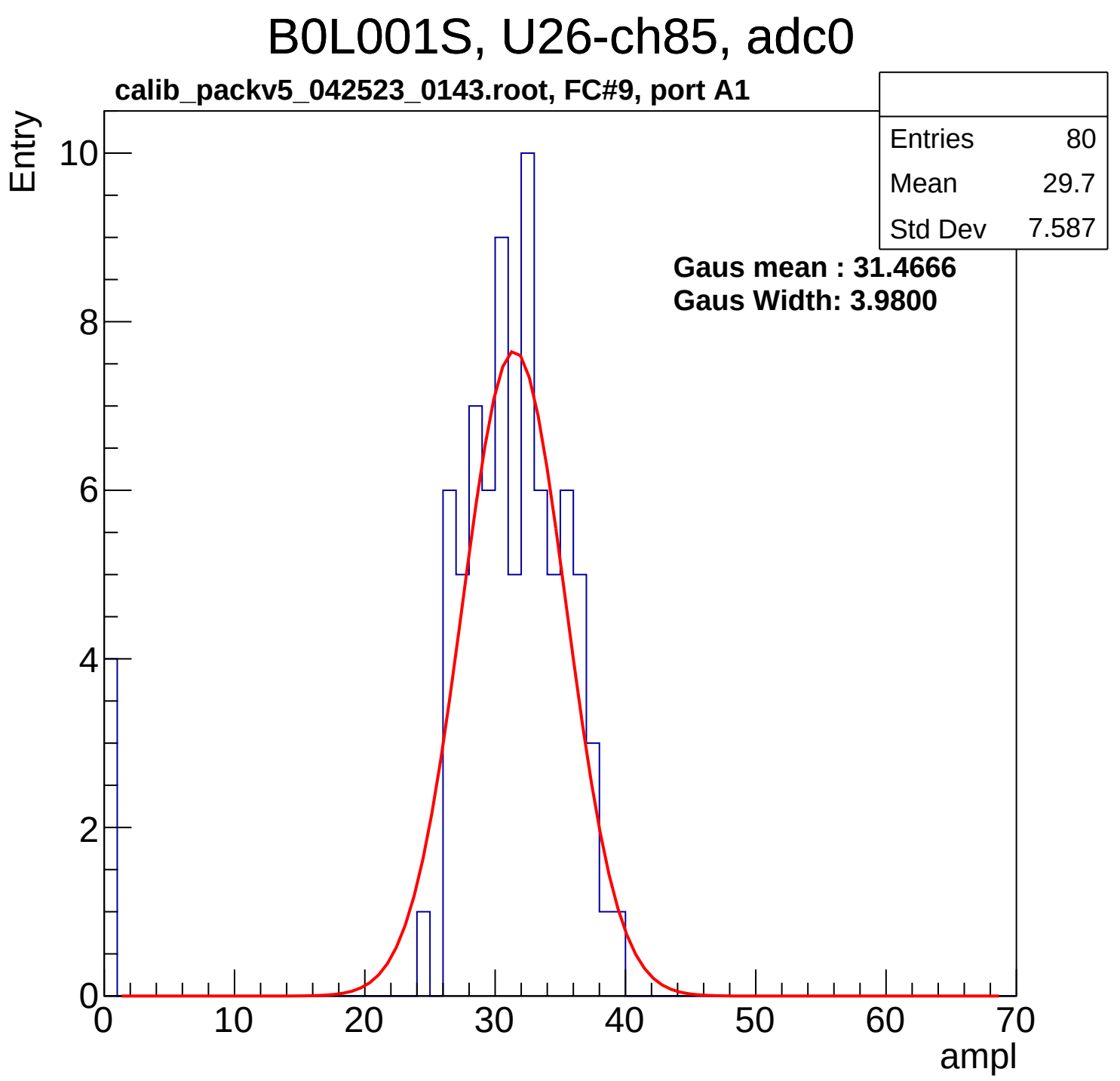
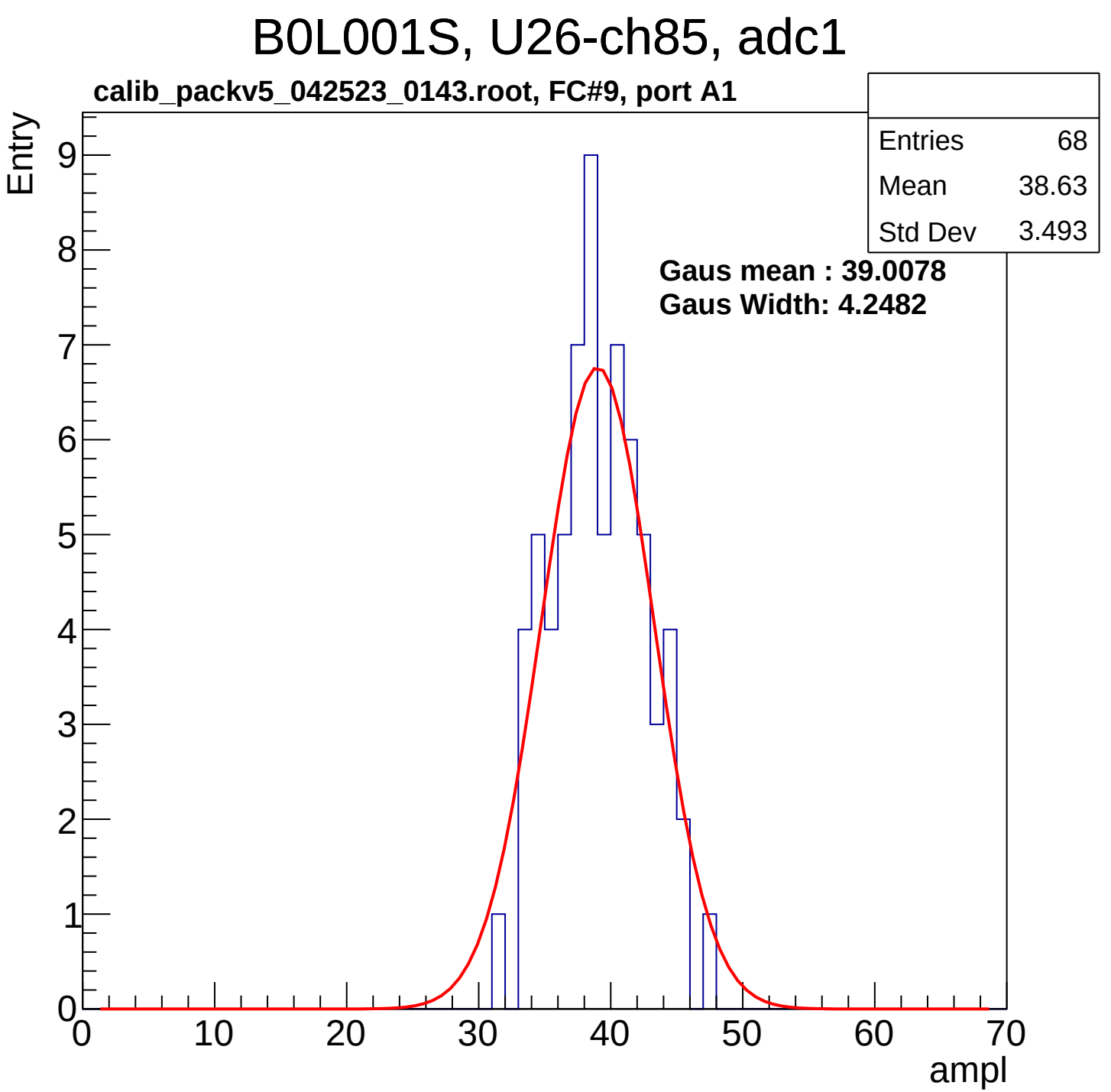
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.7
Std Dev 7.587
Gaus mean : 31.4666
Gaus Width: 3.9800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 38.63
Std Dev 3.493
Gaus mean : 39.0078
Gaus Width: 4.2482
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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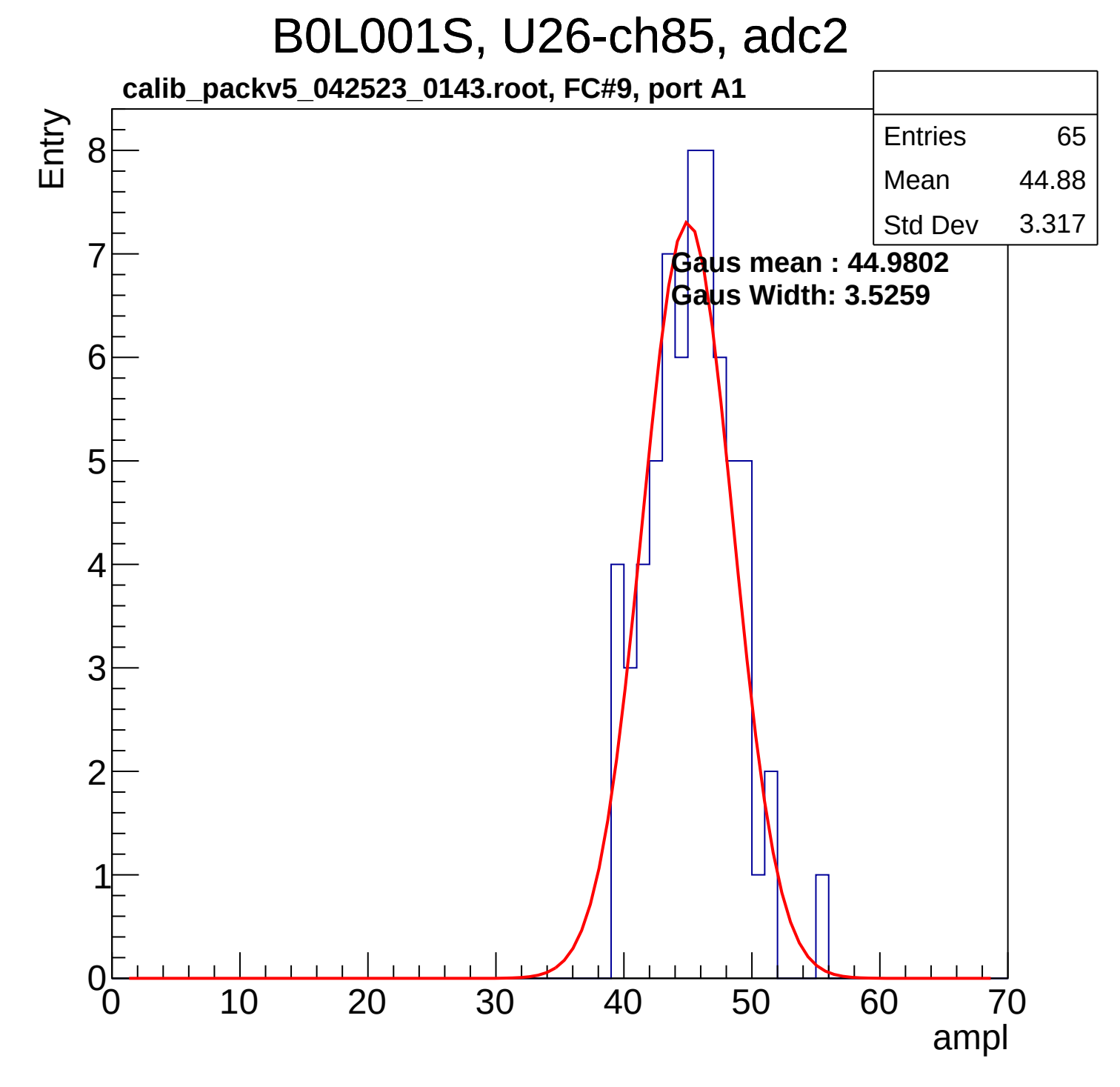
B0L001S, U26-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.88
Std Dev 3.317
Gaus mean : 44.9802
Gaus Width: 3.5259
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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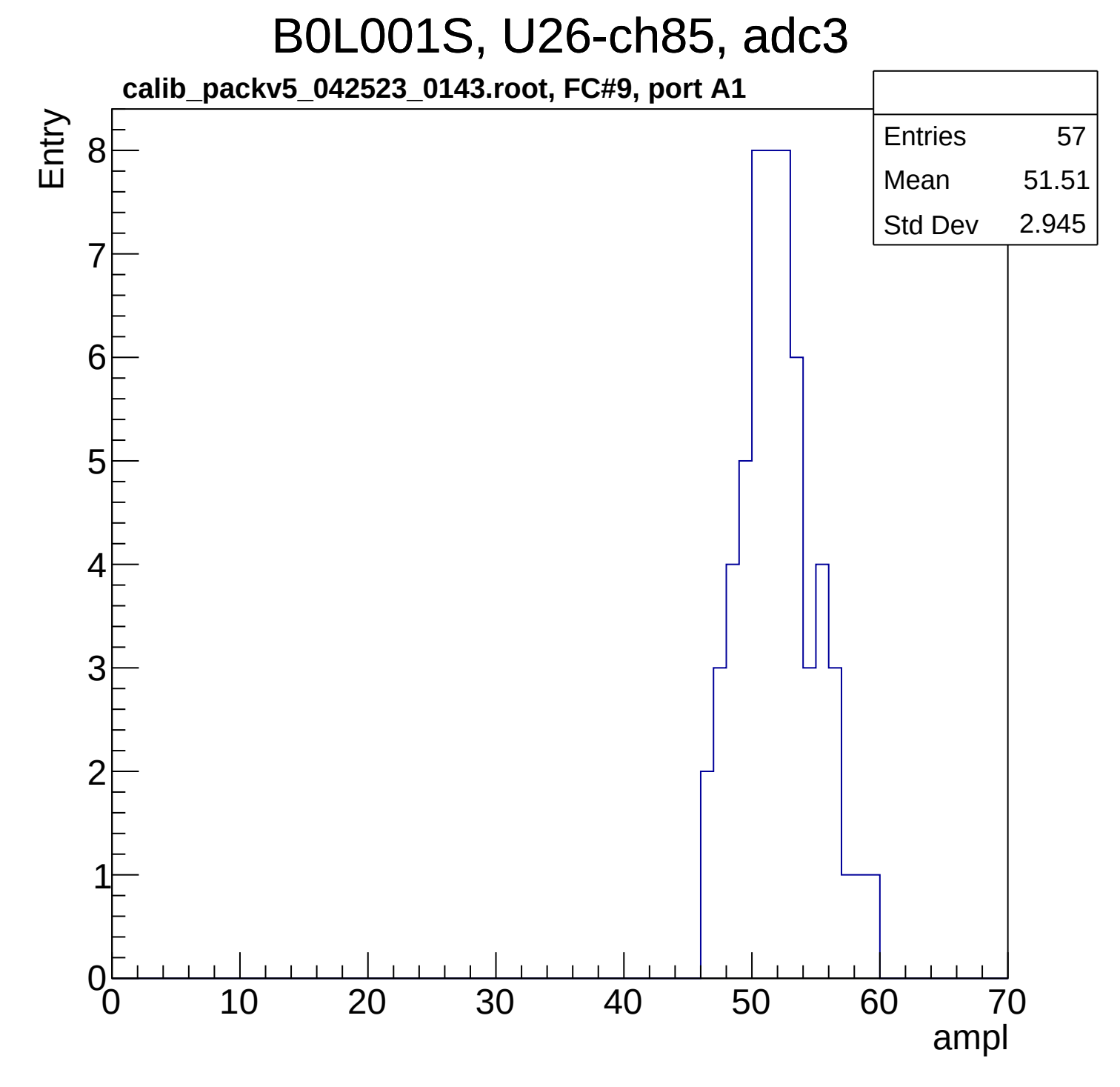
B0L001S, U26-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.51
Std Dev 2.945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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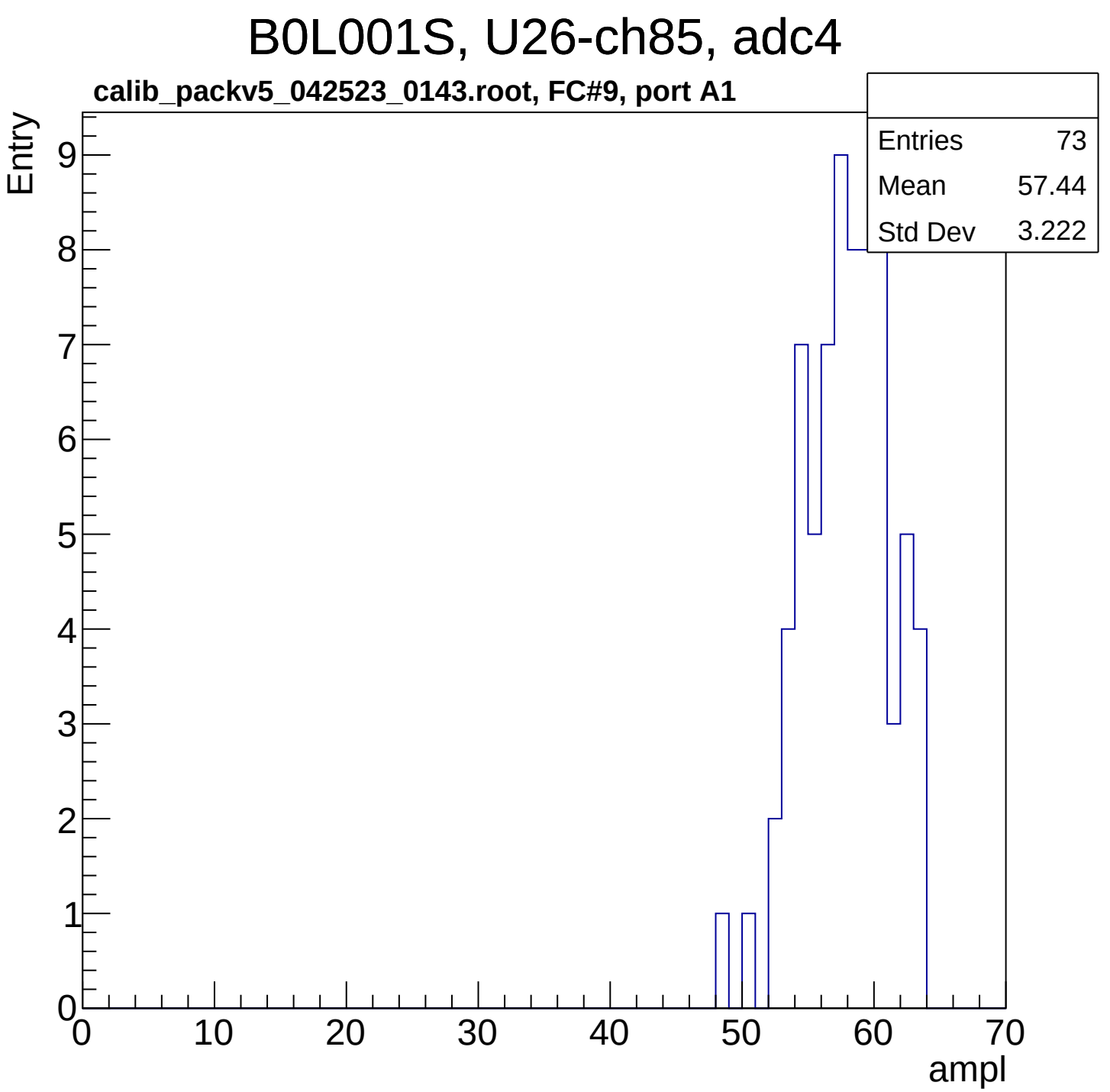
B0L001S, U26-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 57.44
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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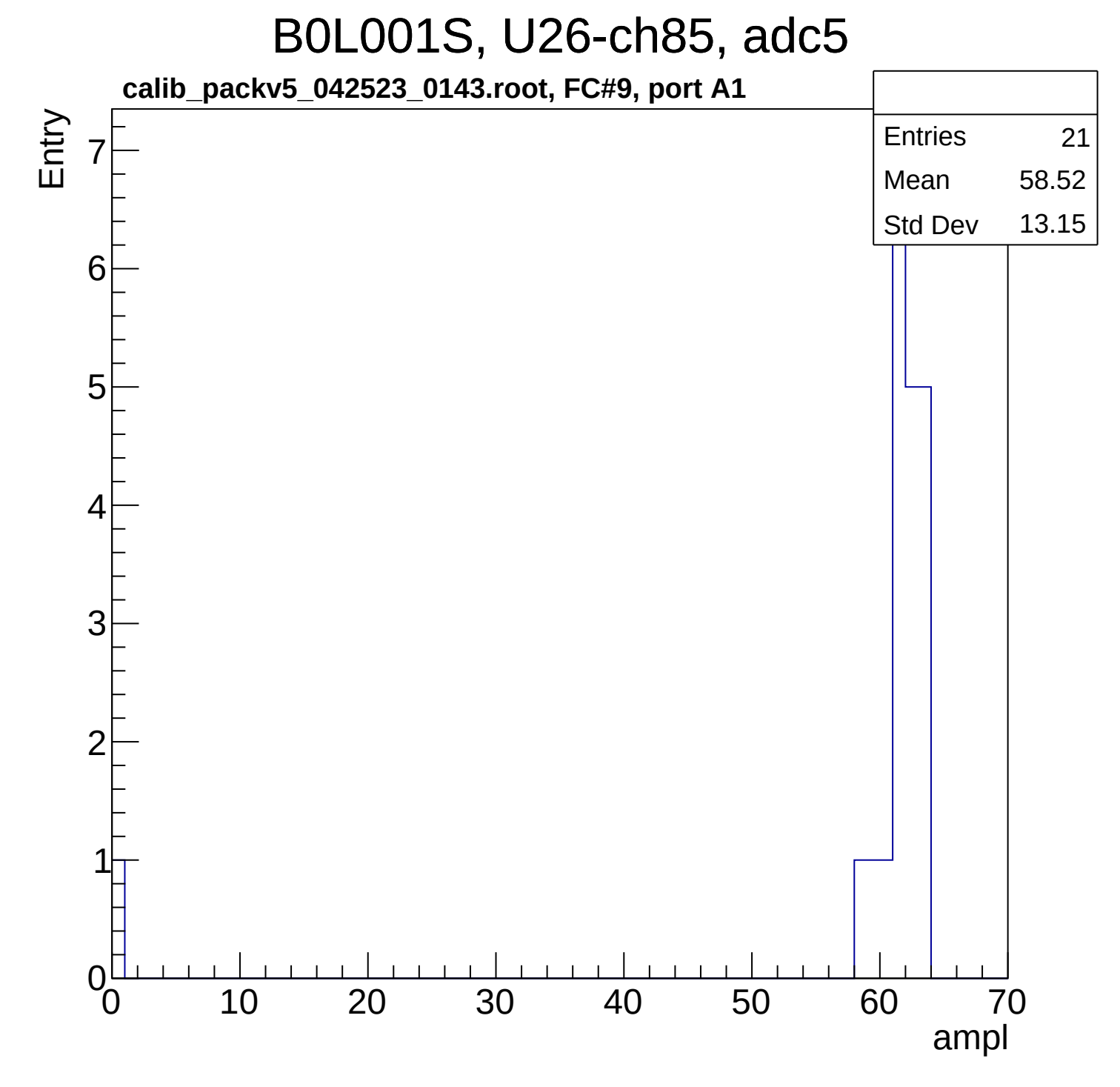
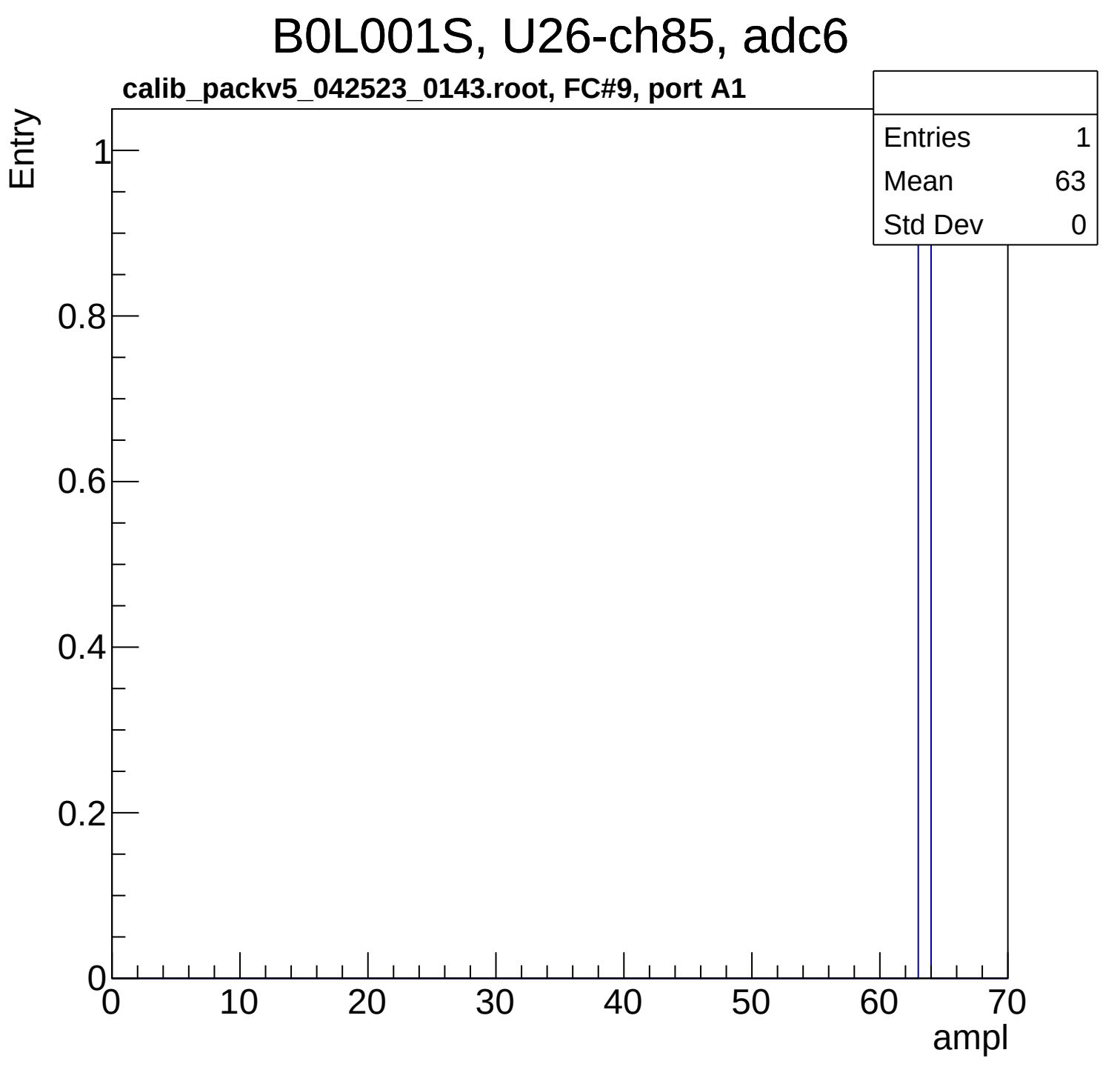
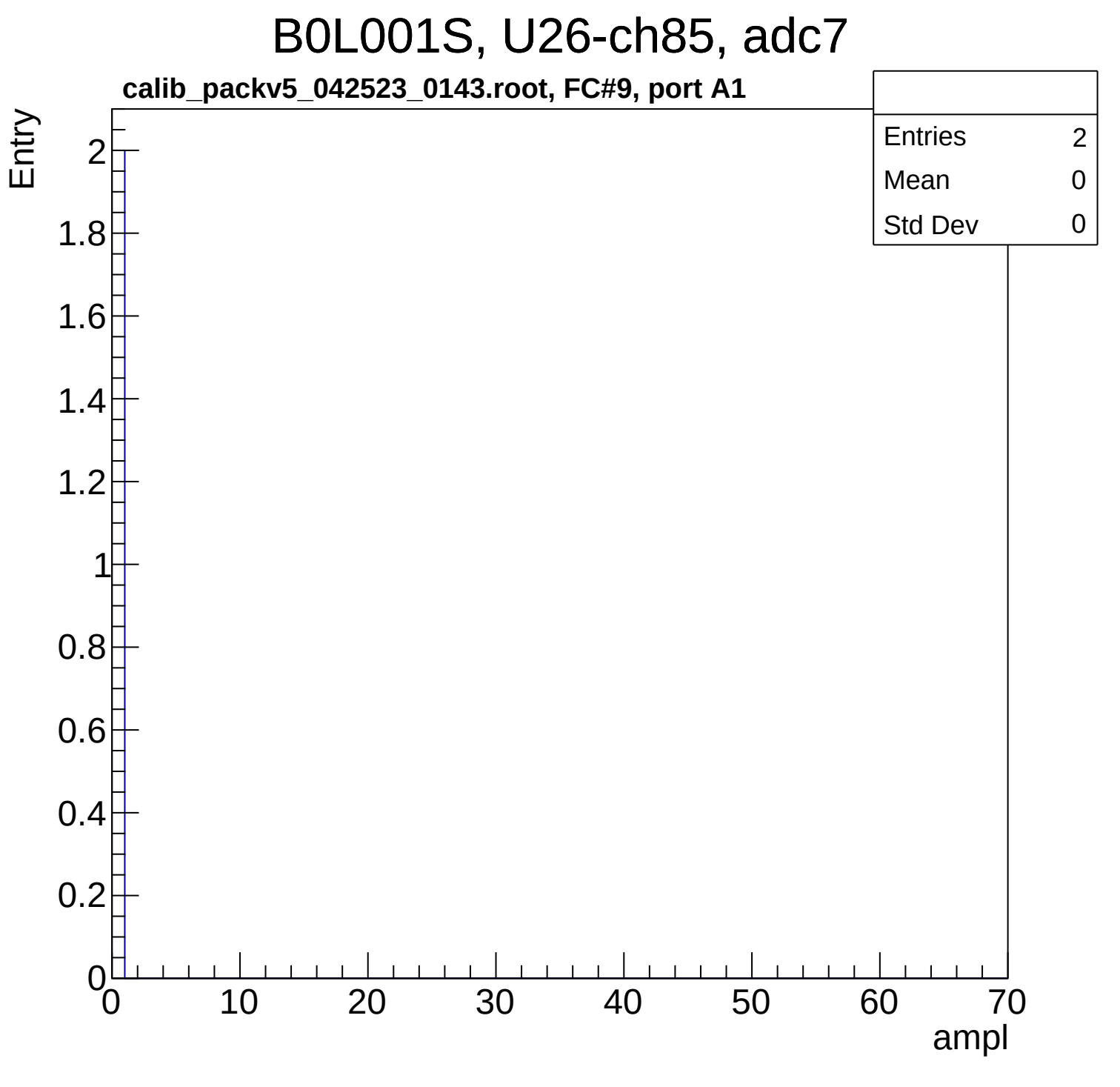
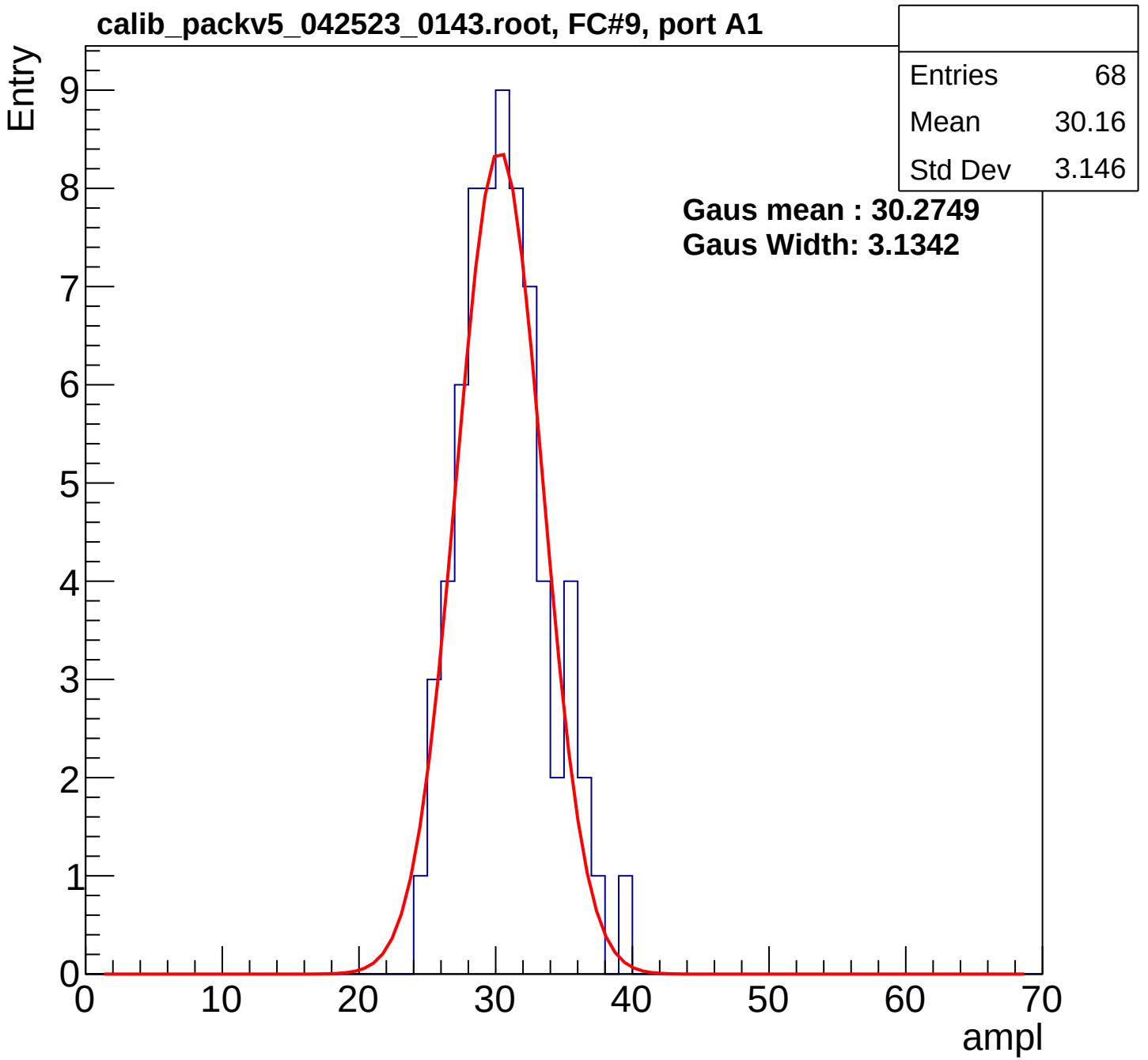
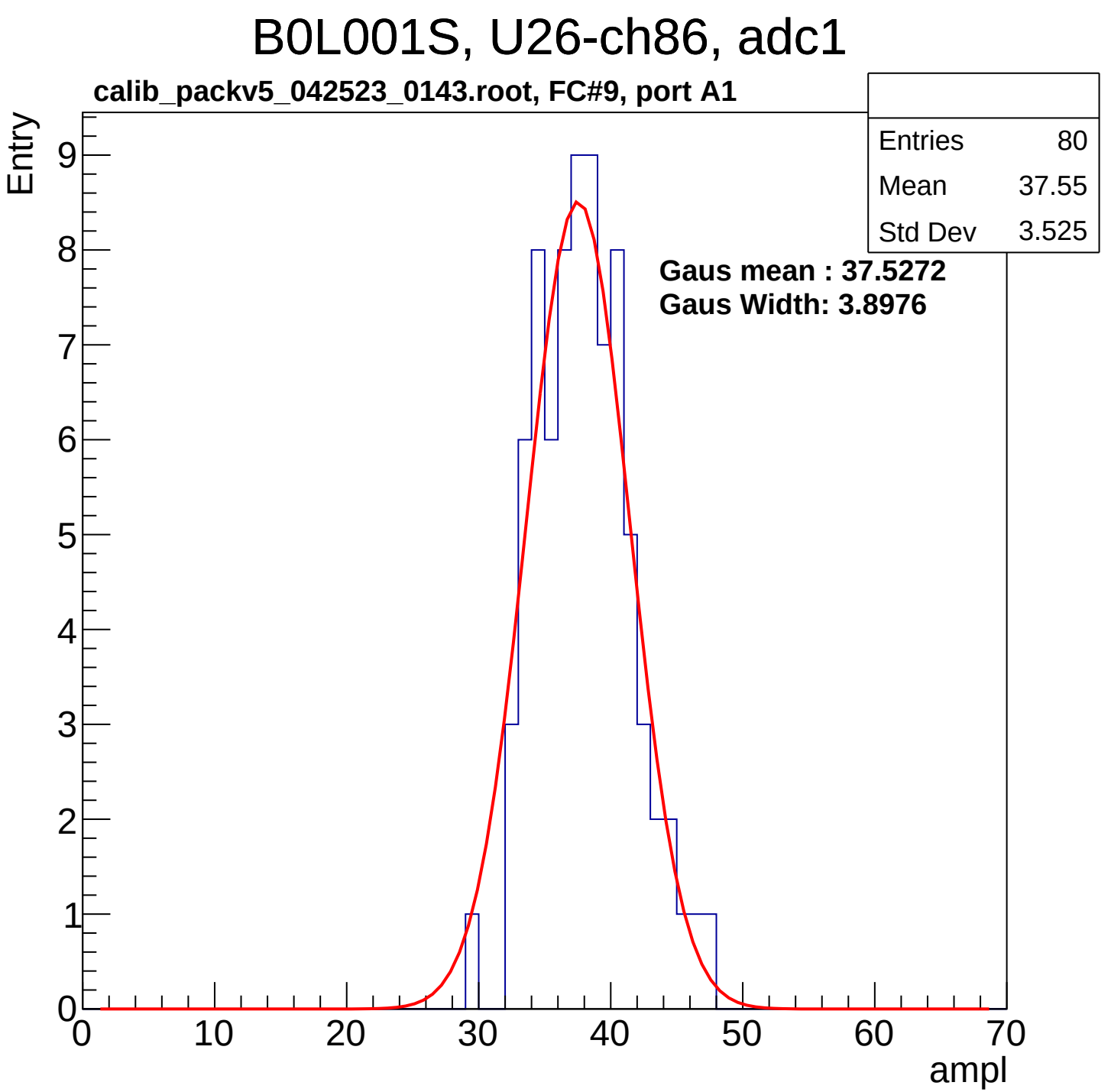
30
40
50
60
70

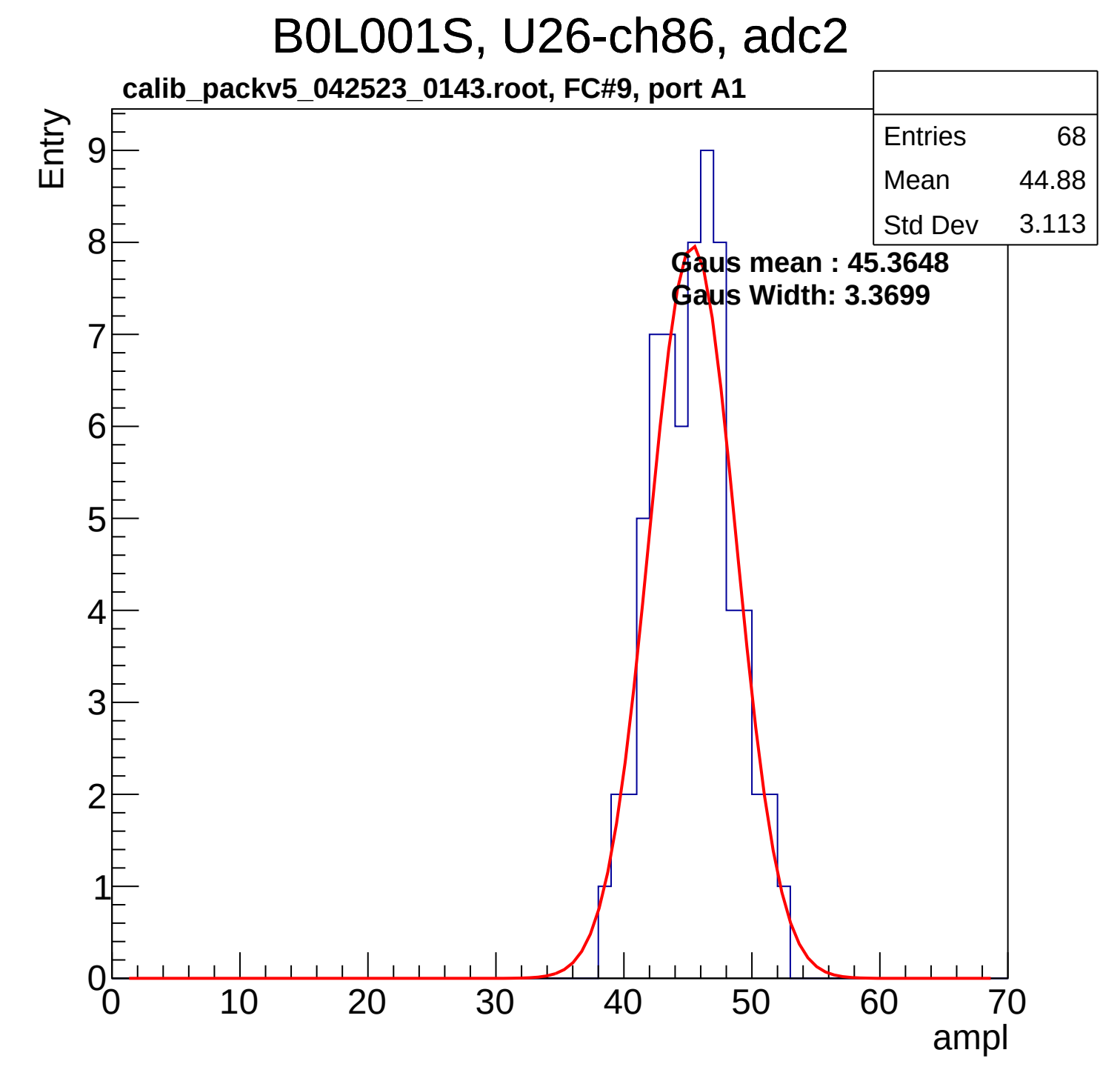

B0L001S, U26-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.52
Std Dev 13.15
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

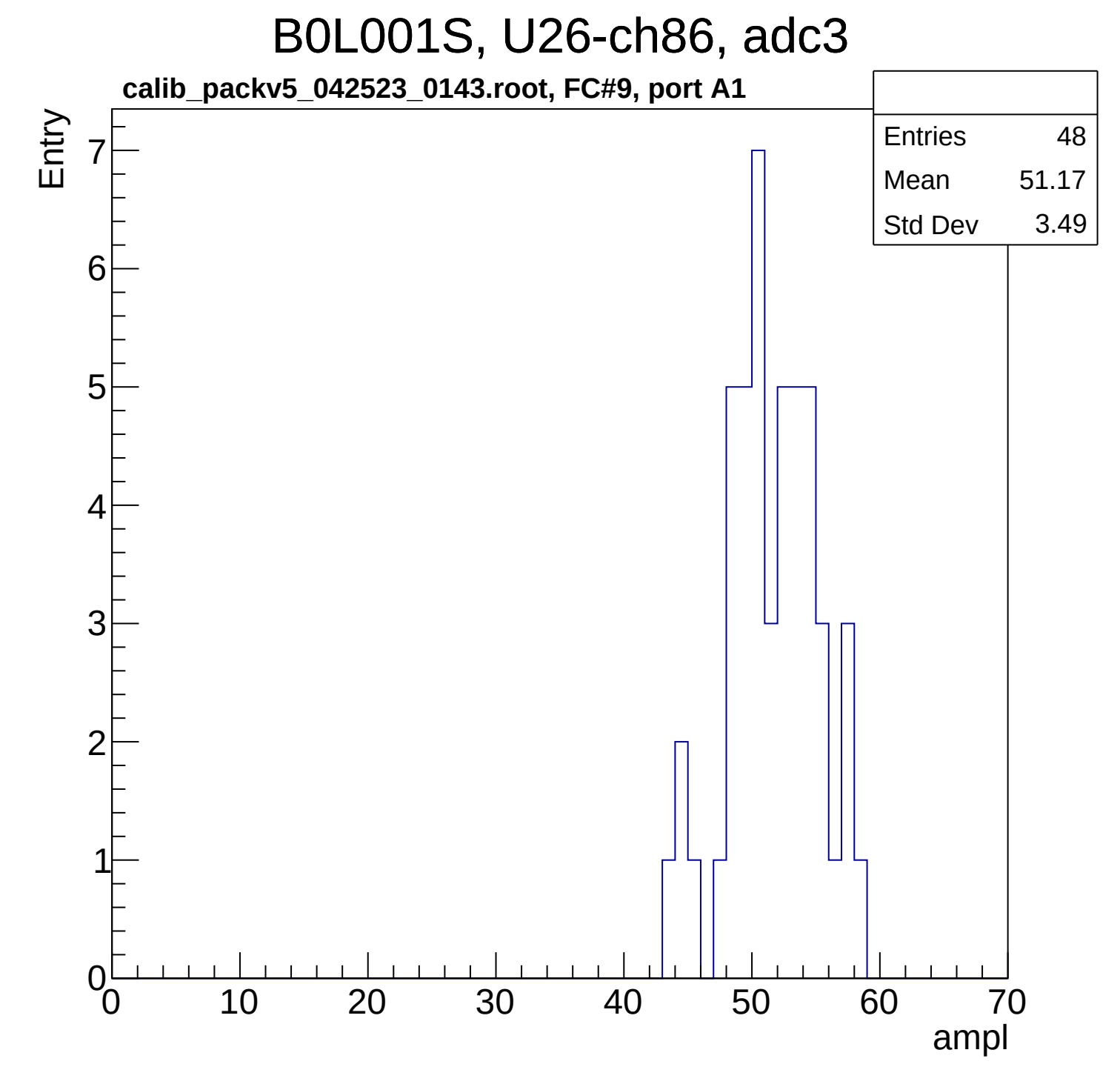

B0L001S, U26-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.16
Std Dev 3.146
Gaus mean : 30.2749
Gaus Width: 3.1342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.55
Std Dev 3.525
Gaus mean : 37.5272
Gaus Width: 3.8976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.88
Std Dev 3.113
Gaus mean : 45.3648
Gaus Width: 3.3699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 51.17
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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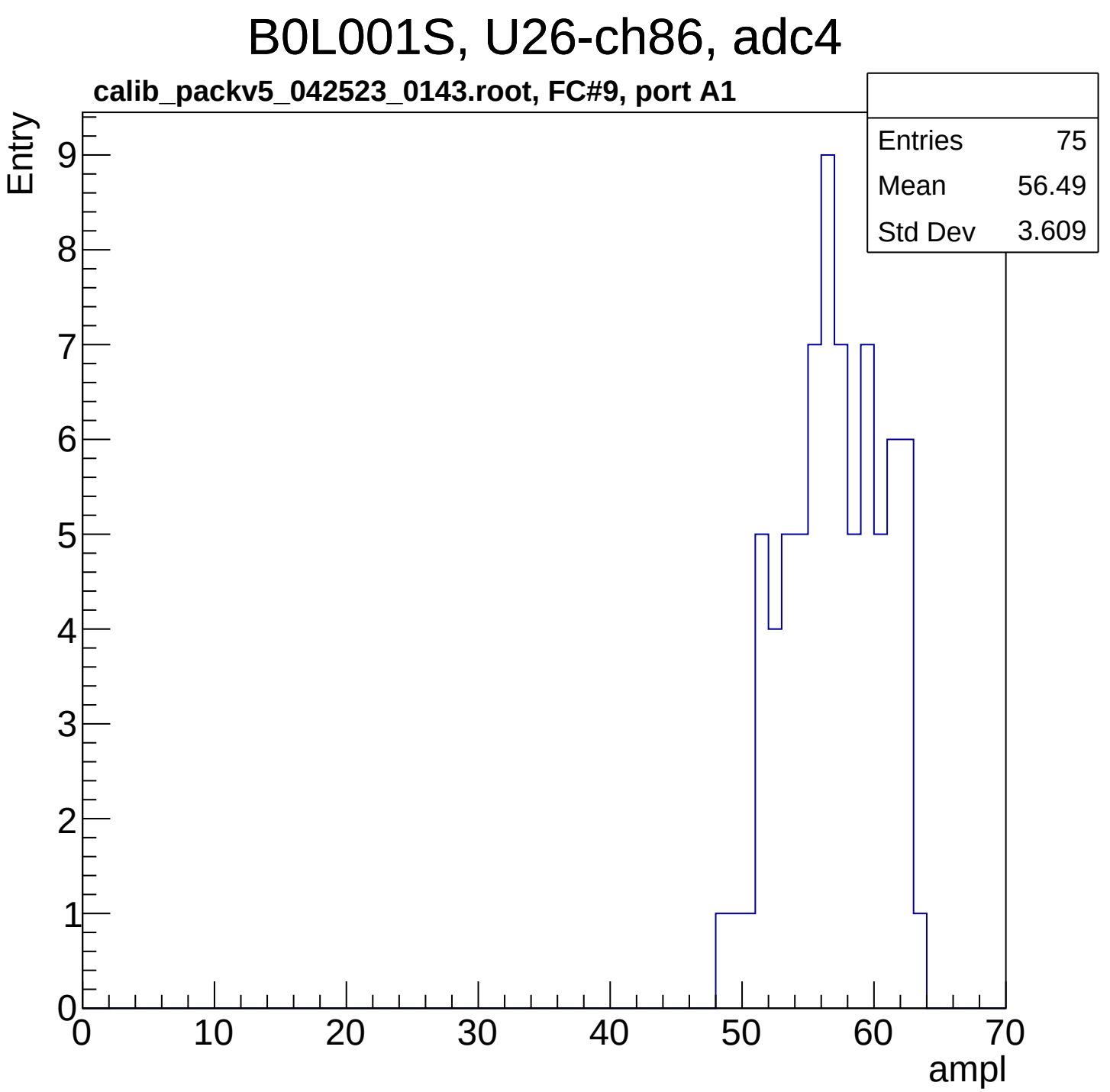
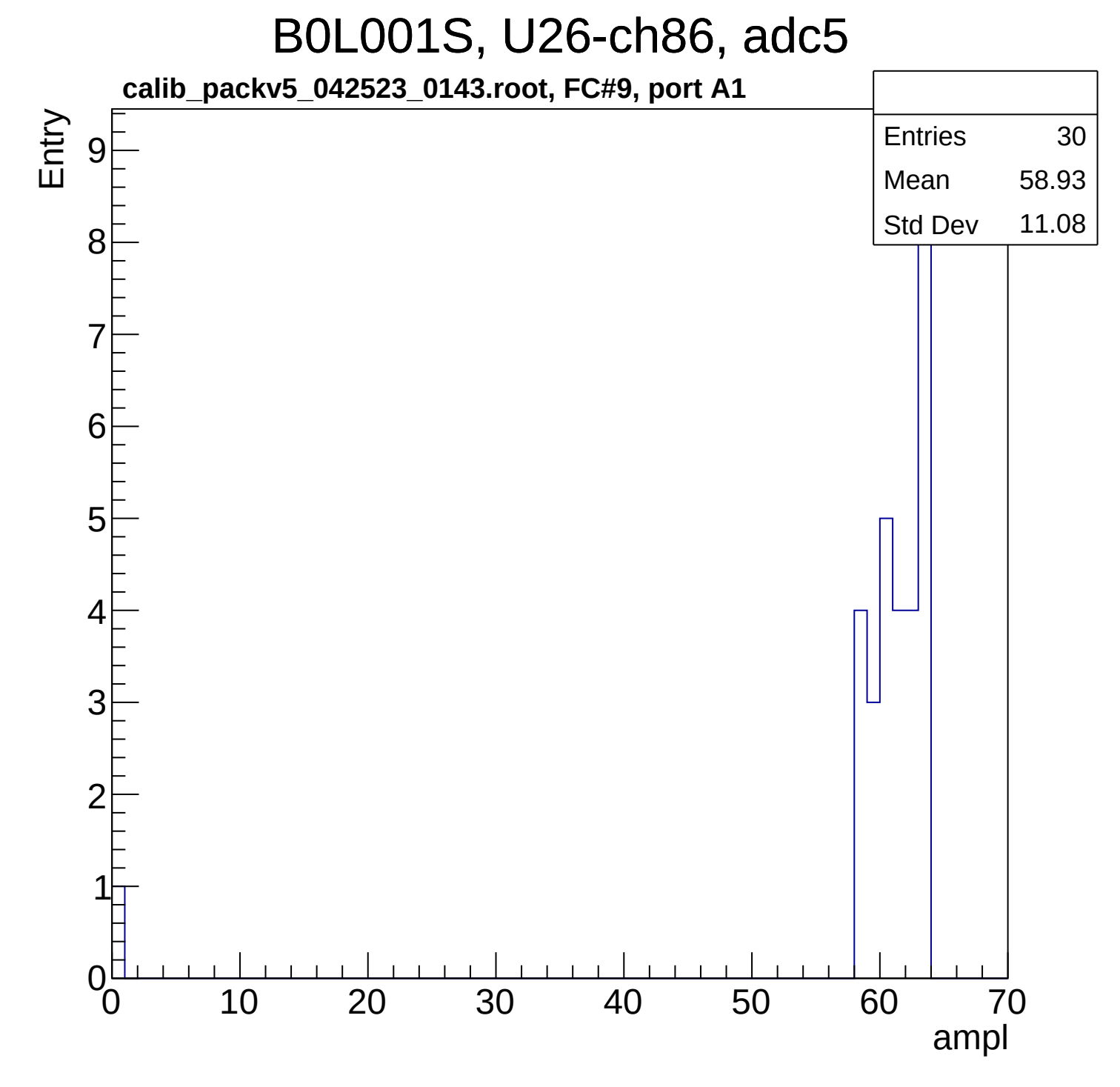
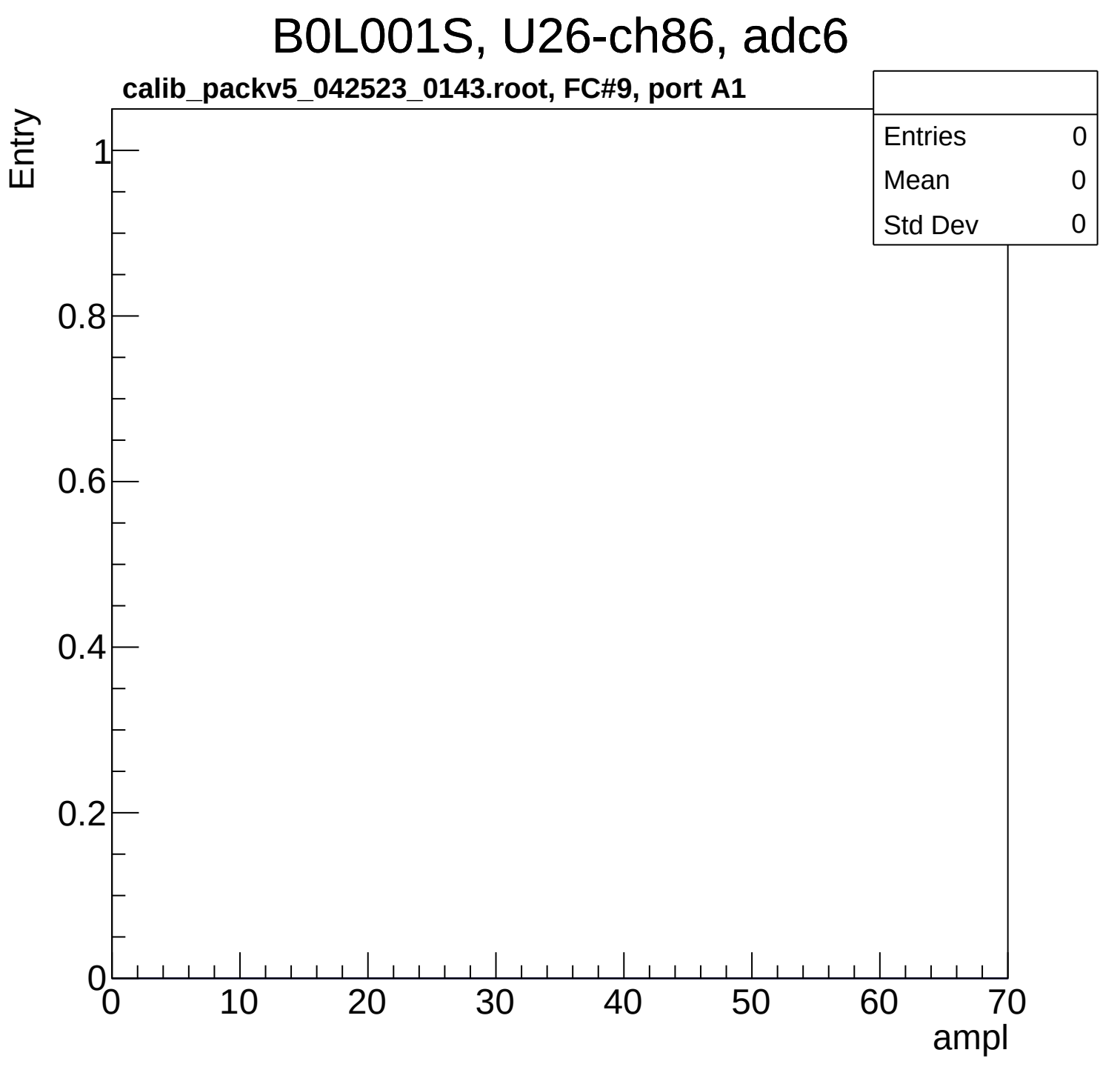
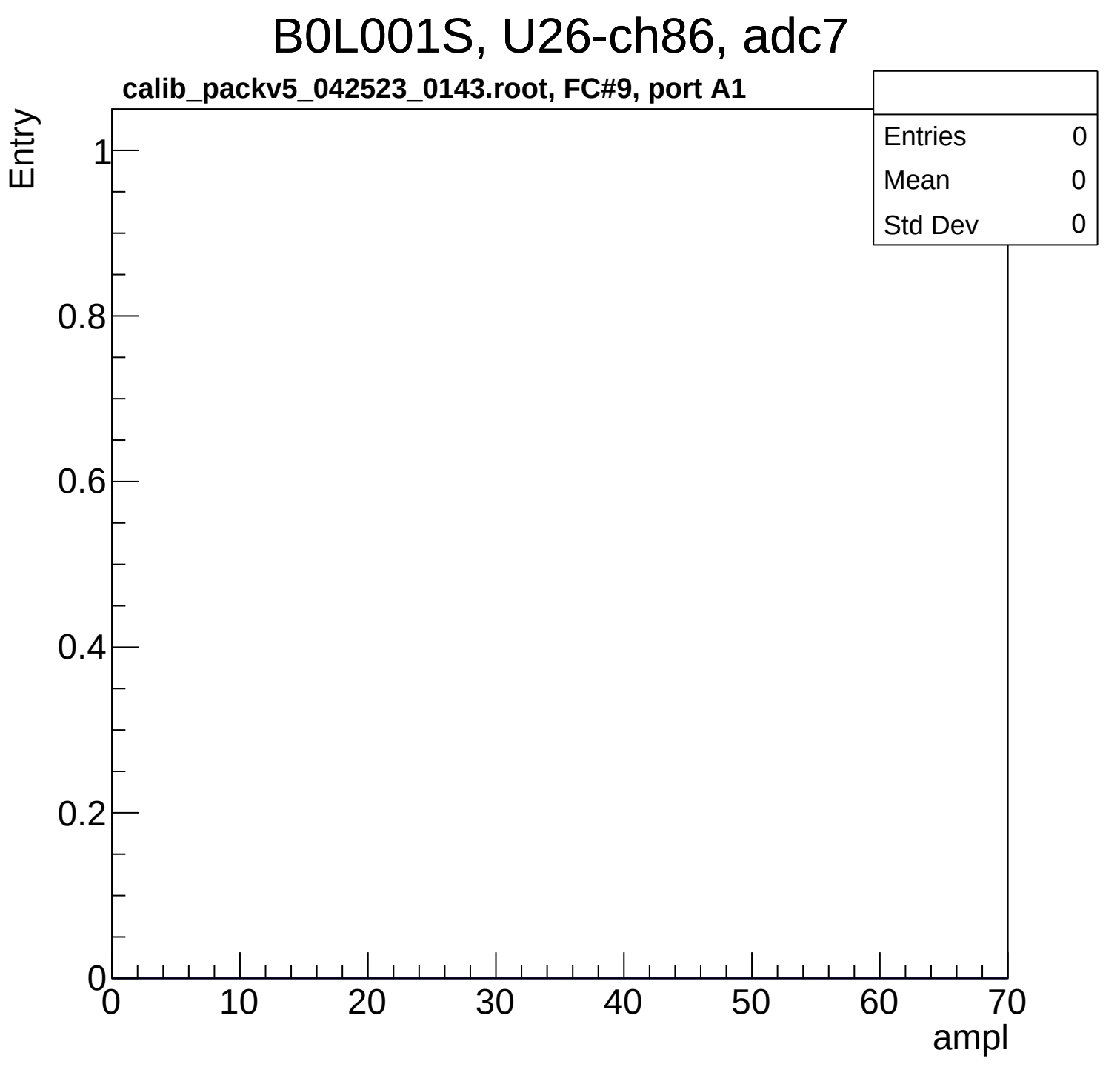
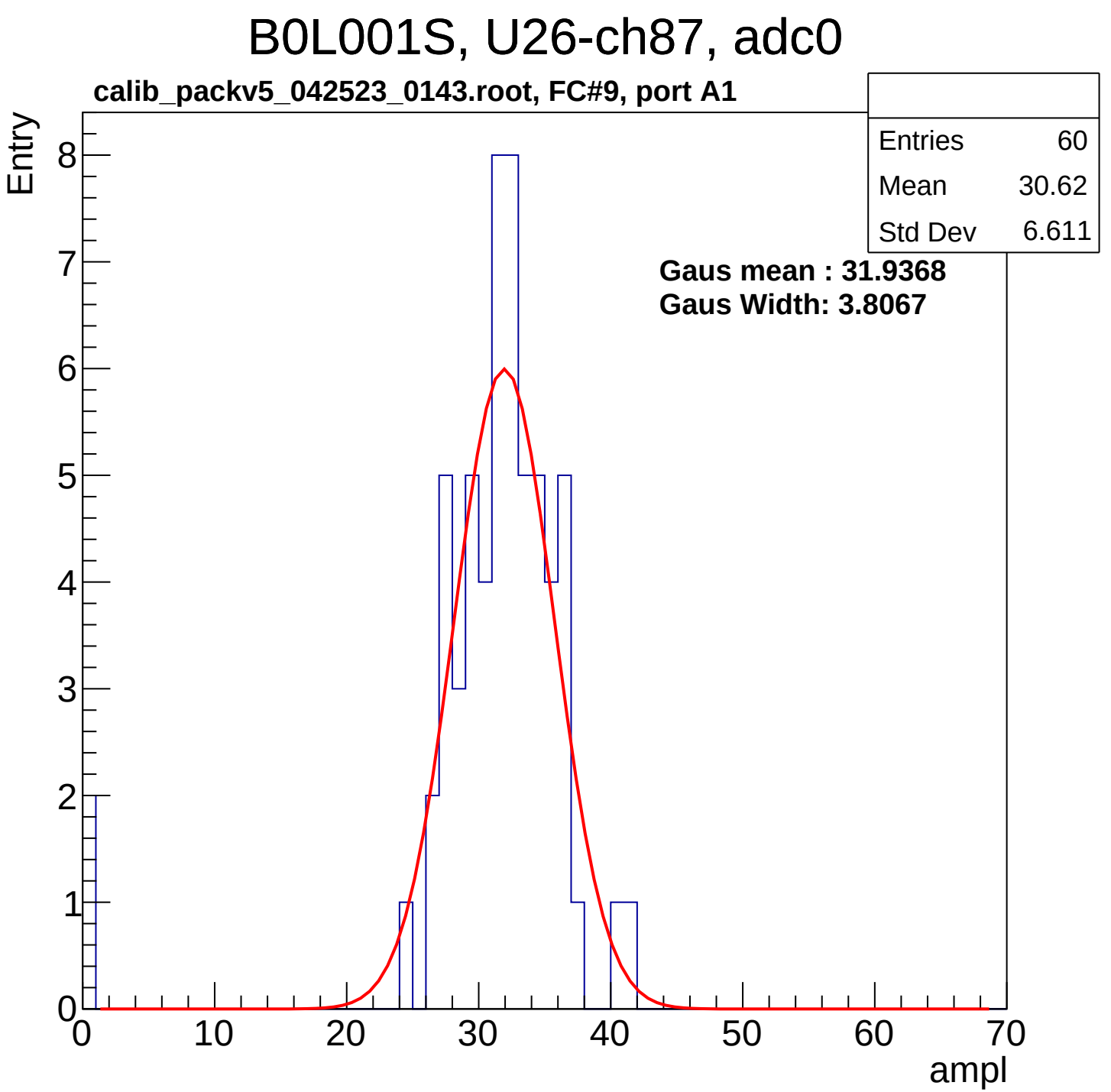

B0L001S, U26-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 56.49
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.93
Std Dev 11.08
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.62
Std Dev 6.611
Gaus mean : 31.9368
Gaus Width: 3.8067
Entry
ampl
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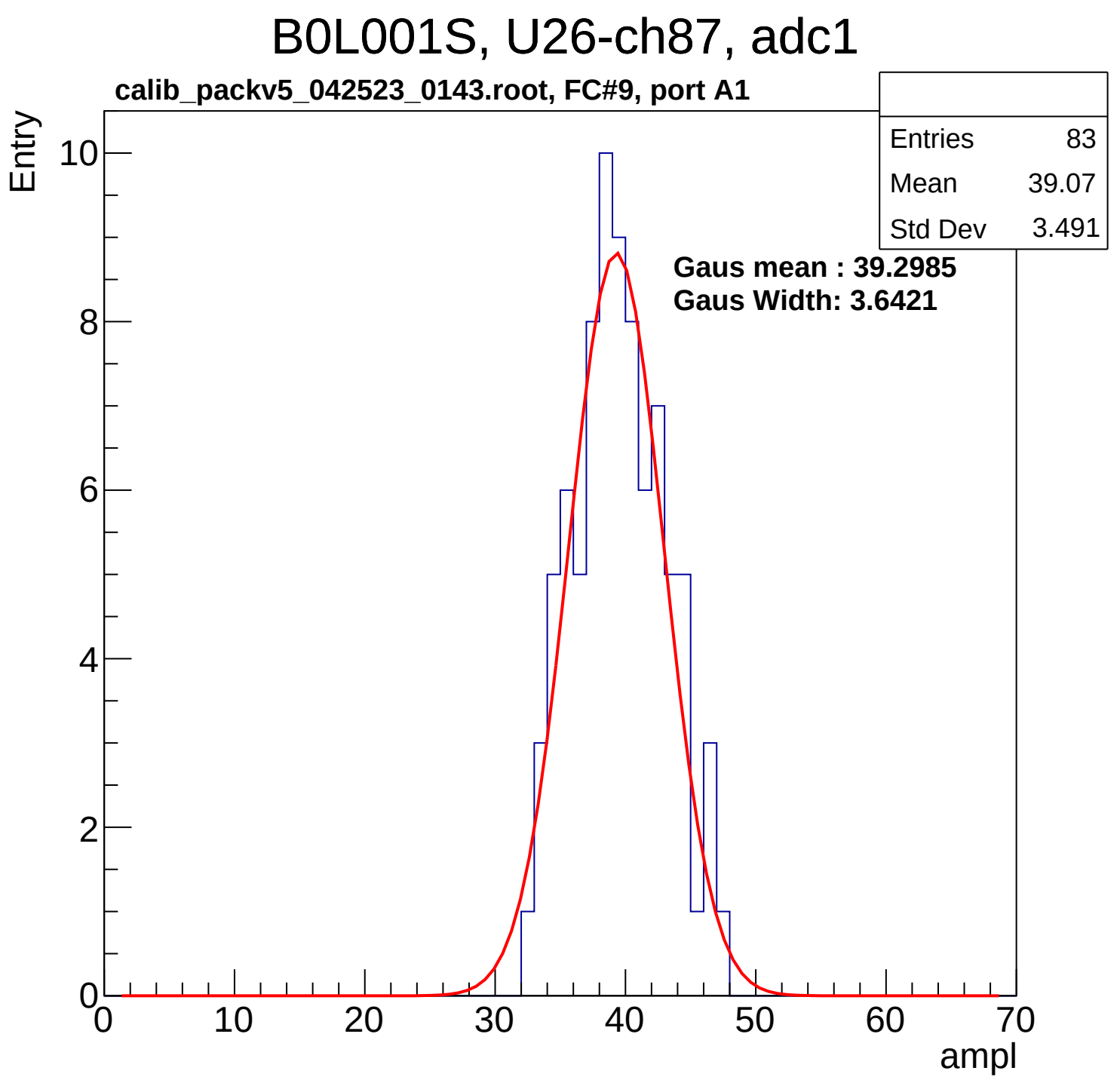
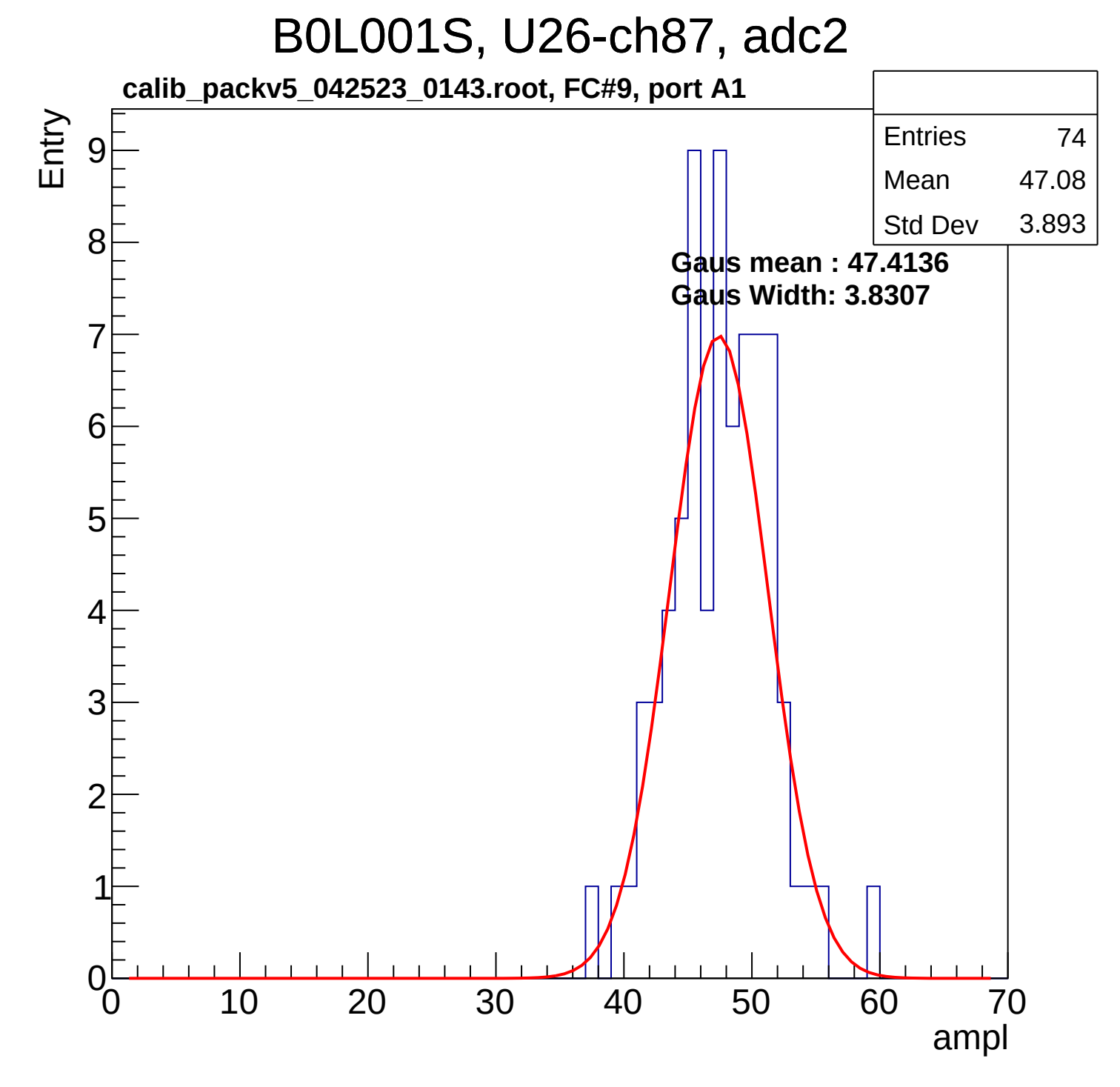
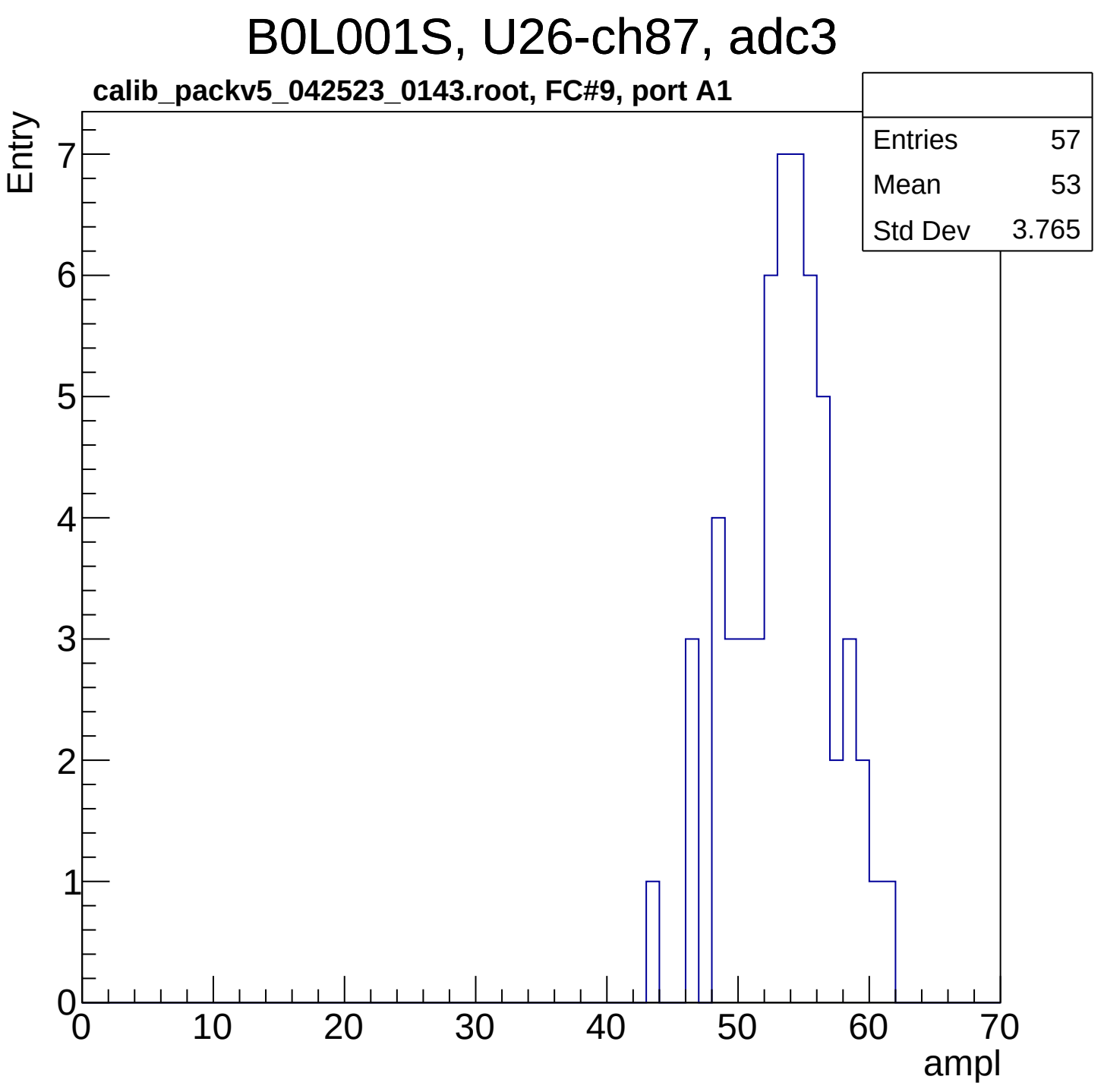
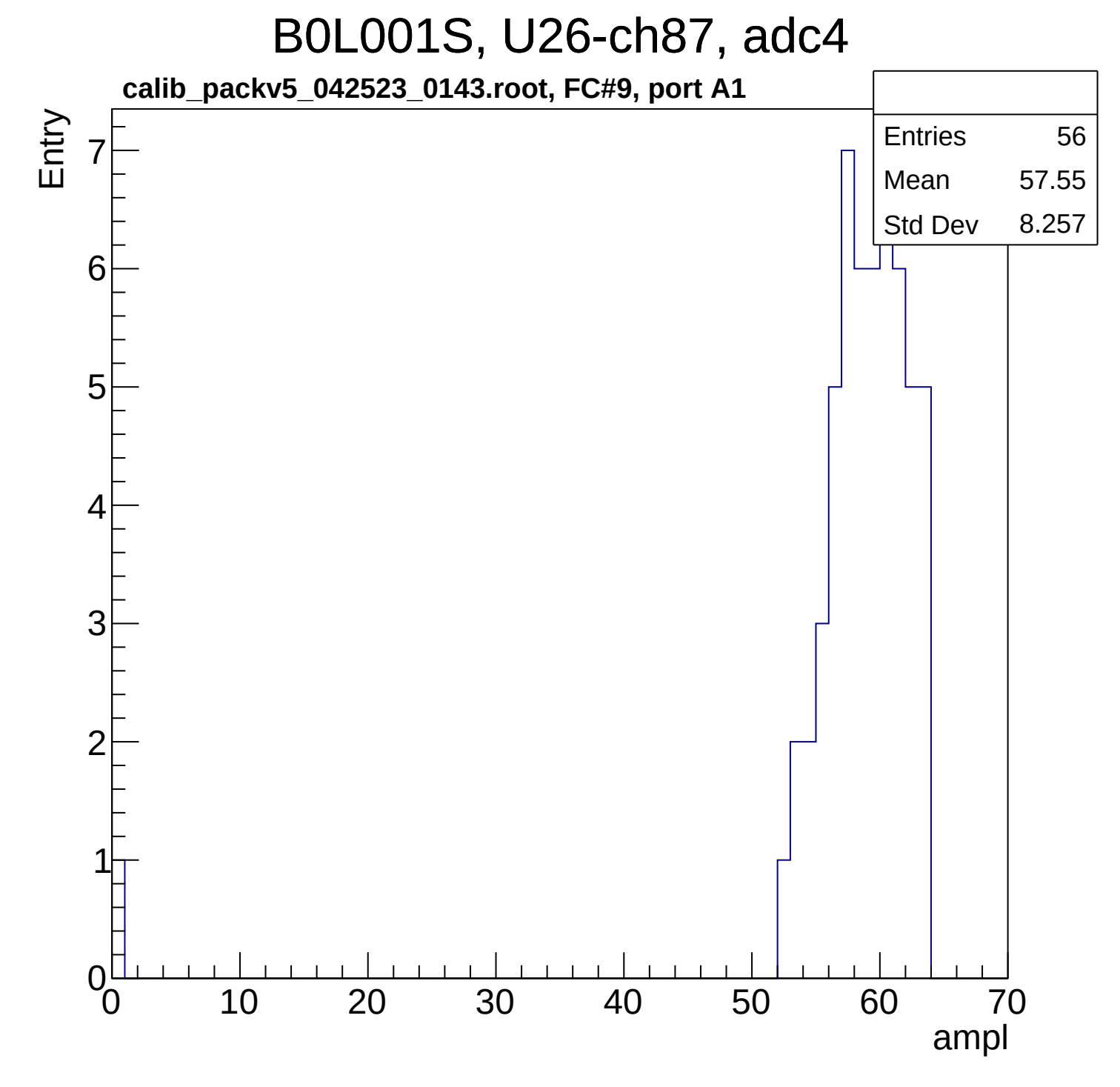
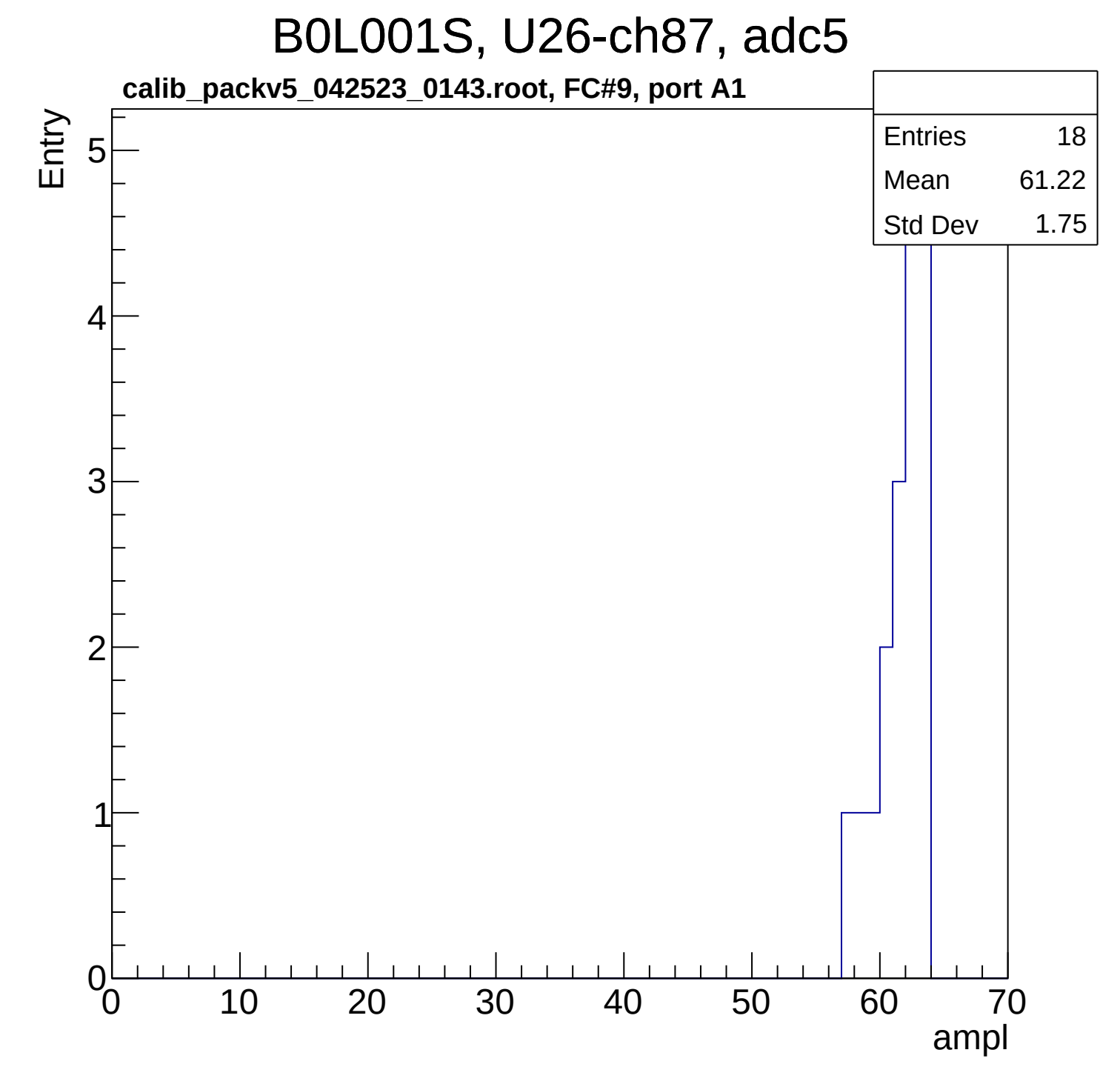
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

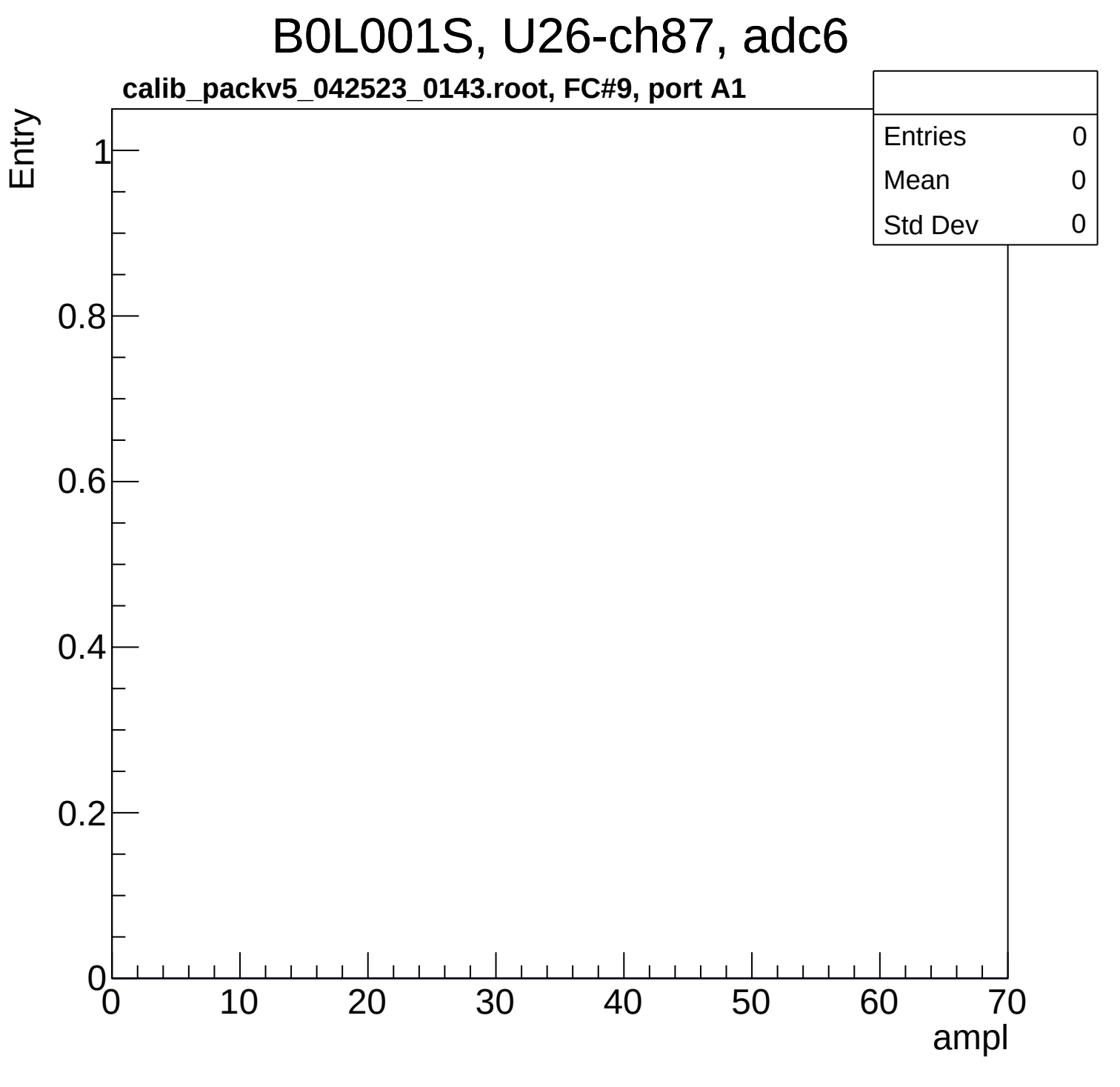

B0L001S, U26-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 39.07
Std Dev 3.491
Gaus mean : 39.2985
Gaus Width: 3.6421
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

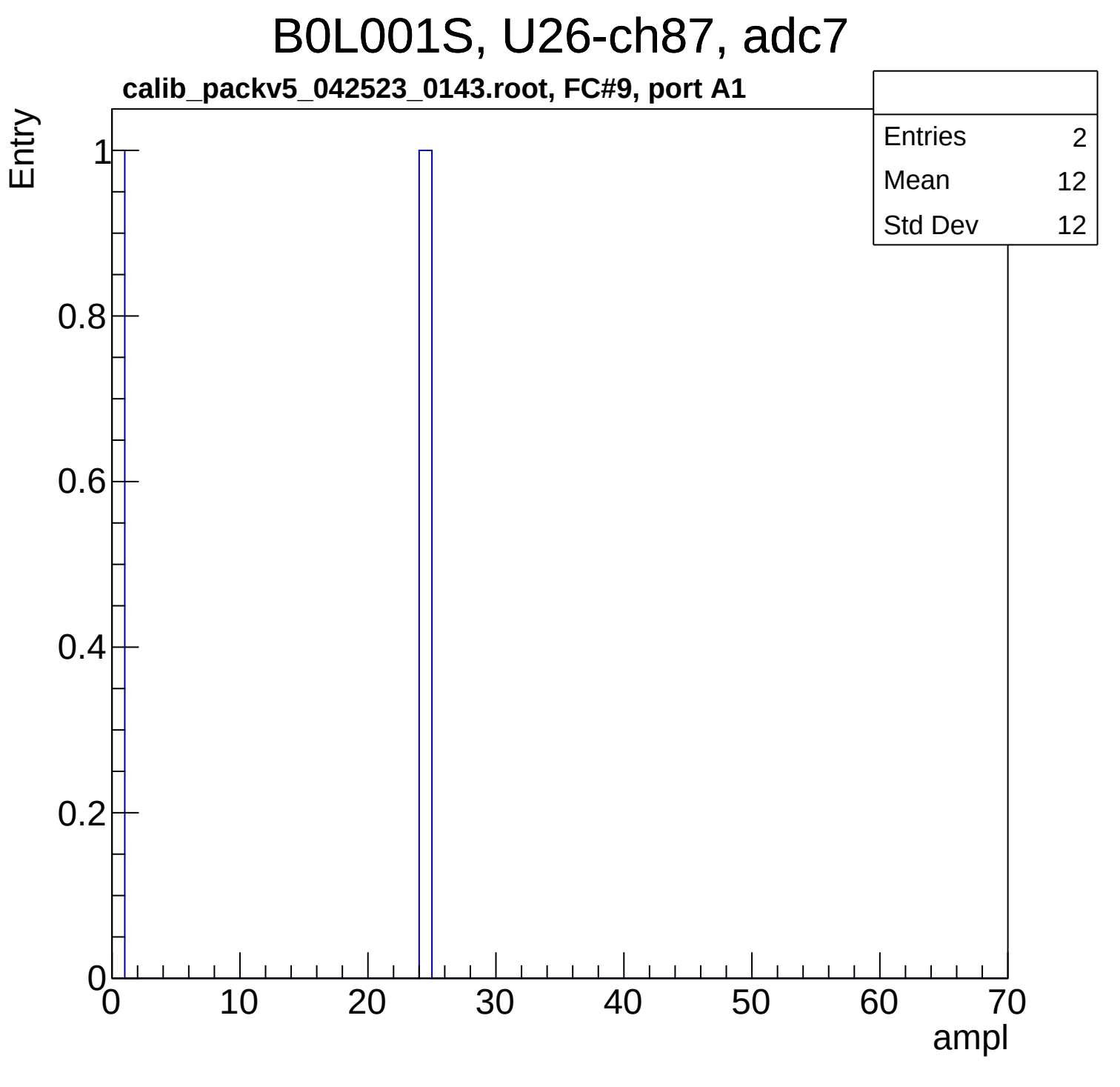

B0L001S, U26-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 47.08
Std Dev 3.893
Gaus mean : 47.4136
Gaus Width: 3.8307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

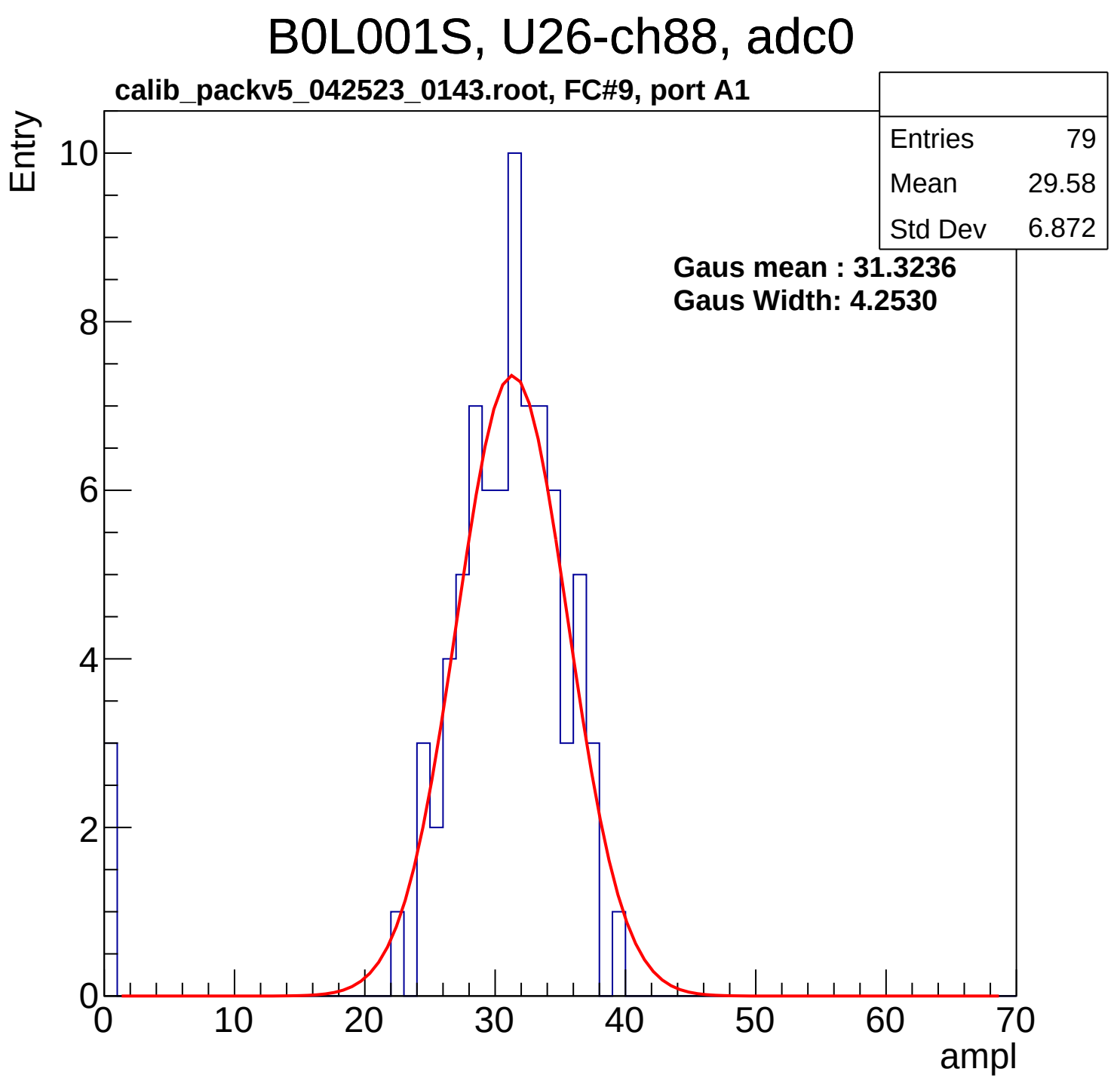

B0L001S, U26-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 53
Std Dev 3.765
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

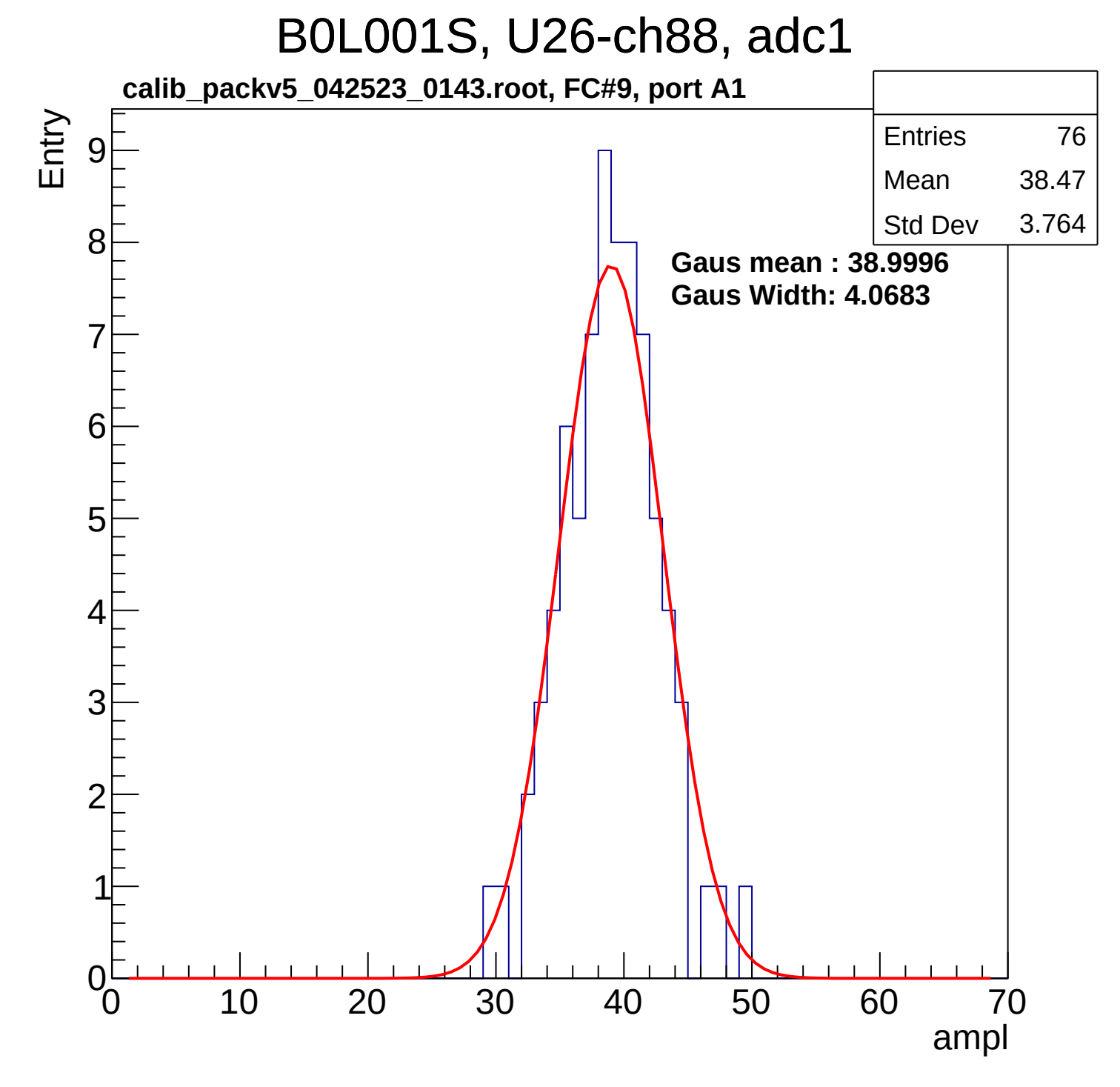

B0L001S, U26-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.55
Std Dev 8.257
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

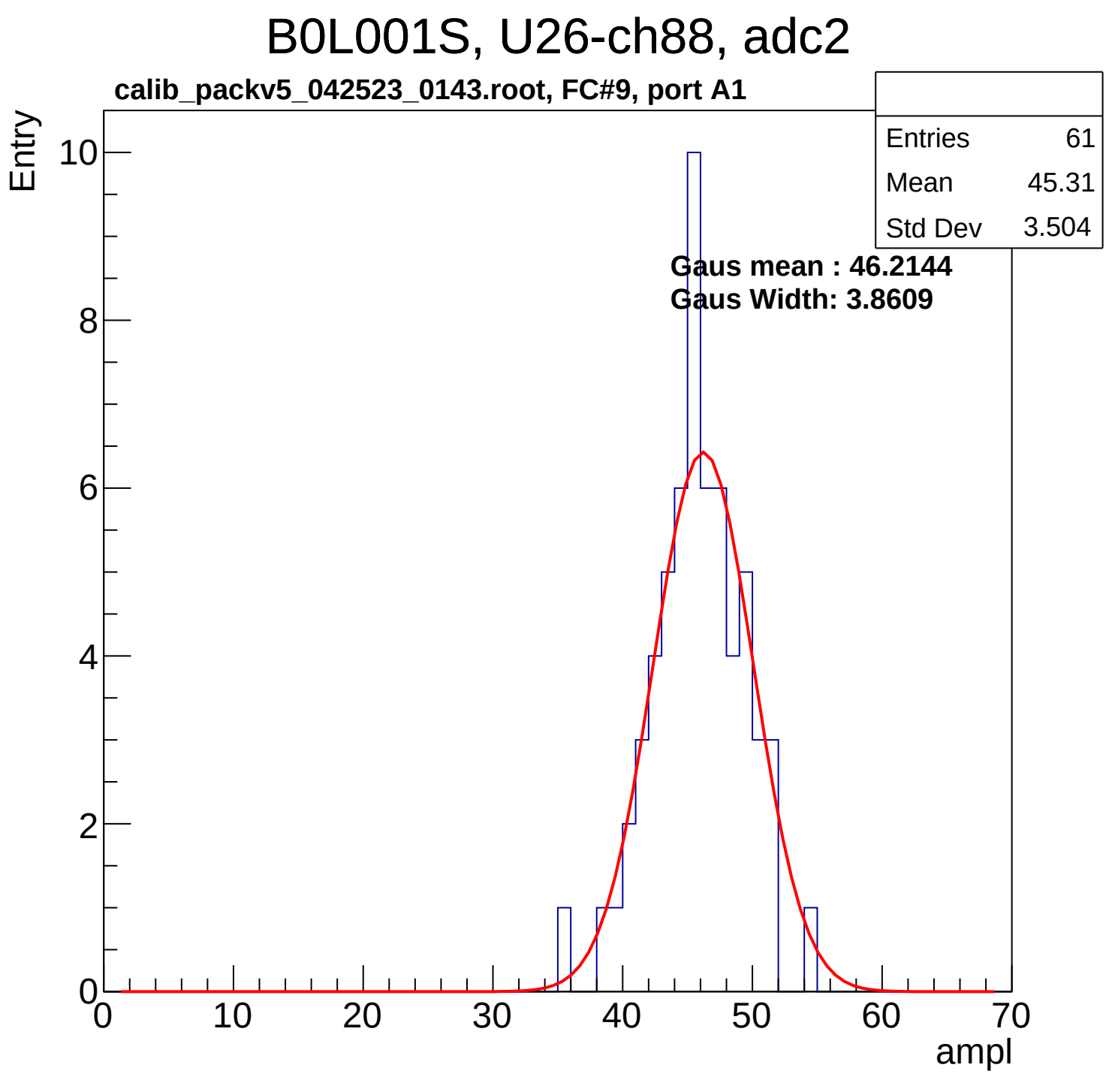

B0L001S, U26-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 61.22
Std Dev 1.75
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

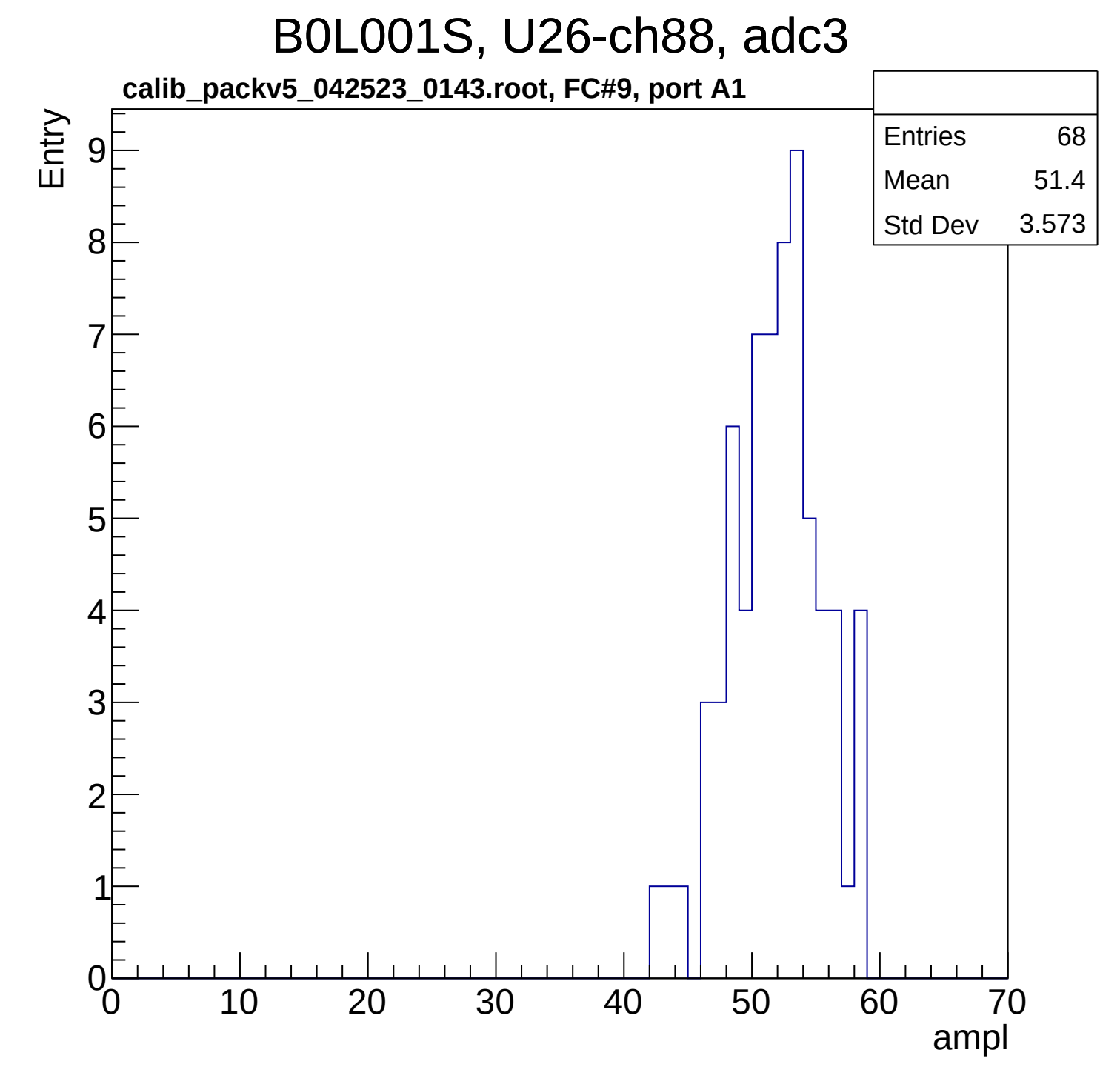

B0L001S, U26-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 29.58
Std Dev 6.872
Gaus mean : 31.3236
Gaus Width: 4.2530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.47
Std Dev 3.764
Gaus mean : 38.9996
Gaus Width: 4.0683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.31
Std Dev 3.504
Gaus mean : 46.2144
Gaus Width: 3.8609
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.4
Std Dev 3.573
Entry
ampl
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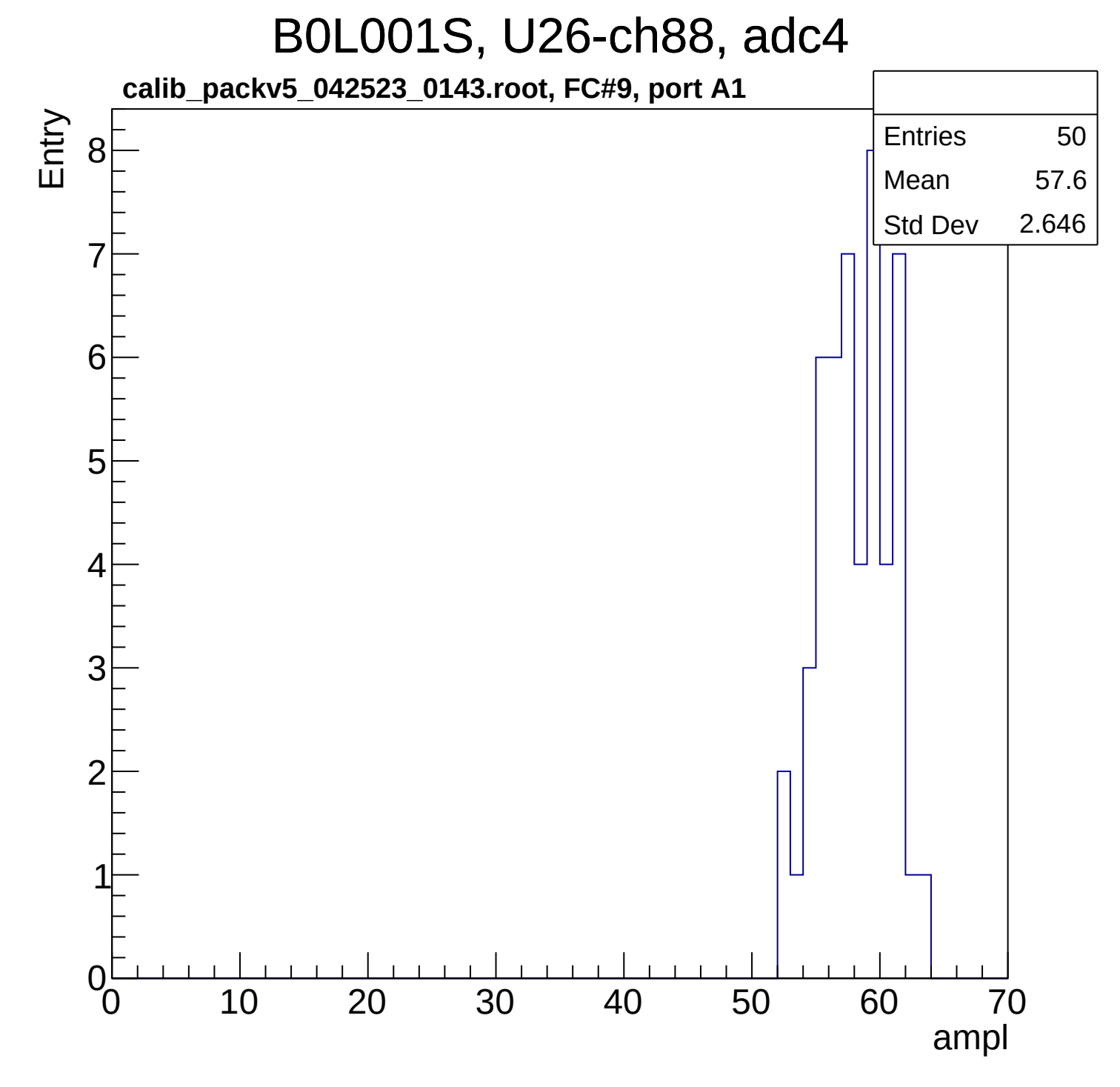
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.6
Std Dev 2.646
Entry
ampl
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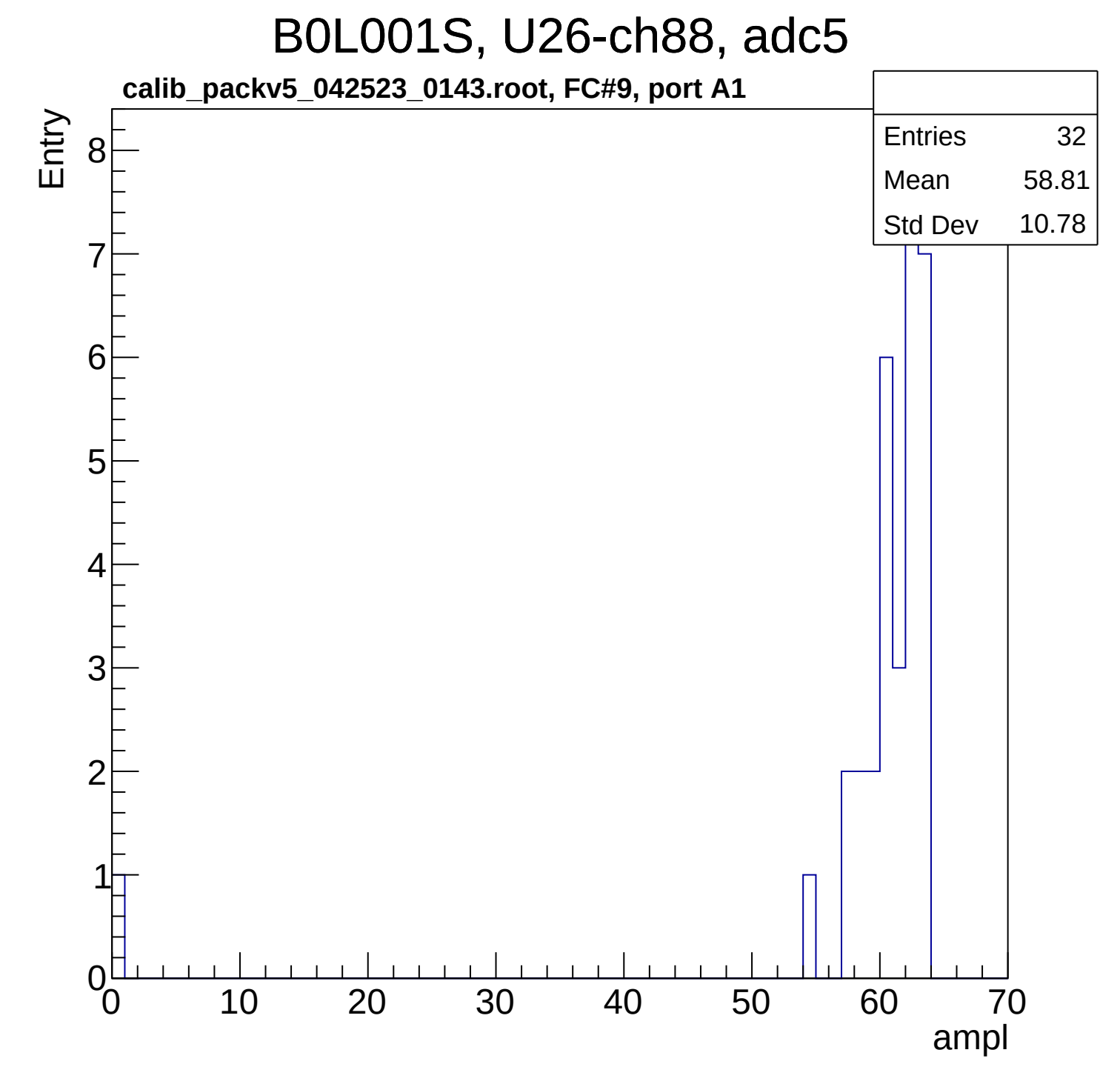
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.81
Std Dev 10.78
Entry
ampl
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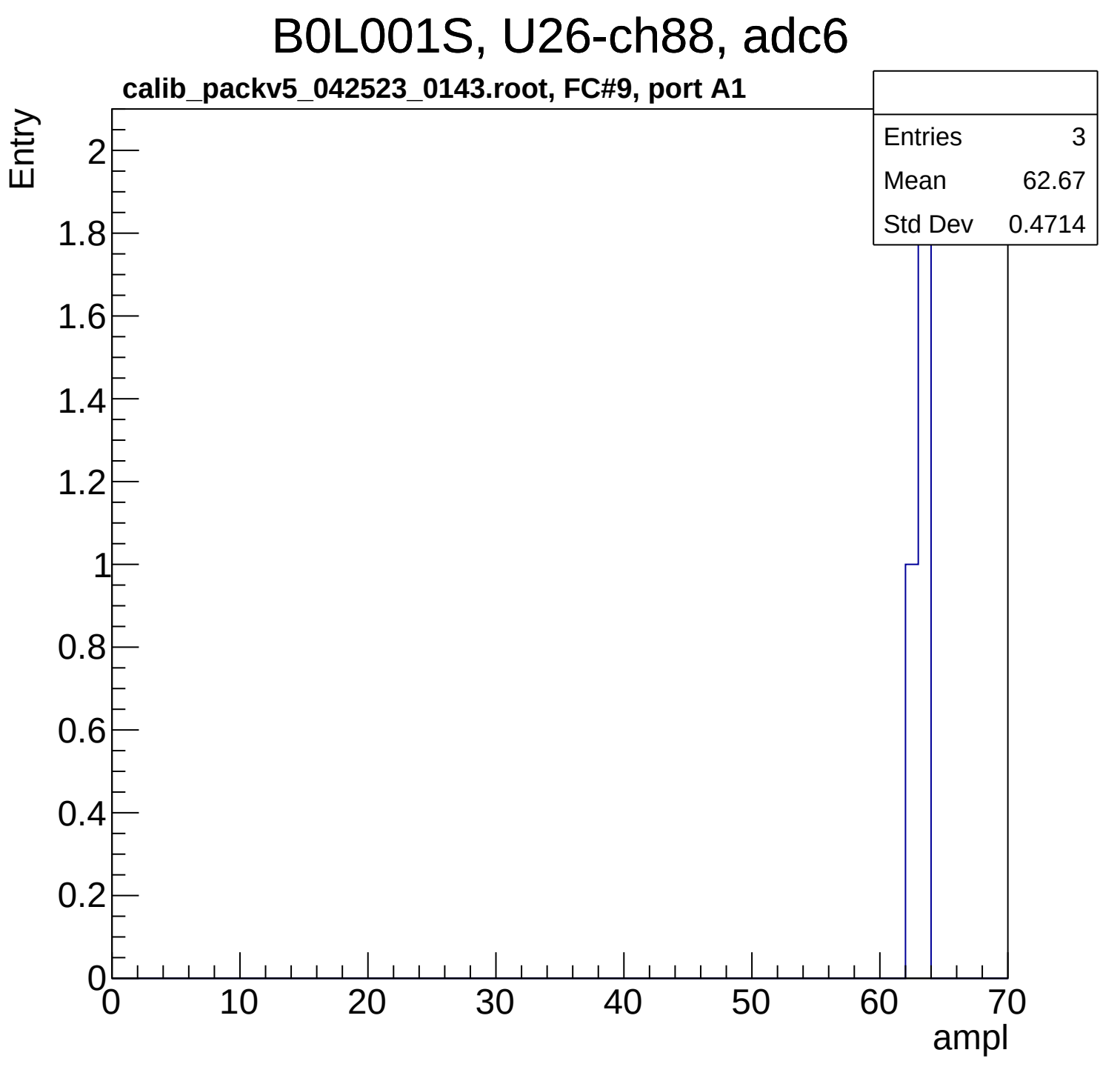
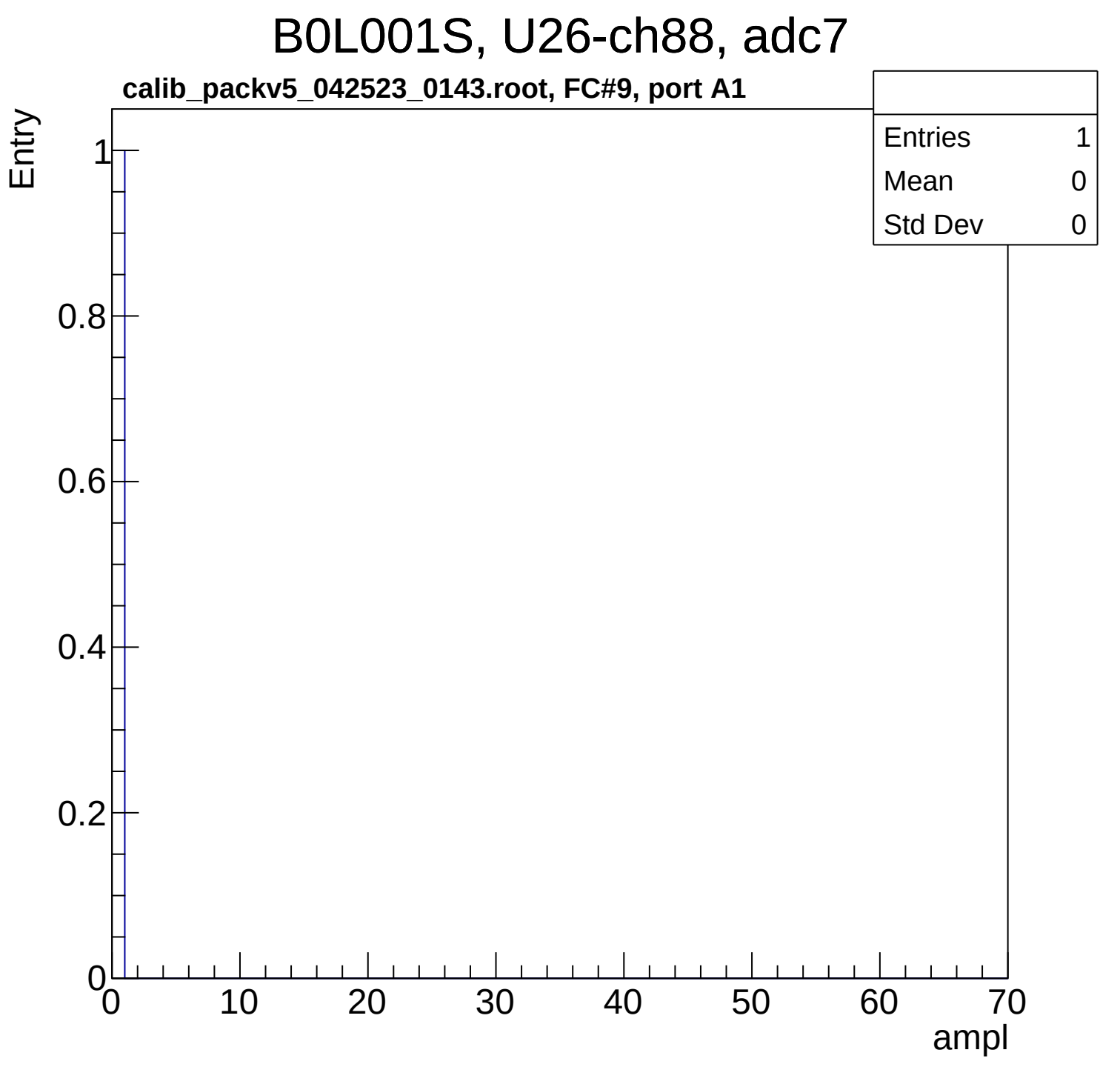
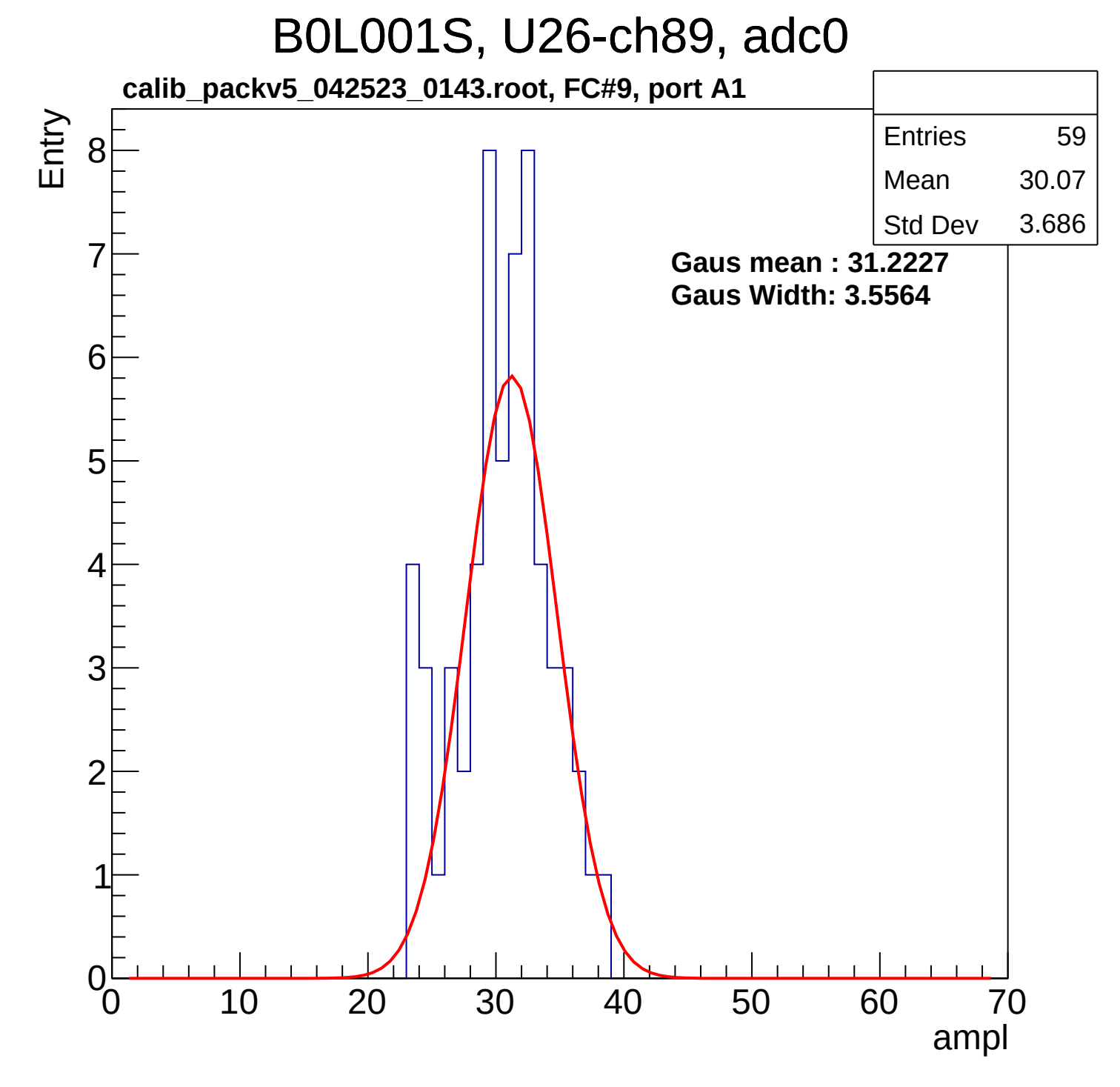
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.07
Std Dev 3.686
Gaus mean : 31.2227
Gaus Width: 3.5564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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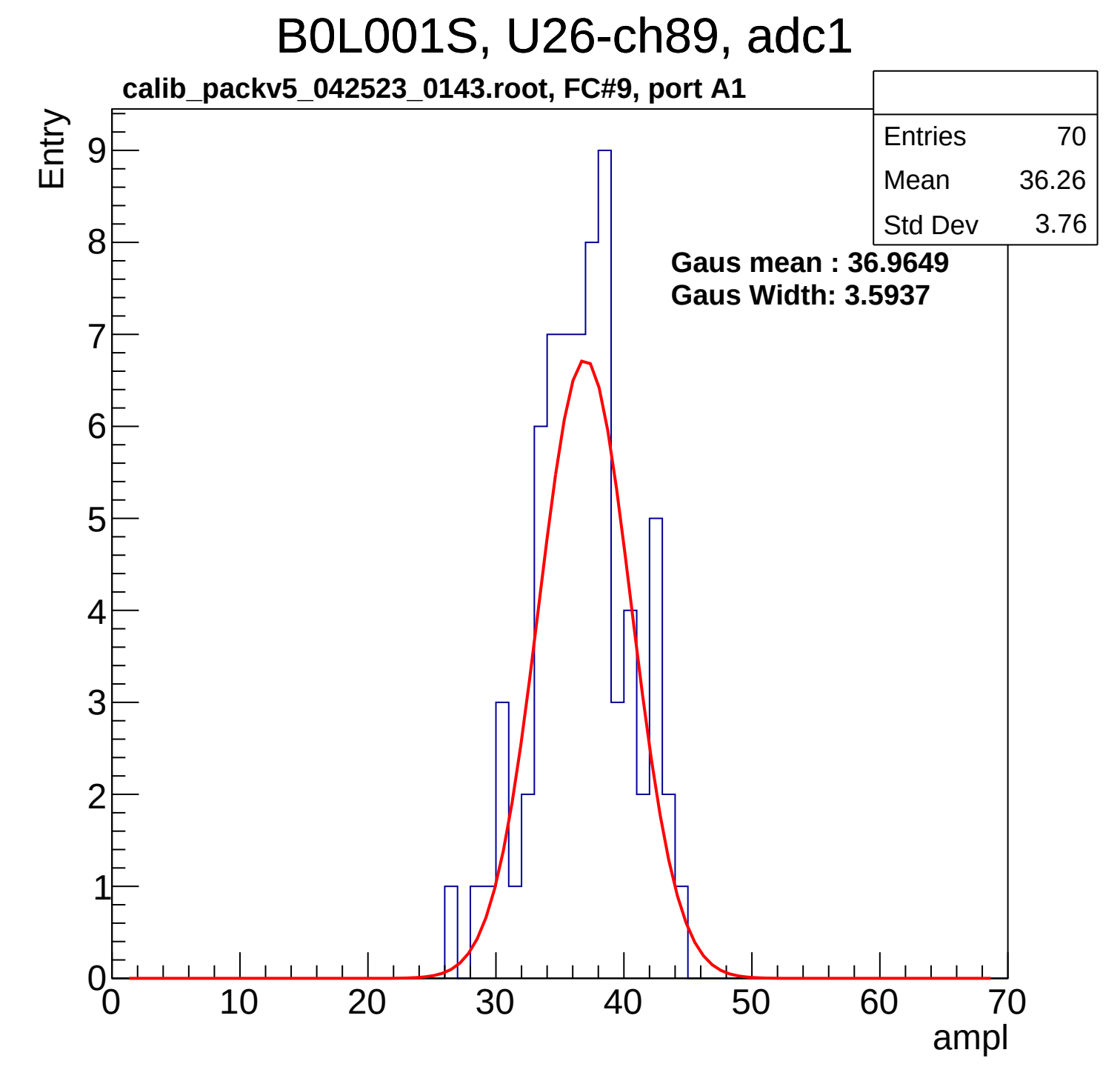
B0L001S, U26-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.26
Std Dev 3.76
Gaus mean : 36.9649
Gaus Width: 3.5937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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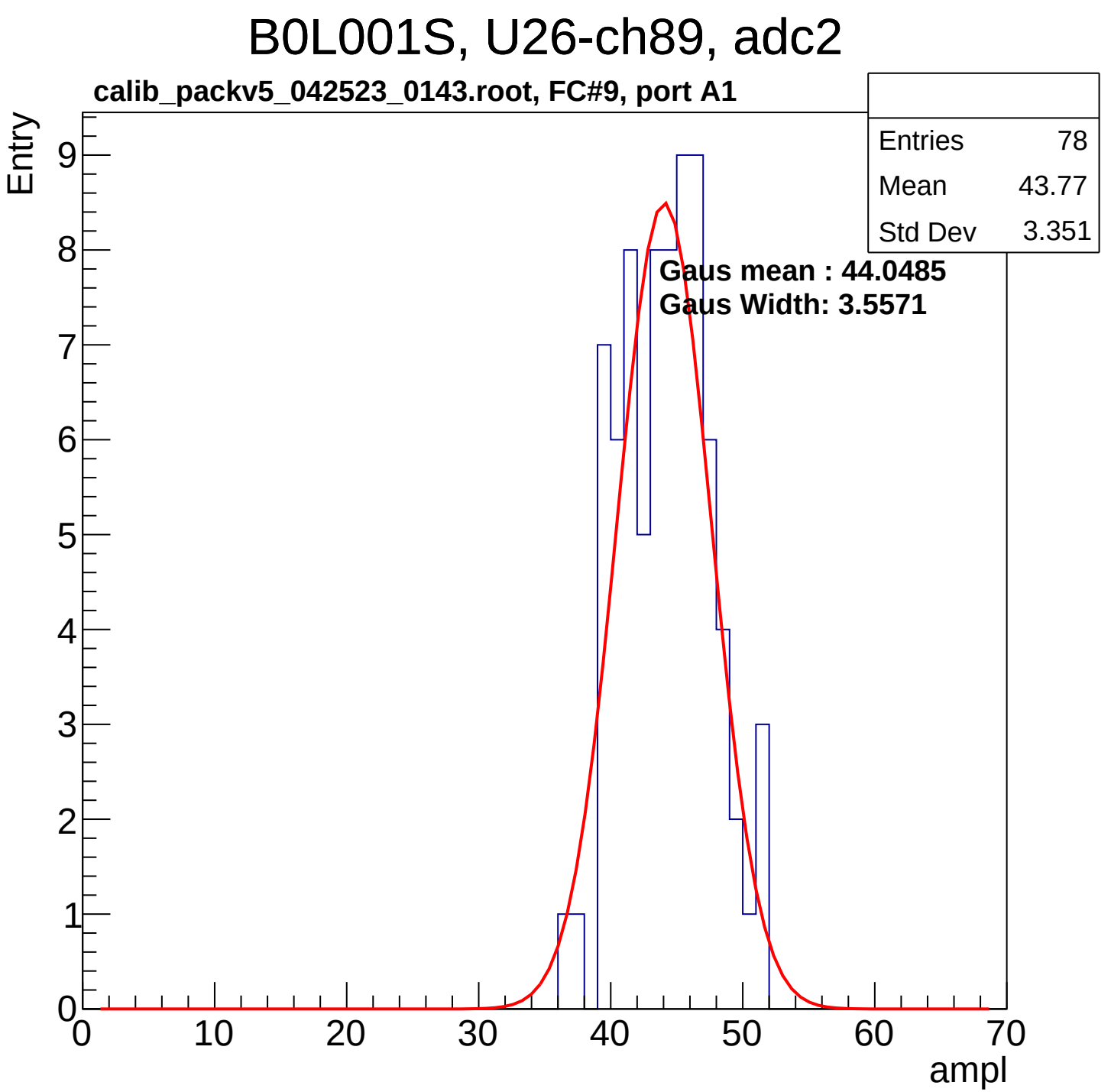
B0L001S, U26-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.77
Std Dev 3.351
Gaus mean : 44.0485
Gaus Width: 3.5571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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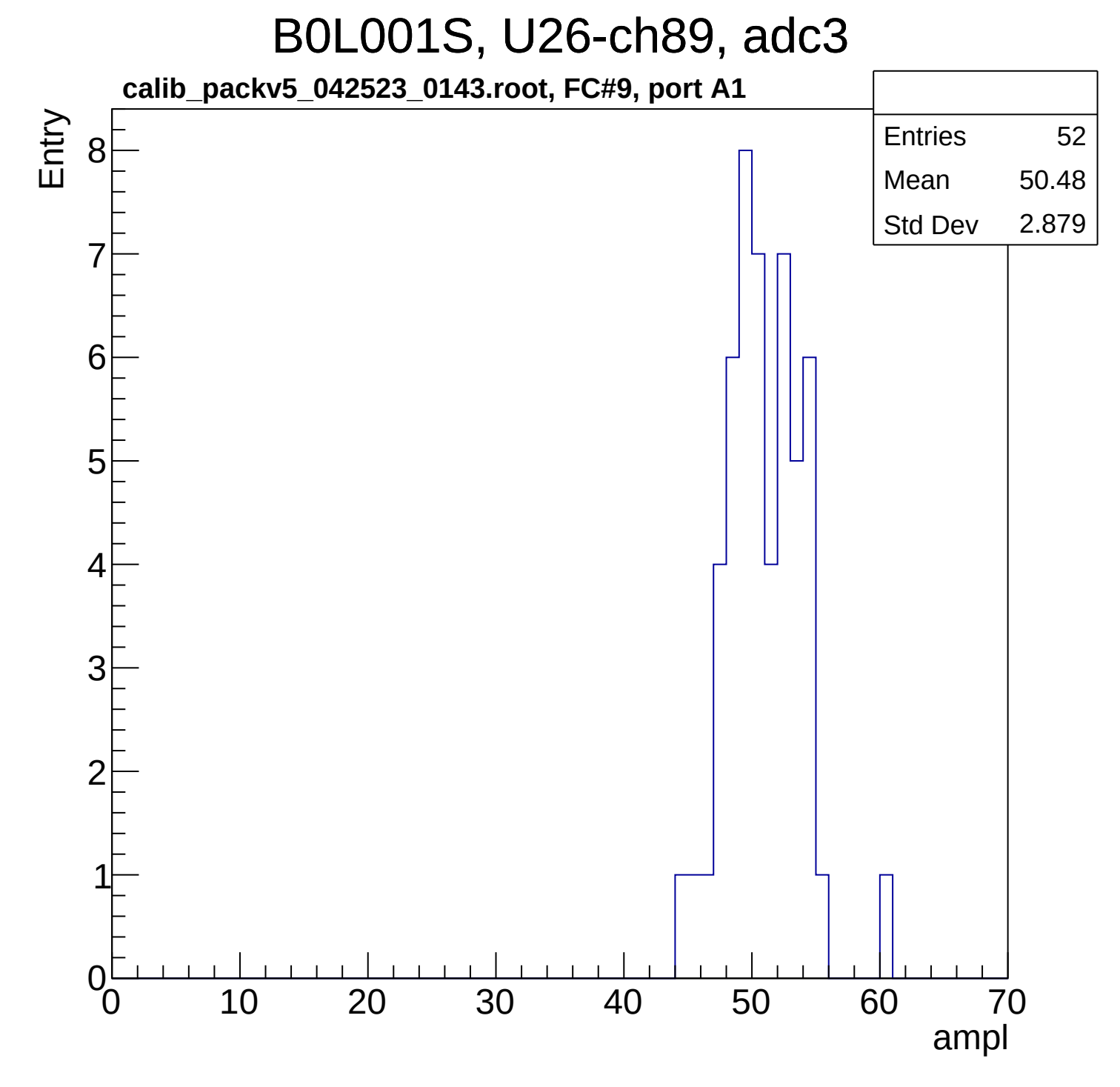
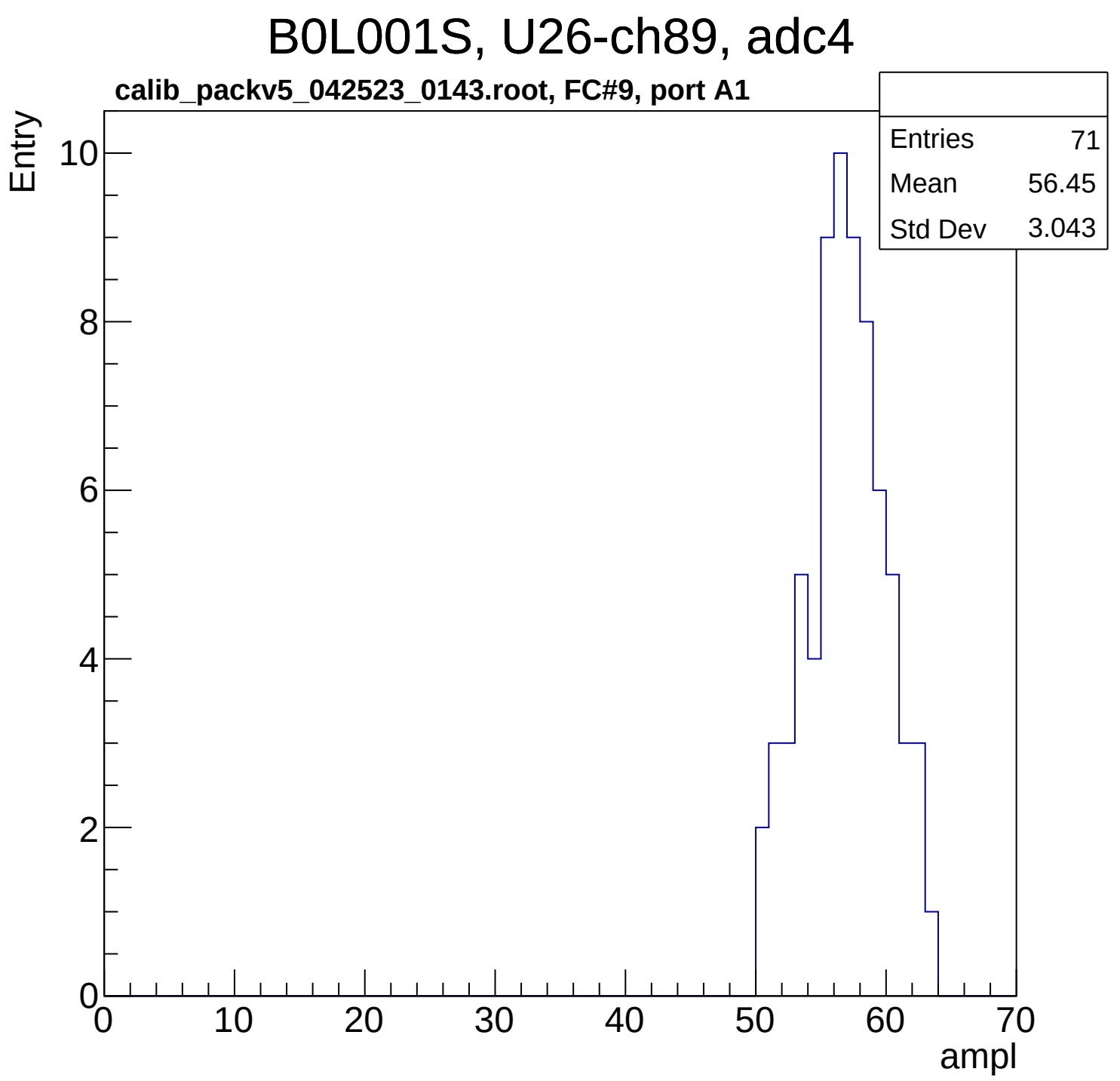
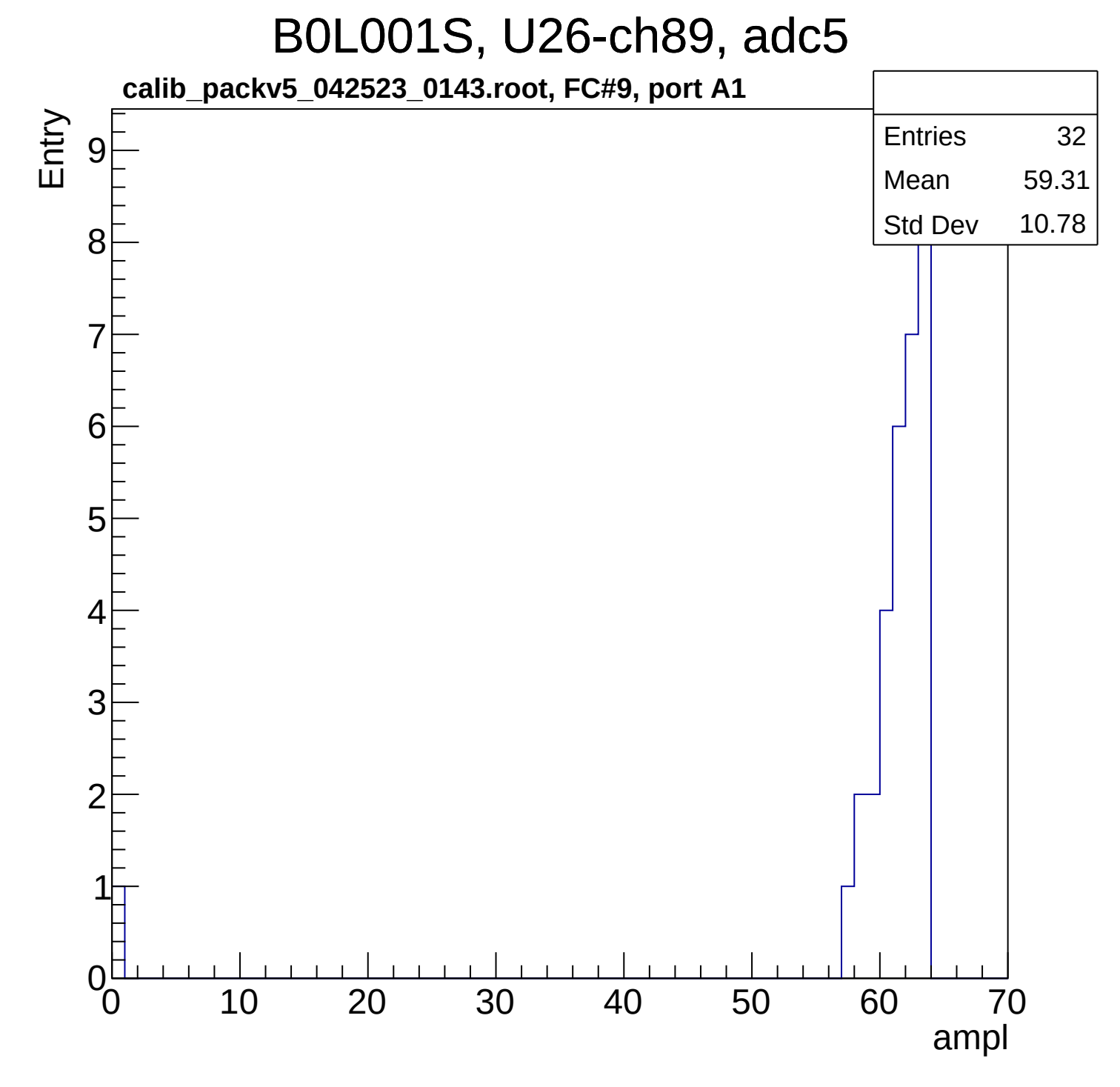

B0L001S, U26-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.48
Std Dev 2.879
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 56.45
Std Dev 3.043
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.31
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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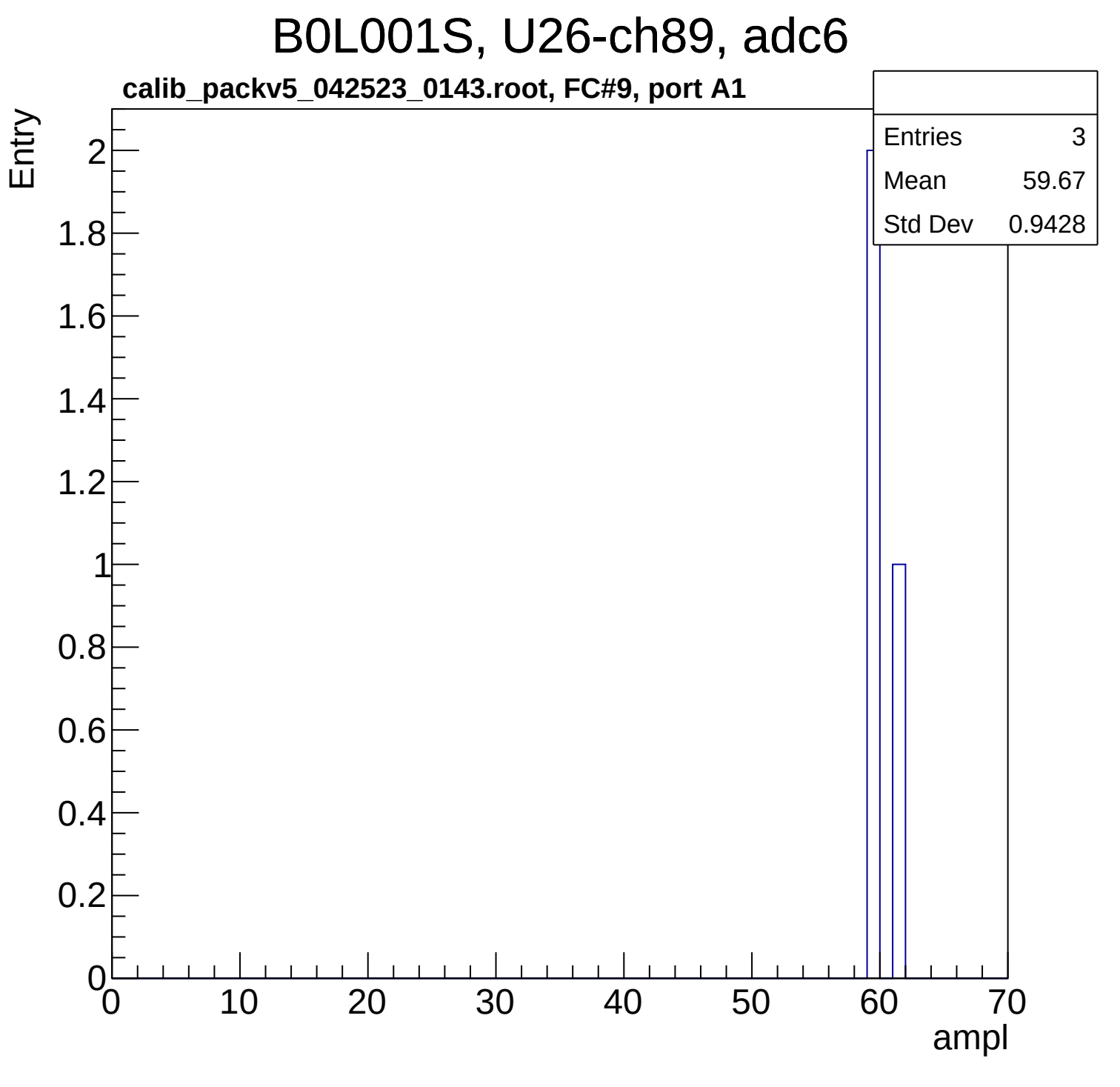
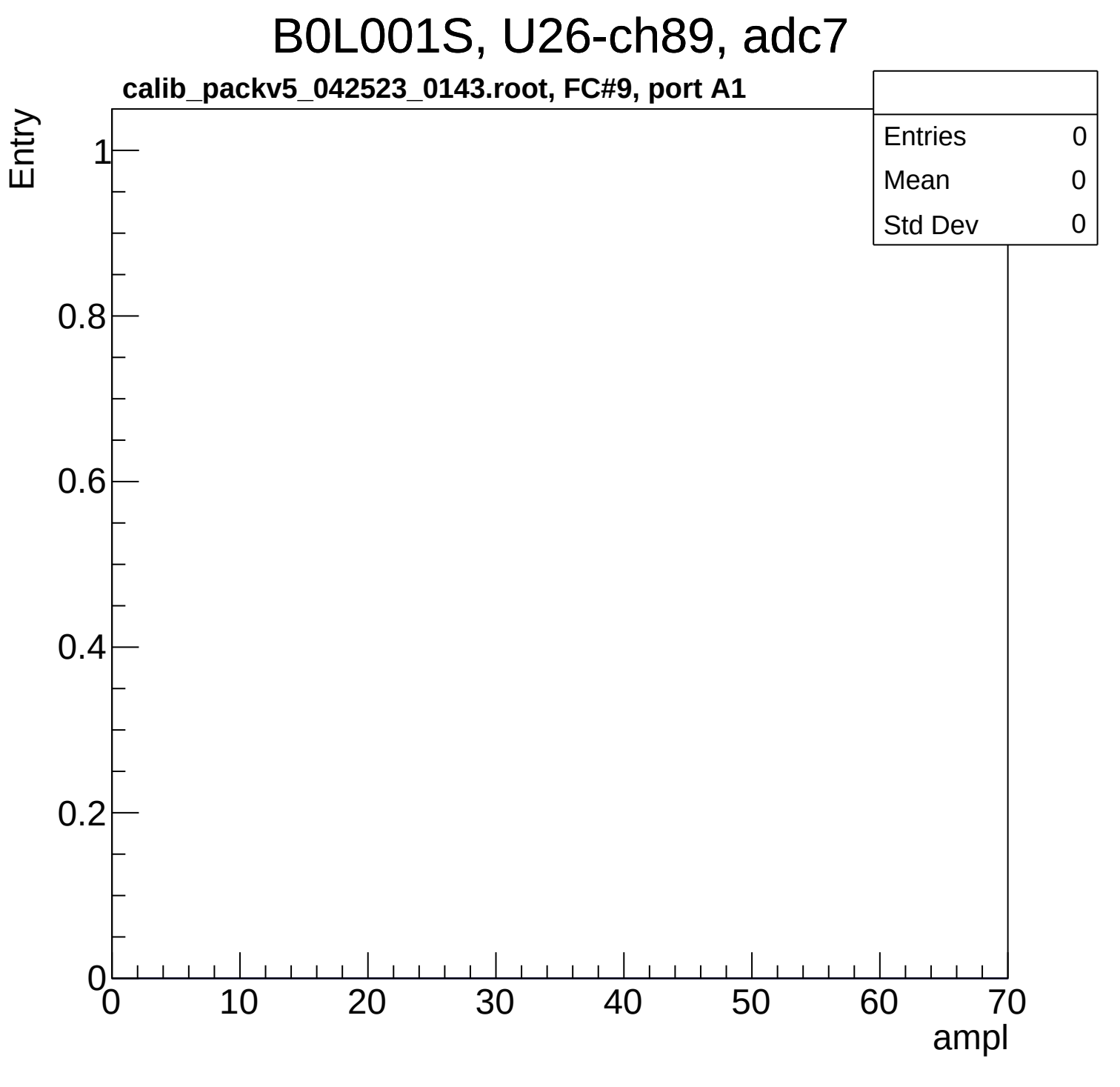
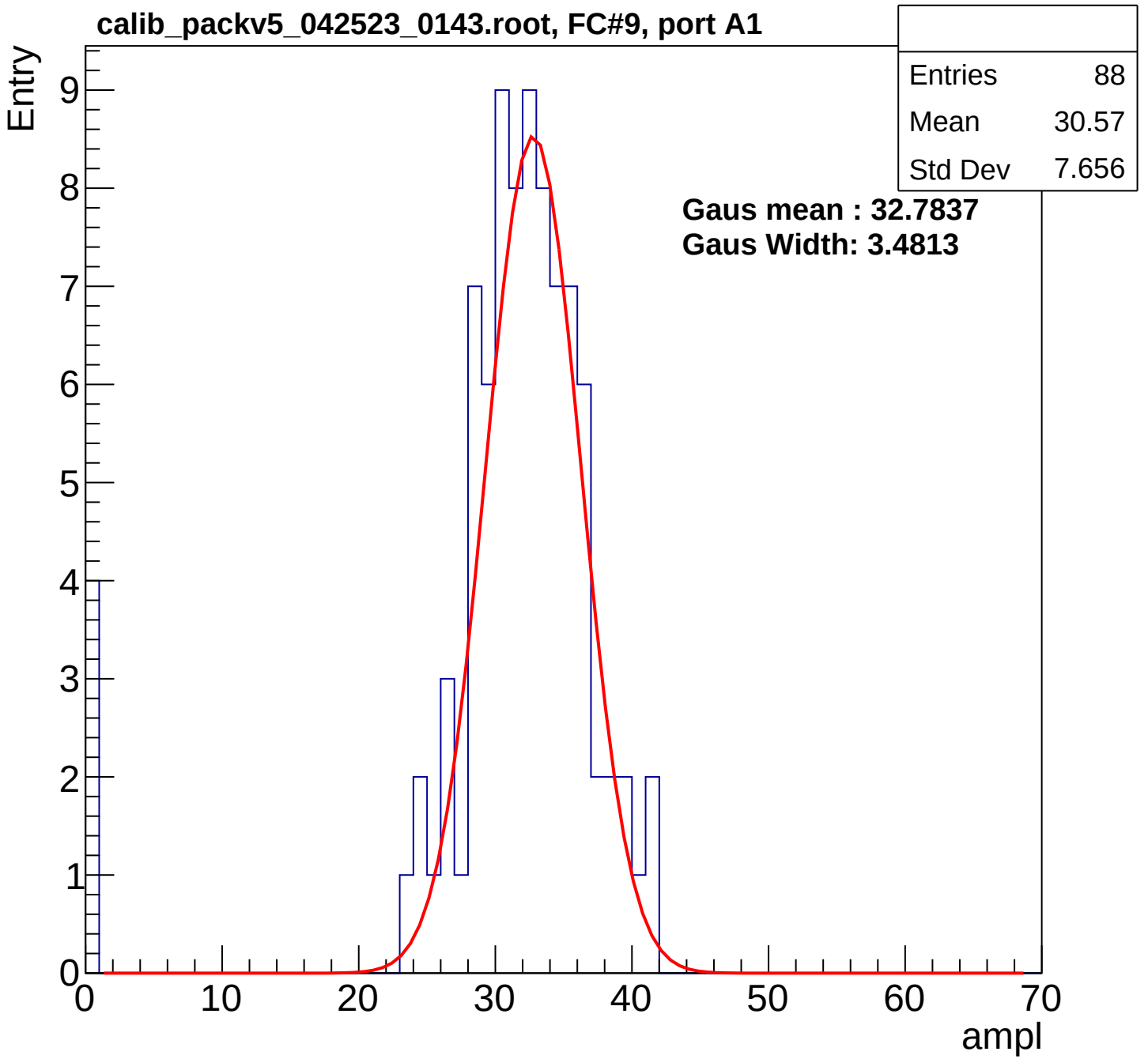
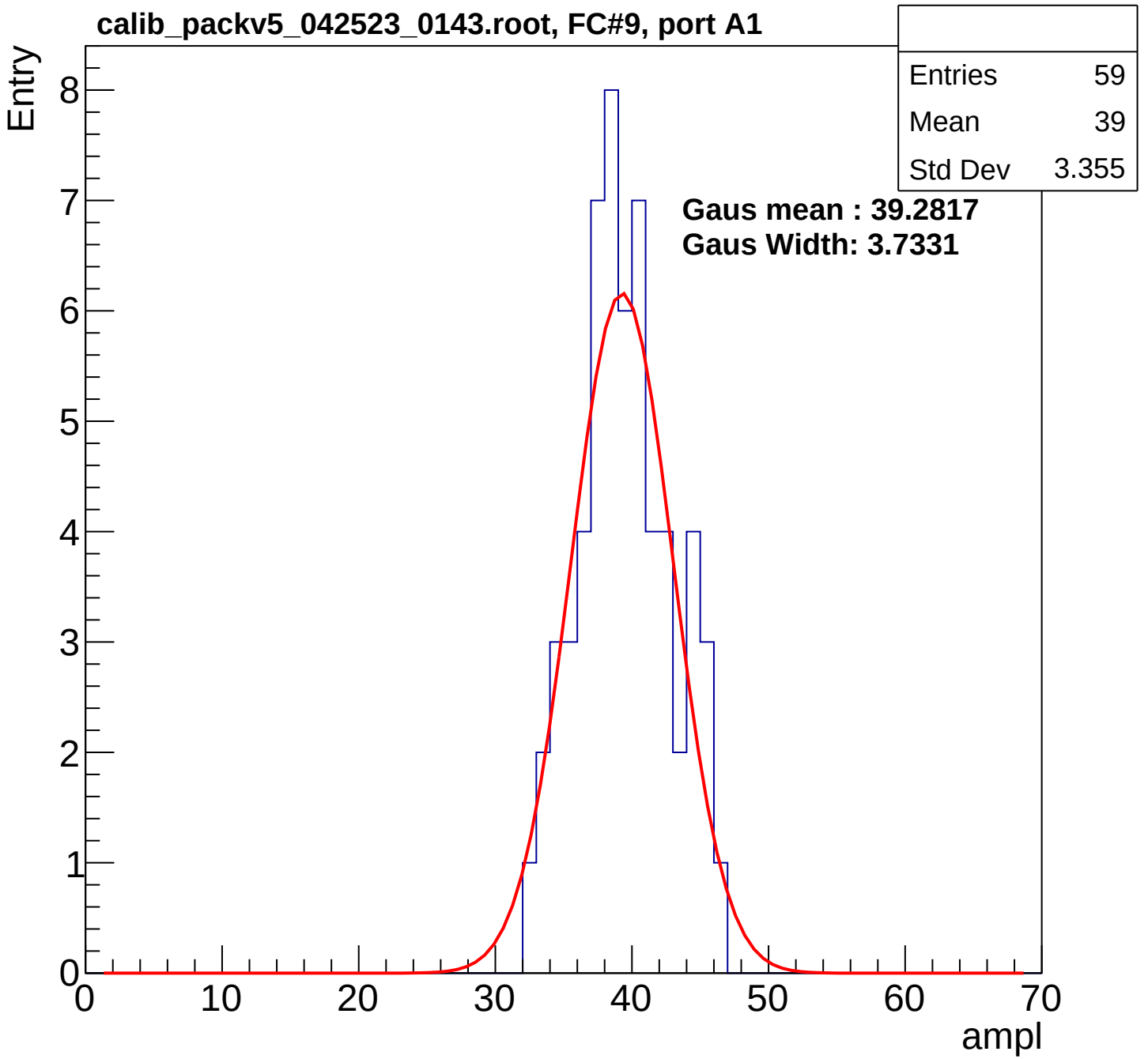
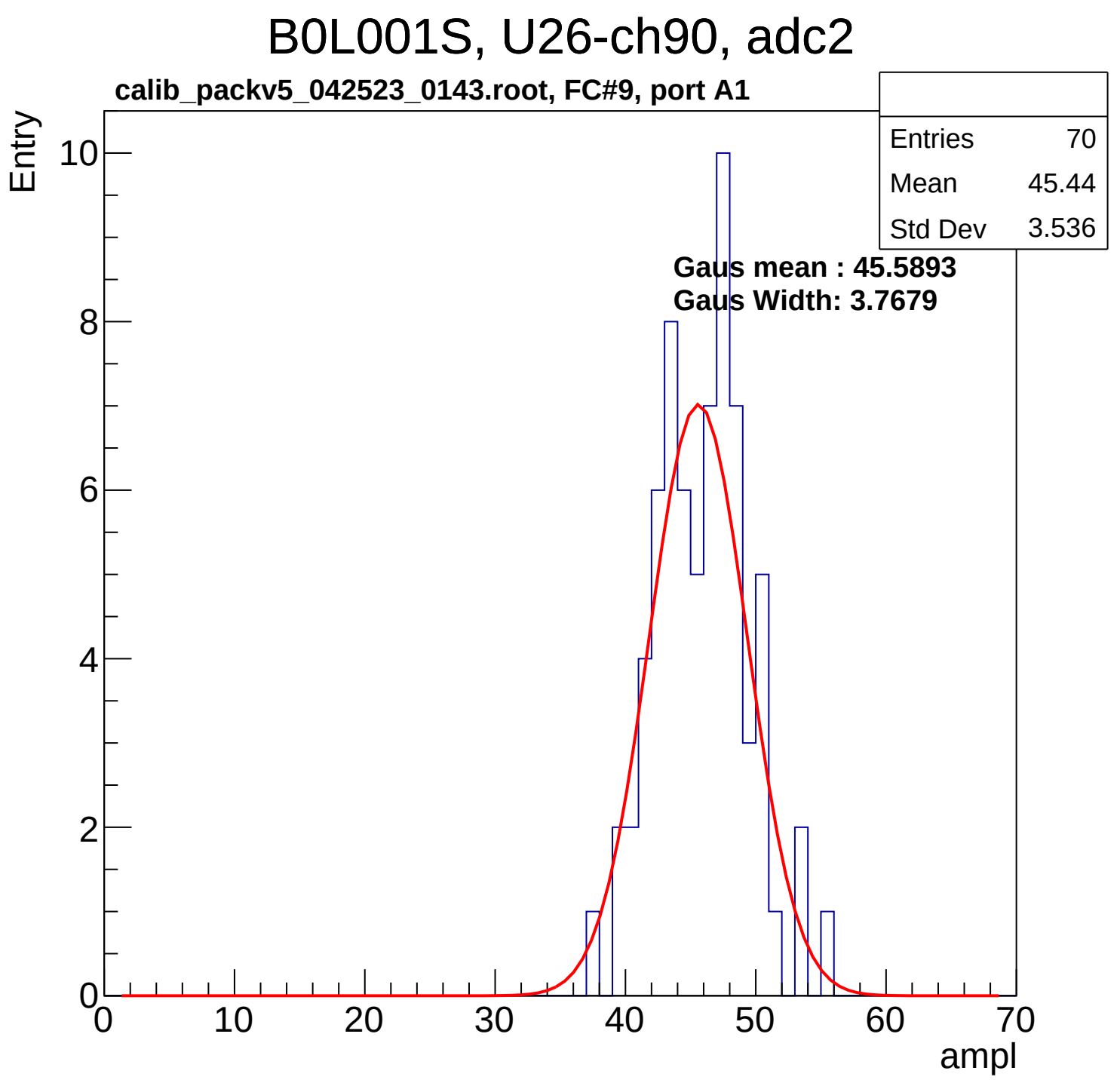
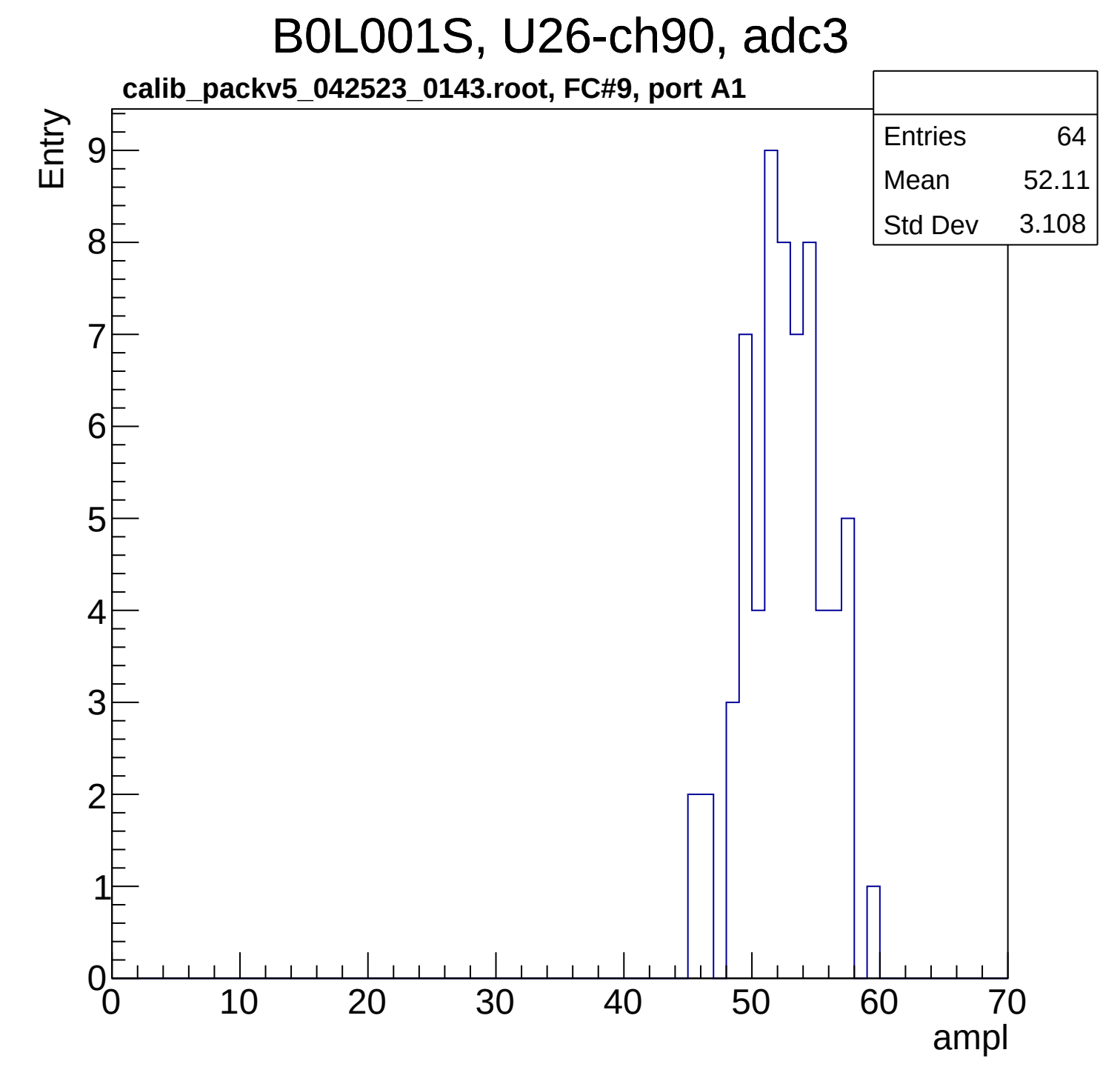

B0L001S, U26-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 59.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 30.57
Std Dev 7.656
Gaus mean : 32.7837
Gaus Width: 3.4813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 39
Std Dev 3.355
Gaus mean : 39.2817
Gaus Width: 3.7331
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 45.44
Std Dev 3.536
Gaus mean : 45.5893
Gaus Width: 3.7679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.11
Std Dev 3.108
Entry
ampl
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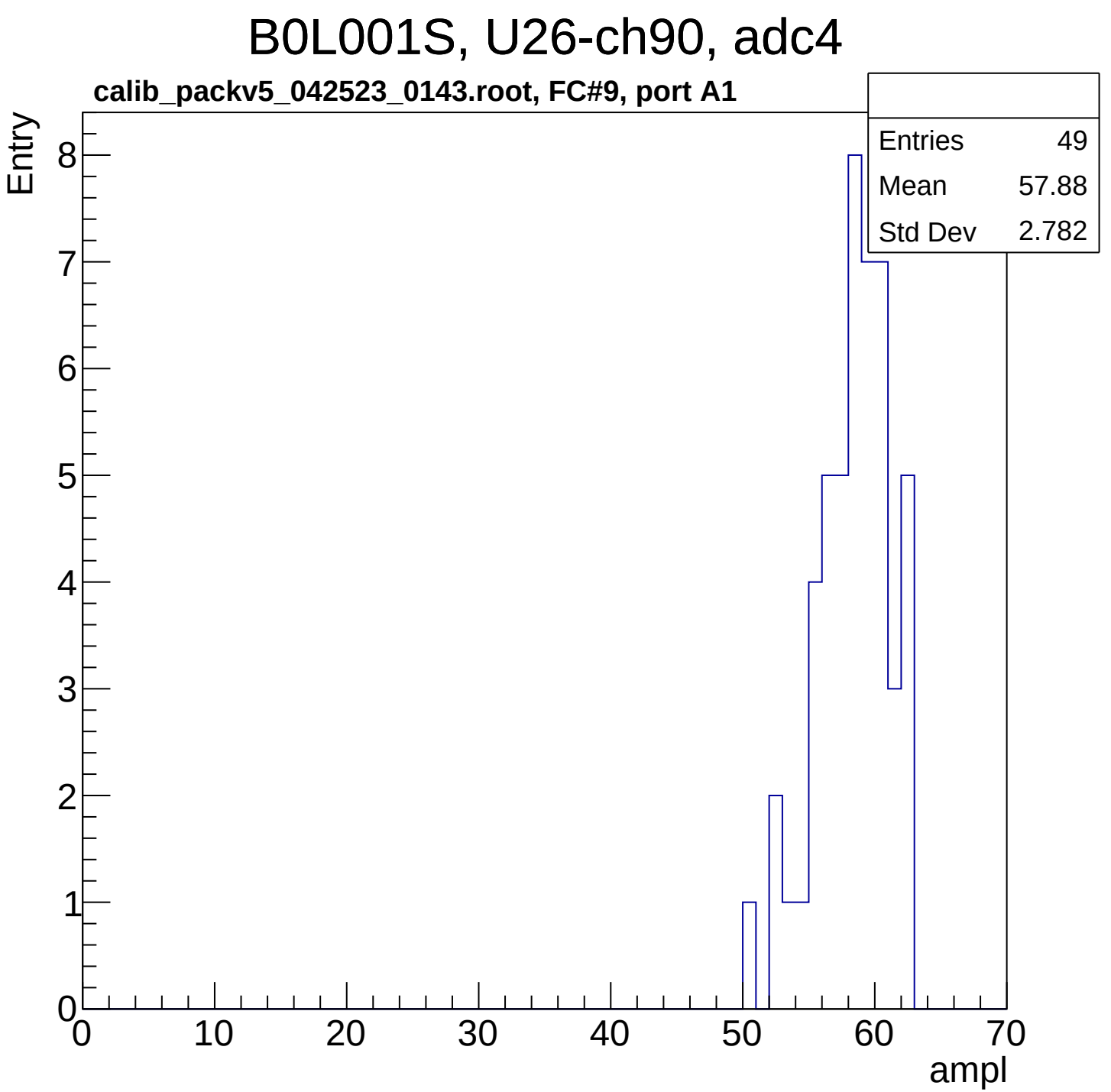
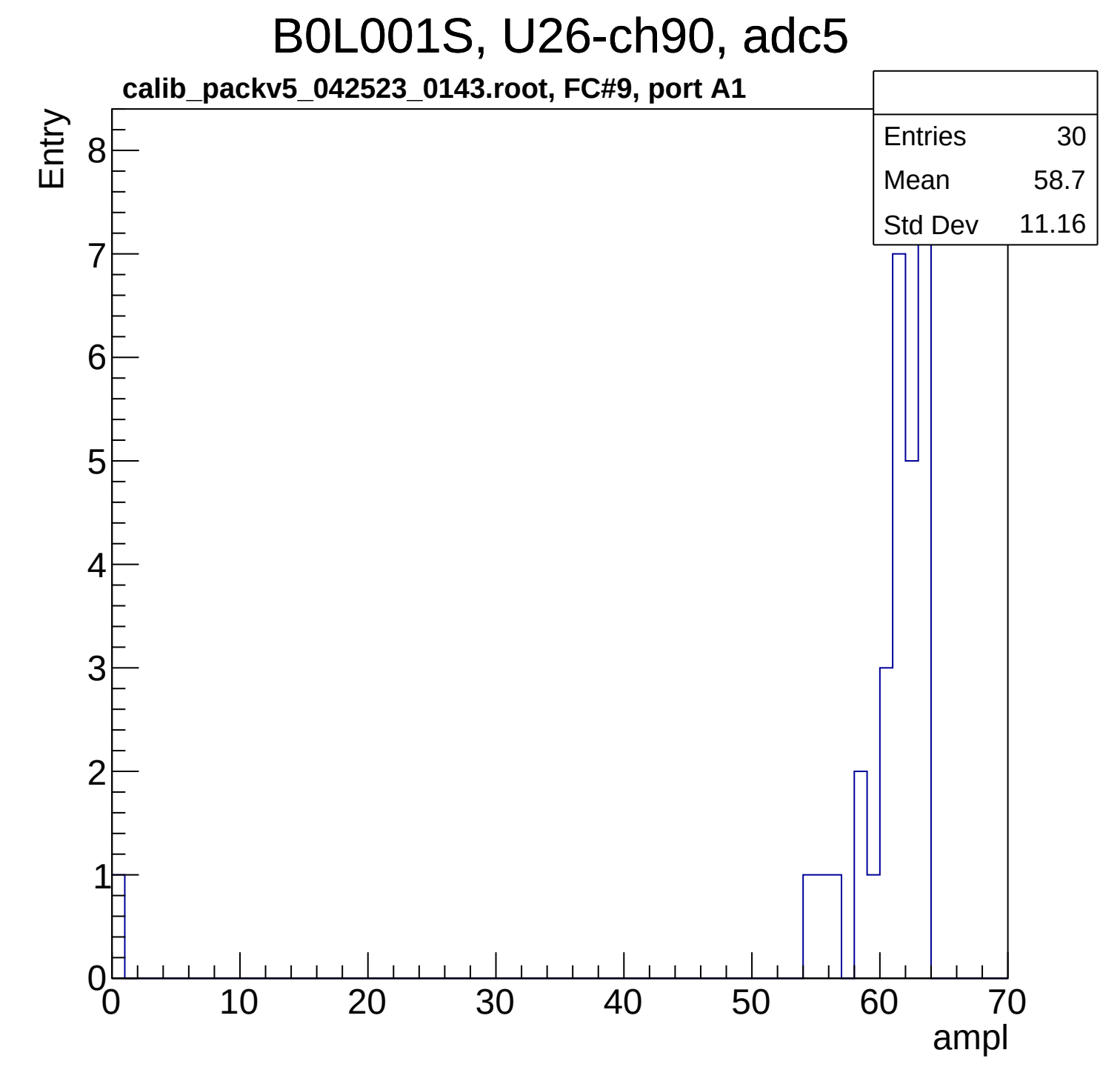
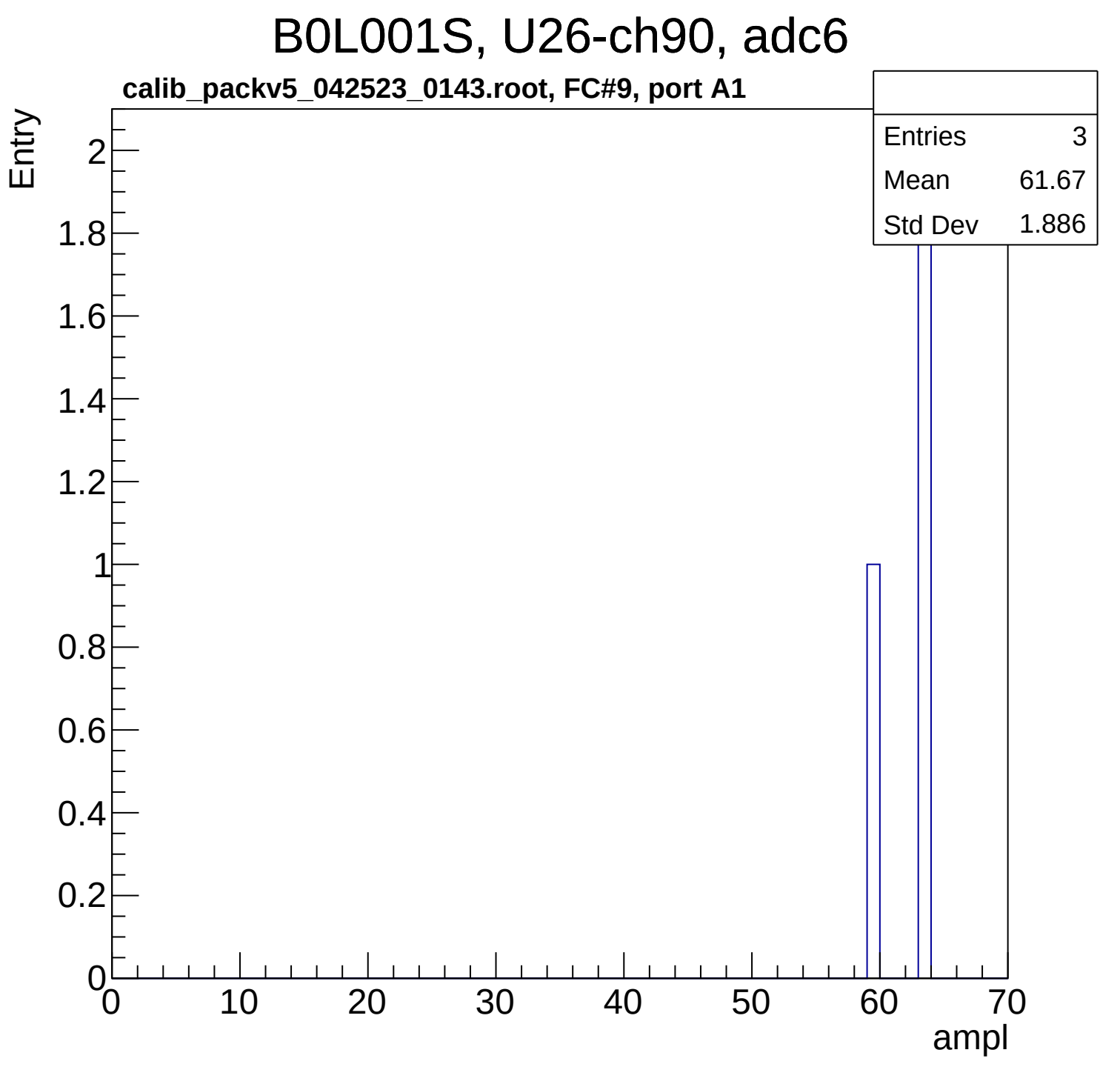
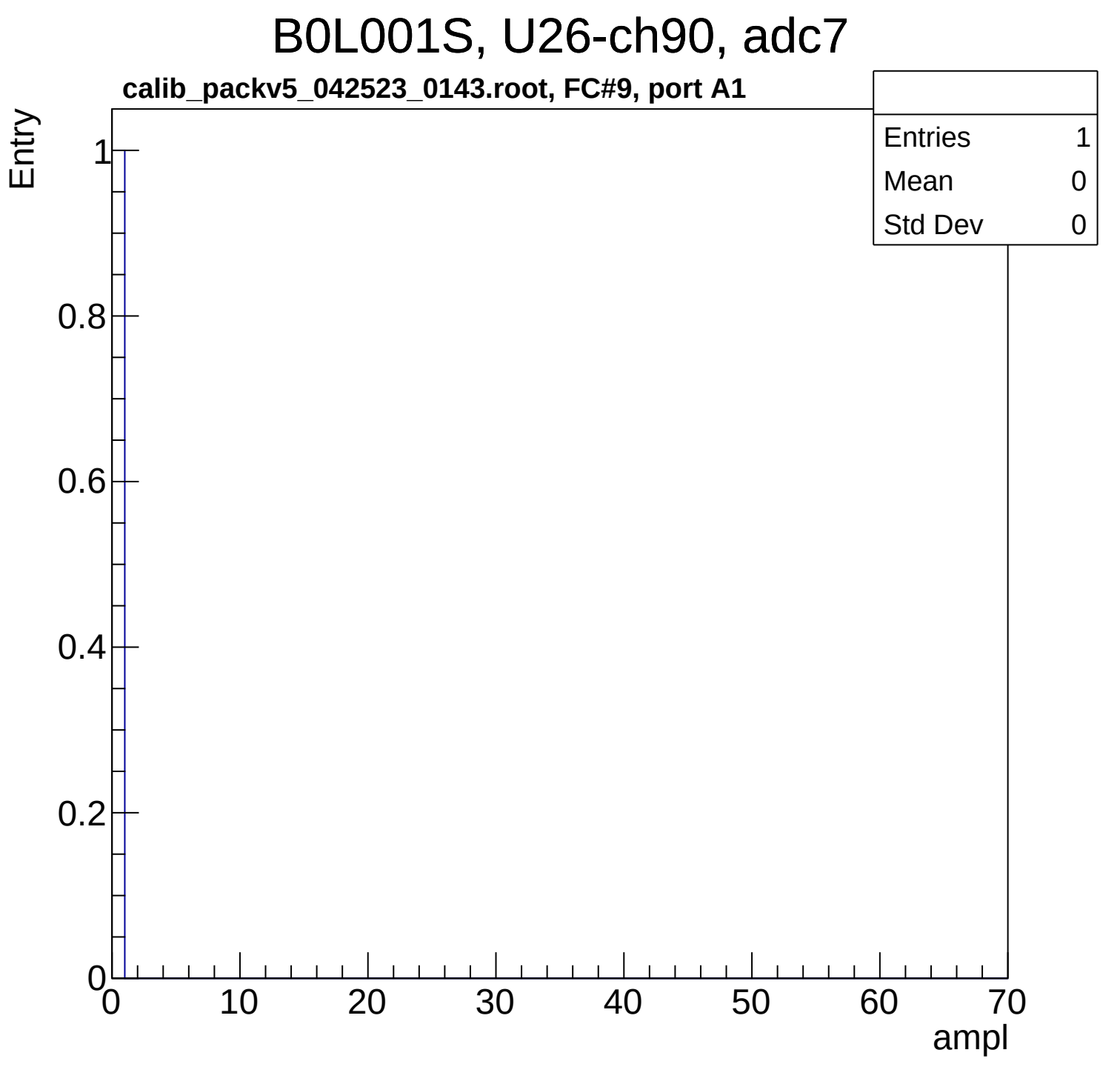
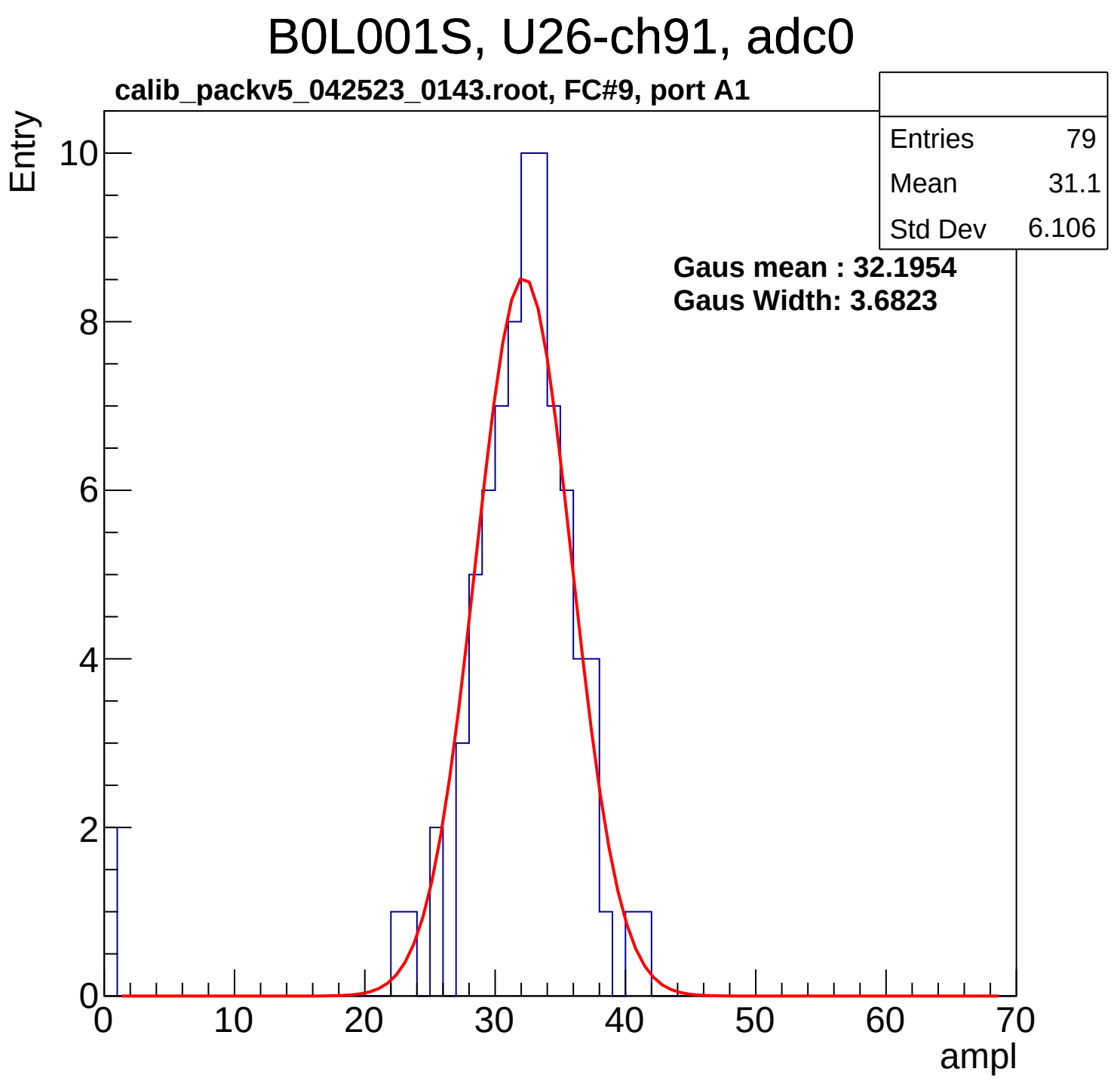
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

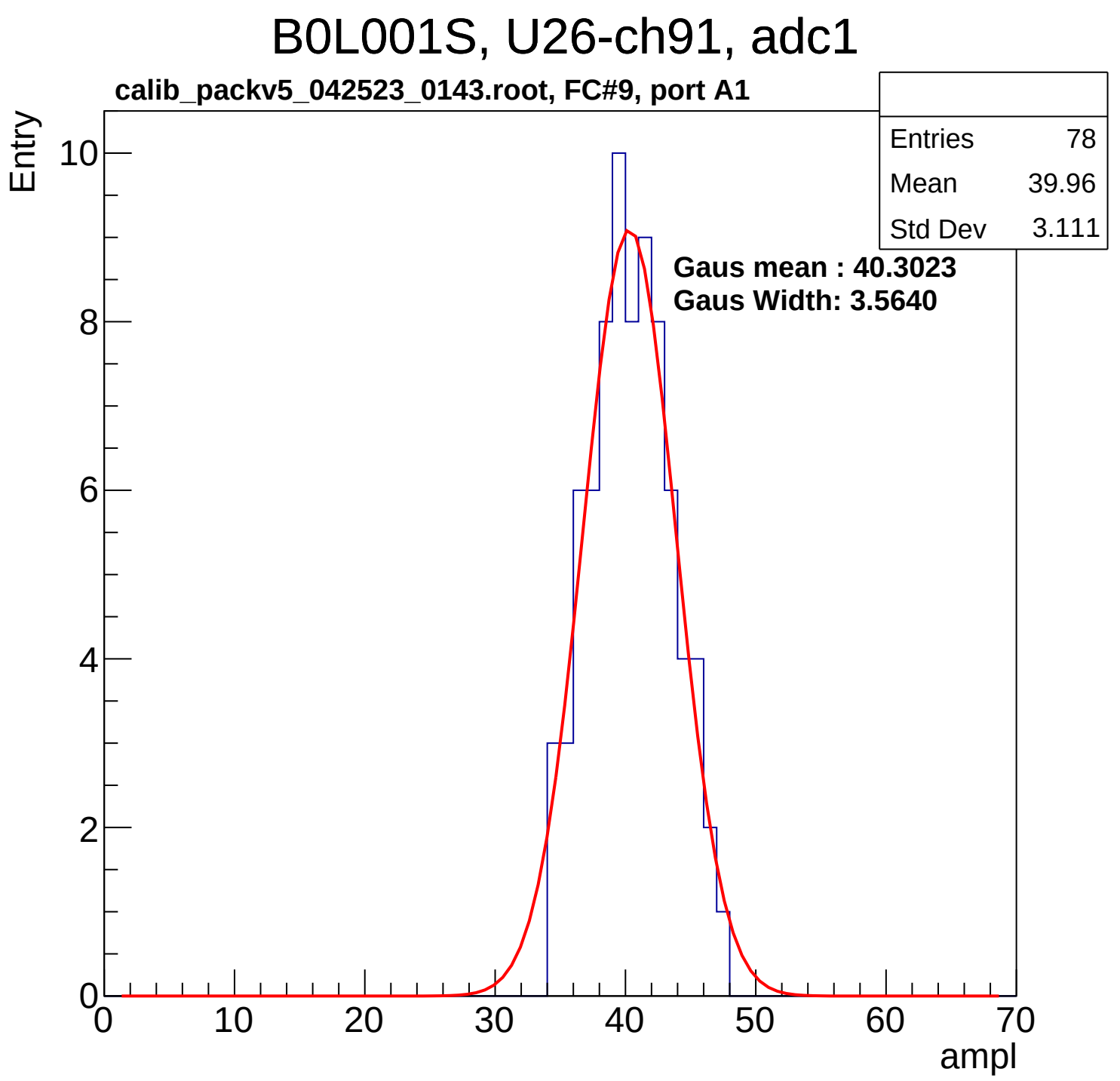

B0L001S, U26-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.88
Std Dev 2.782
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

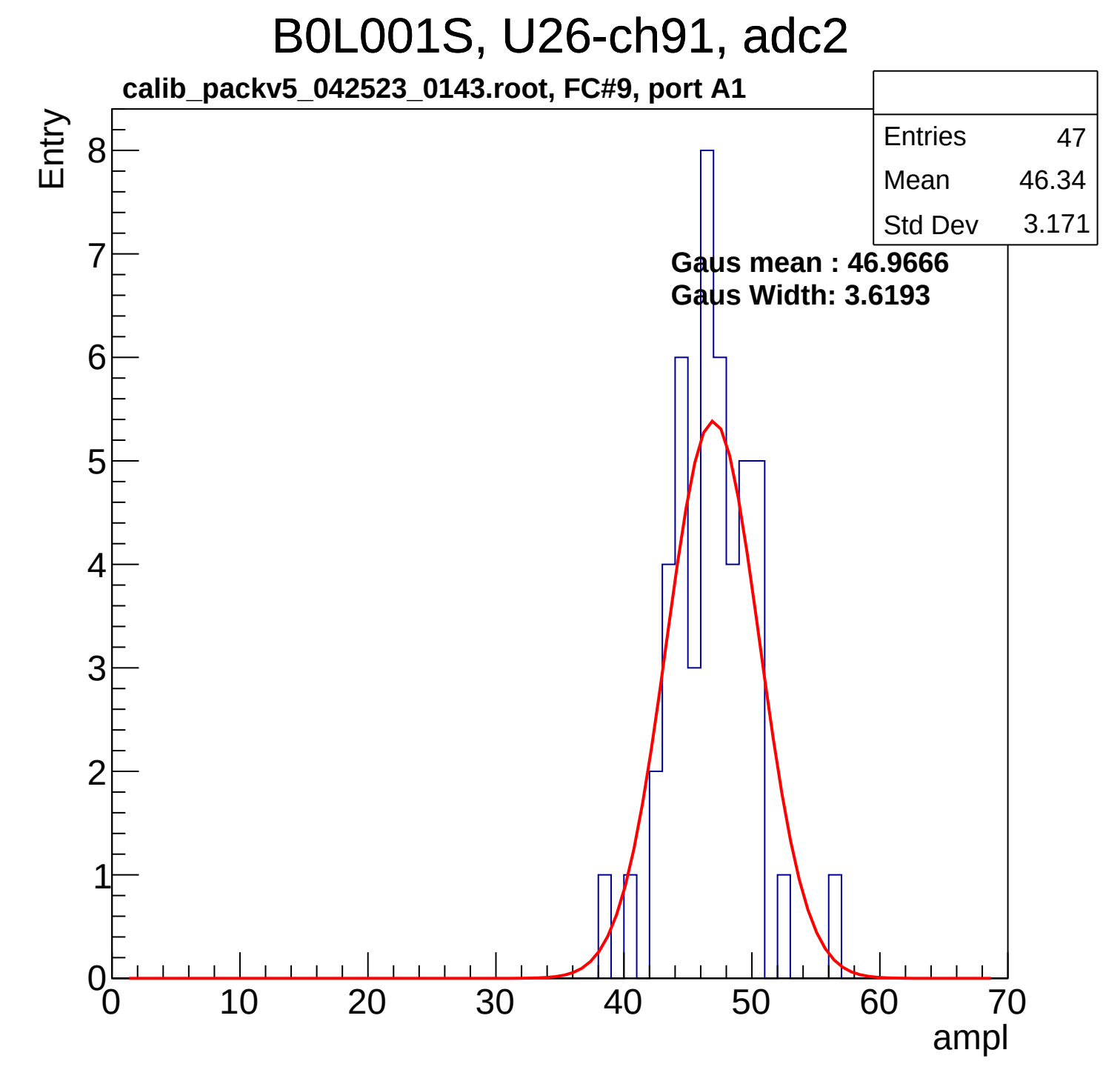

B0L001S, U26-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.7
Std Dev 11.16
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

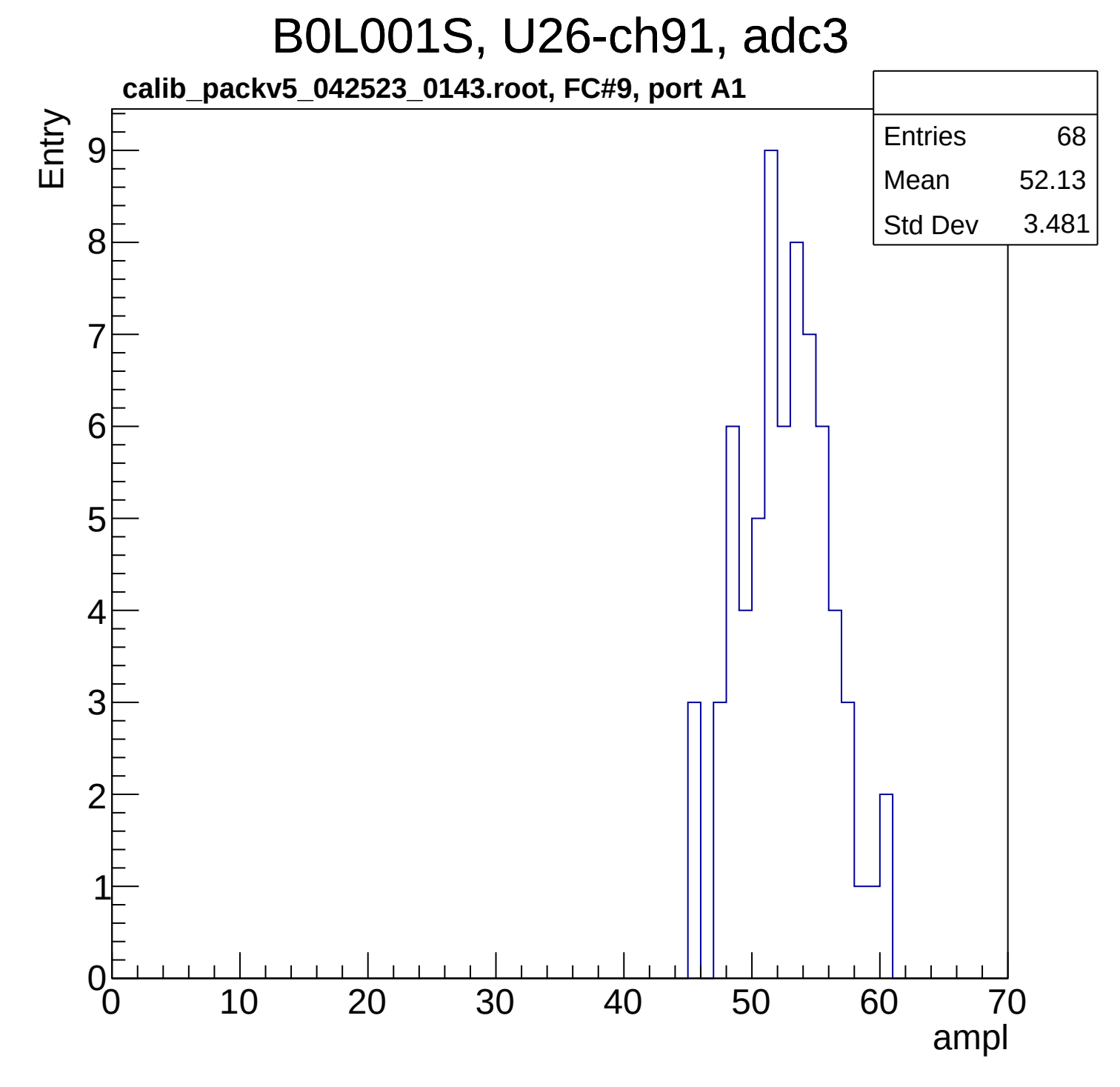

B0L001S, U26-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 31.1
Std Dev 6.106
Gaus mean : 32.1954
Gaus Width: 3.6823
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 39.96
Std Dev 3.111
Gaus mean : 40.3023
Gaus Width: 3.5640
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 46.34
Std Dev 3.171
Gaus mean : 46.9666
Gaus Width: 3.6193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.13
Std Dev 3.481
Entry
ampl
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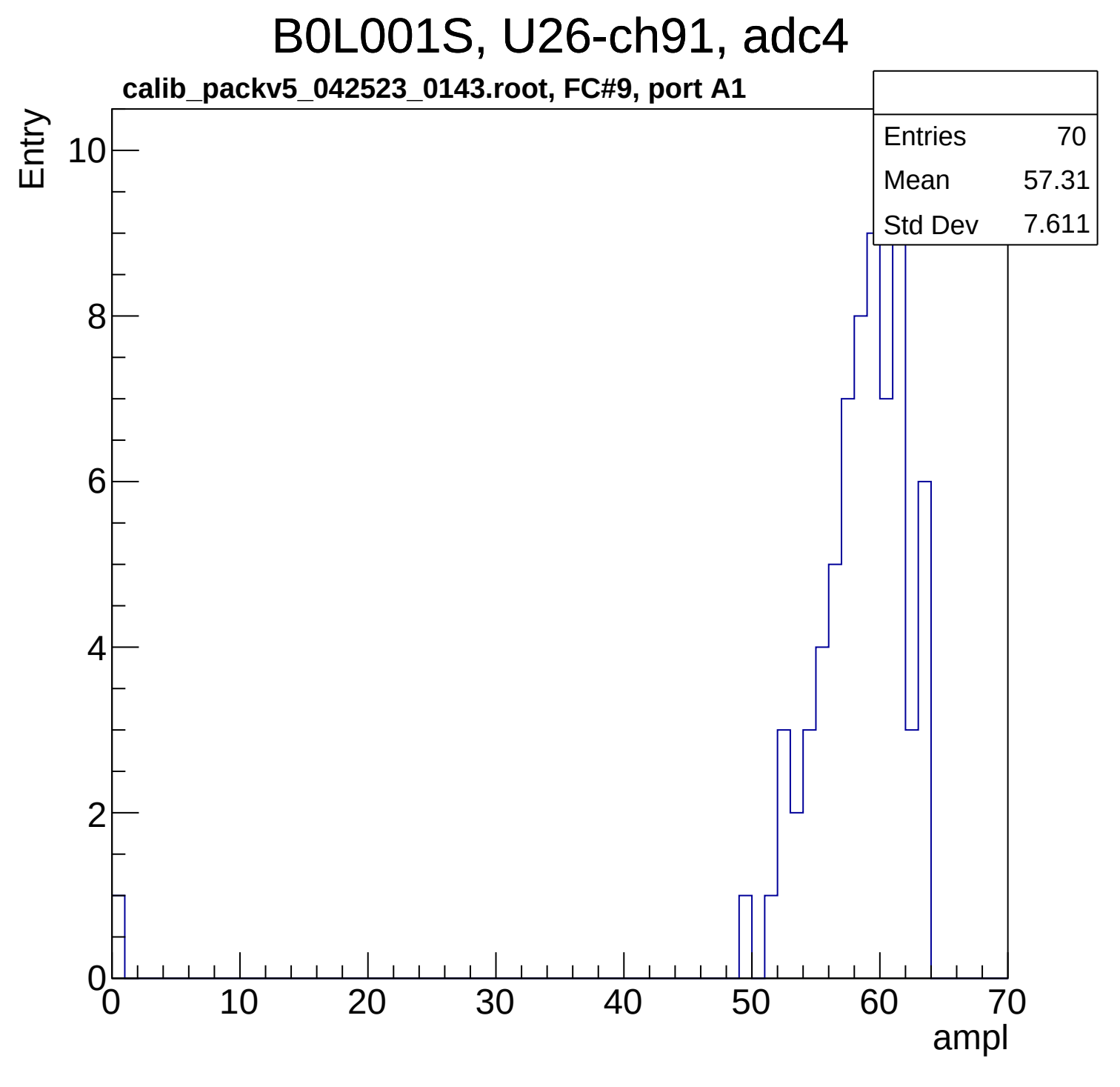
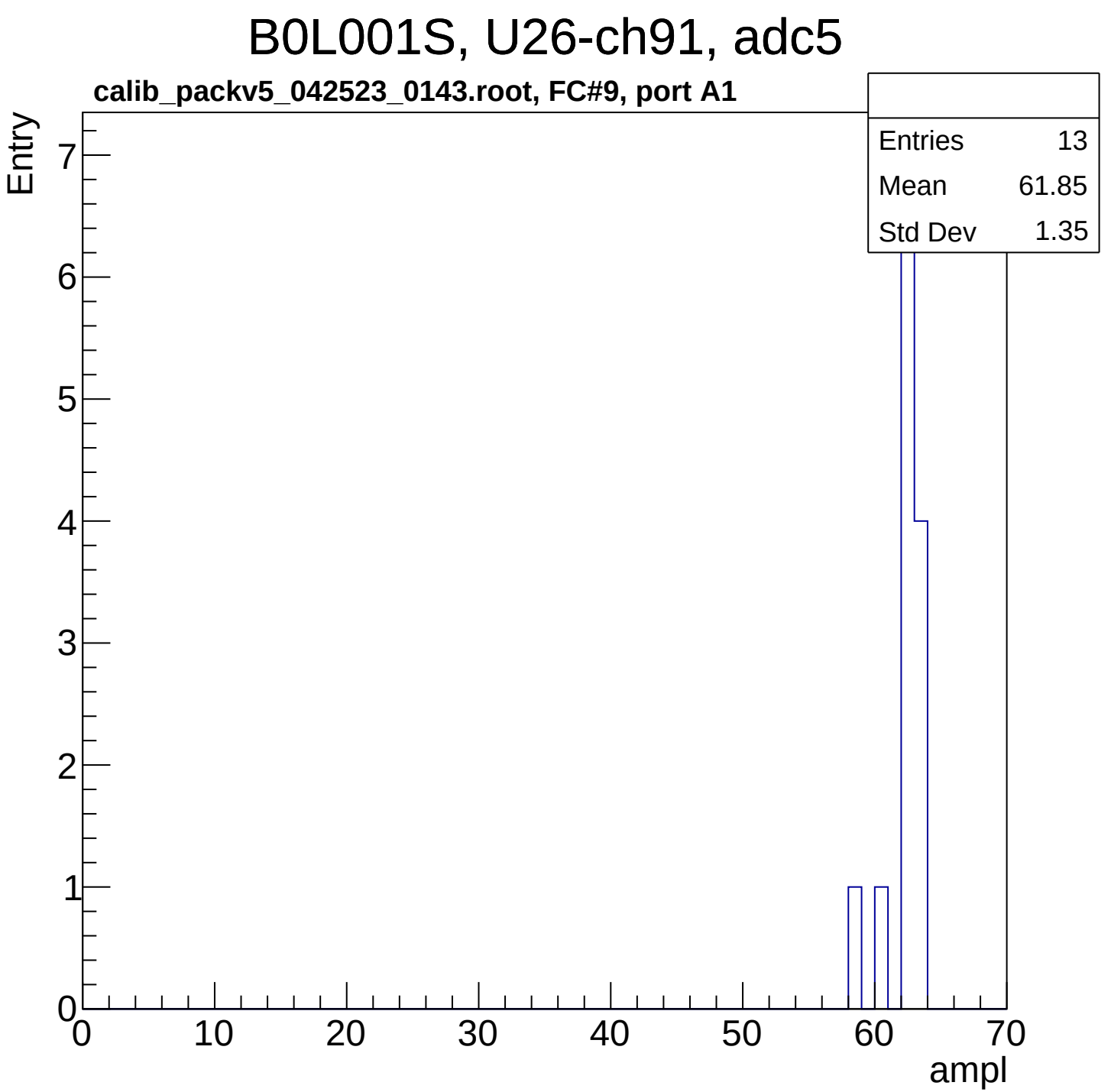
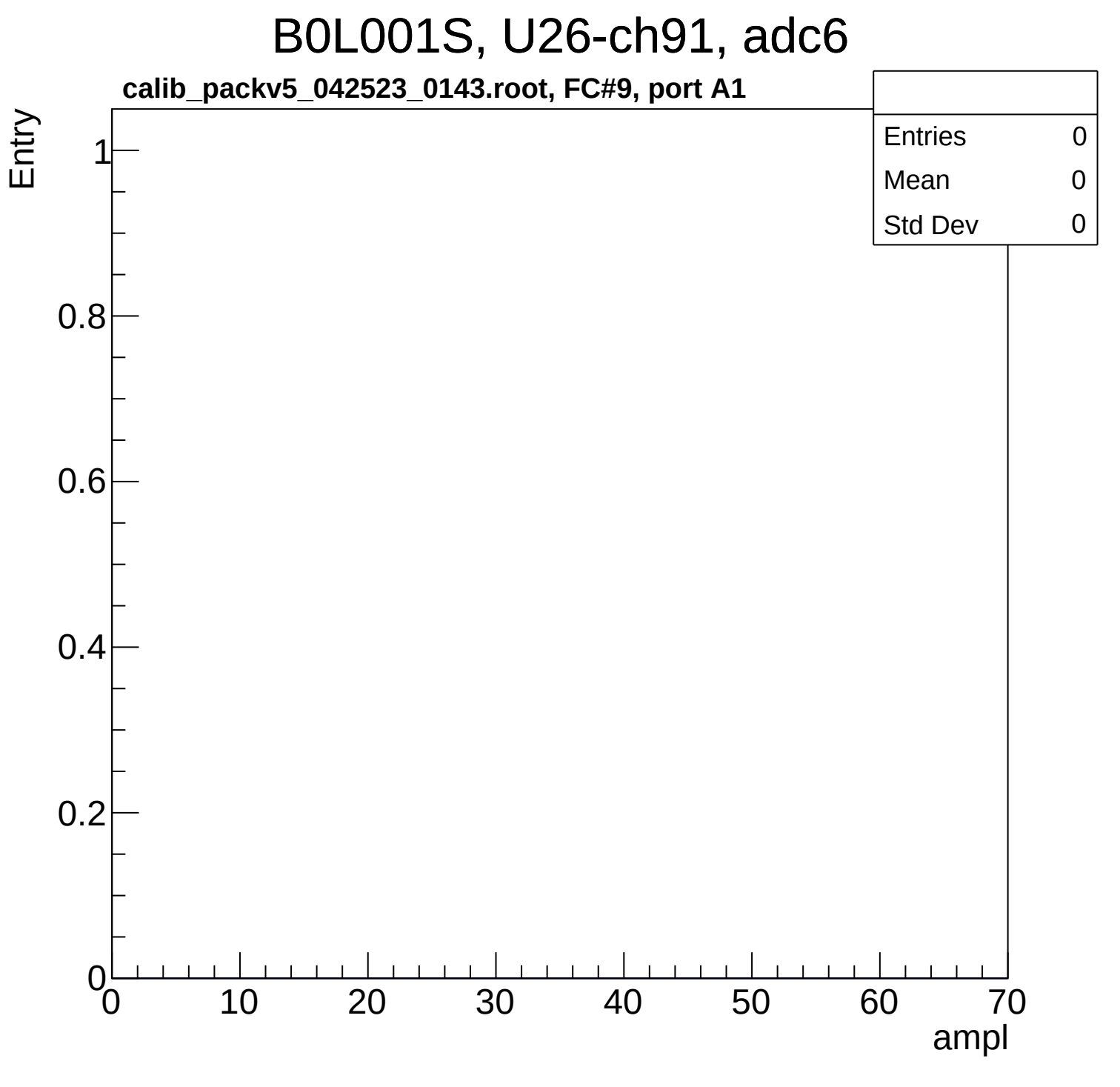
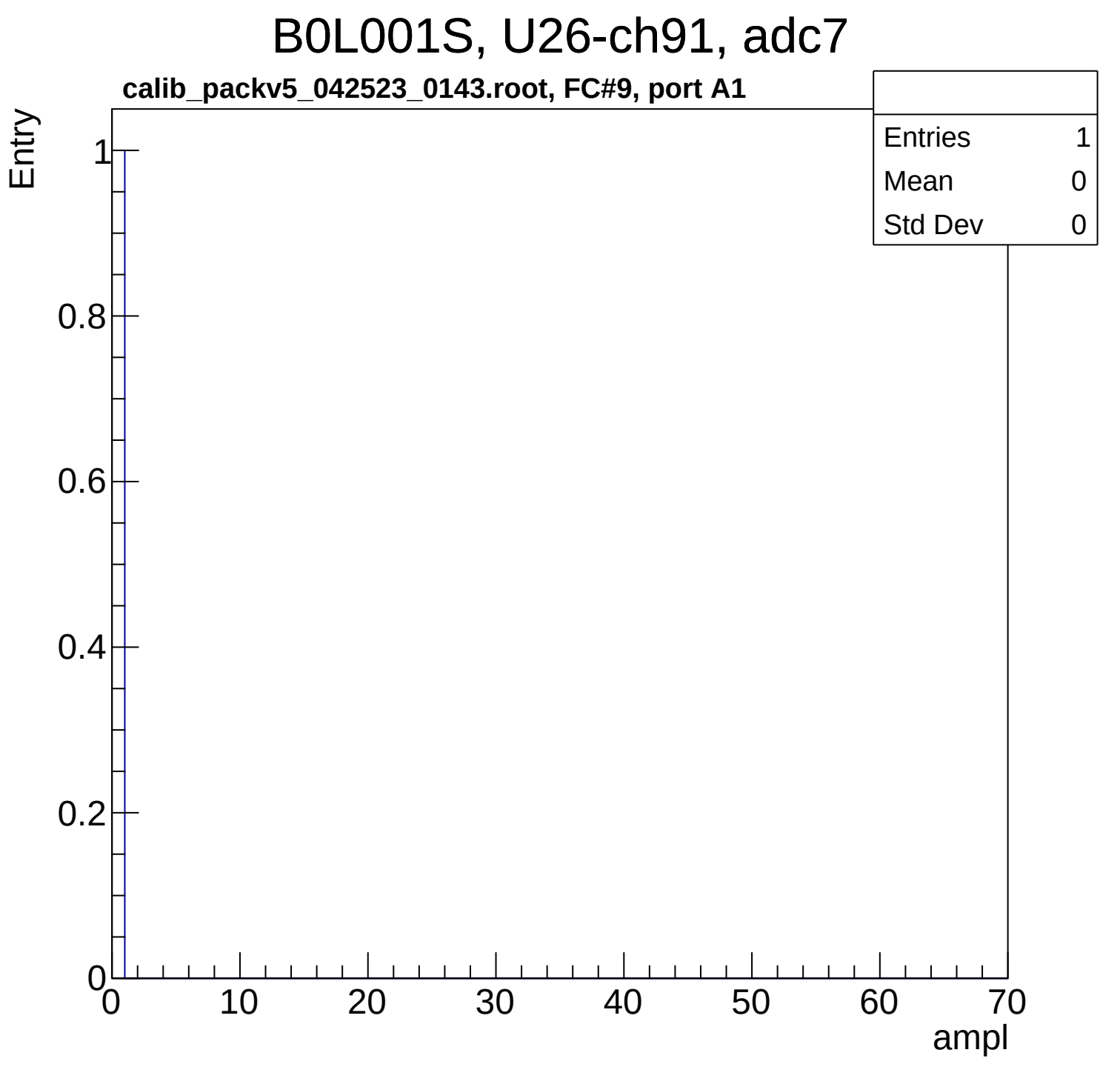
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 57.31
Std Dev 7.611
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.85
Std Dev 1.35
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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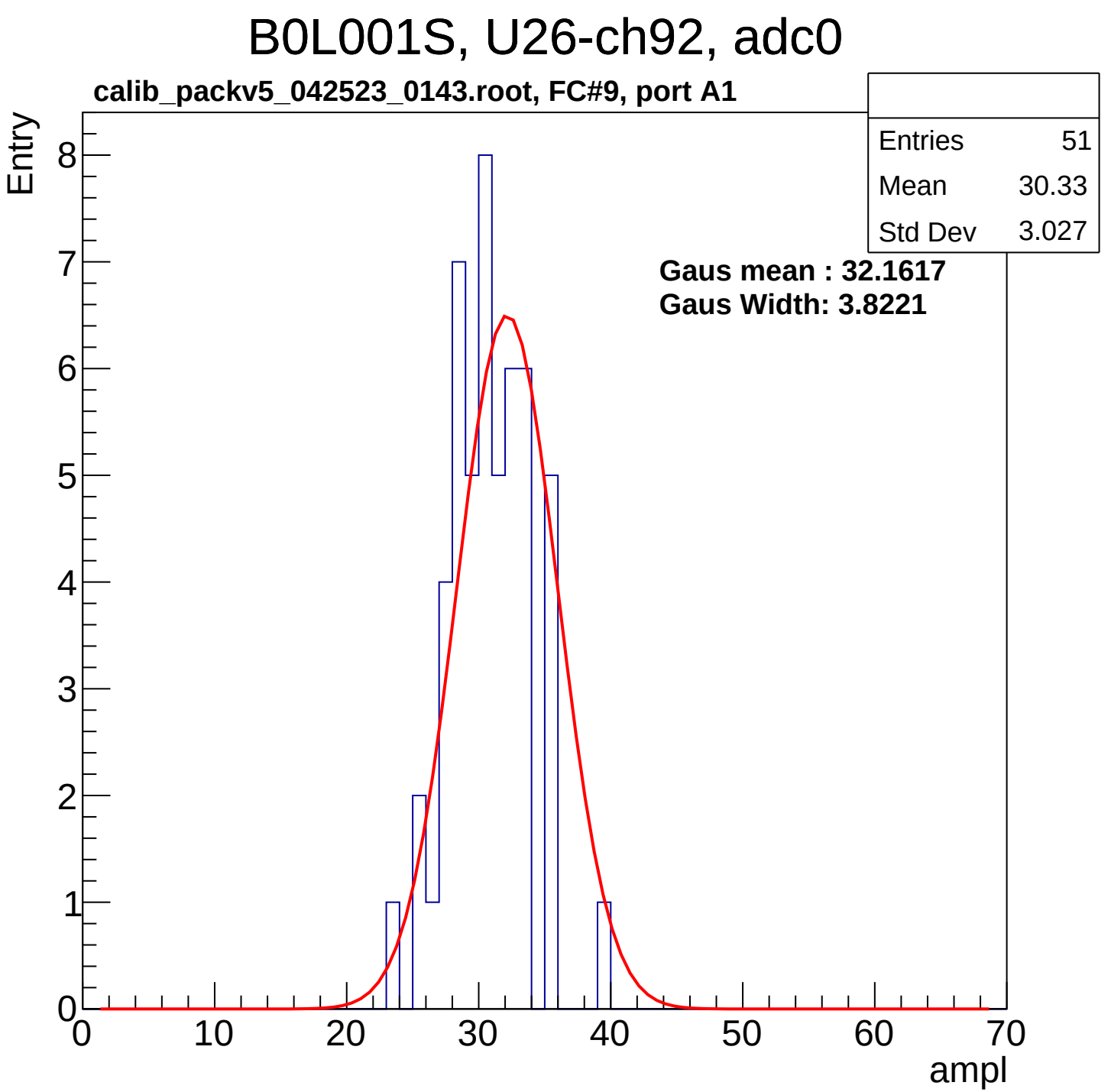
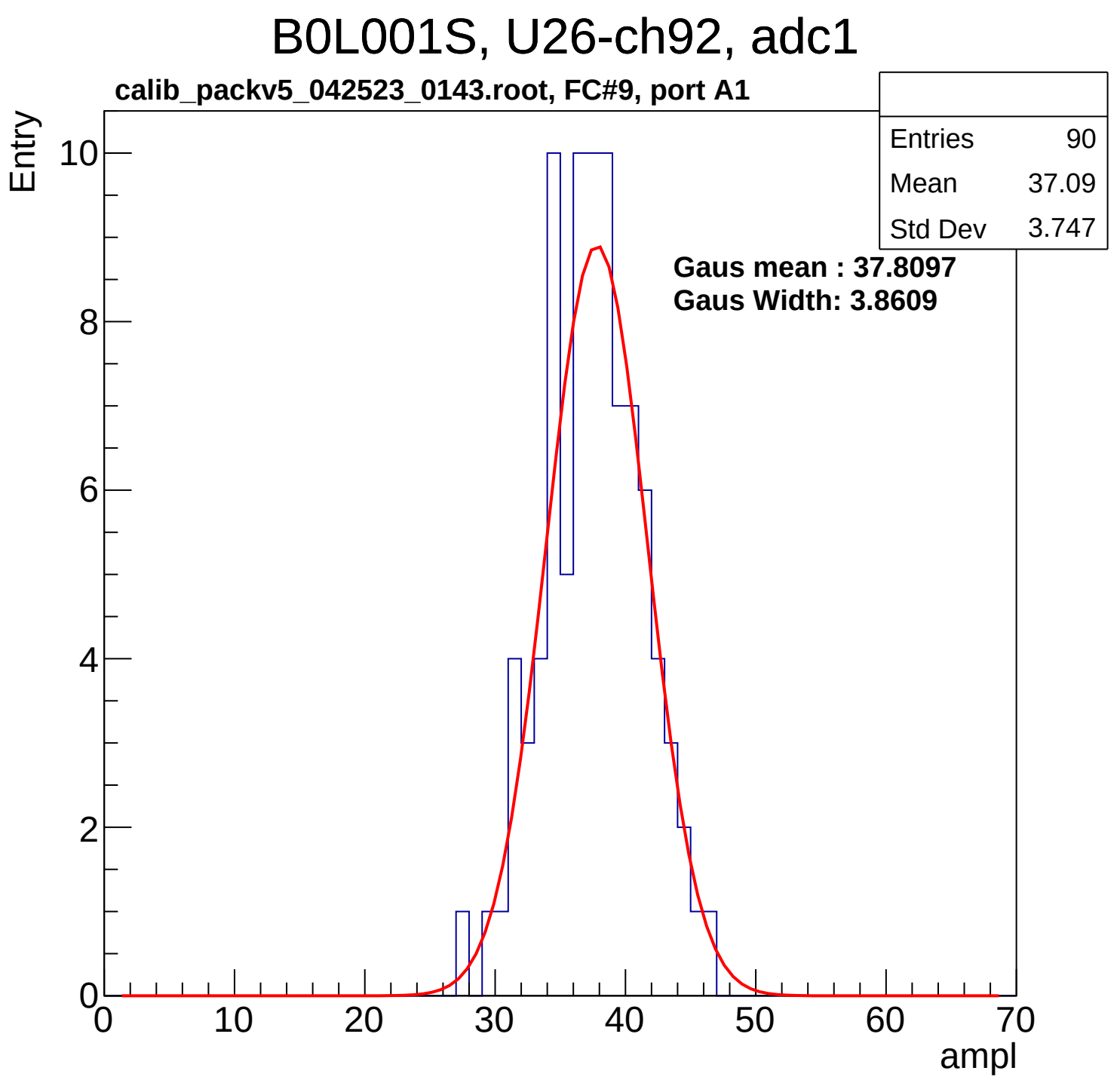
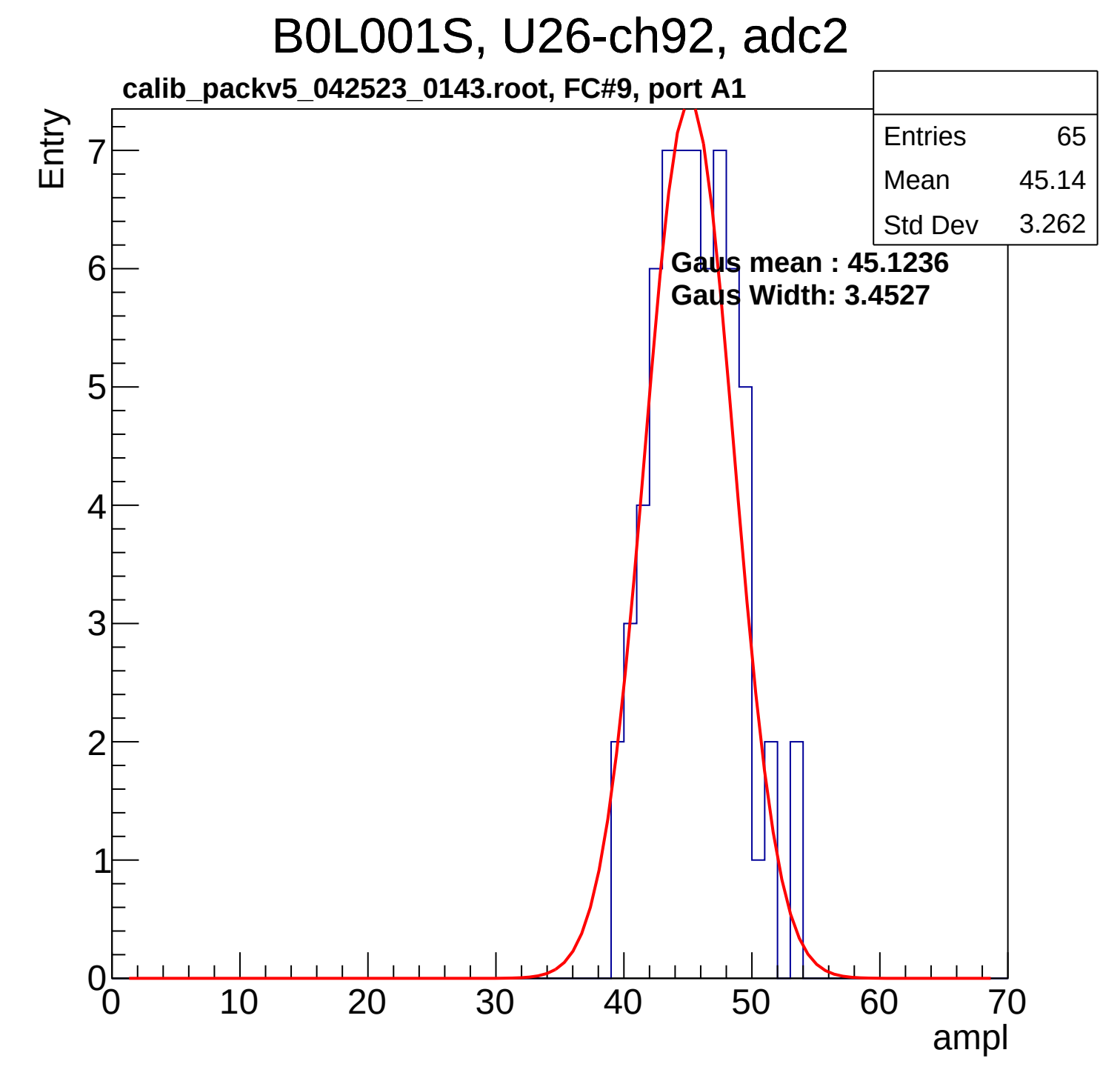
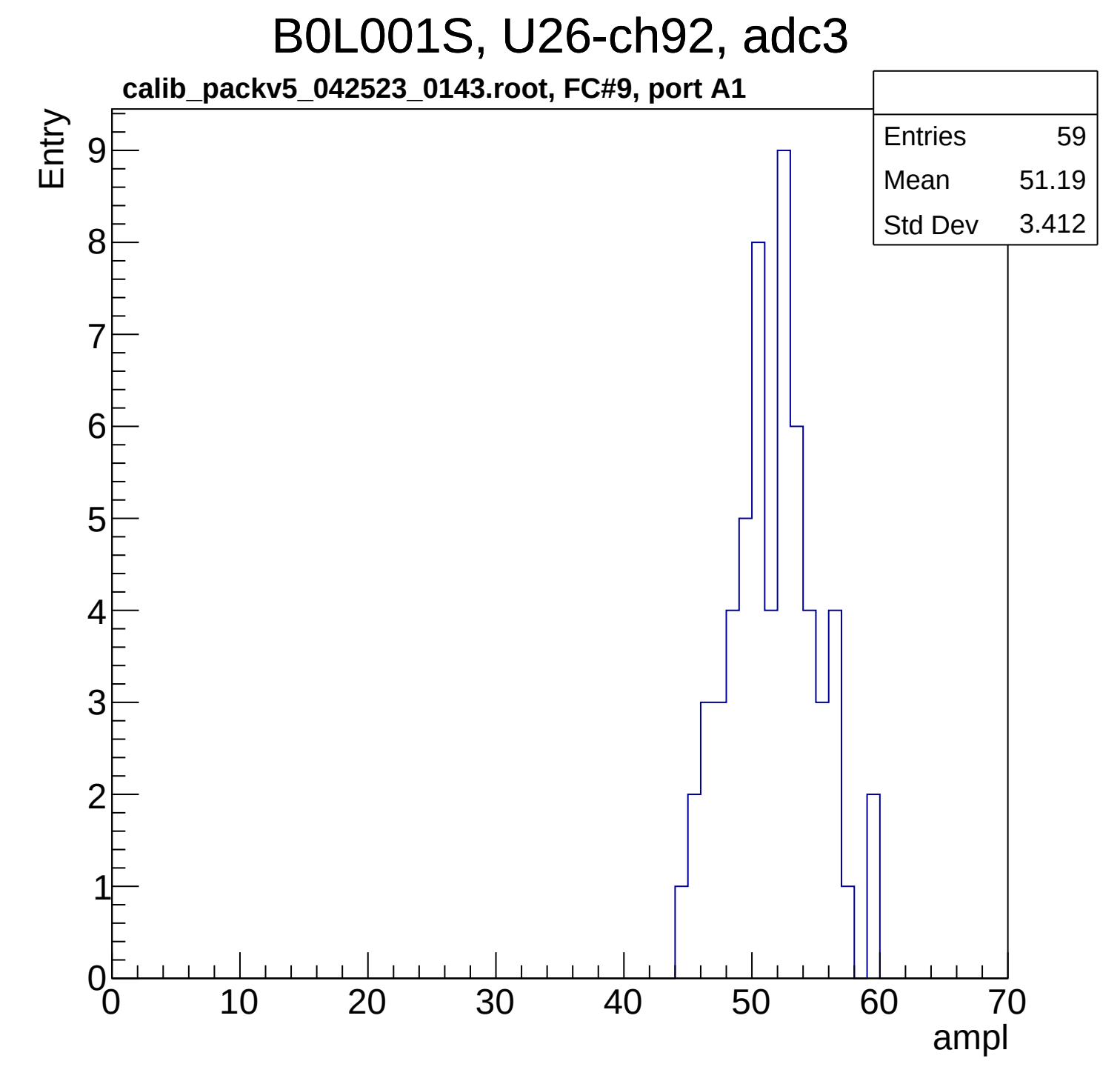
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 30.33
Std Dev 3.027
Gaus mean : 32.1617
Gaus Width: 3.8221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 37.09
Std Dev 3.747
Gaus mean : 37.8097
Gaus Width: 3.8609
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.14
Std Dev 3.262
Gaus mean : 45.1236
Gaus Width: 3.4527
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.19
Std Dev 3.412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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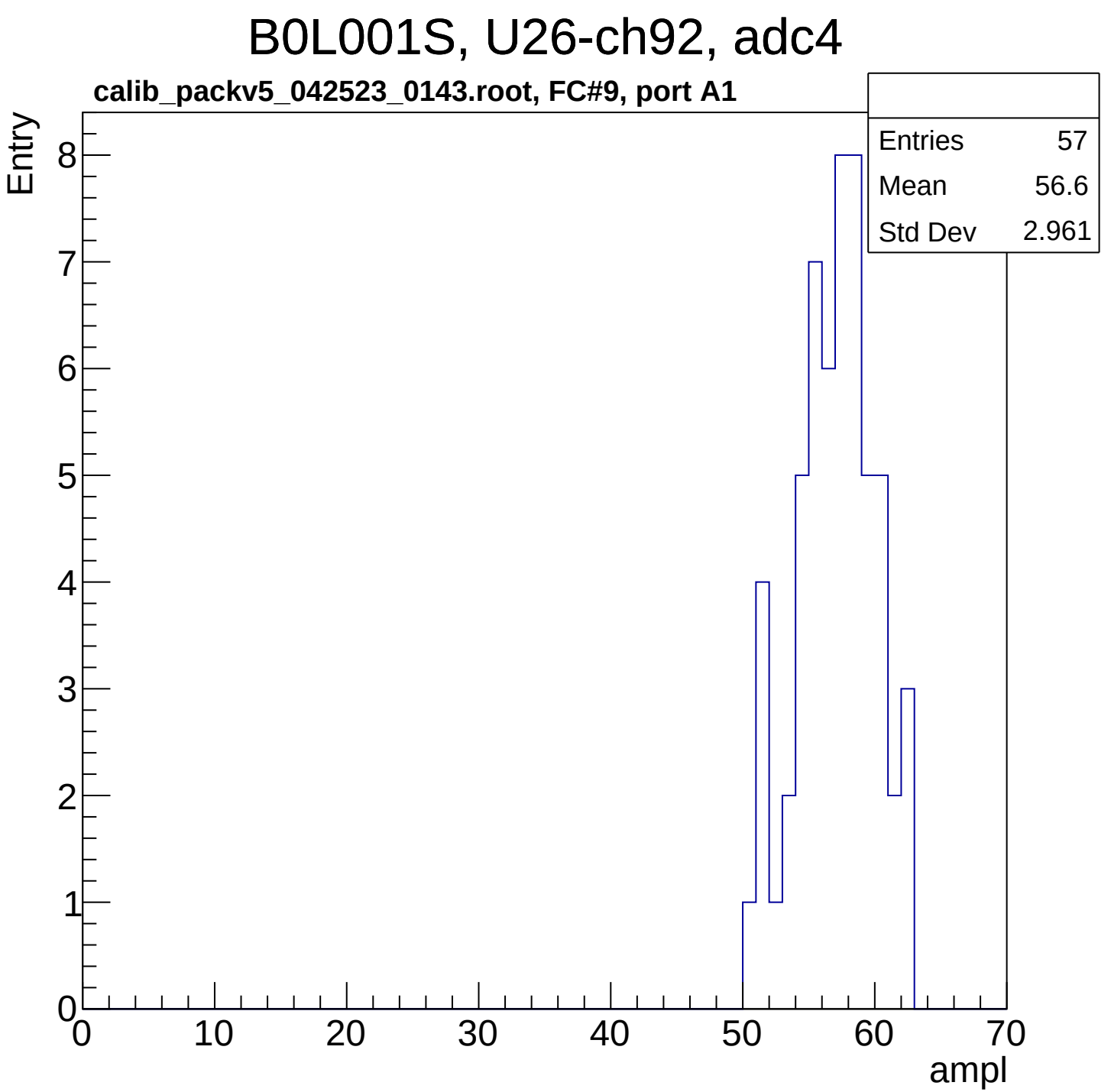
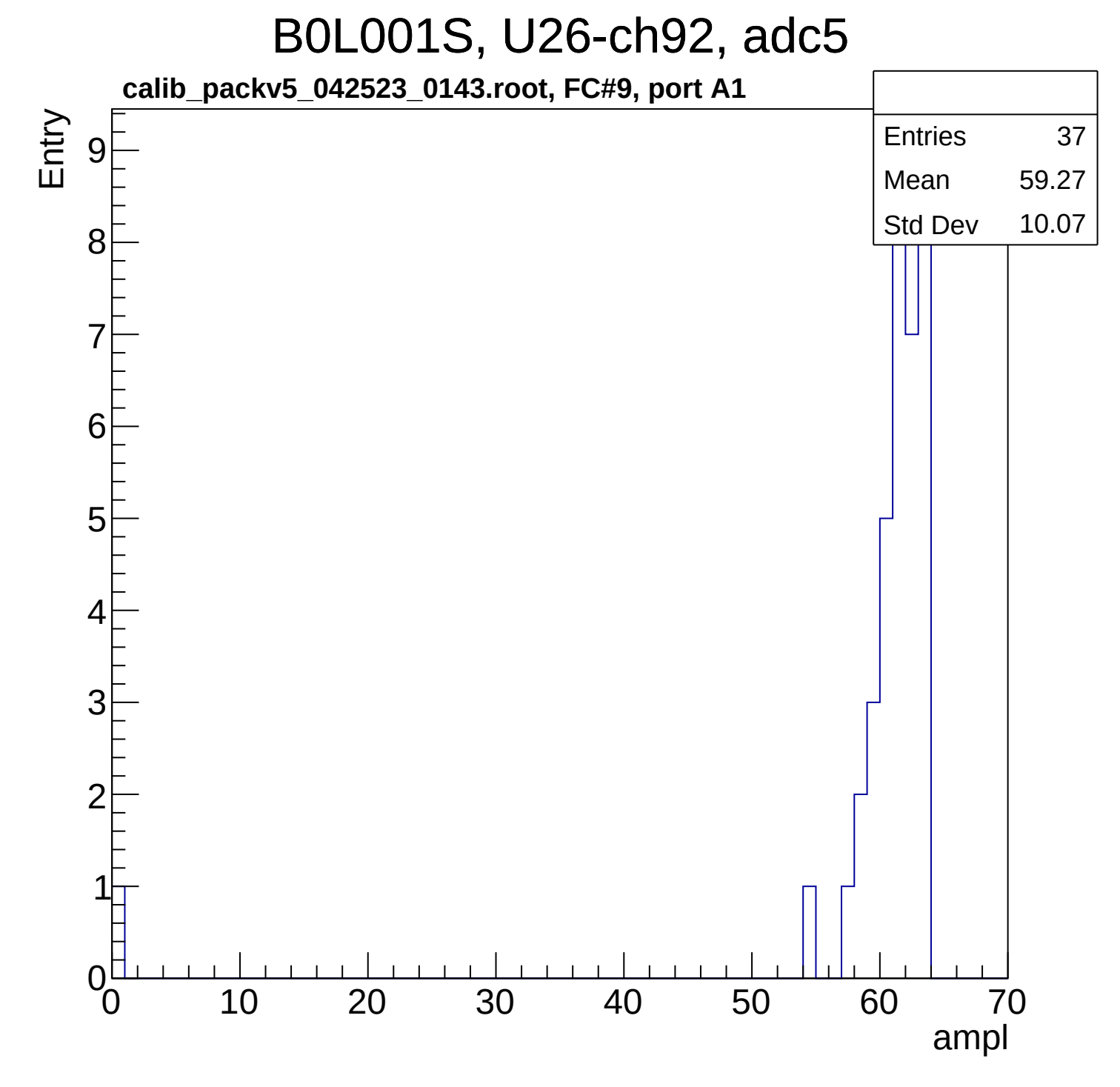
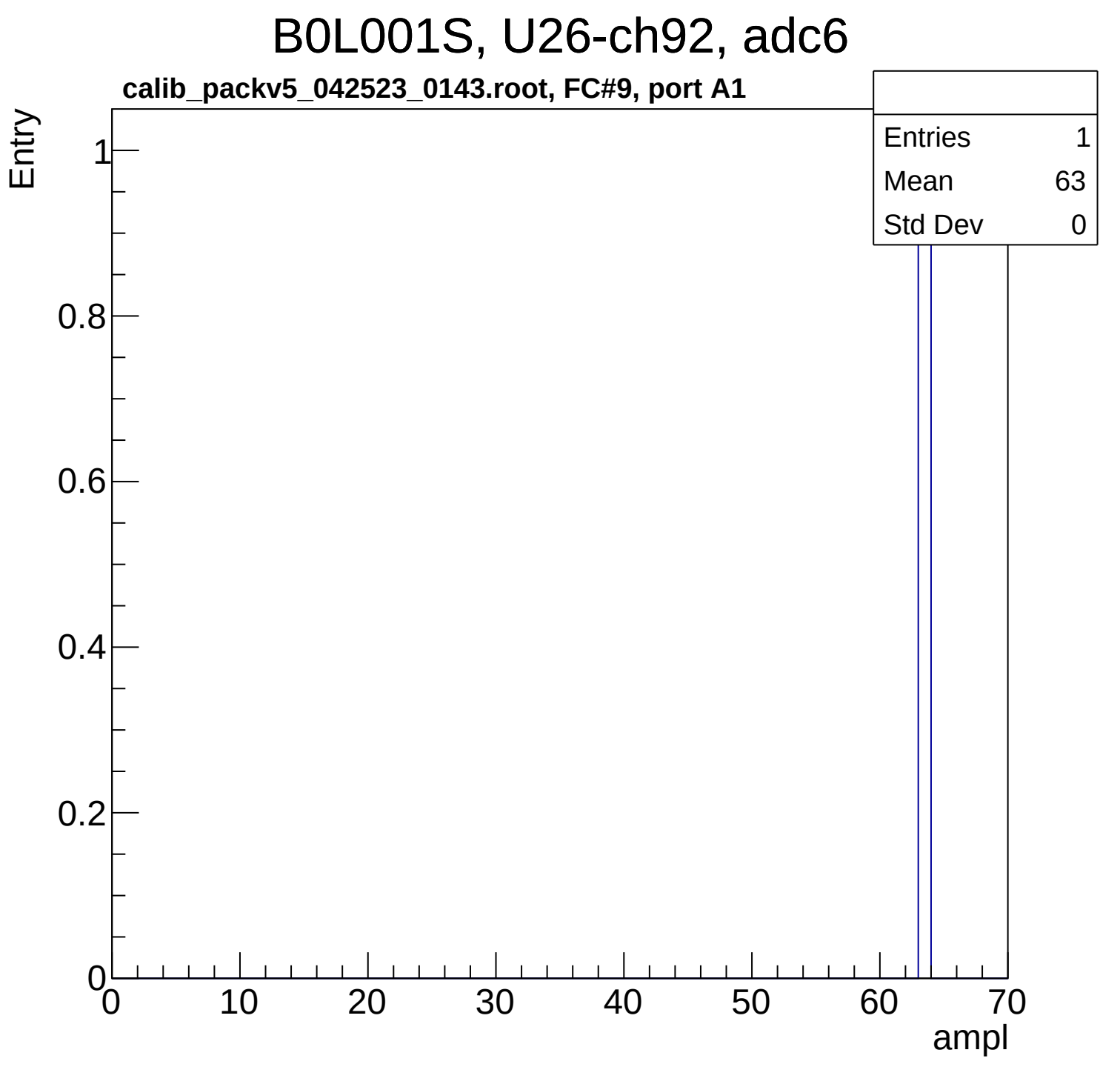
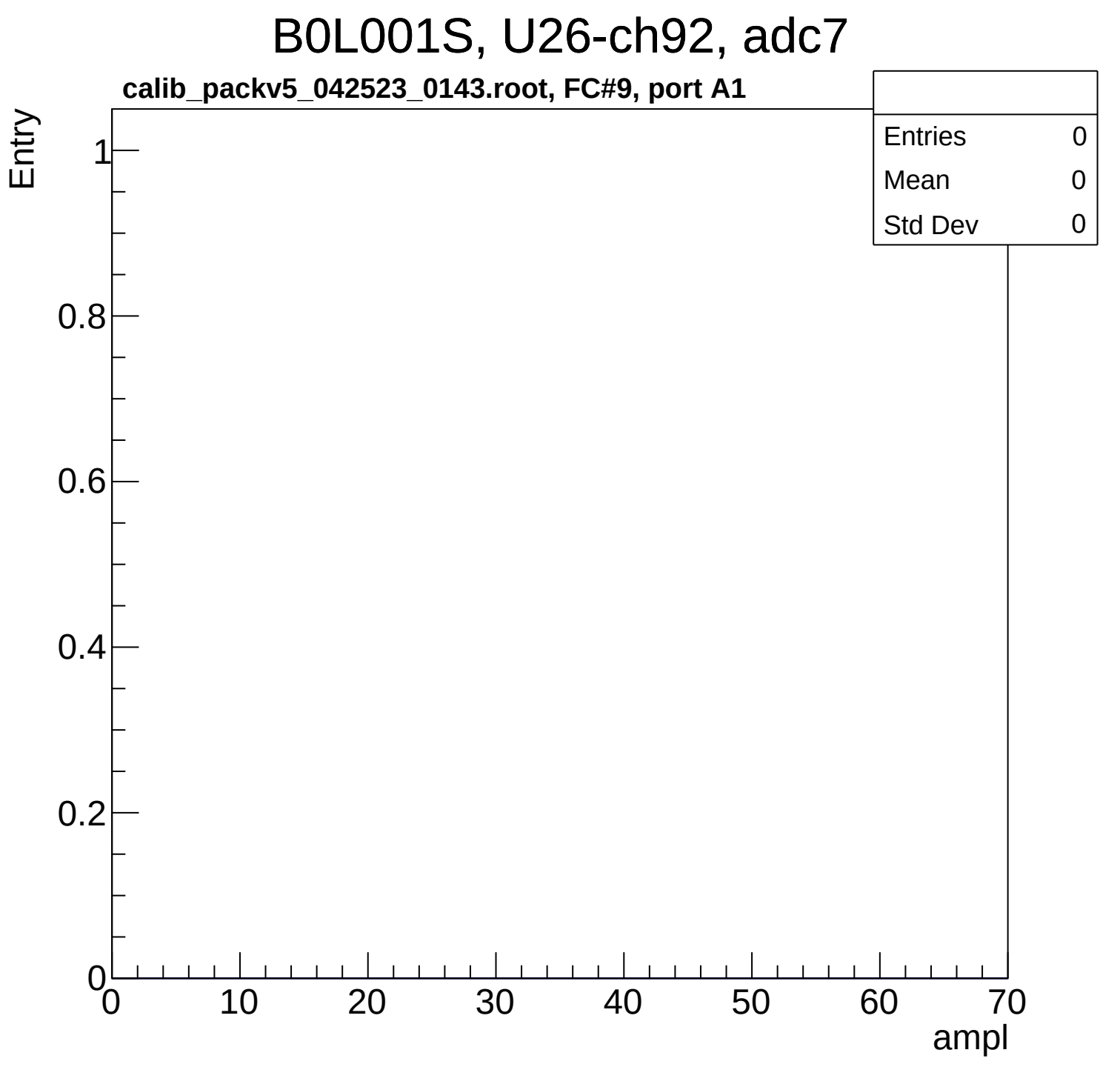

B0L001S, U26-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.6
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.27
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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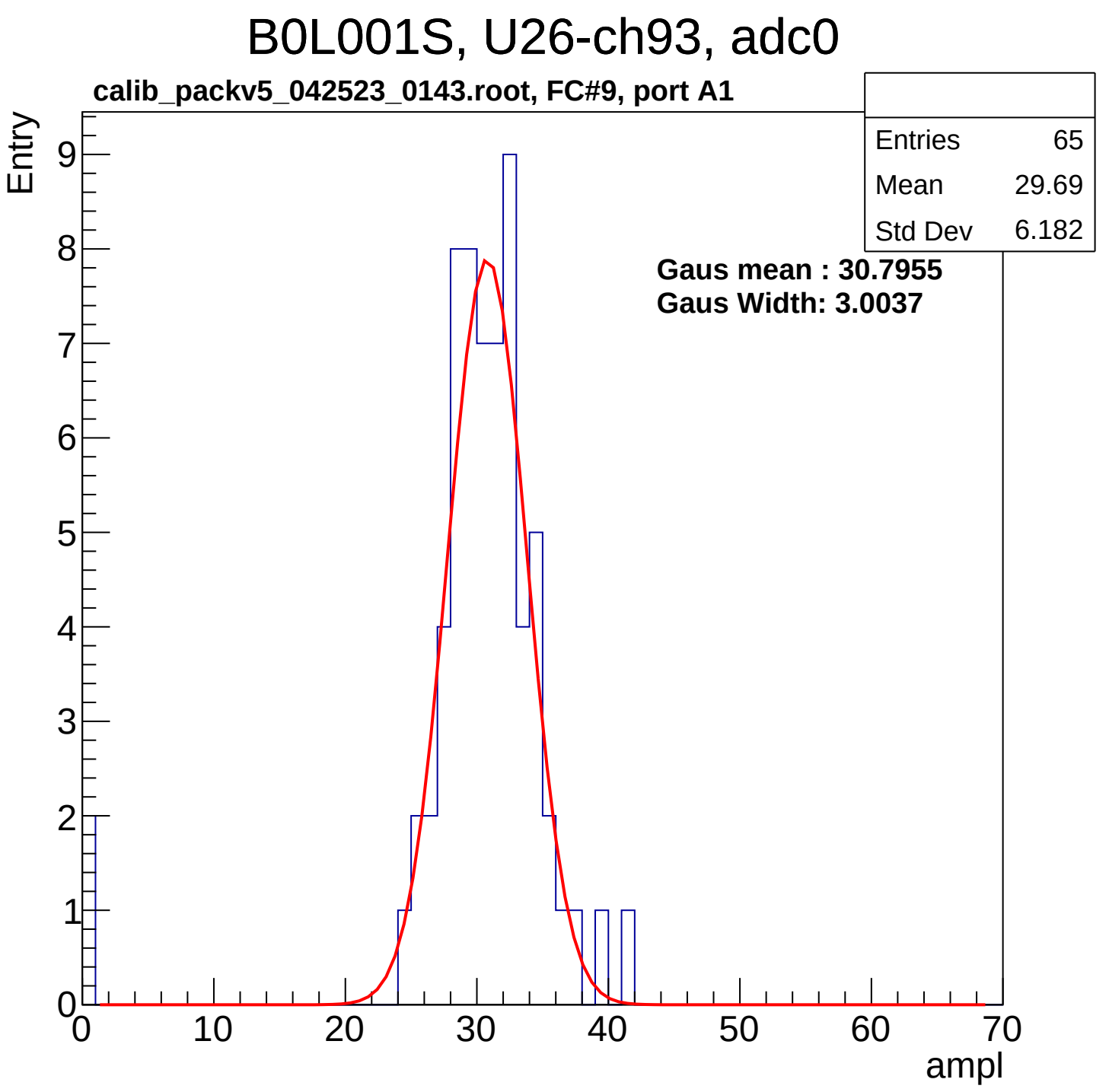
50
60
70
ampl


B0L001S, U26-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.69
Std Dev 6.182
Gaus mean : 30.7955
Gaus Width: 3.0037
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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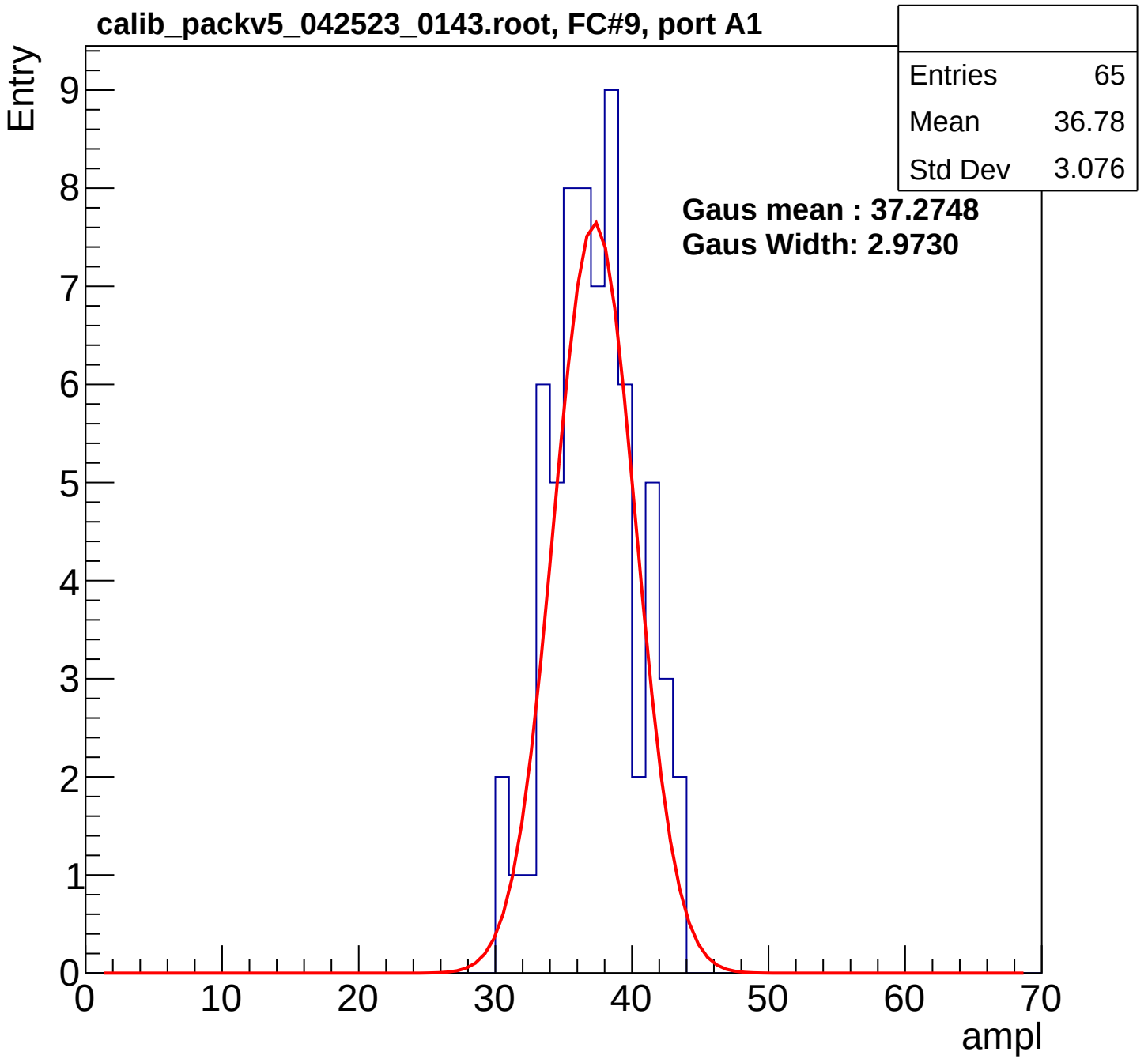
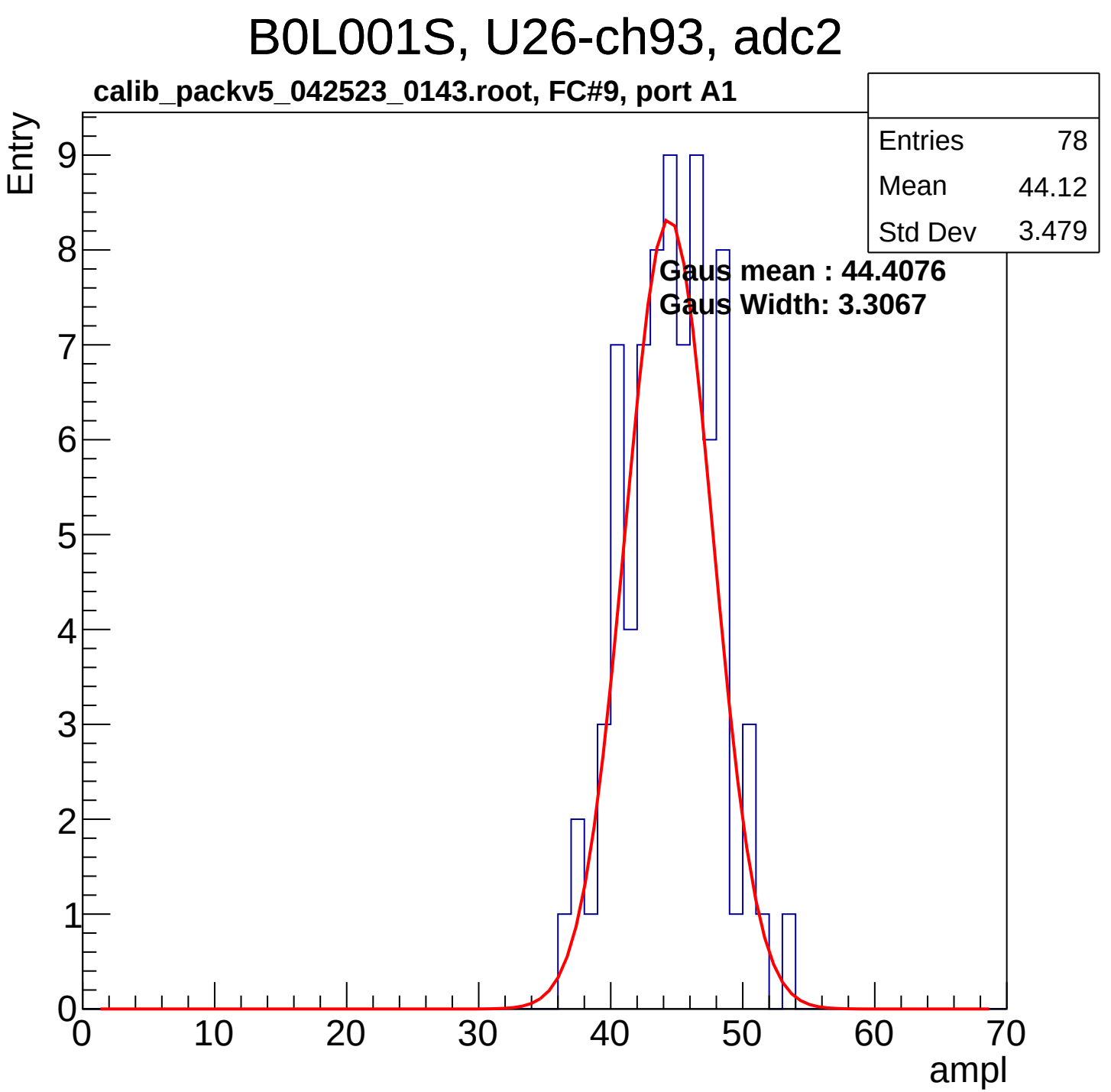

B0L001S, U26-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.78
Std Dev 3.076
Gaus mean : 37.2748
Gaus Width: 2.9730
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.12
Std Dev 3.479
Gaus mean : 44.4076
Gaus Width: 3.3067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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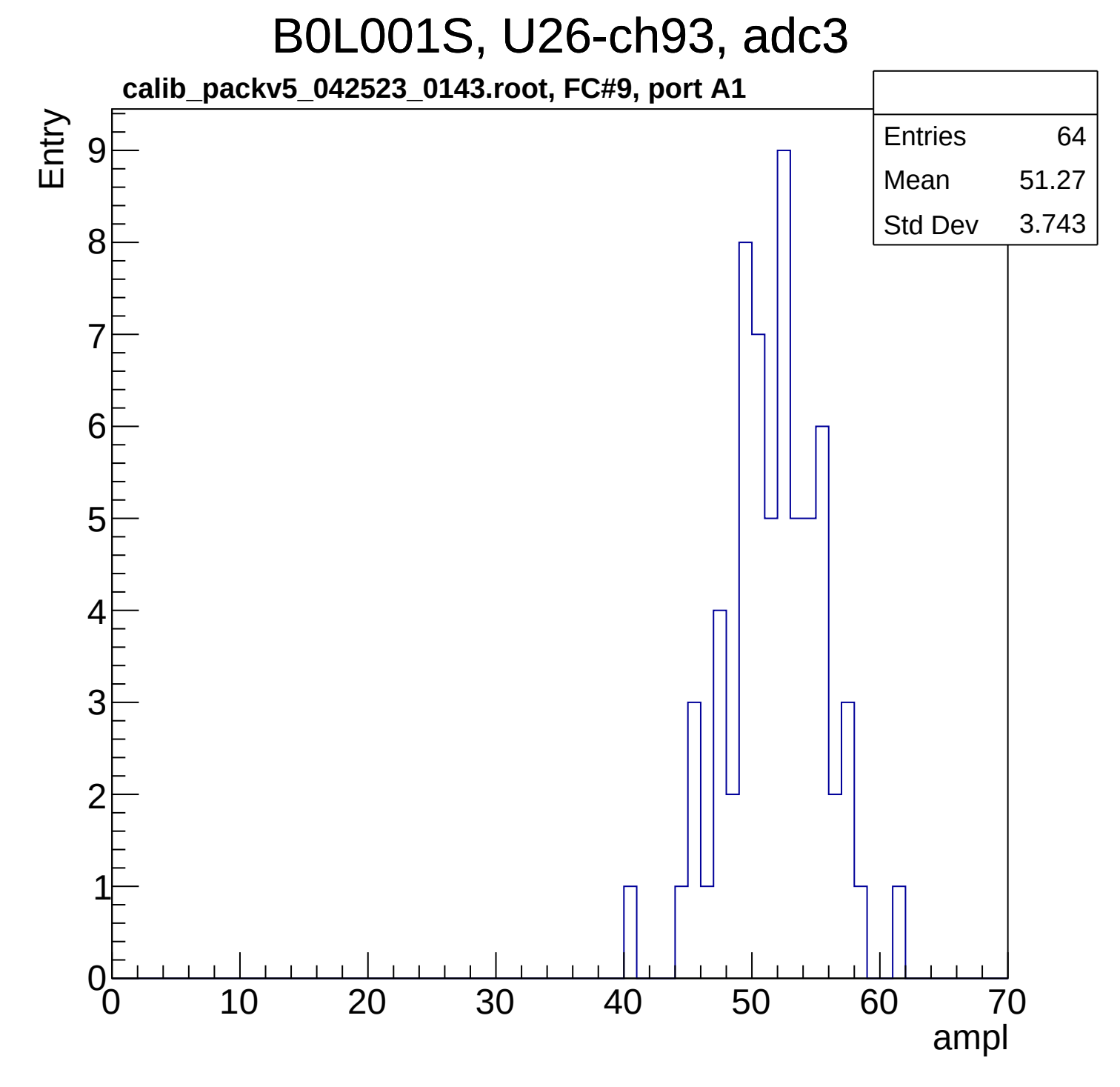
B0L001S, U26-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.27
Std Dev 3.743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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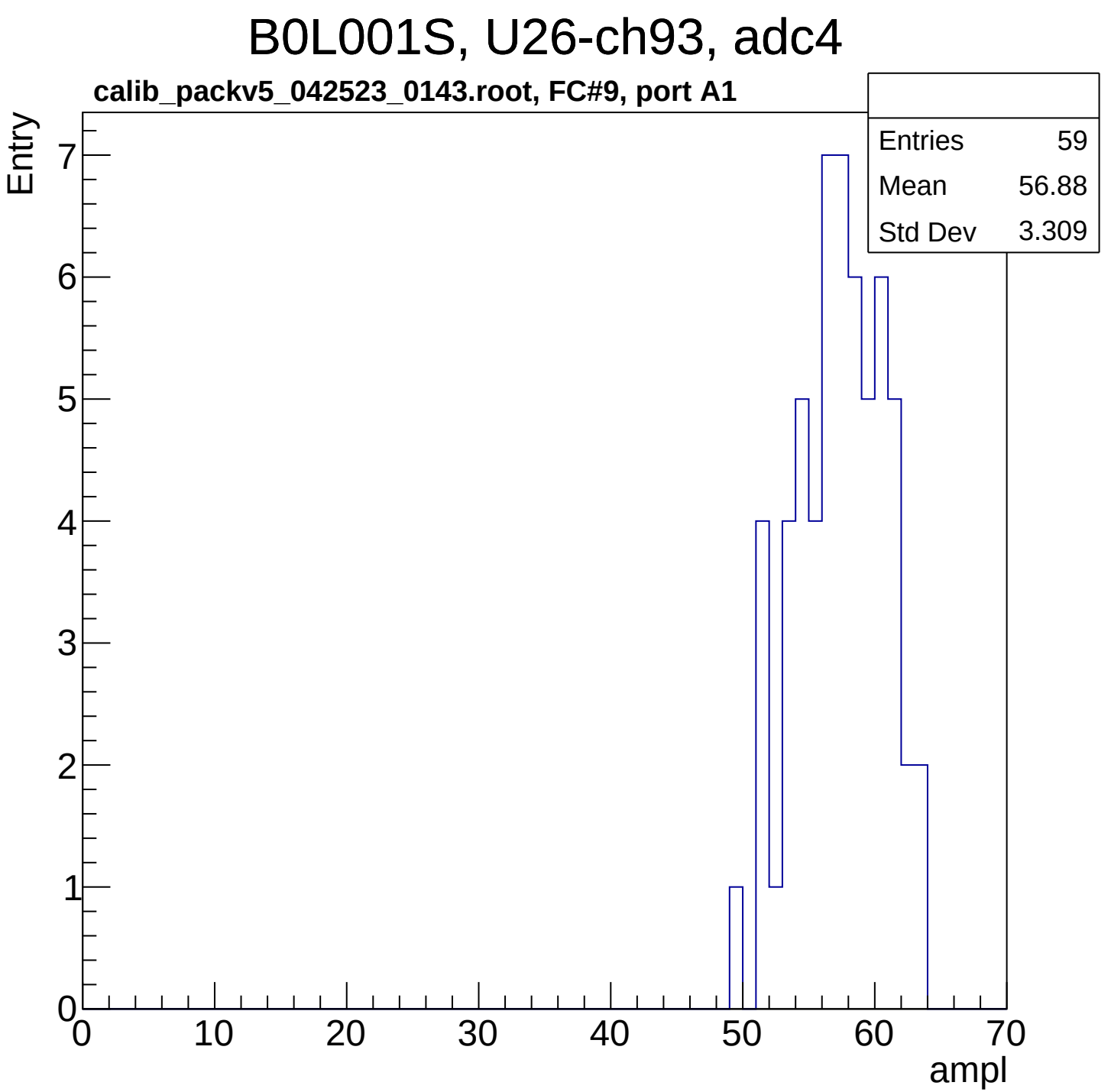
B0L001S, U26-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.88
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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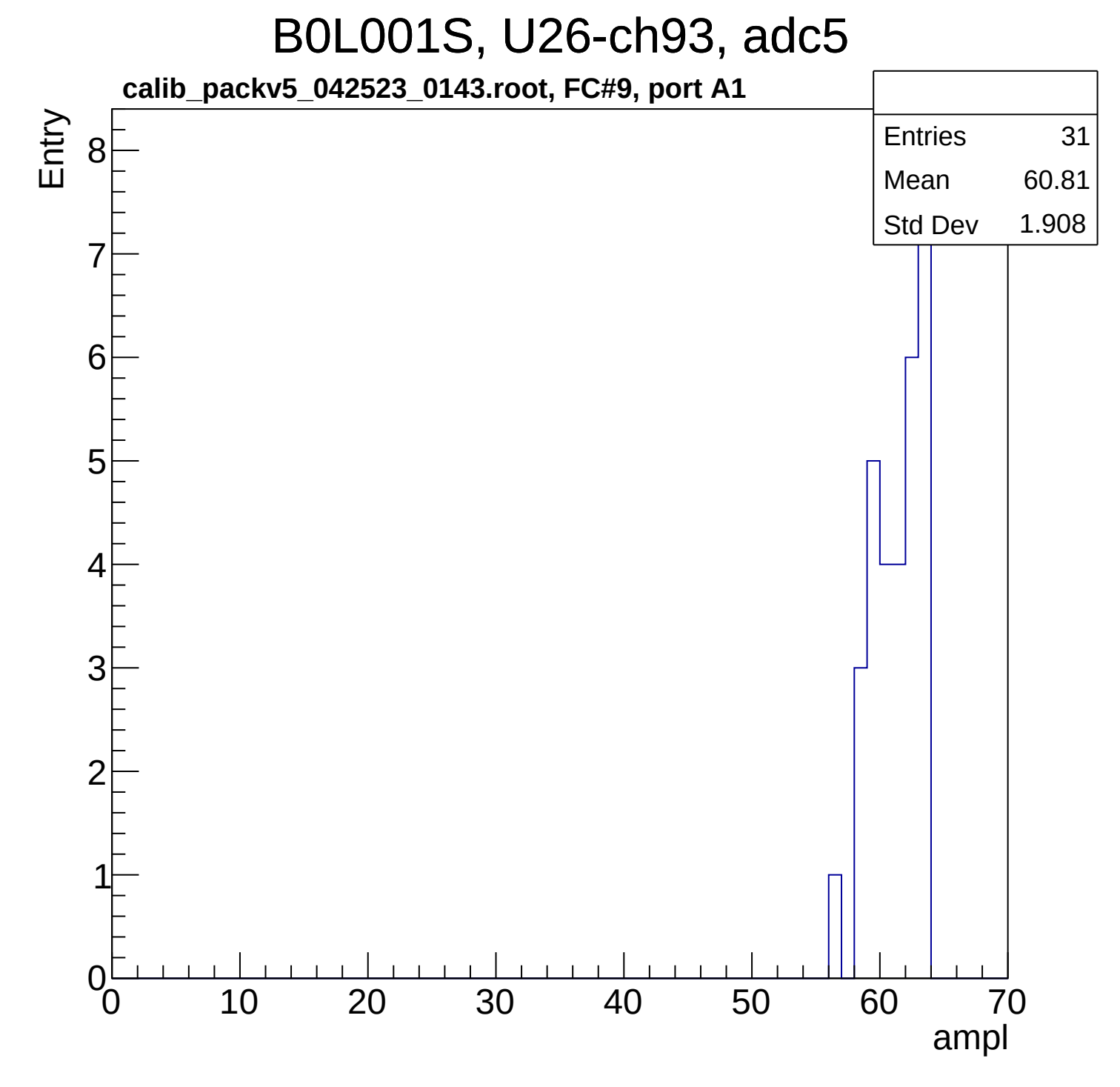
B0L001S, U26-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.81
Std Dev 1.908
Entry
ampl
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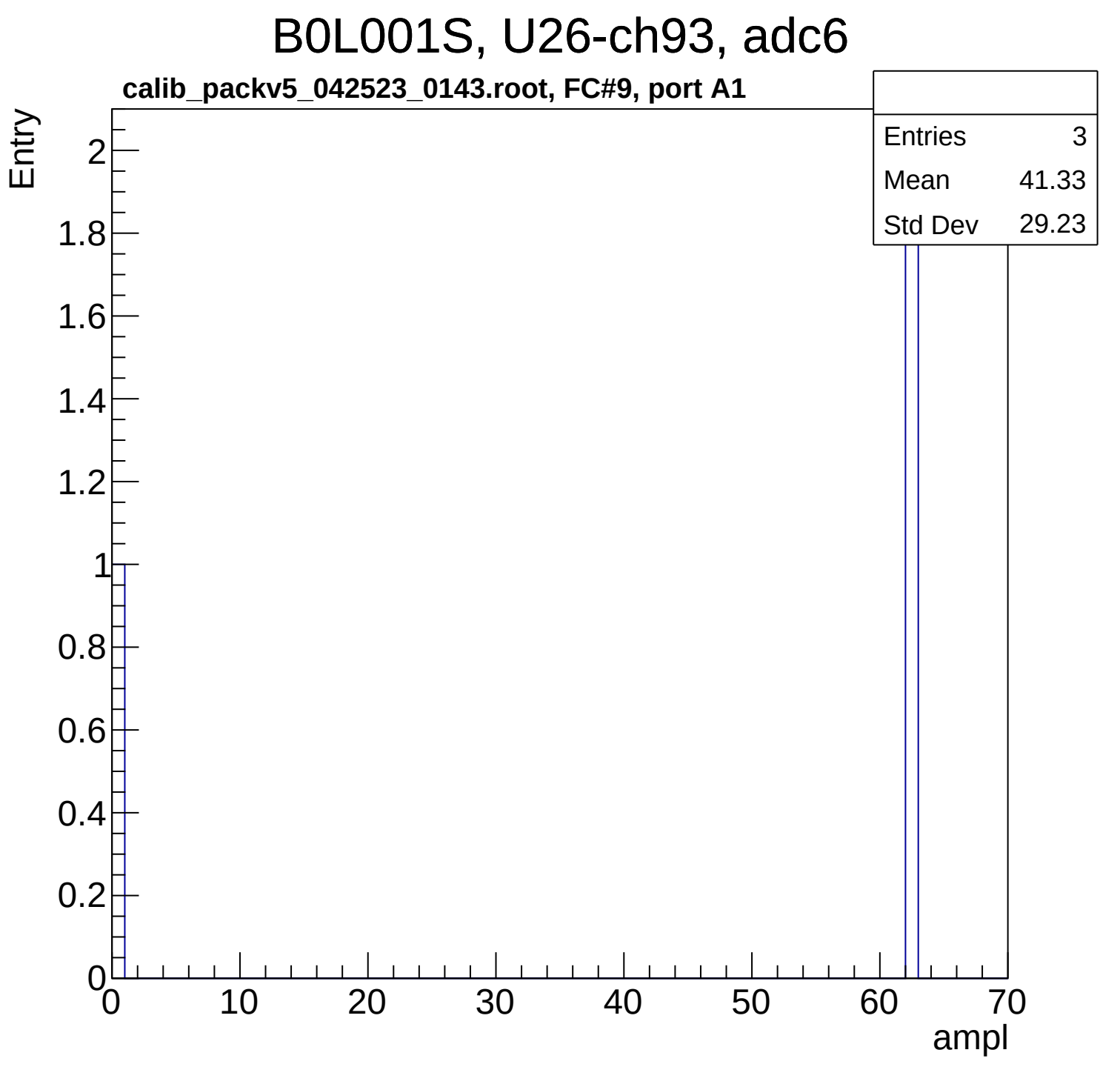
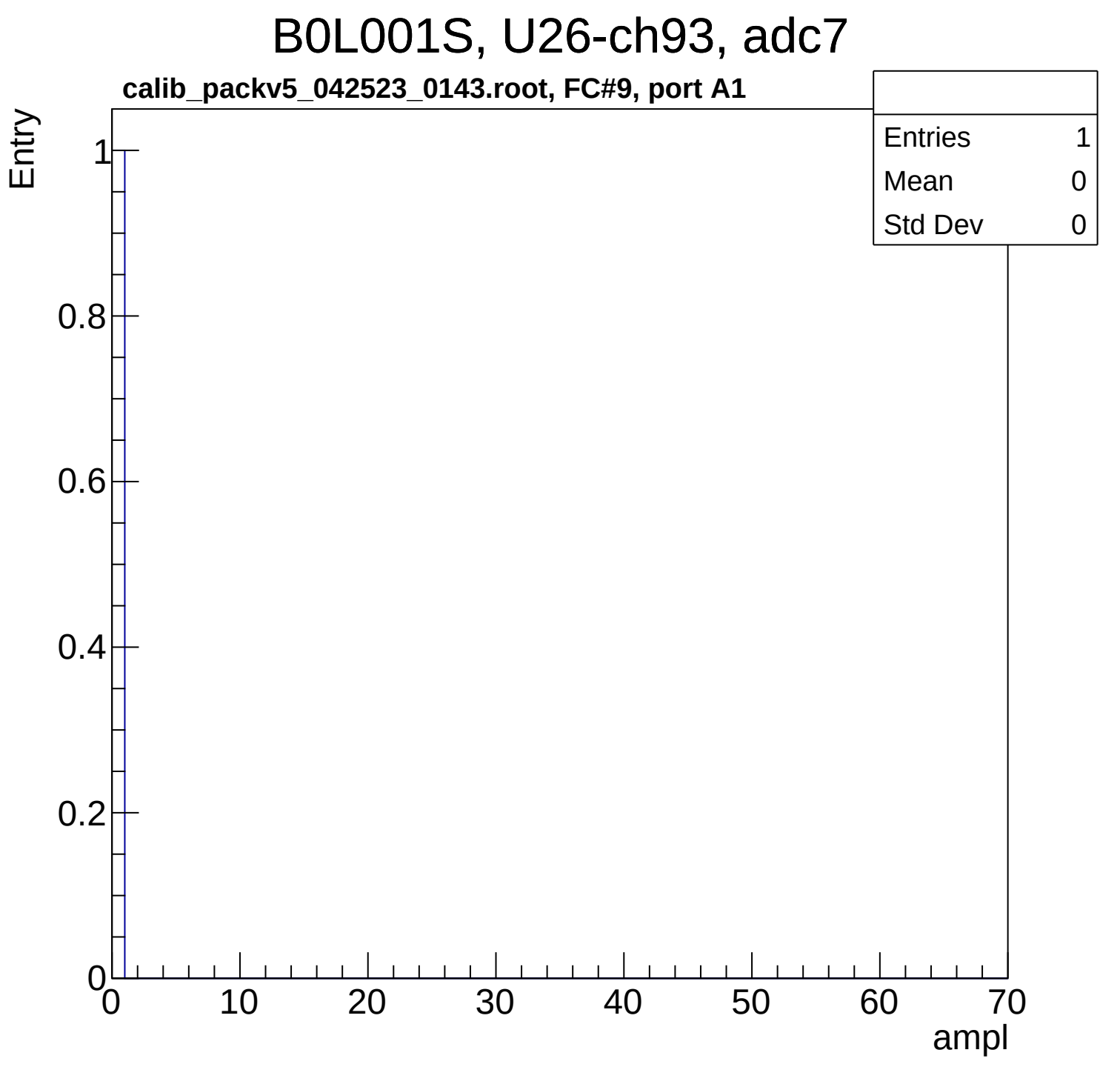
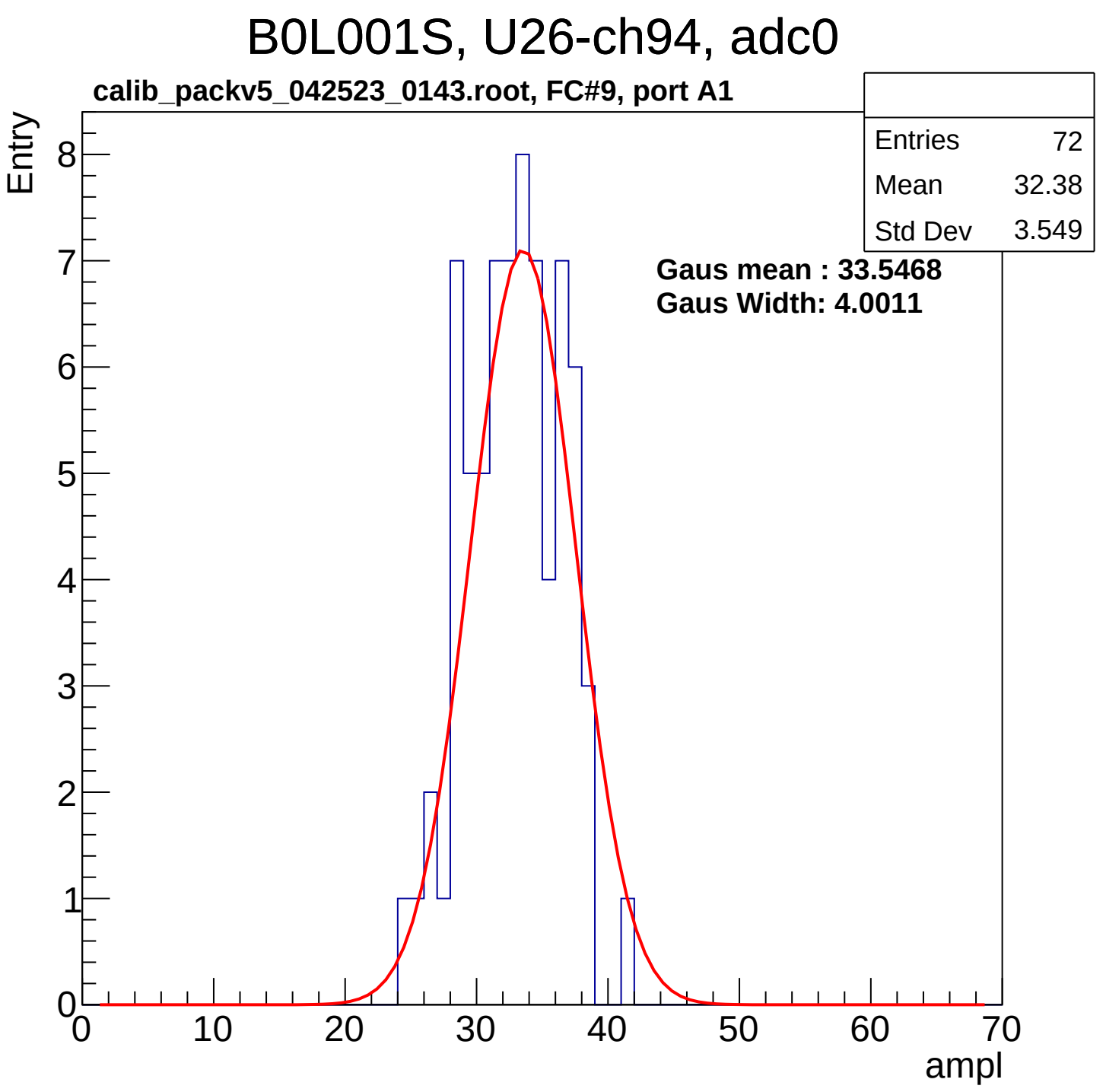
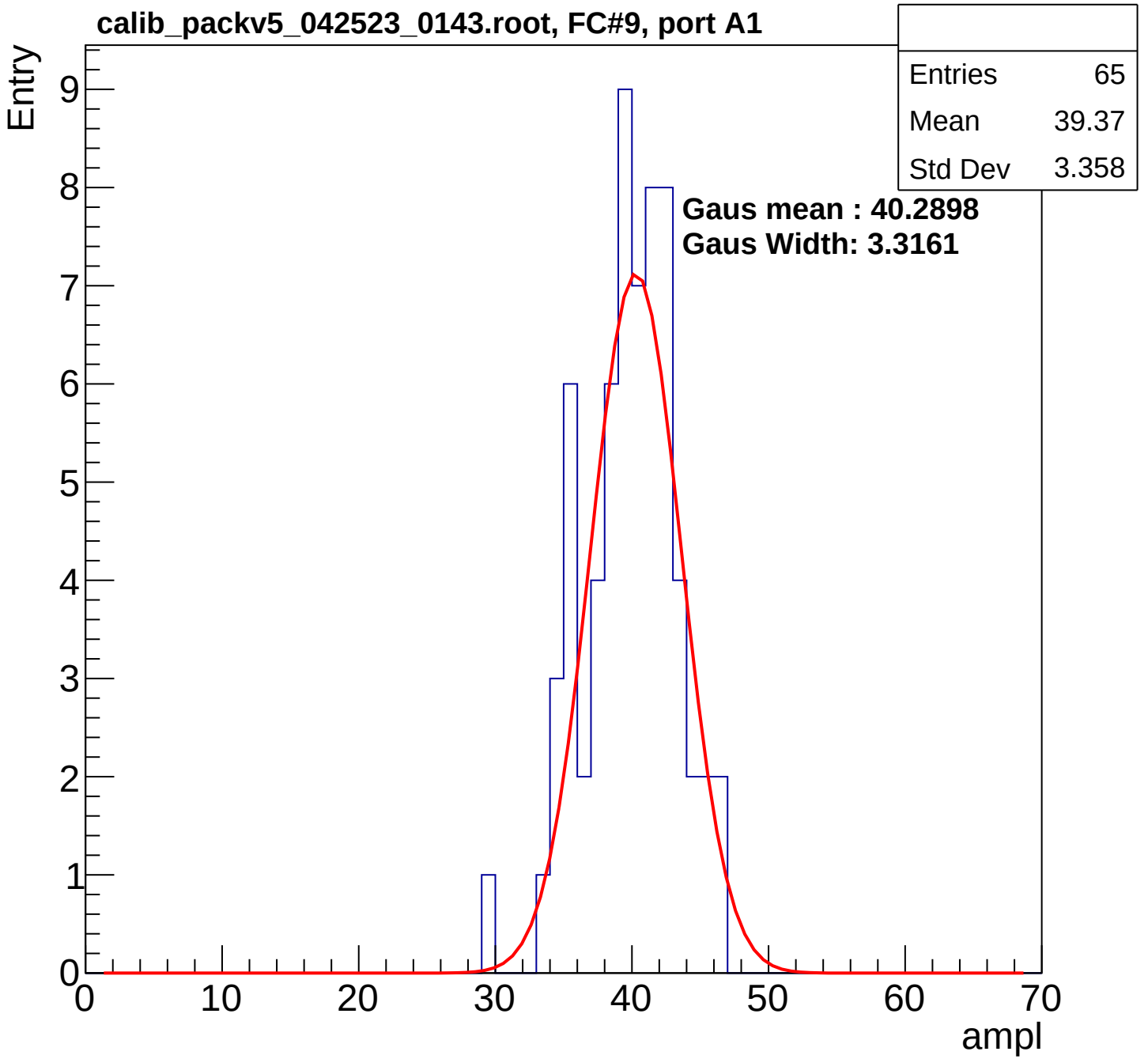
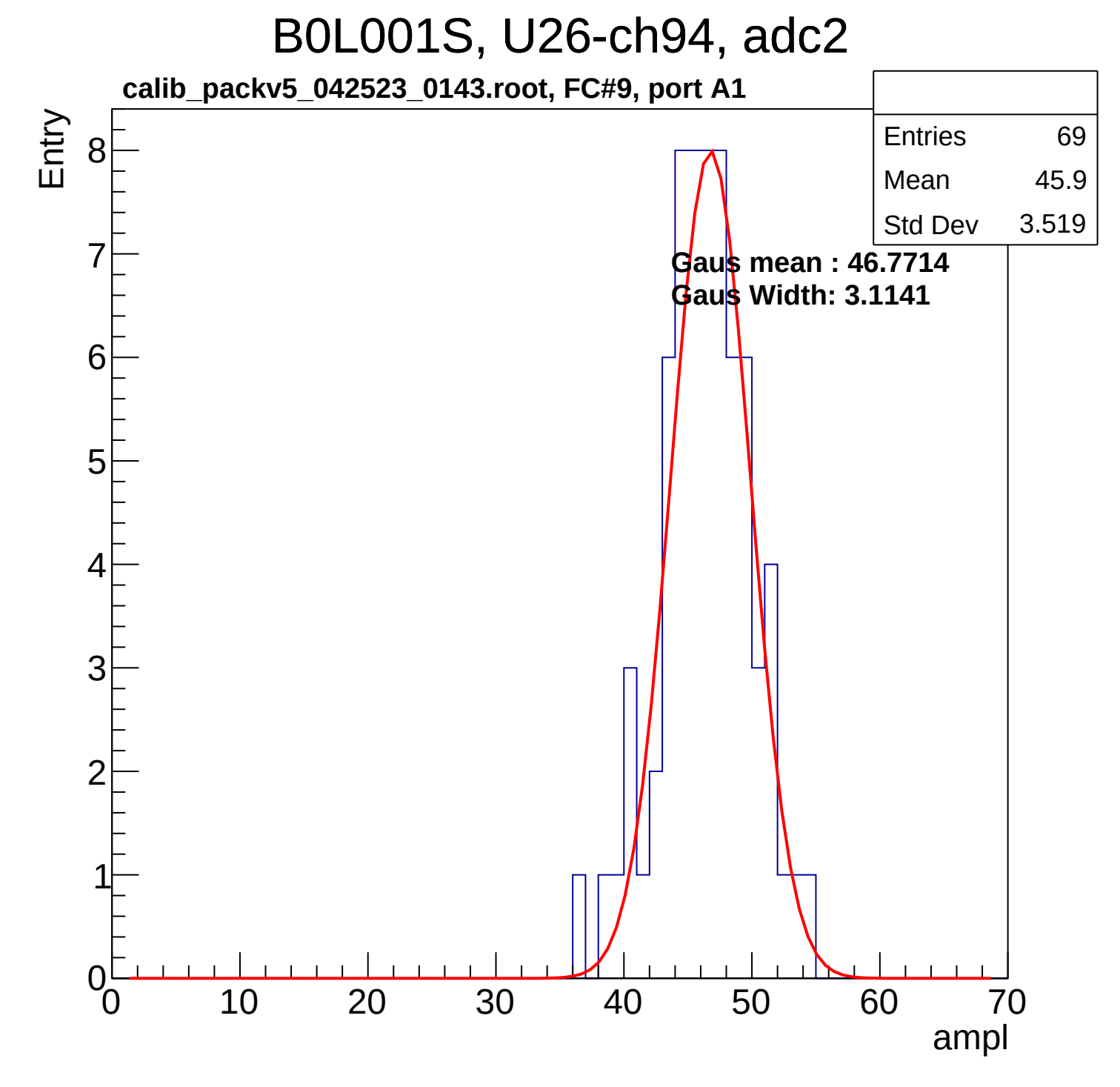
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

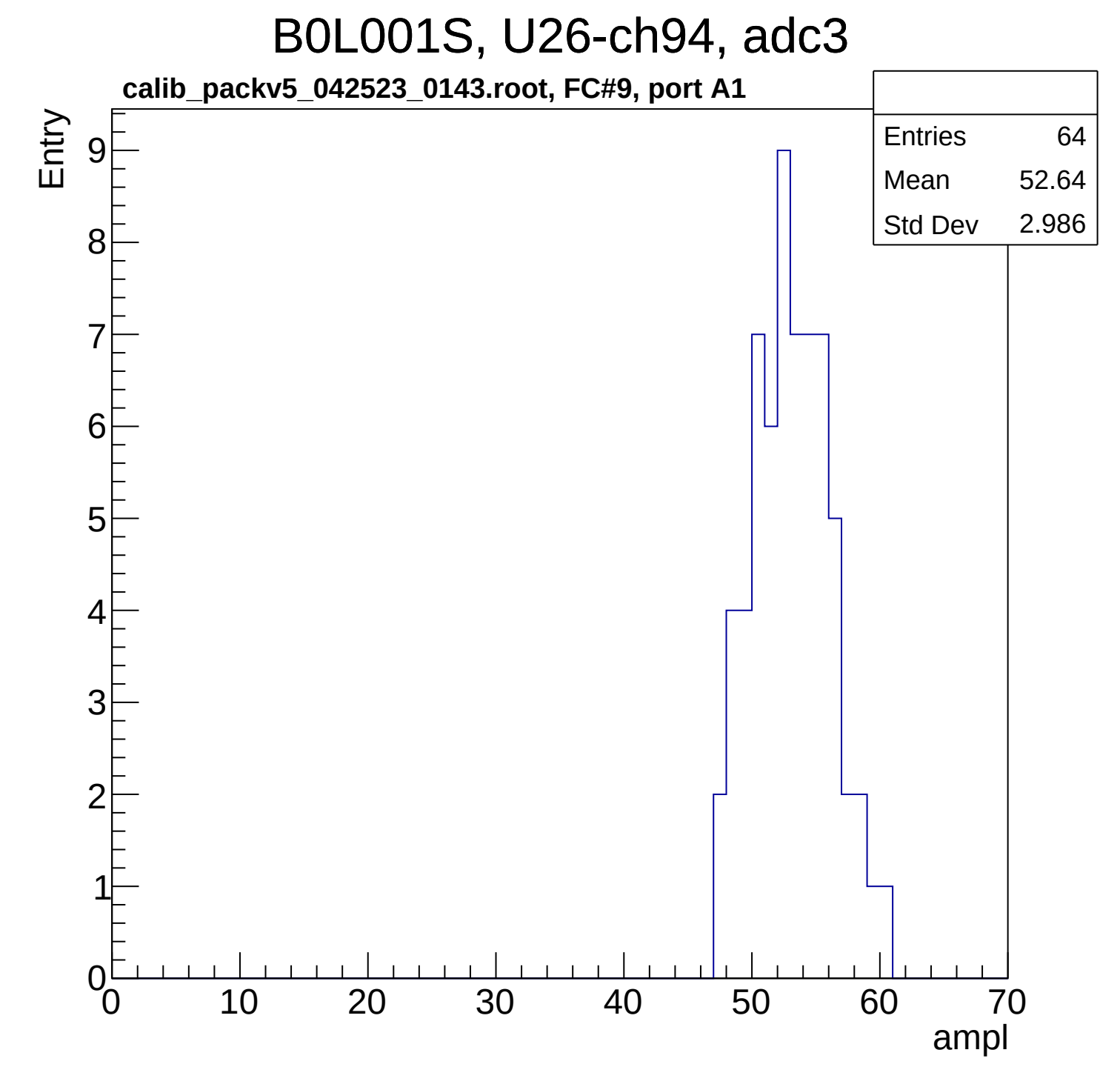

B0L001S, U26-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

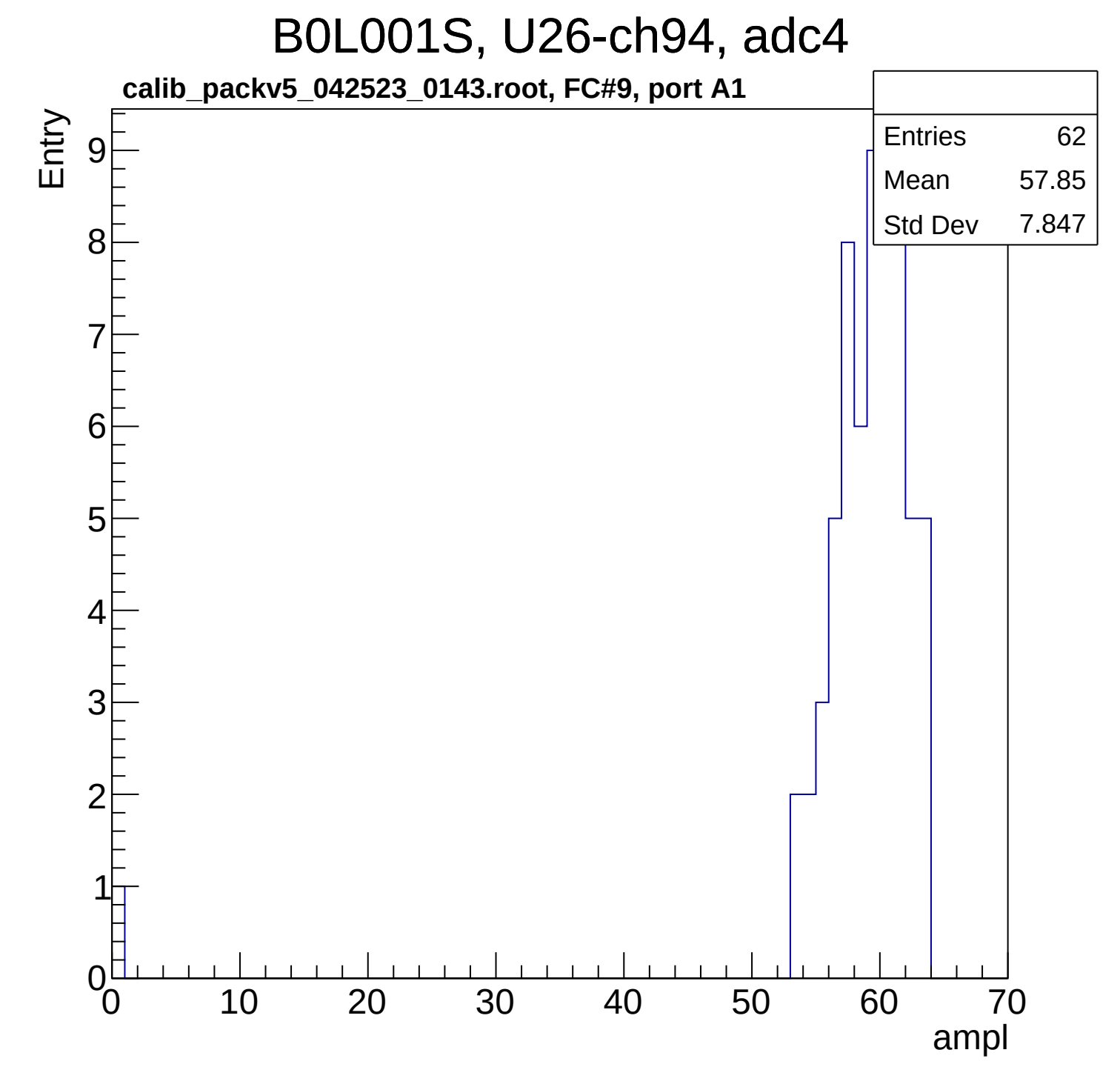

B0L001S, U26-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

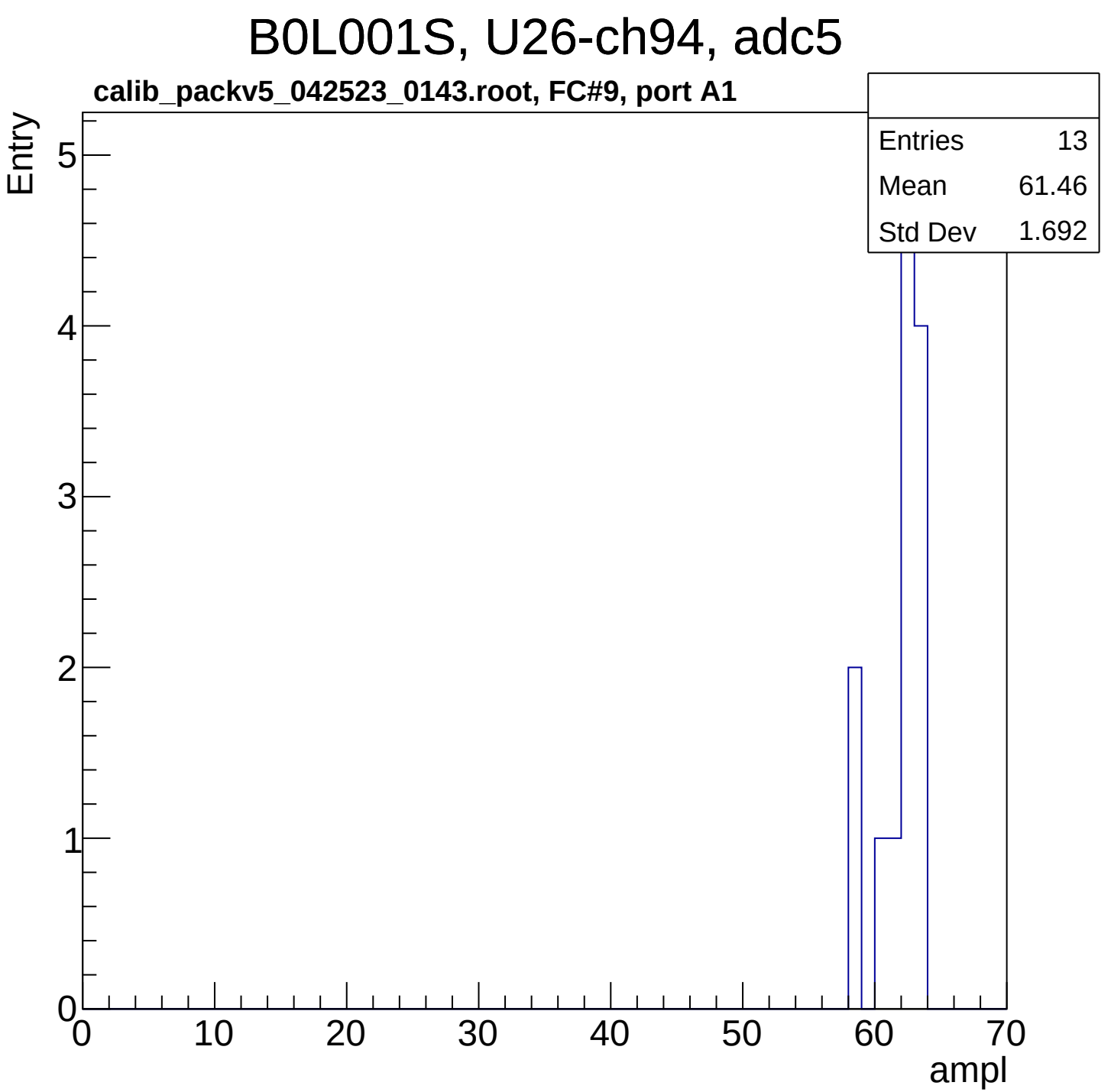

B0L001S, U26-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 32.38
Std Dev 3.549
Gaus mean : 33.5468
Gaus Width: 4.0011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

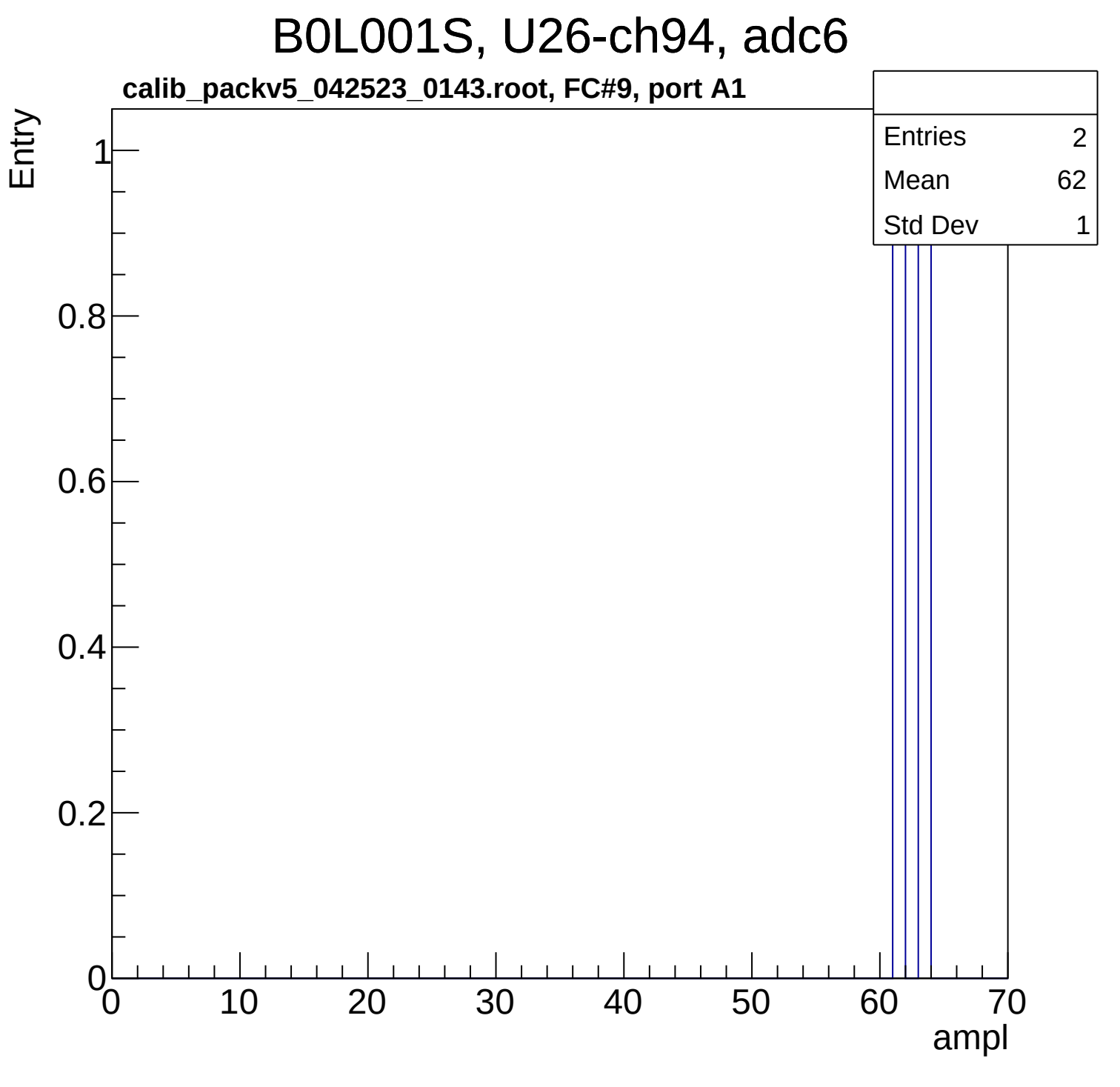

B0L001S, U26-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 39.37
Std Dev 3.358
Gaus mean : 40.2898
Gaus Width: 3.3161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

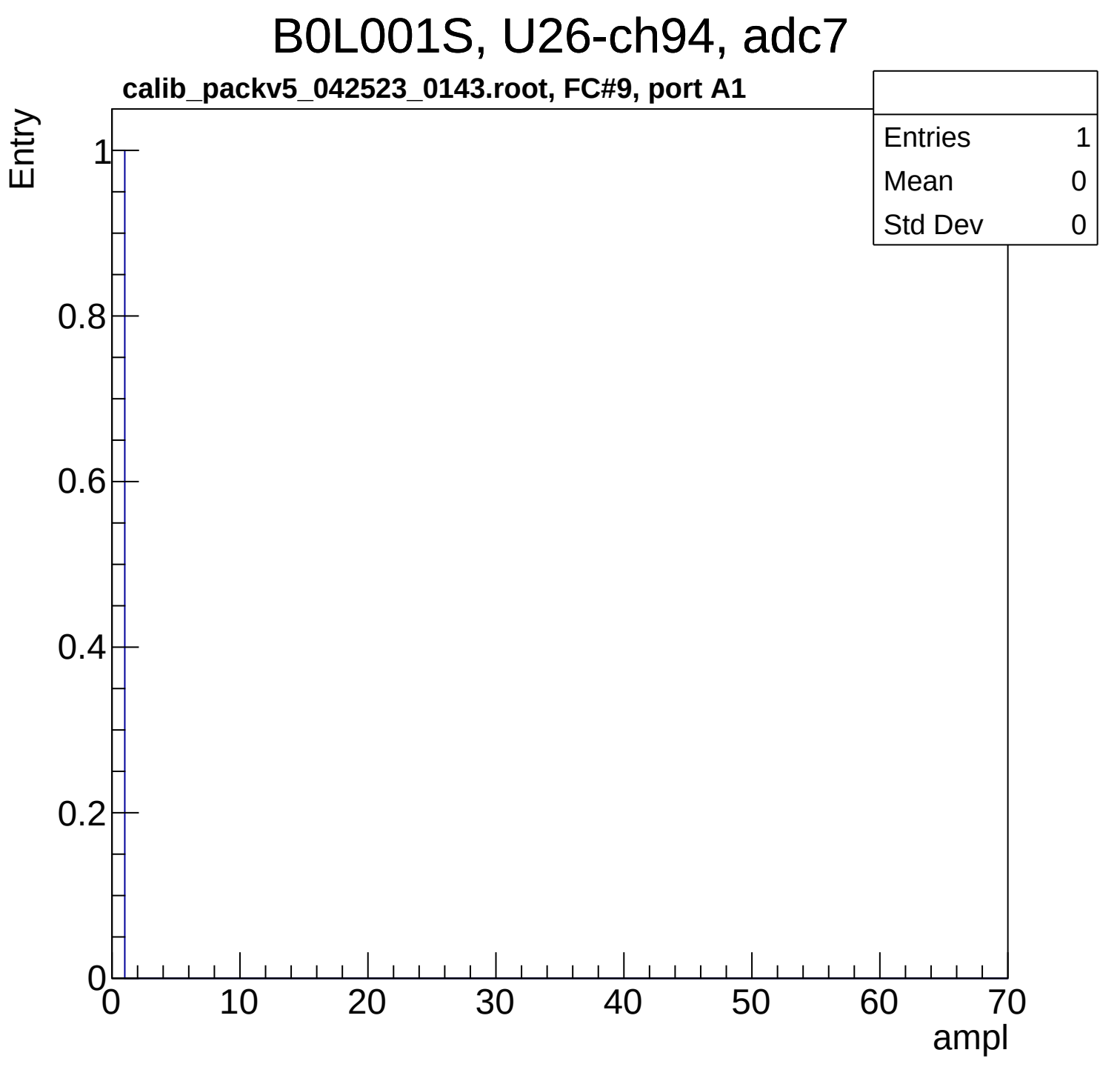

B0L001S, U26-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.9
Std Dev 3.519
Gaus mean : 46.7714
Gaus Width: 3.1141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

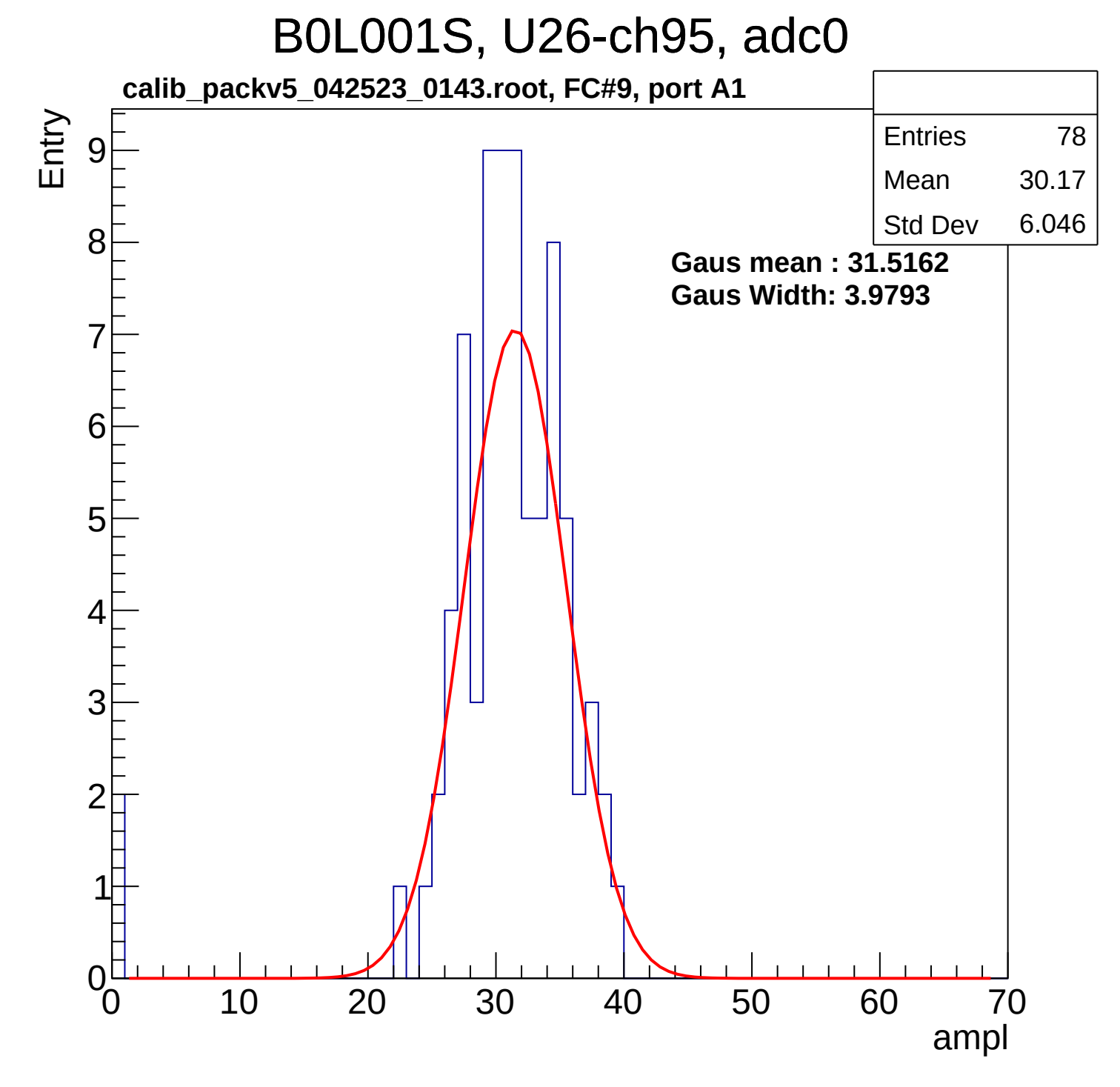

B0L001S, U26-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.64
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 57.85
Std Dev 7.847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.46
Std Dev 1.692
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.17
Std Dev 6.046
Gaus mean : 31.5162
Gaus Width: 3.9793
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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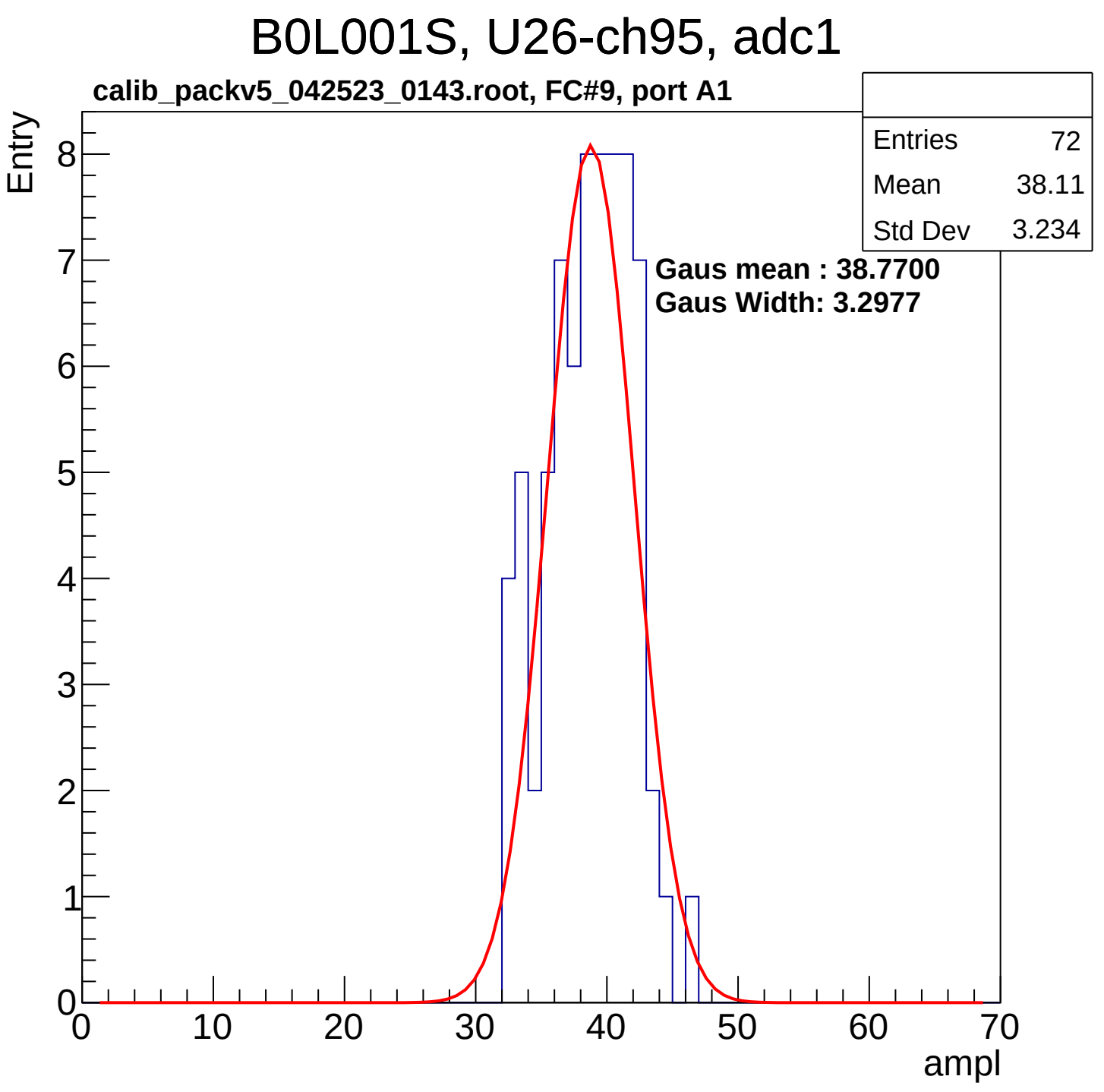
20
30
40
50
60
70


B0L001S, U26-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.11
Std Dev 3.234
Gaus mean : 38.7700
Gaus Width: 3.2977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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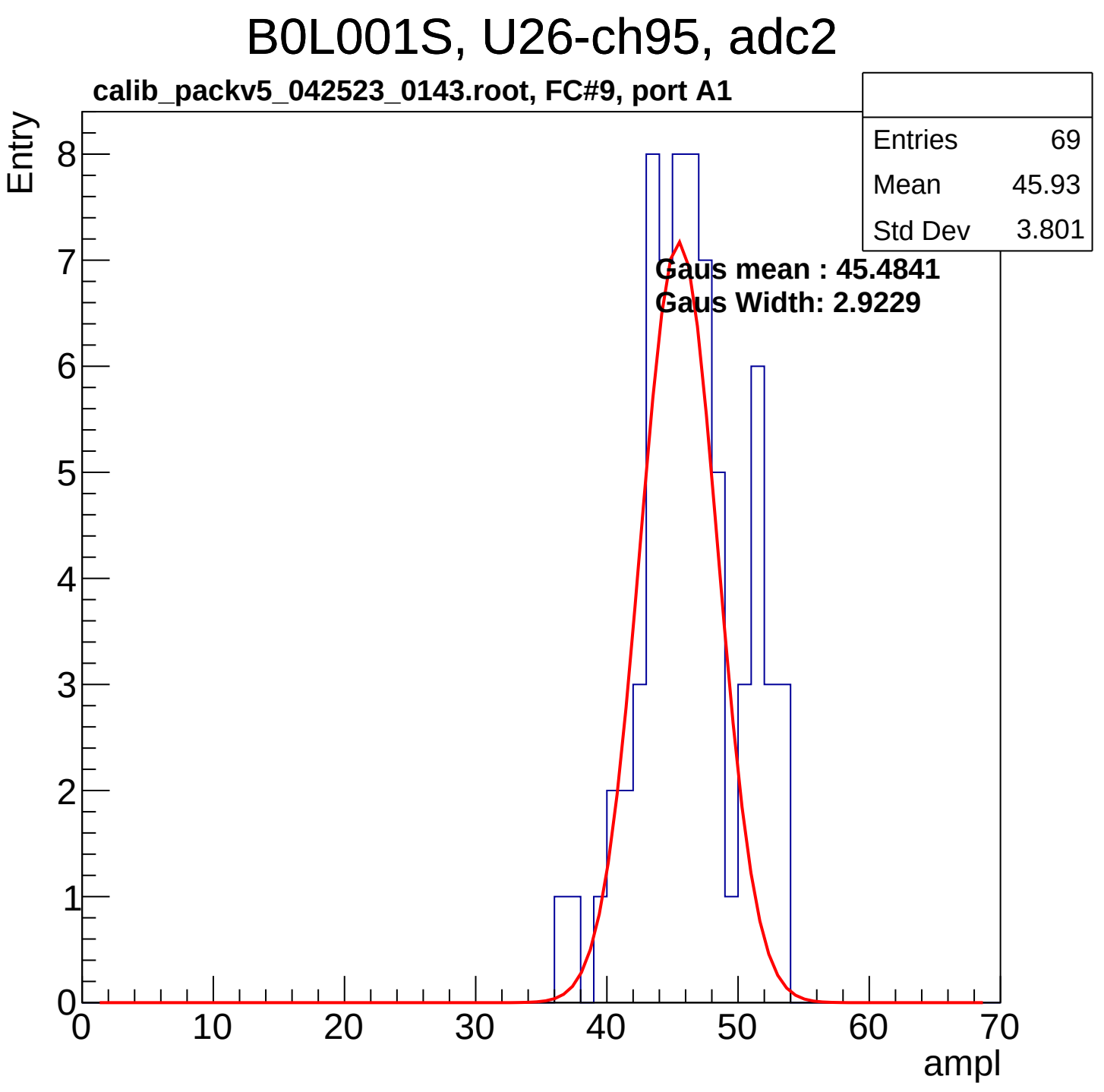
60
70


B0L001S, U26-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.93
Std Dev 3.801
Gaus mean : 45.4841
Gaus Width: 2.9229
Entry
ampl
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1
2
3
4
5
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0
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60
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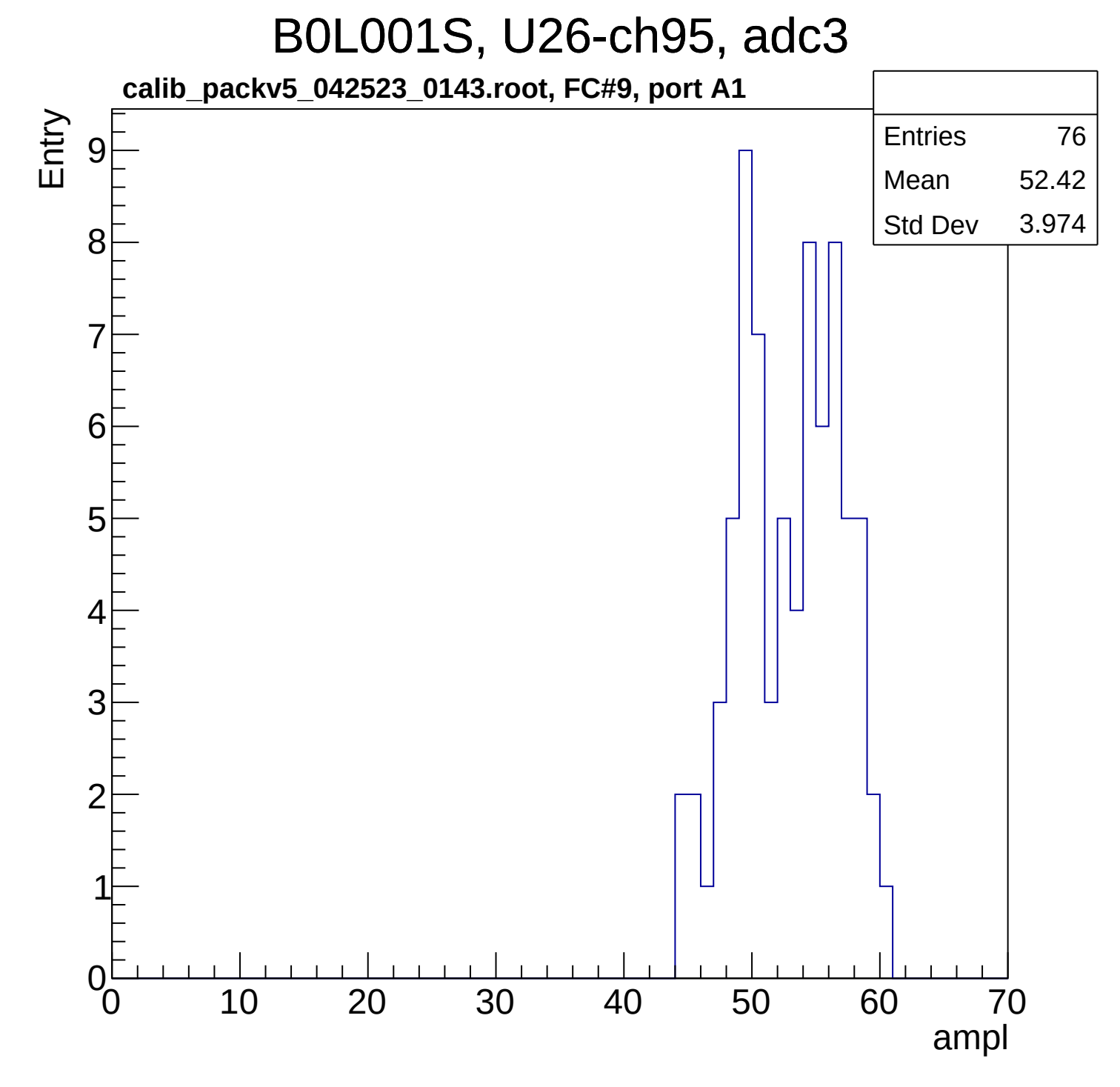
B0L001S, U26-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 52.42
Std Dev 3.974
Entry
ampl
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3
4
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6
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8
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0
10
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60
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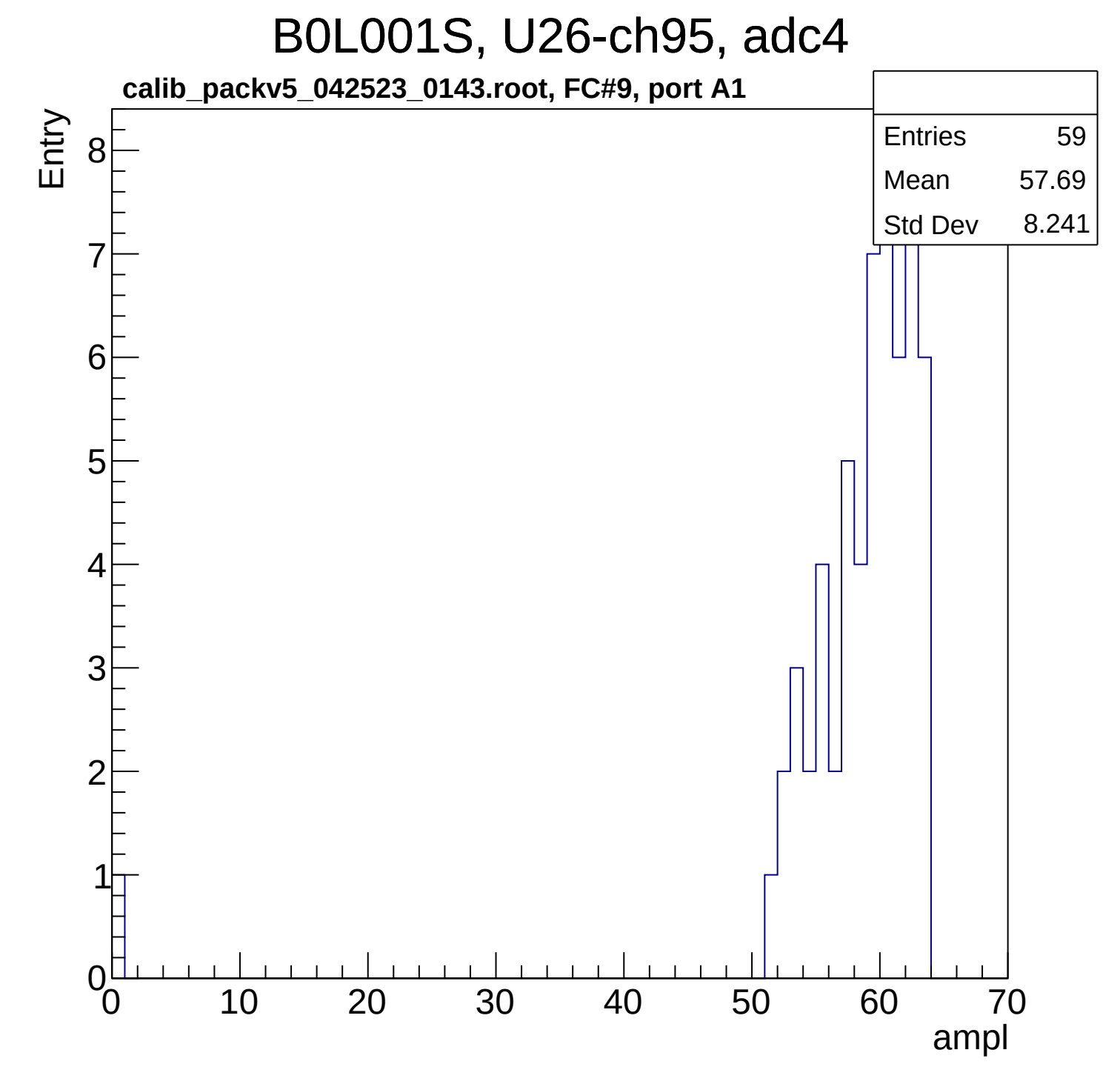
B0L001S, U26-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.69
Std Dev 8.241
Entry
ampl
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1
2
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5
6
7
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0
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40
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60
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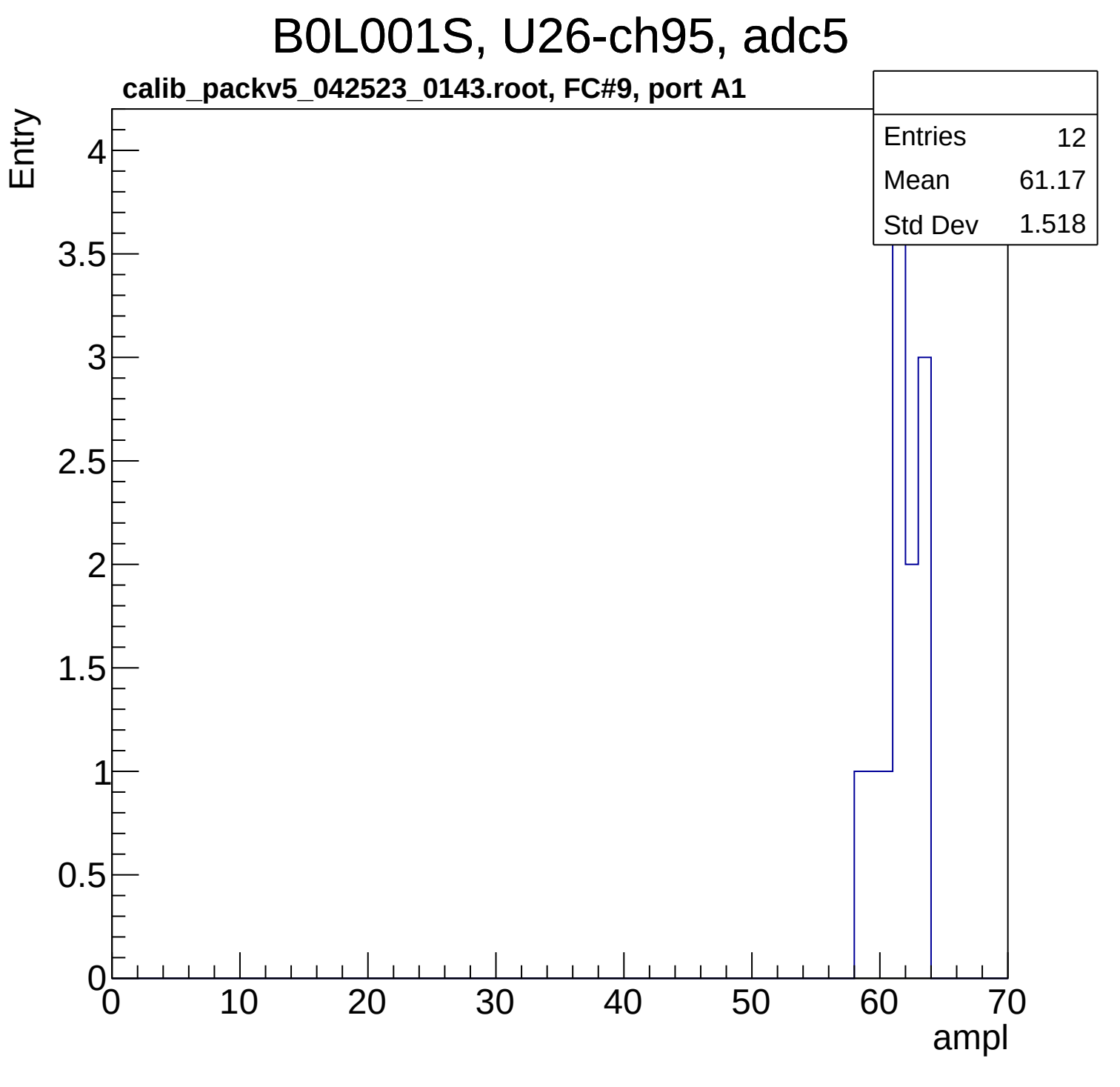
B0L001S, U26-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 61.17
Std Dev 1.518
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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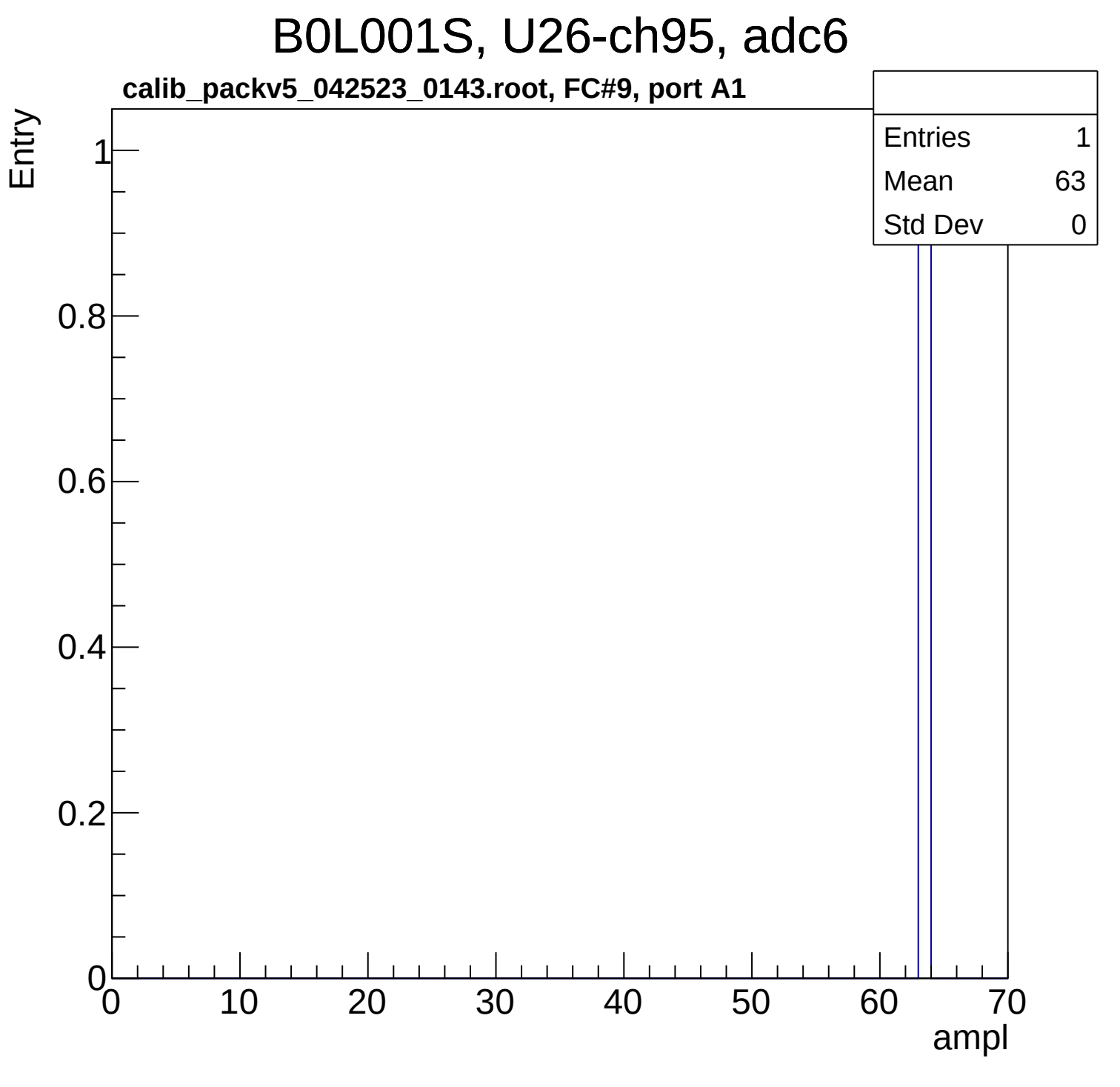
40
50
60
70


B0L001S, U26-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
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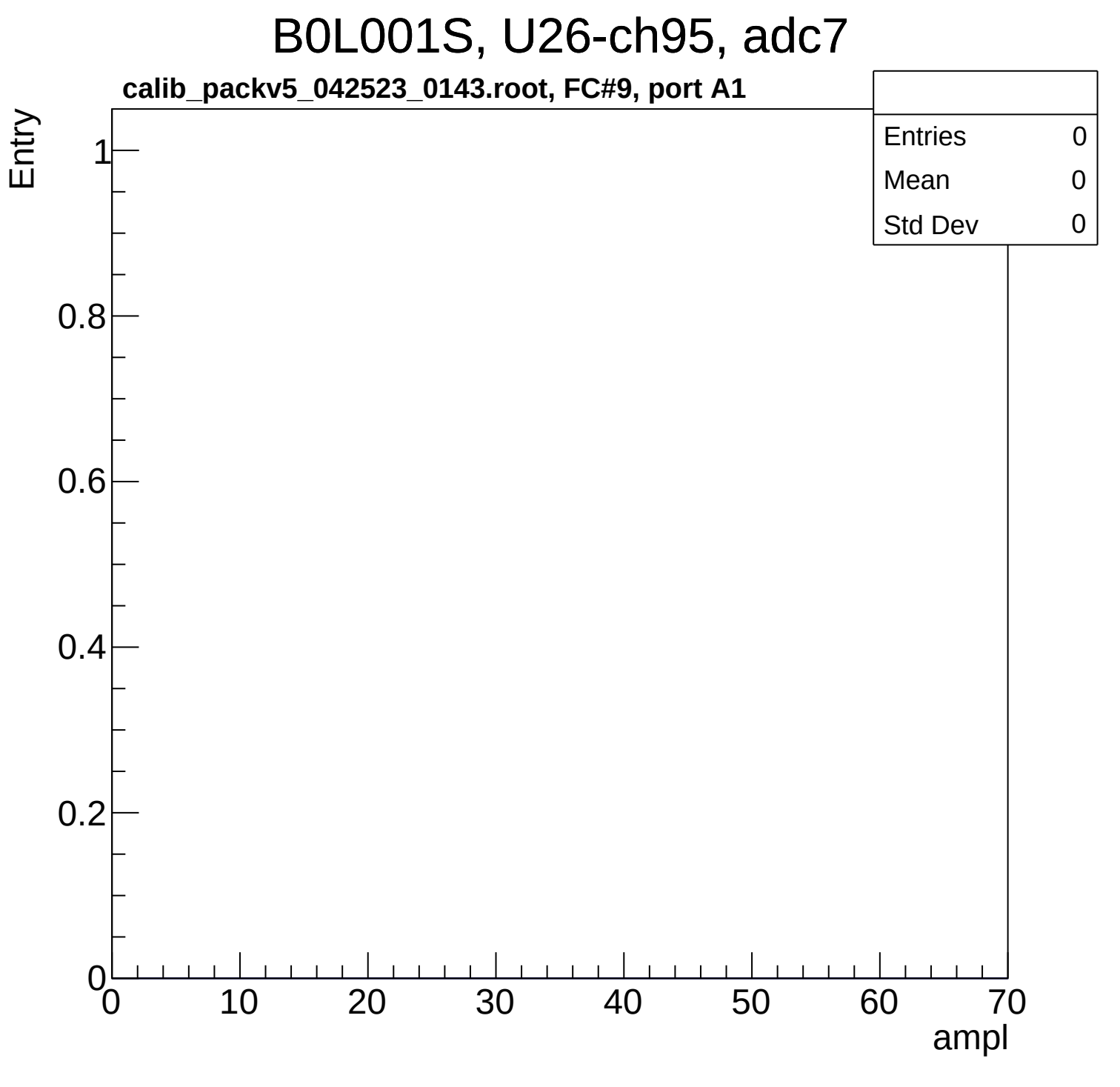
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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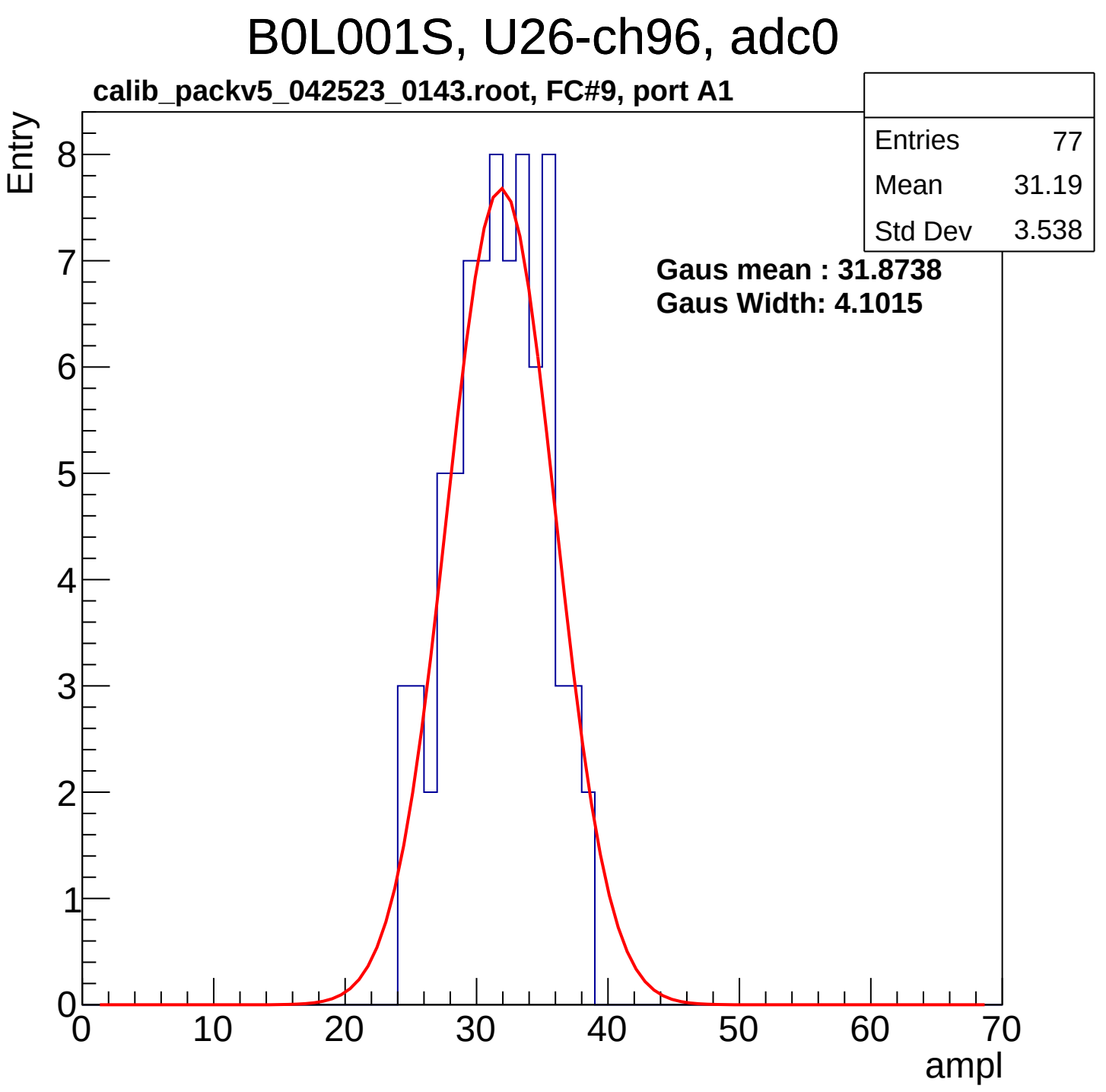
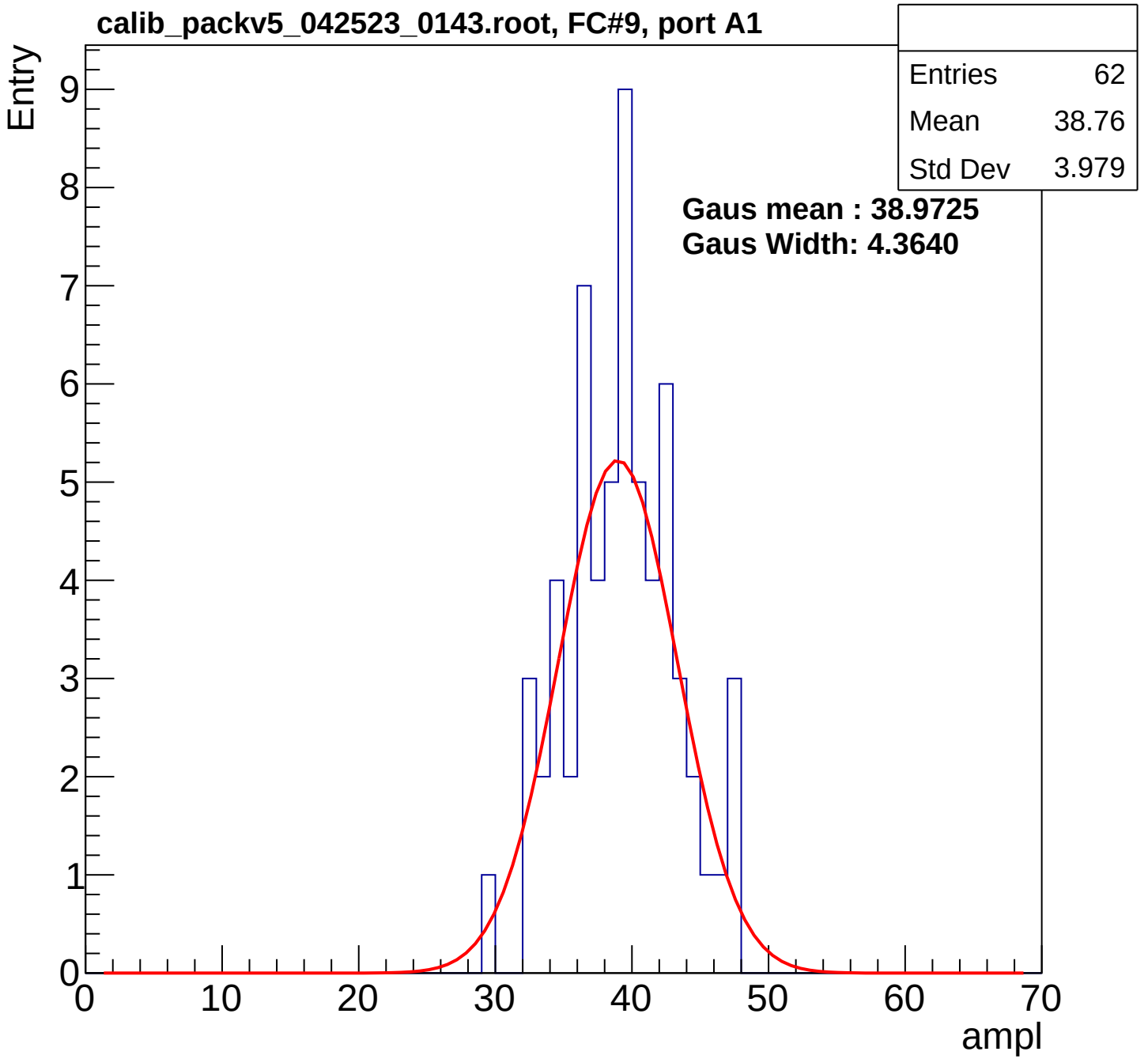
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.19
Std Dev 3.538
Gaus mean : 31.8738
Gaus Width: 4.1015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.76
Std Dev 3.979
Gaus mean : 38.9725
Gaus Width: 4.3640
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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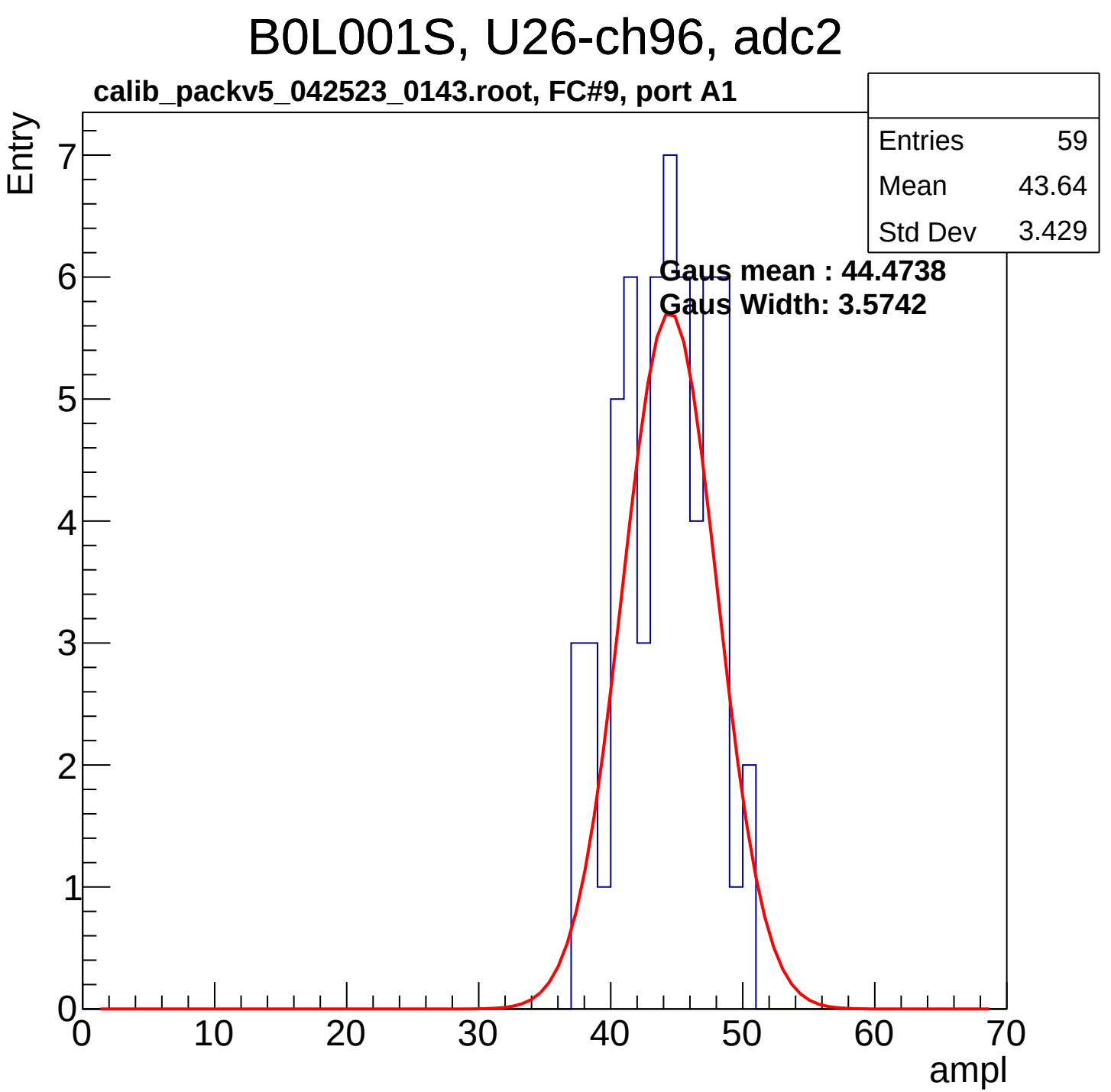
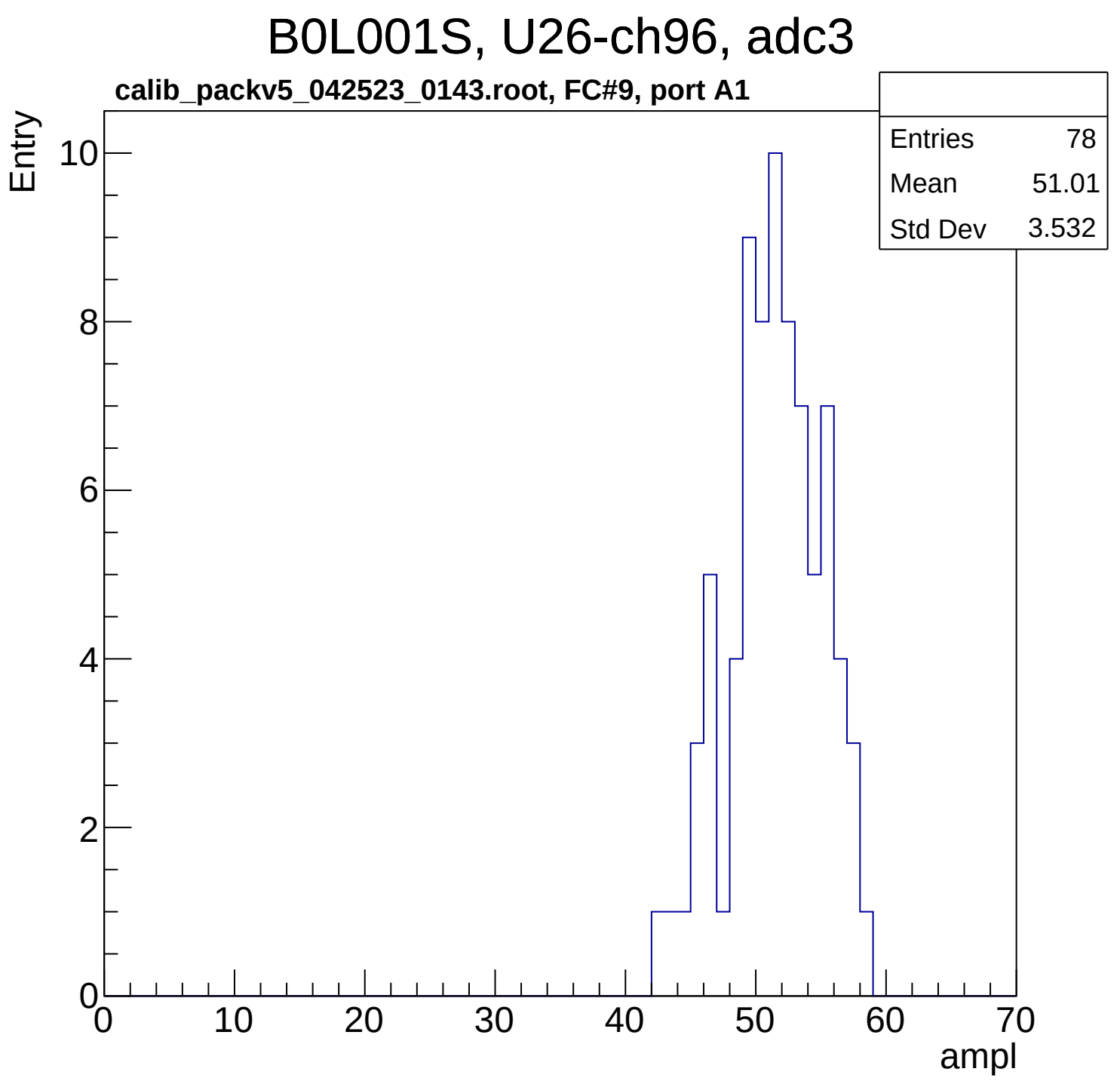
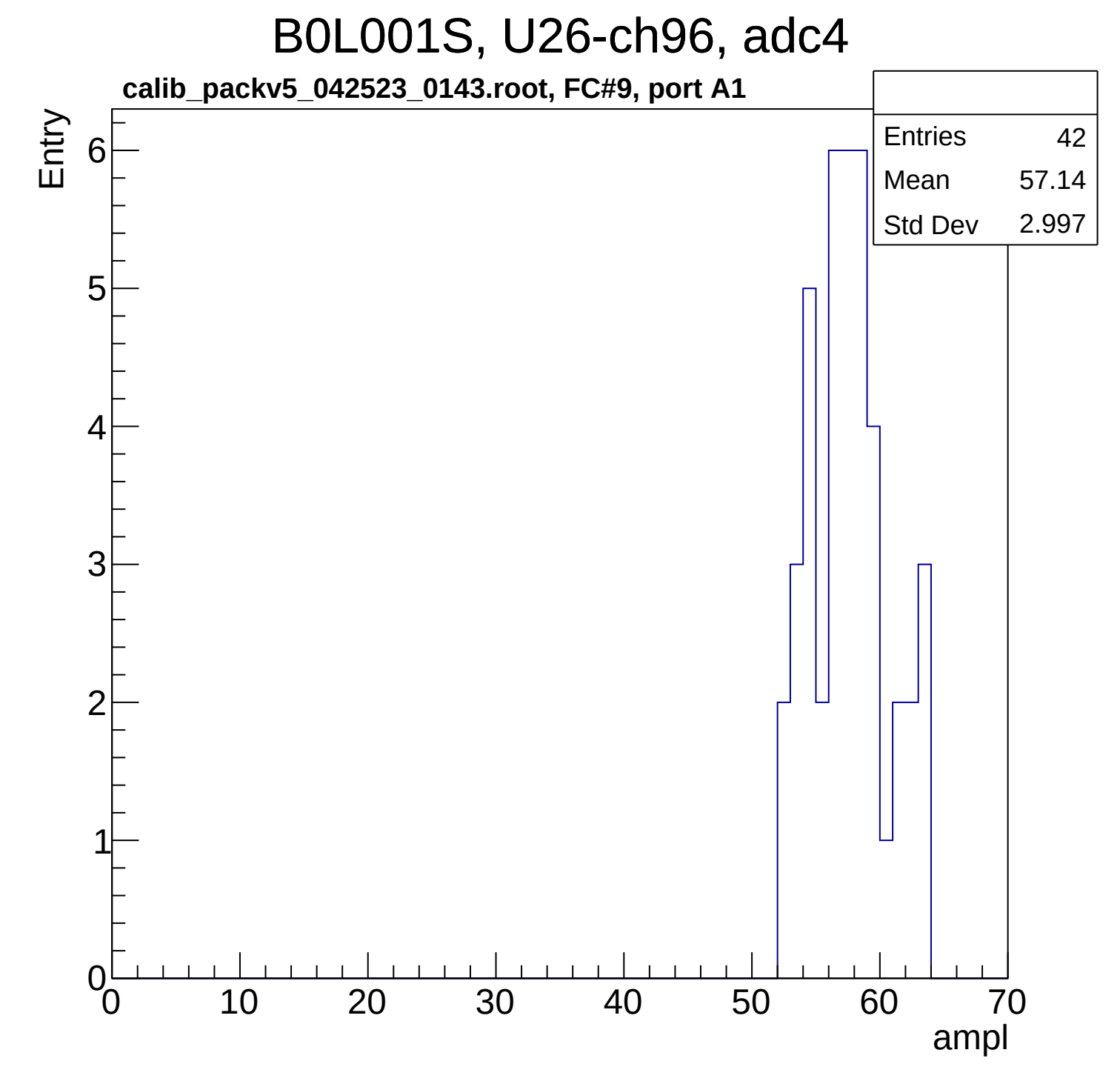
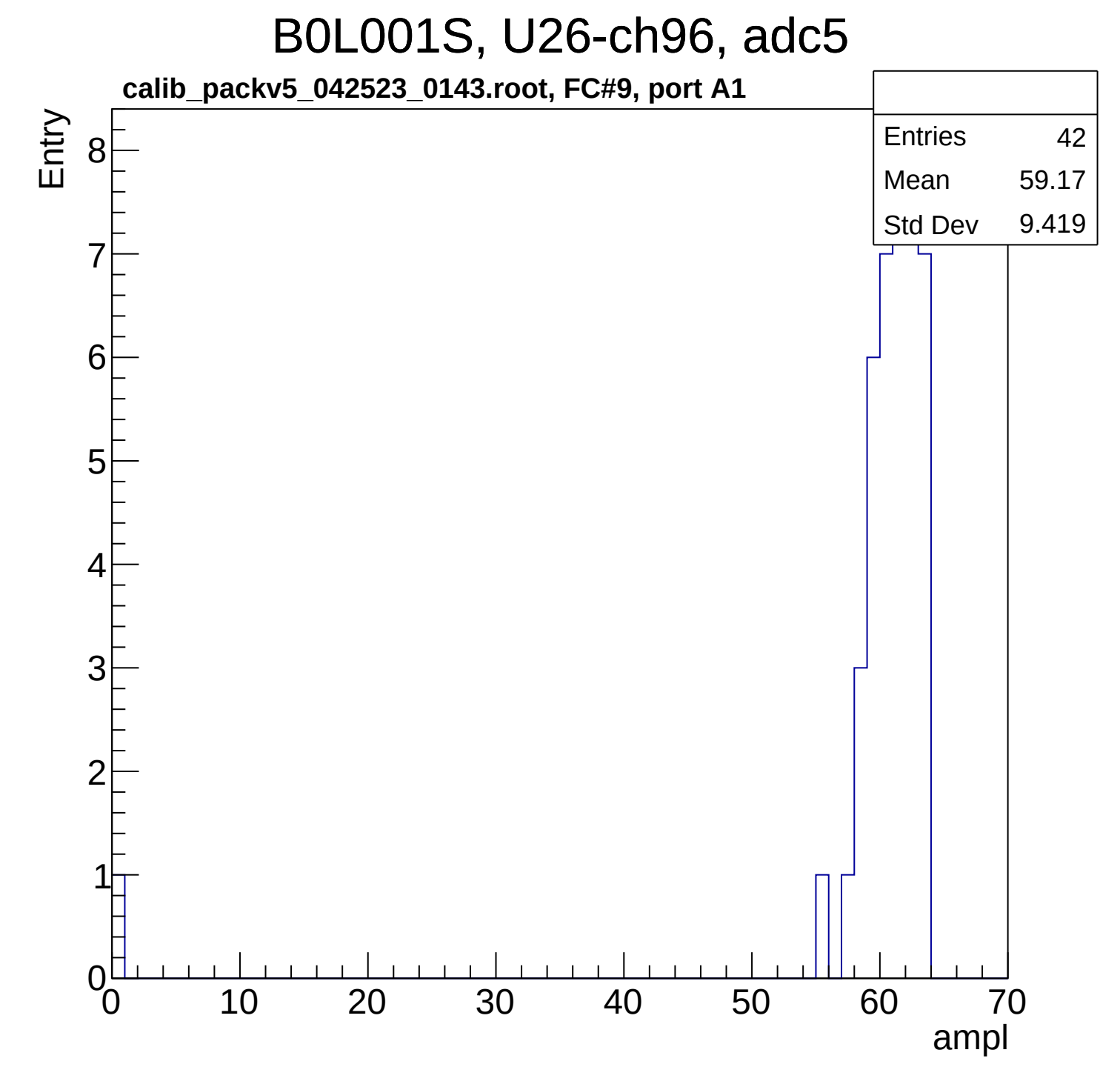

B0L001S, U26-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.64
Std Dev 3.429
Gaus mean : 44.4738
Gaus Width: 3.5742
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 51.01
Std Dev 3.532
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 57.14
Std Dev 2.997
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U26-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.17
Std Dev 9.419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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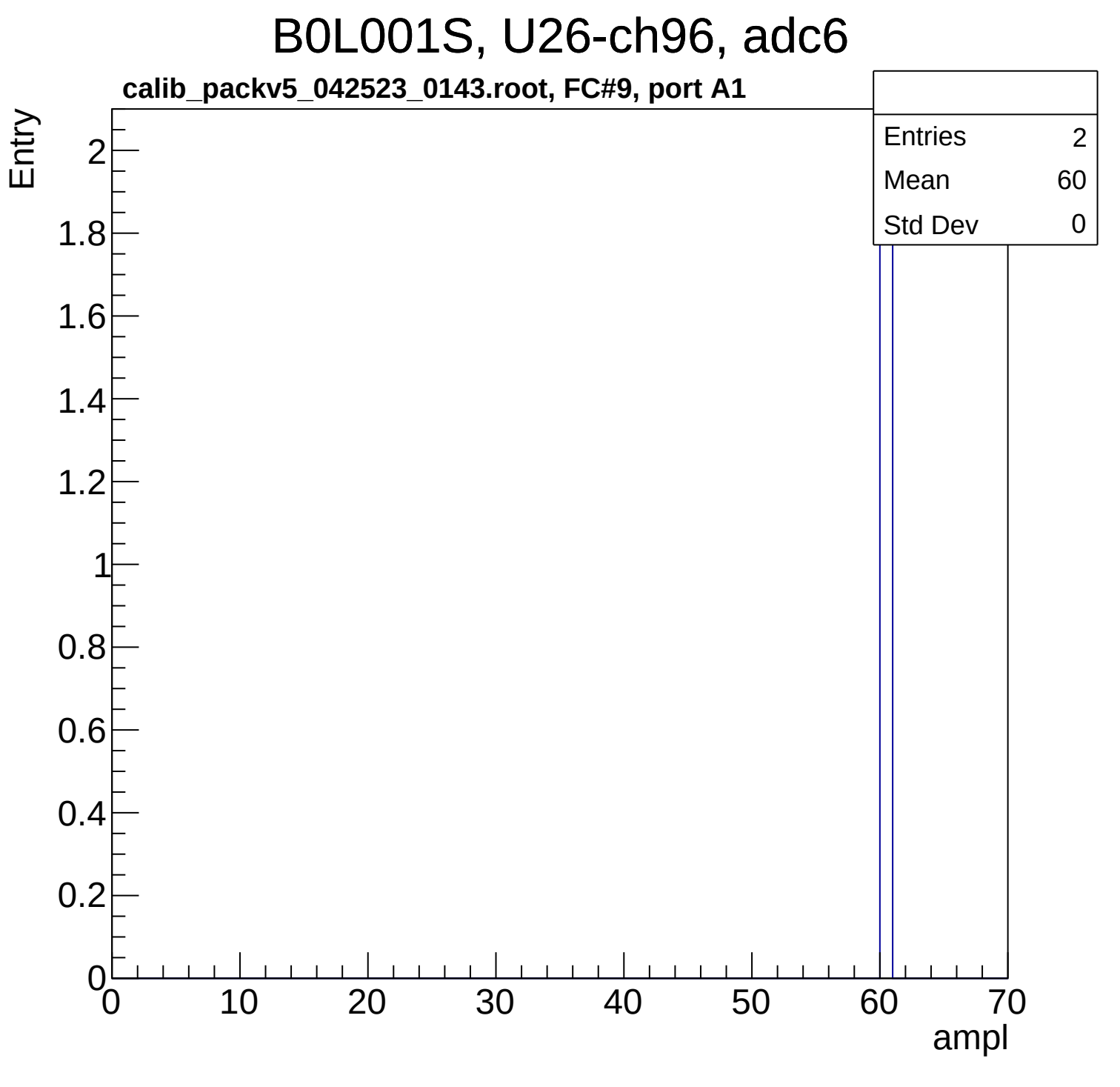
B0L001S, U26-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 60
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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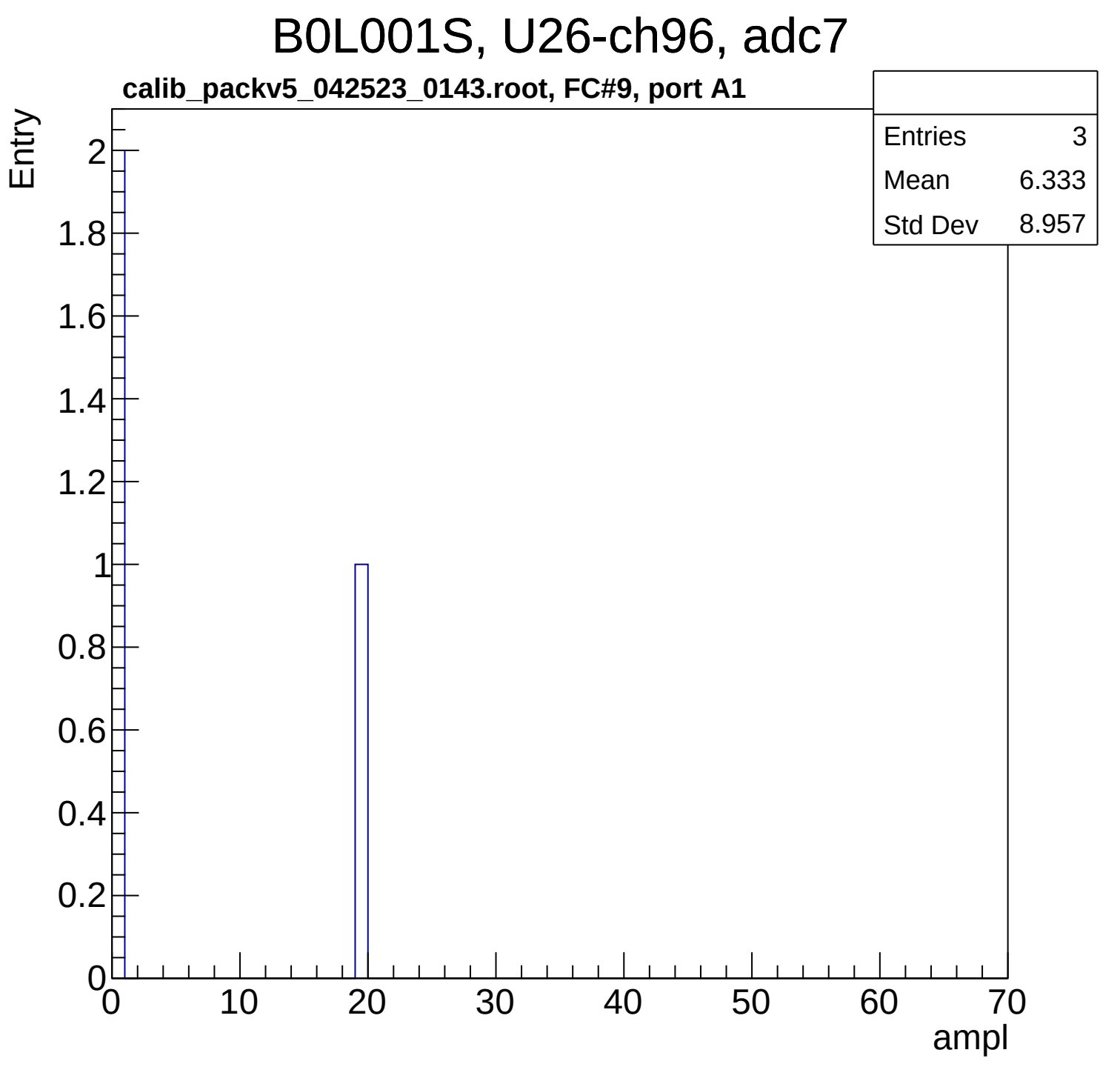
B0L001S, U26-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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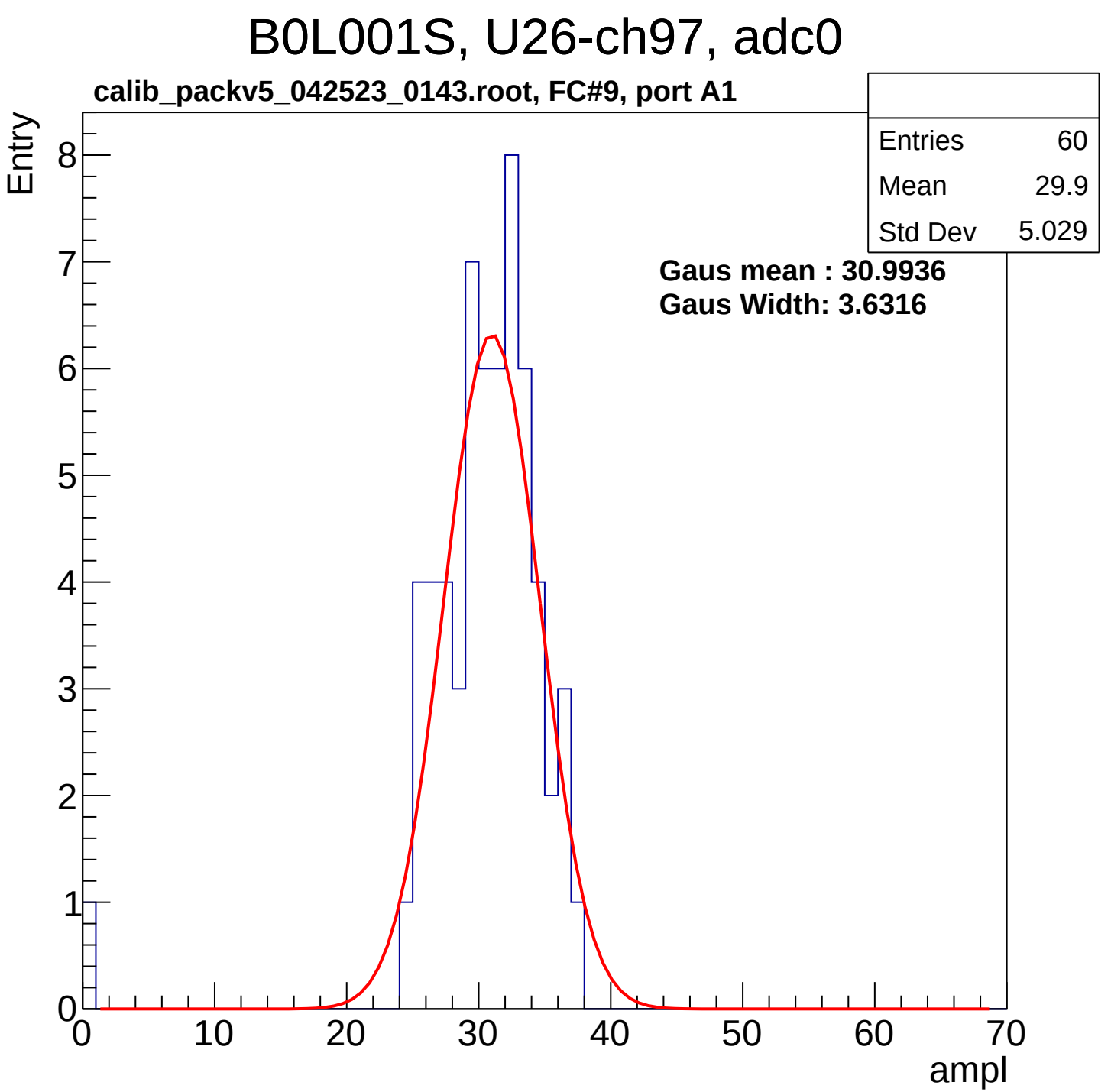
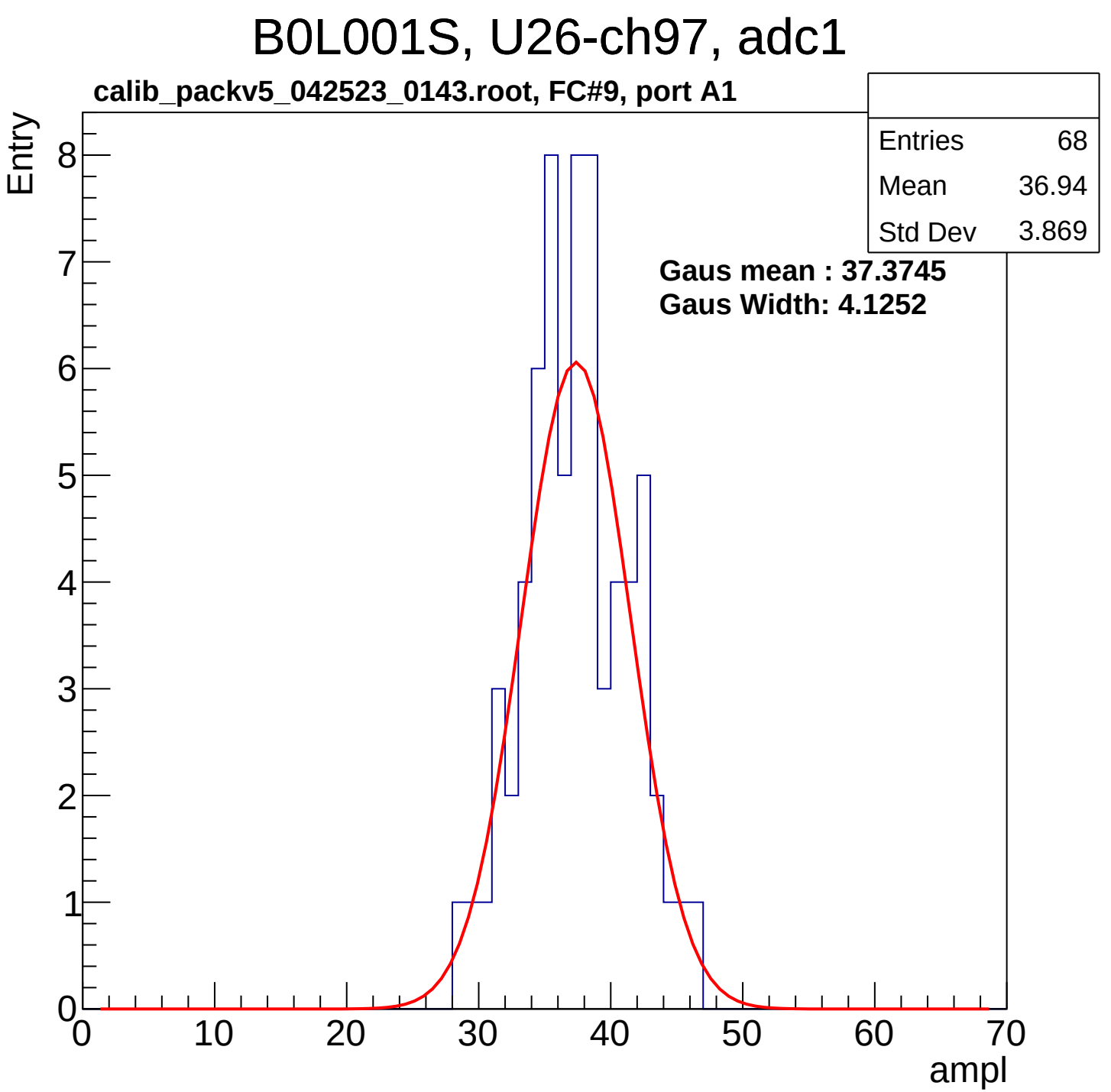
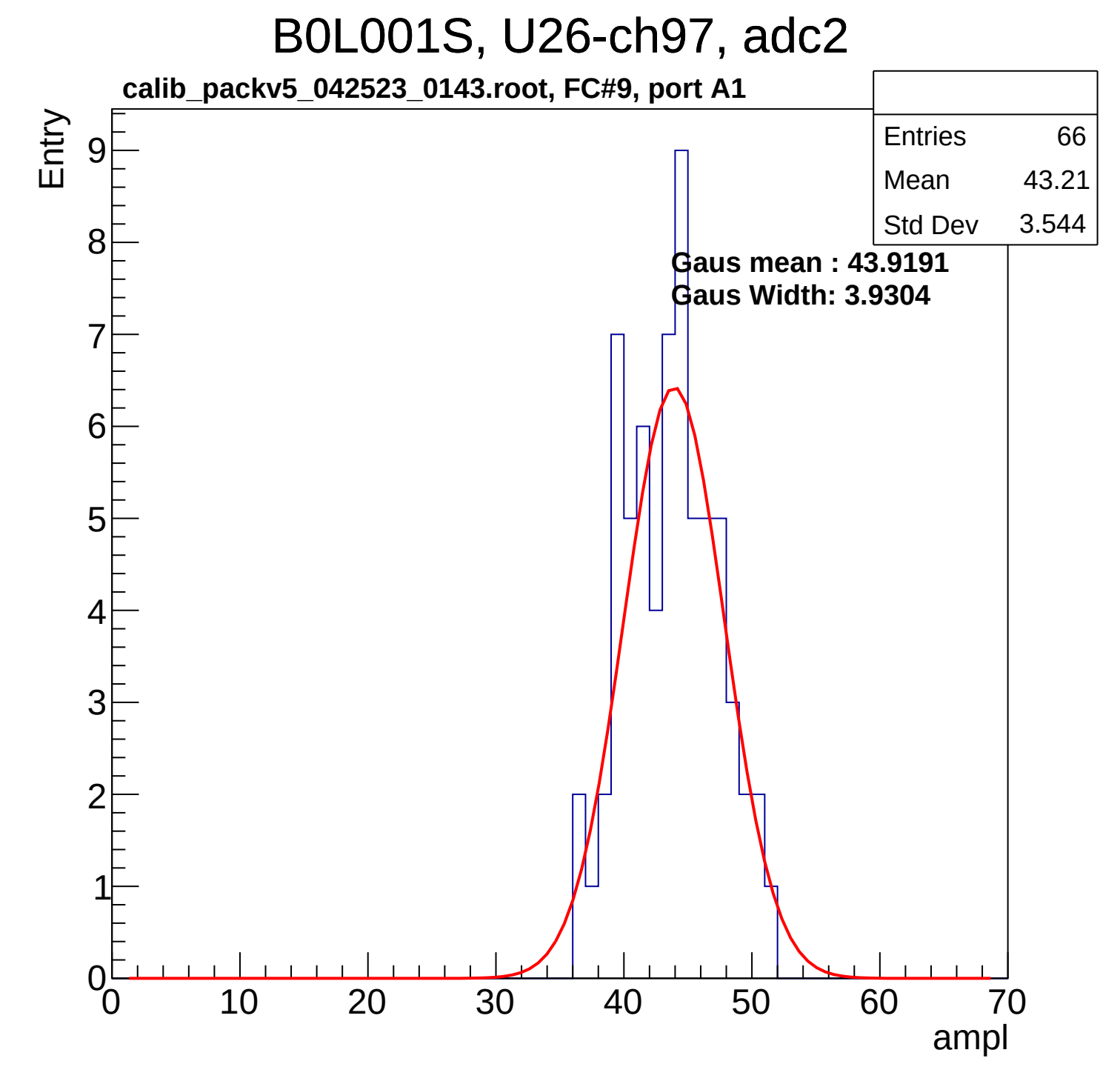
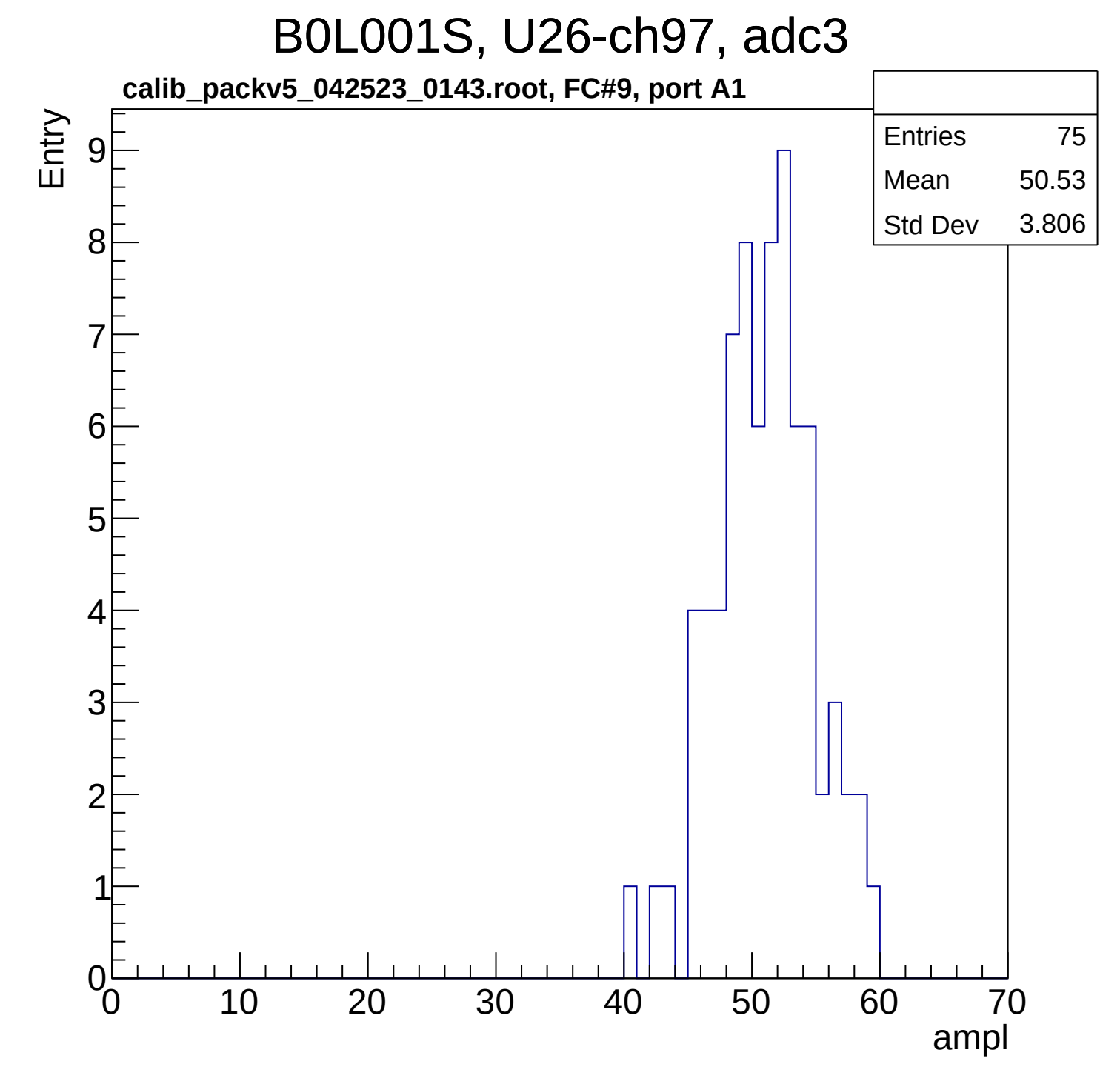
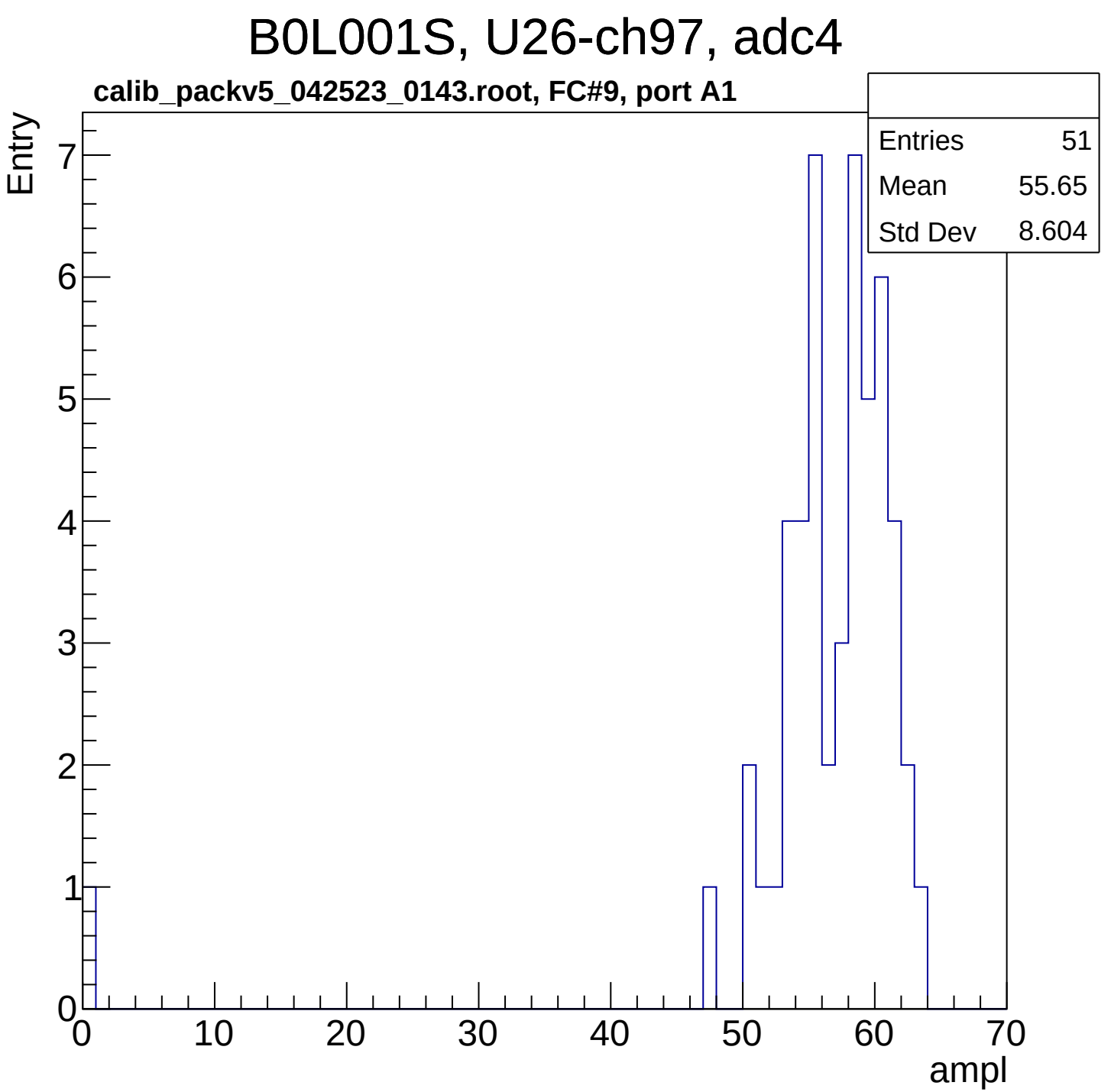
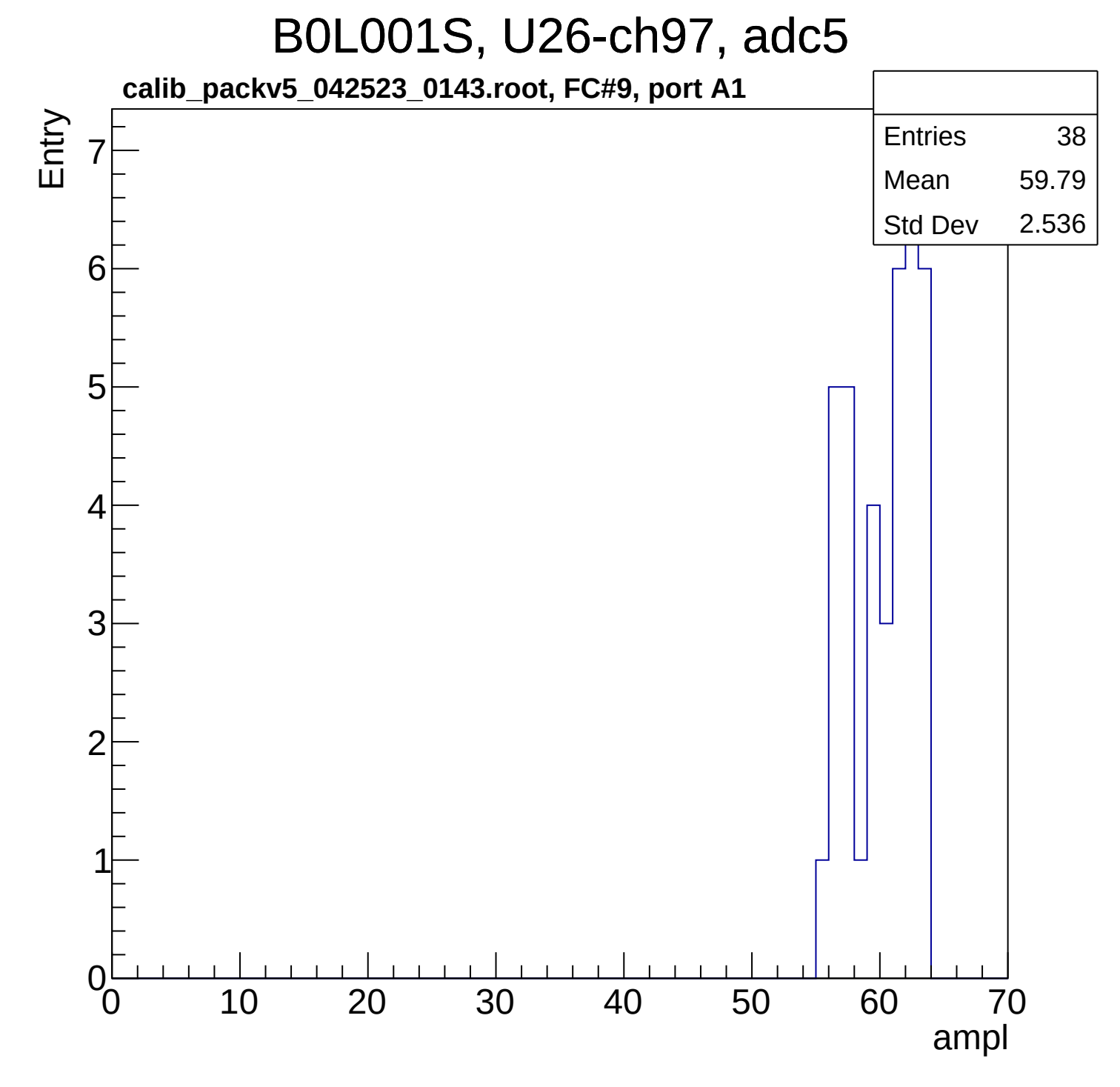

B0L001S, U26-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.9
Std Dev 5.029
Gaus mean : 30.9936
Gaus Width: 3.6316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

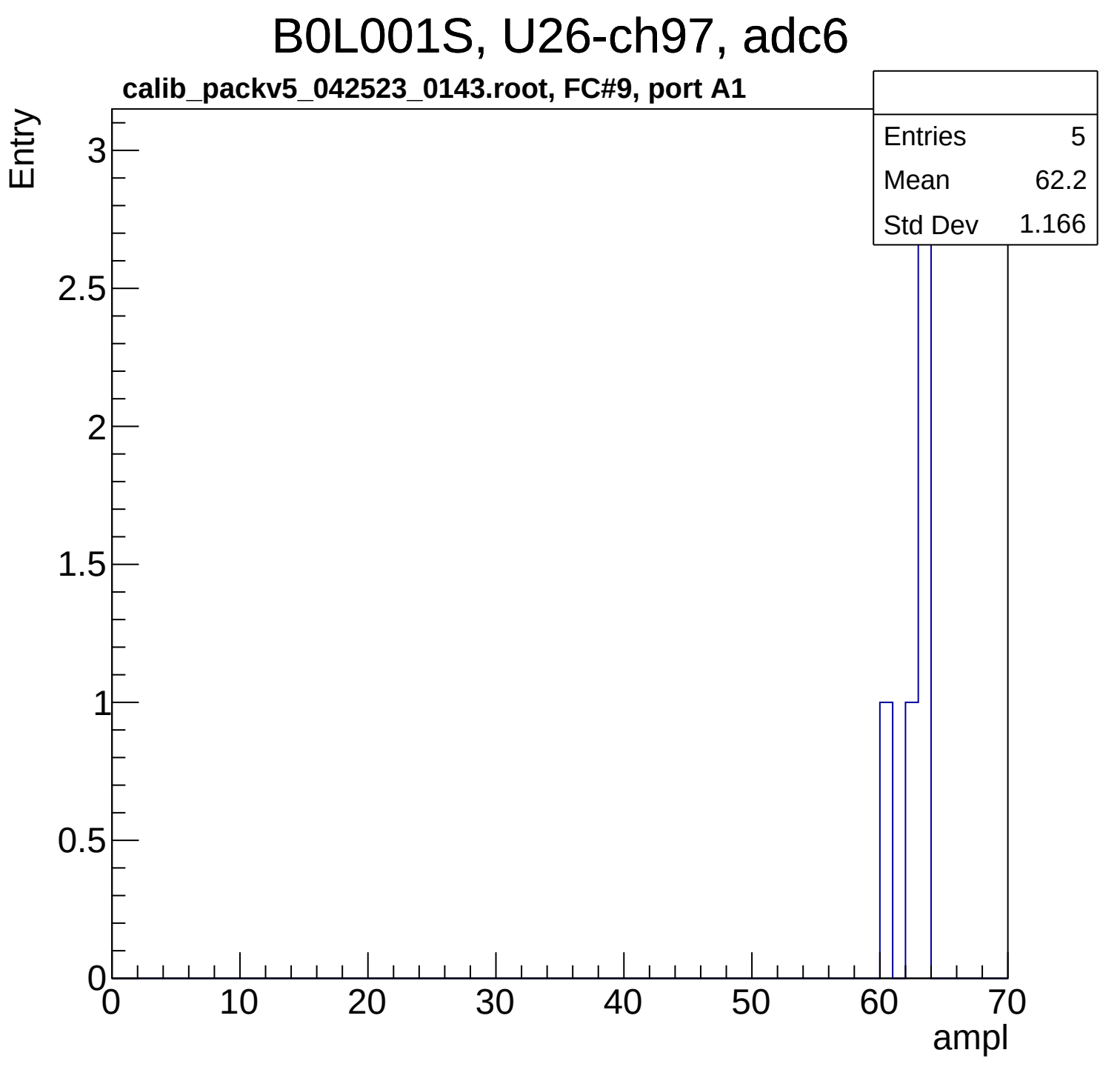

B0L001S, U26-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.94
Std Dev 3.869
Gaus mean : 37.3745
Gaus Width: 4.1252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

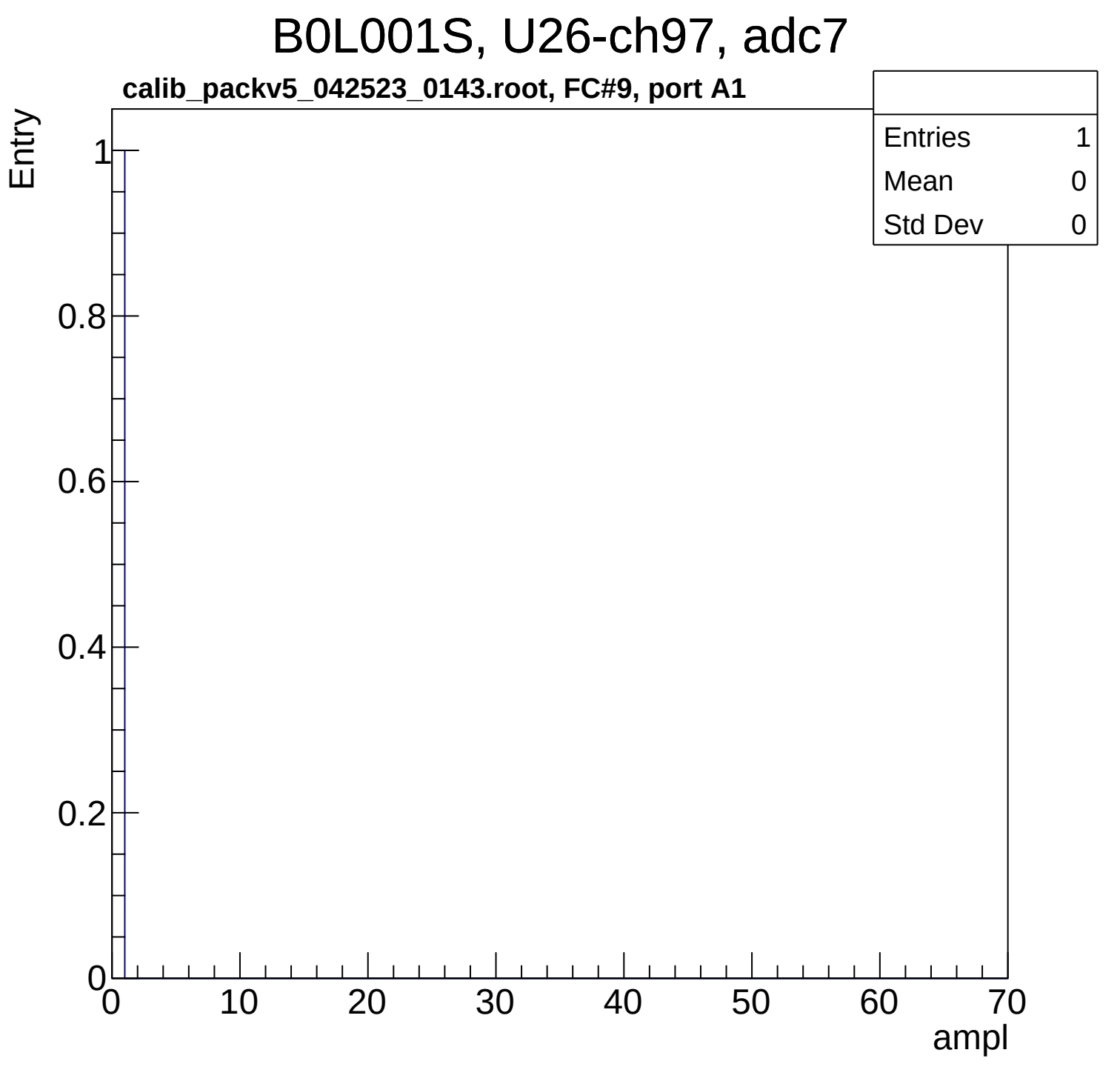

B0L001S, U26-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.21
Std Dev 3.544
Gaus mean : 43.9191
Gaus Width: 3.9304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

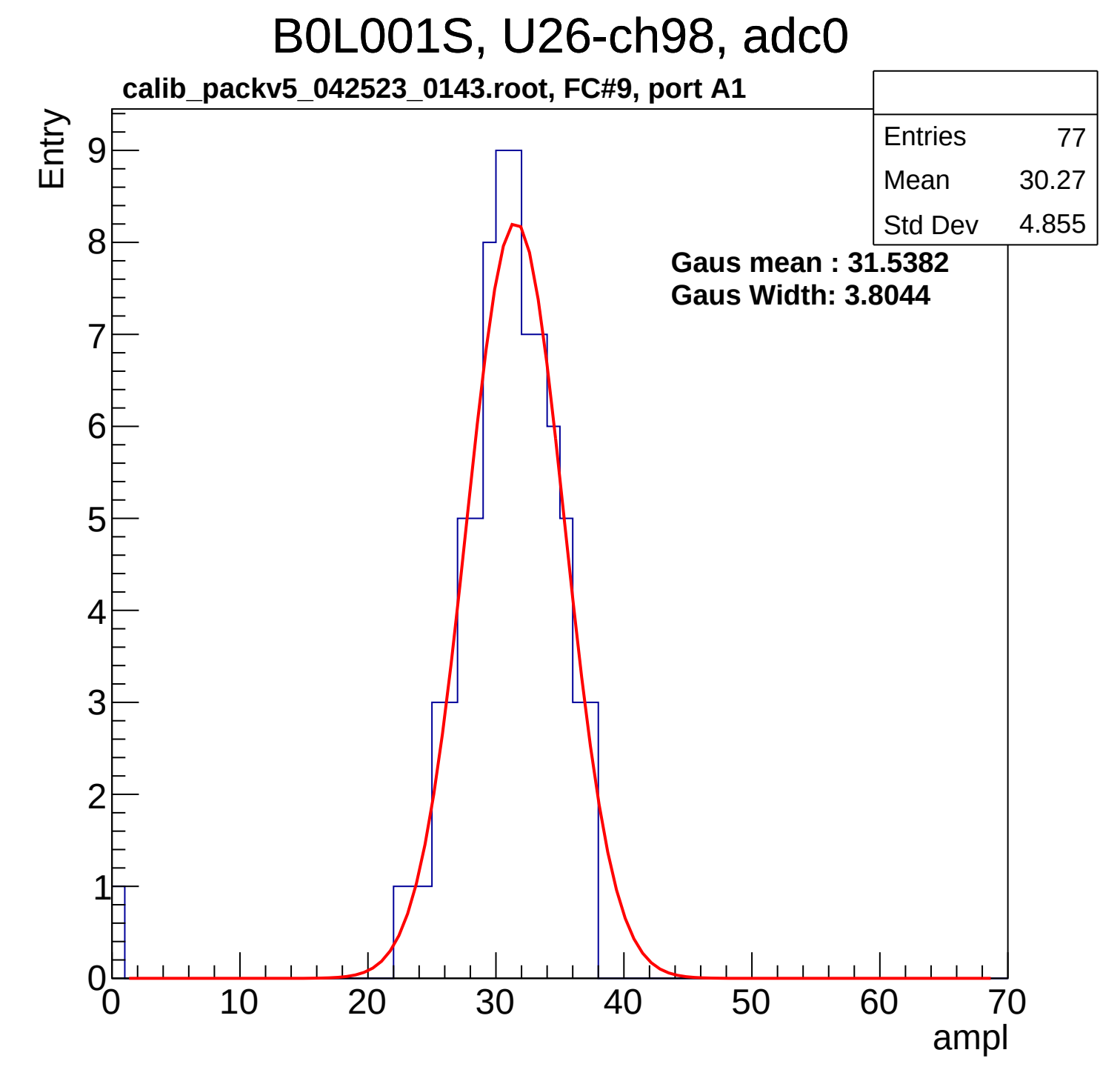

B0L001S, U26-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.53
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

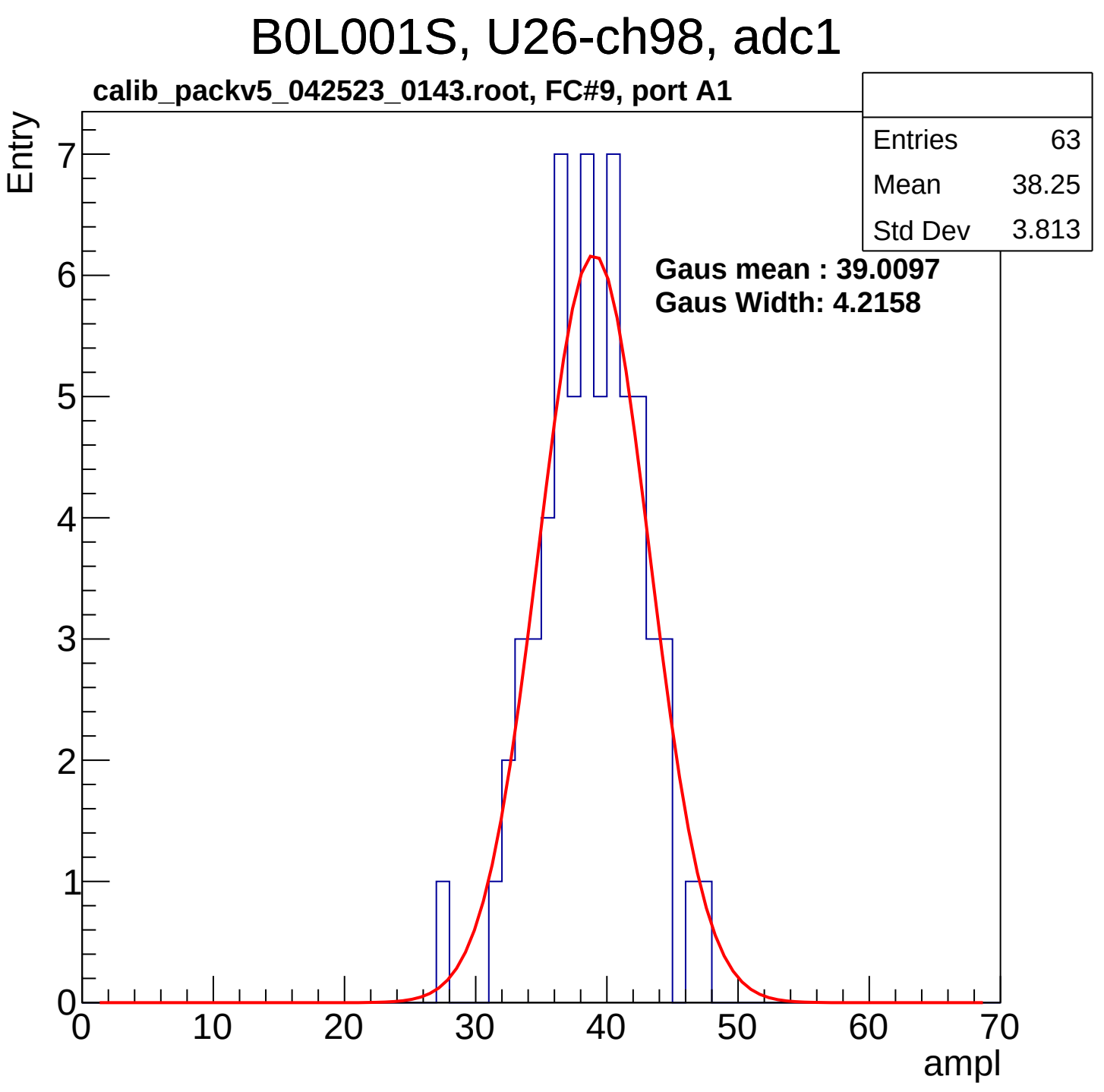

B0L001S, U26-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.65
Std Dev 8.604
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.79
Std Dev 2.536
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U26-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.27
Std Dev 4.855
Gaus mean : 31.5382
Gaus Width: 3.8044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.25
Std Dev 3.813
Gaus mean : 39.0097
Gaus Width: 4.2158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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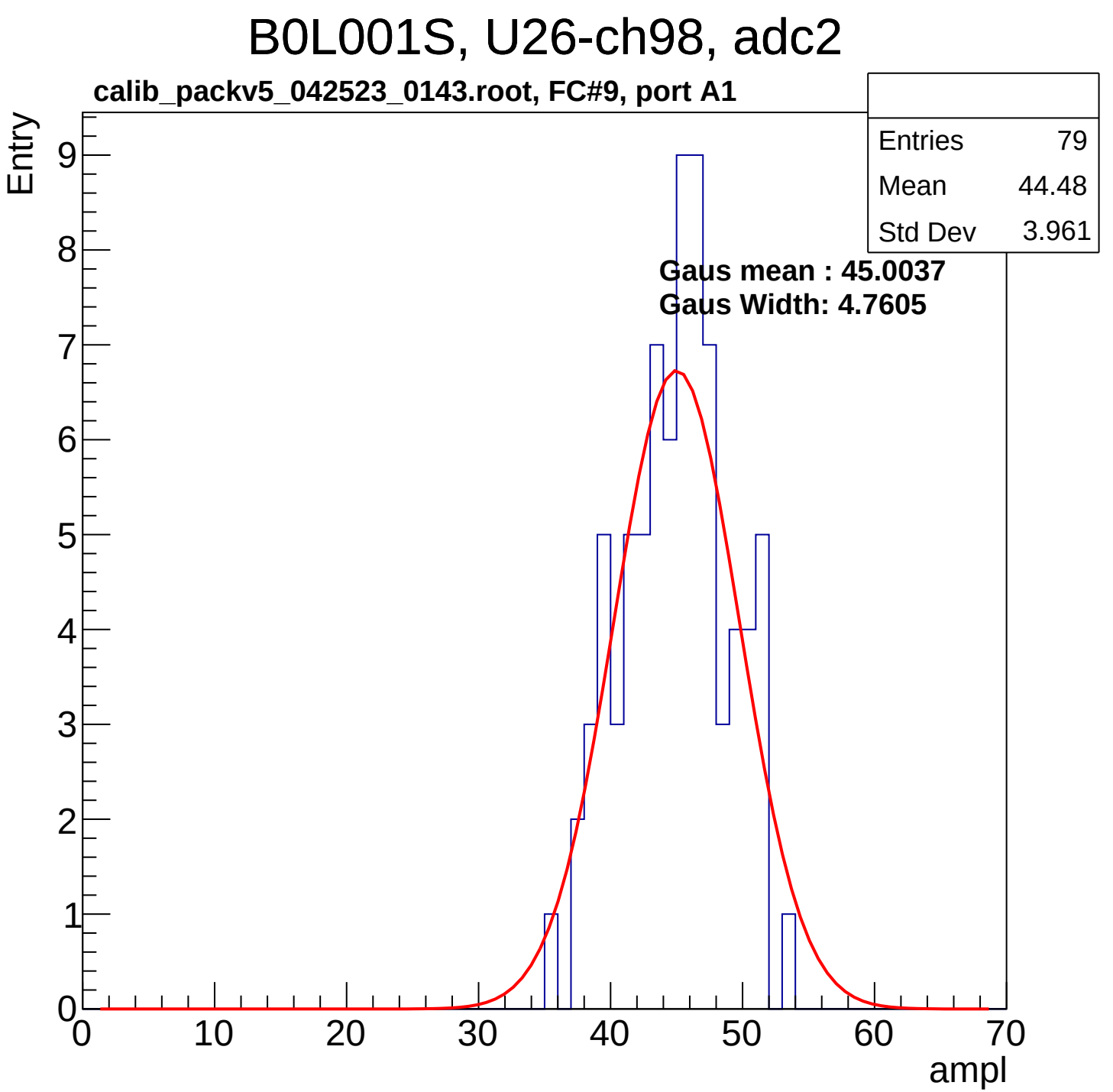
B0L001S, U26-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 44.48
Std Dev 3.961
Gaus mean : 45.0037
Gaus Width: 4.7605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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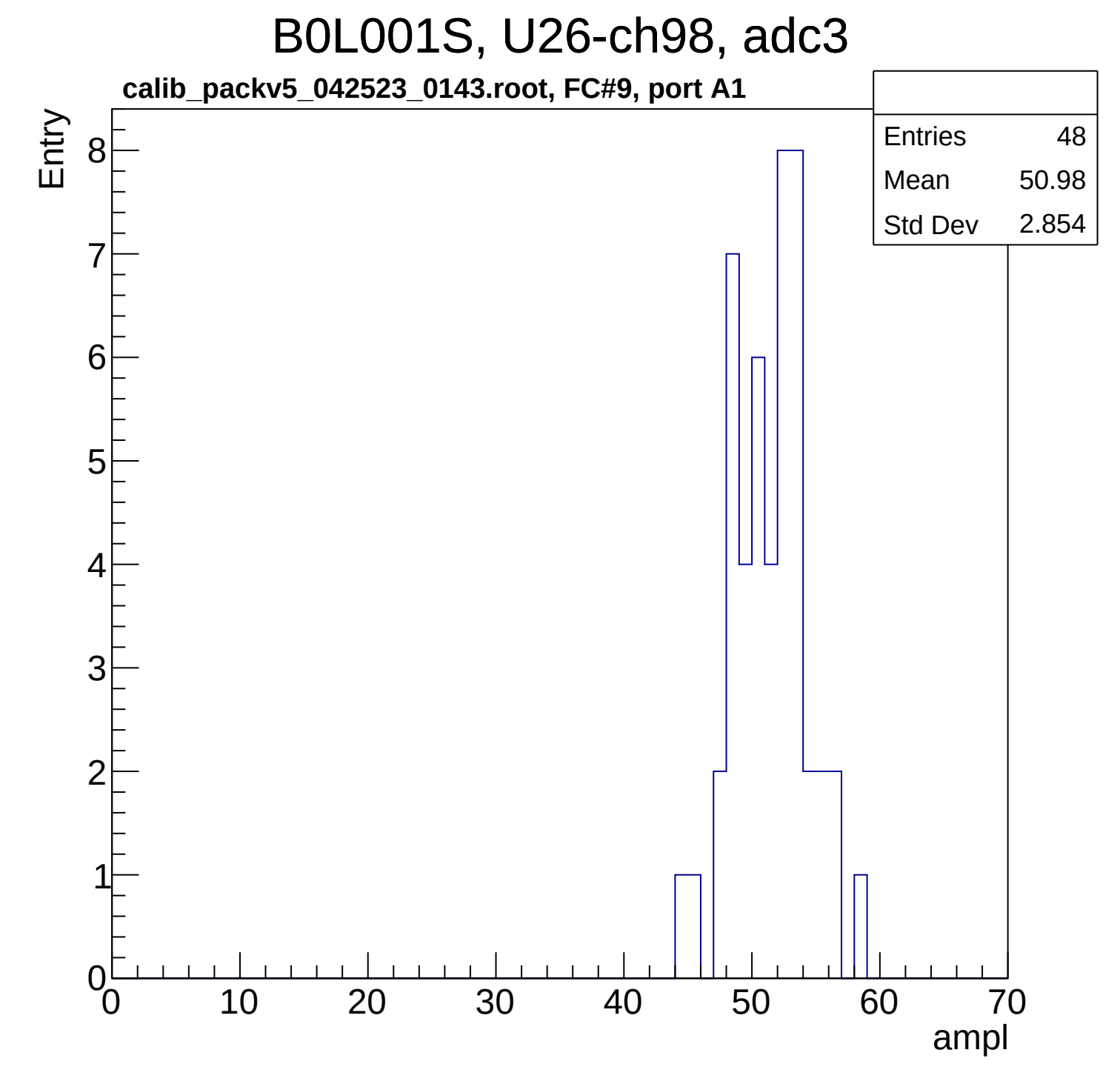
B0L001S, U26-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 50.98
Std Dev 2.854
Entry
ampl
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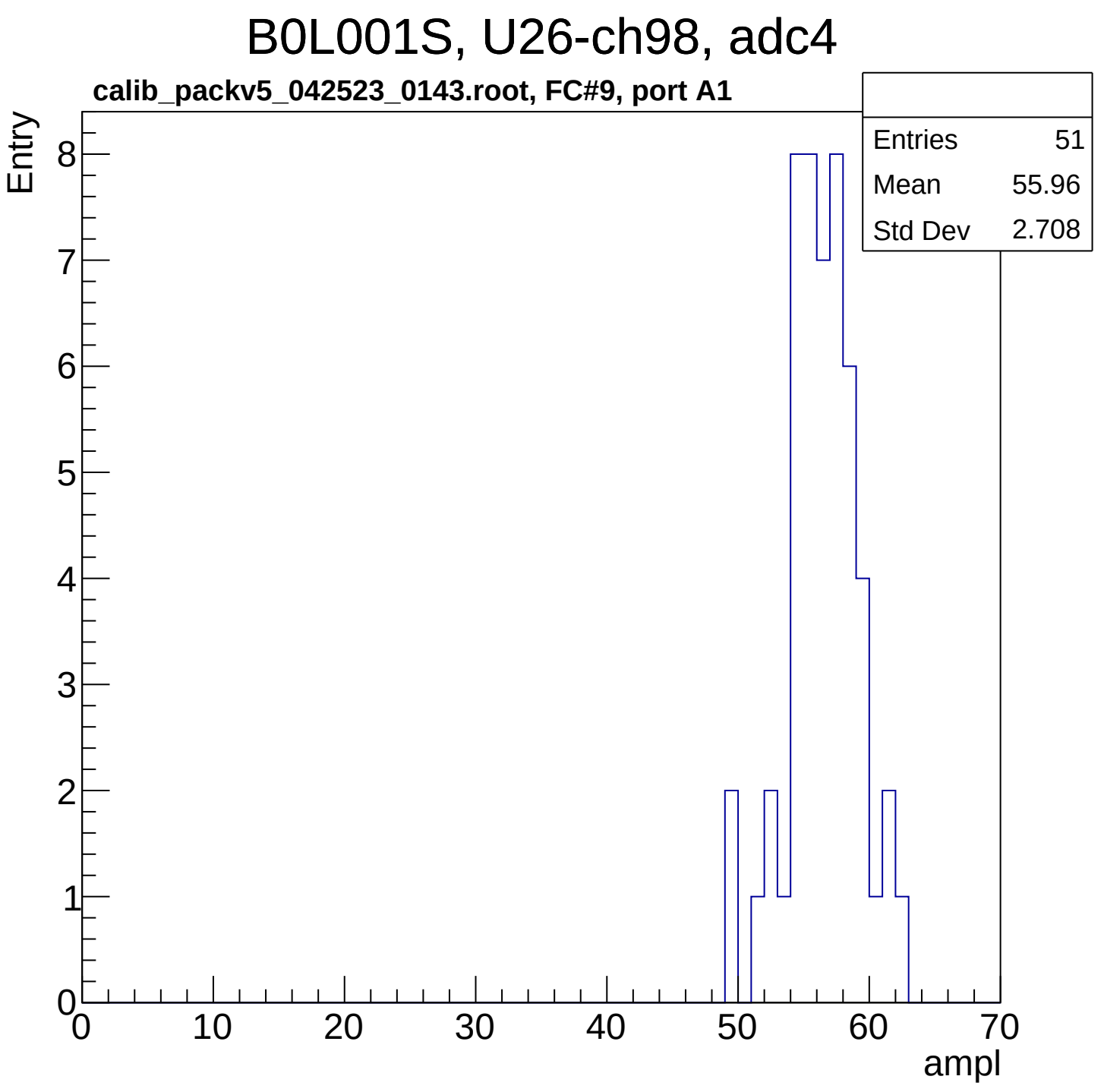
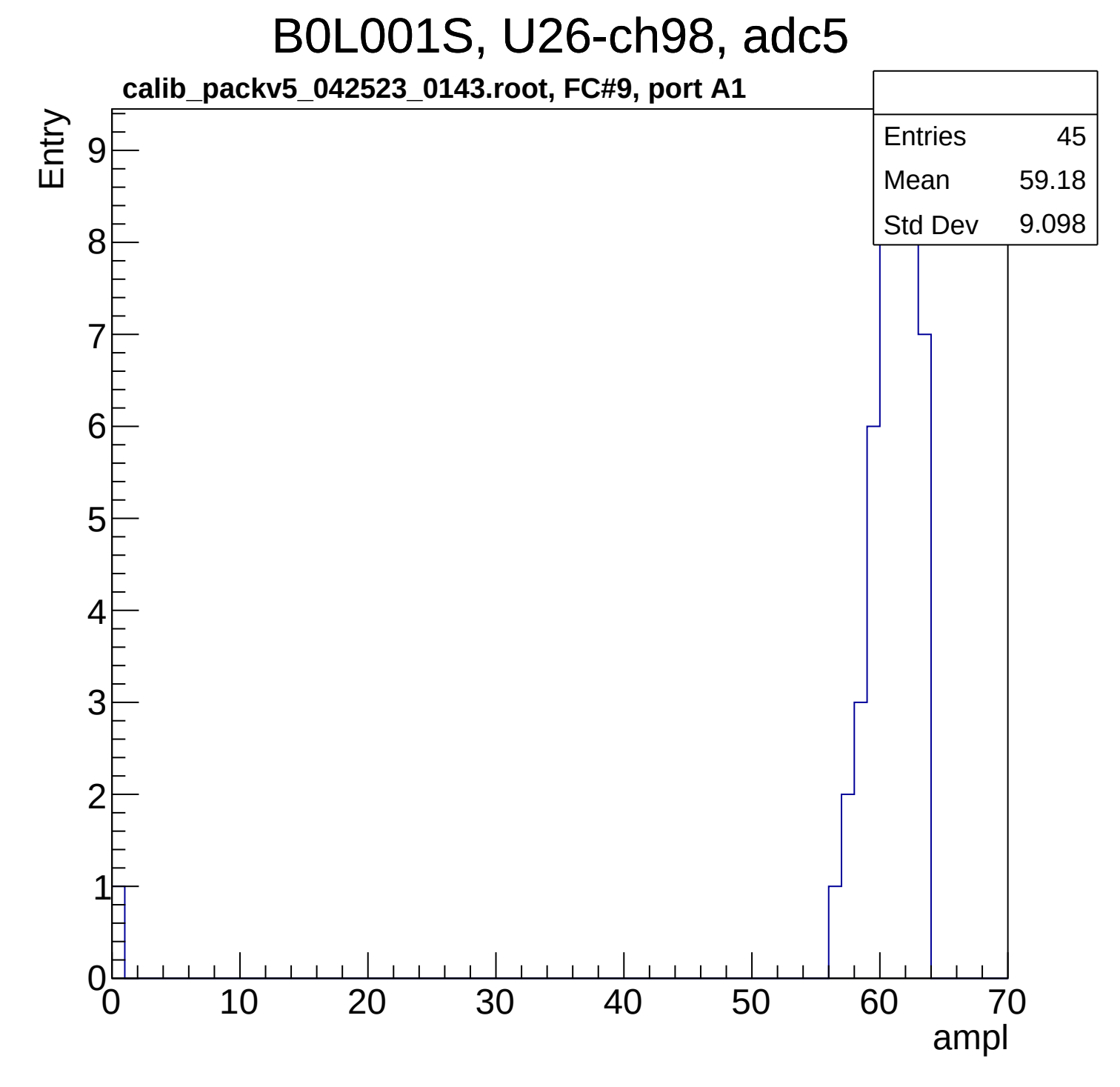
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.96
Std Dev 2.708
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.18
Std Dev 9.098
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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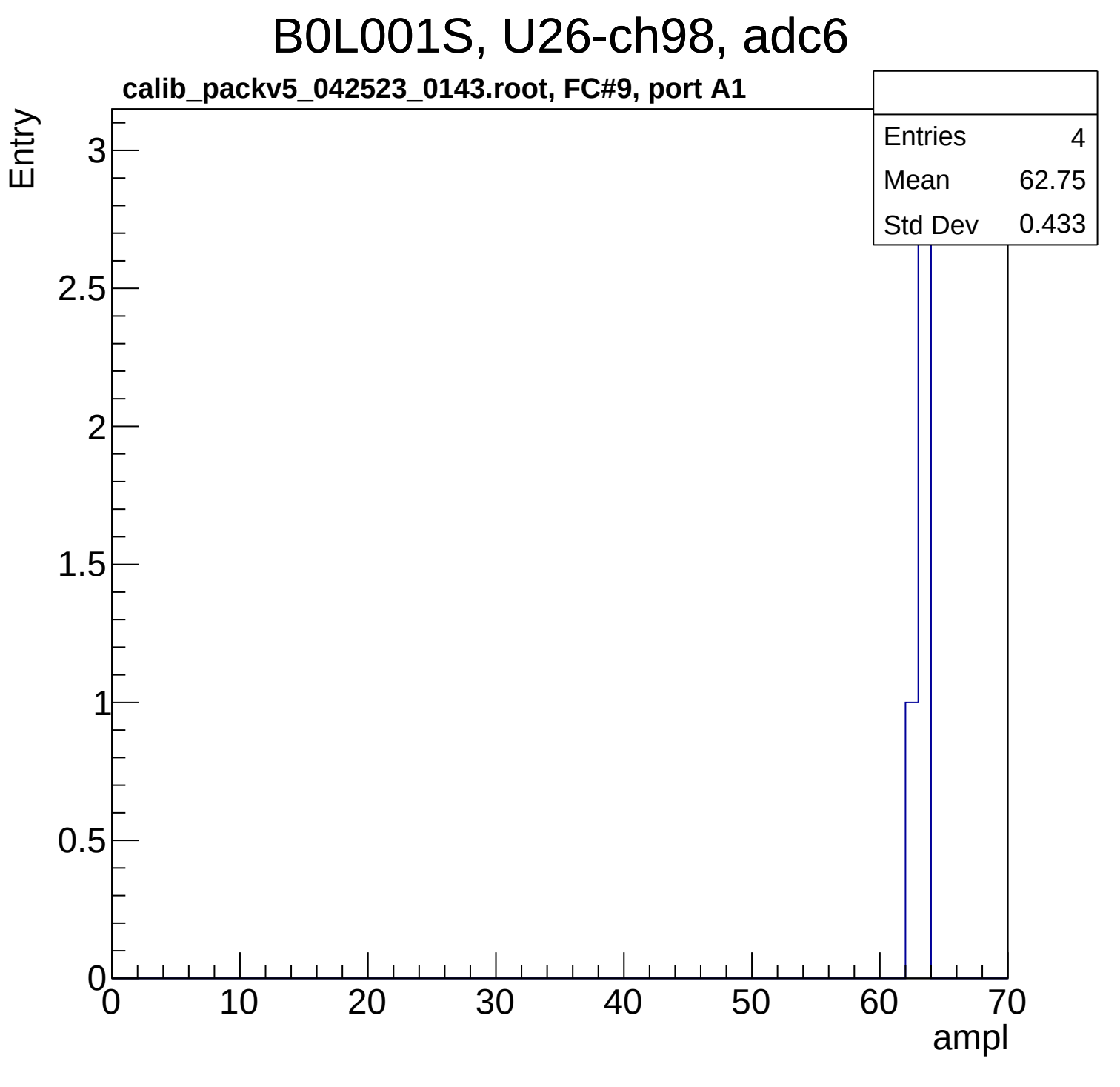
B0L001S, U26-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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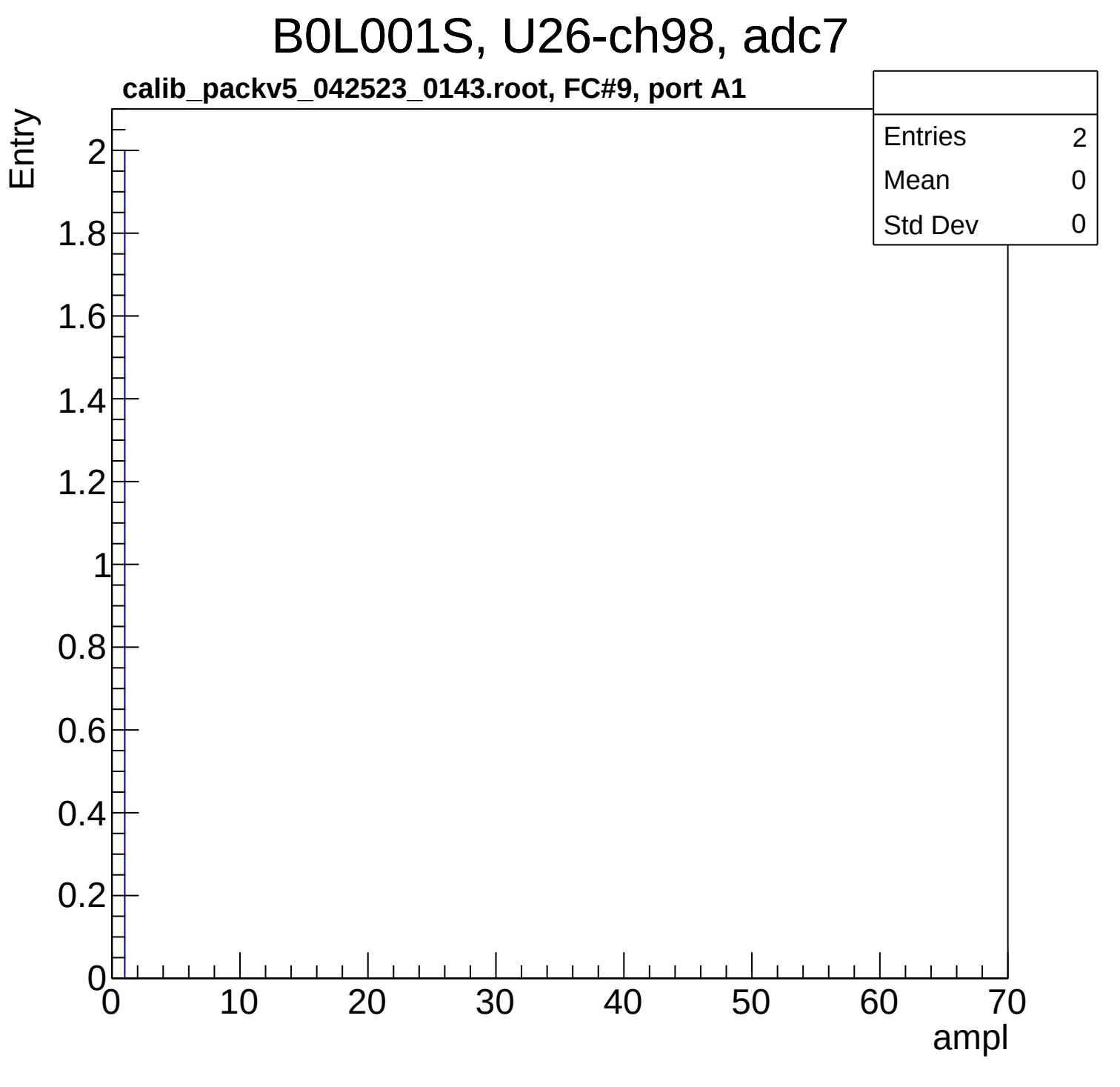
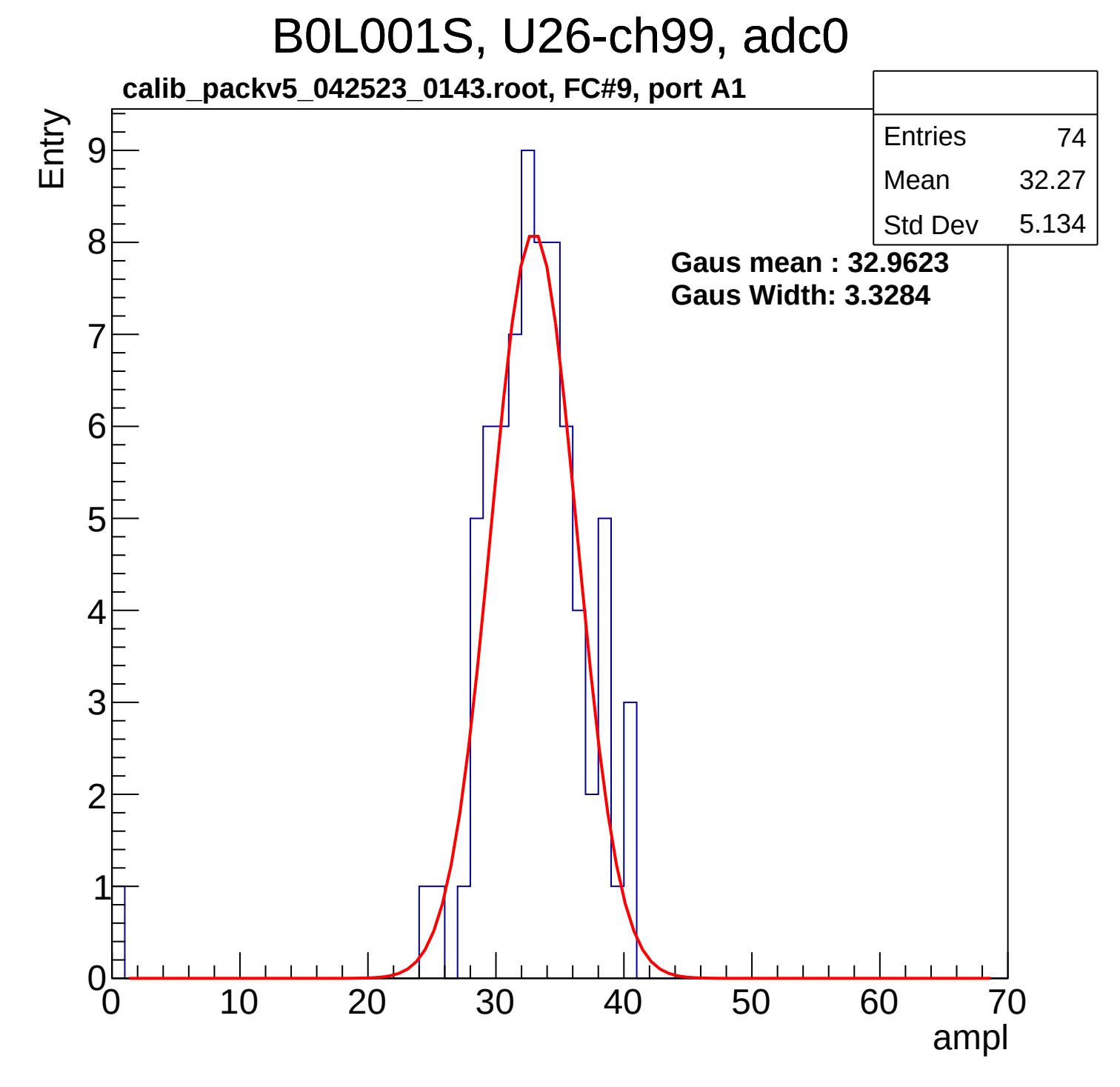
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U26-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 32.27
Std Dev 5.134
Gaus mean : 32.9623
Gaus Width: 3.3284
Entry
ampl
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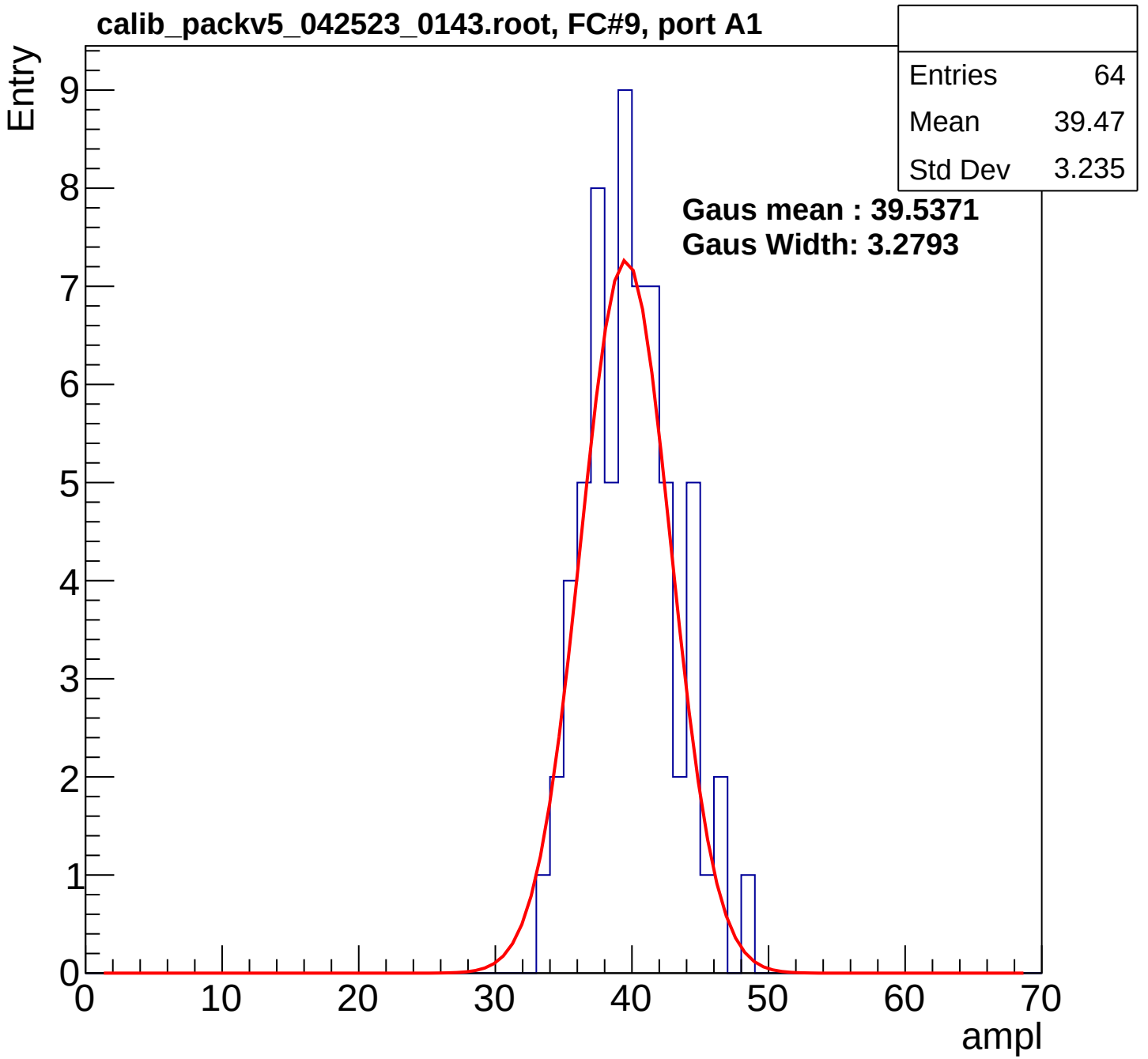
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 39.47
Std Dev 3.235
Gaus mean : 39.5371
Gaus Width: 3.2793
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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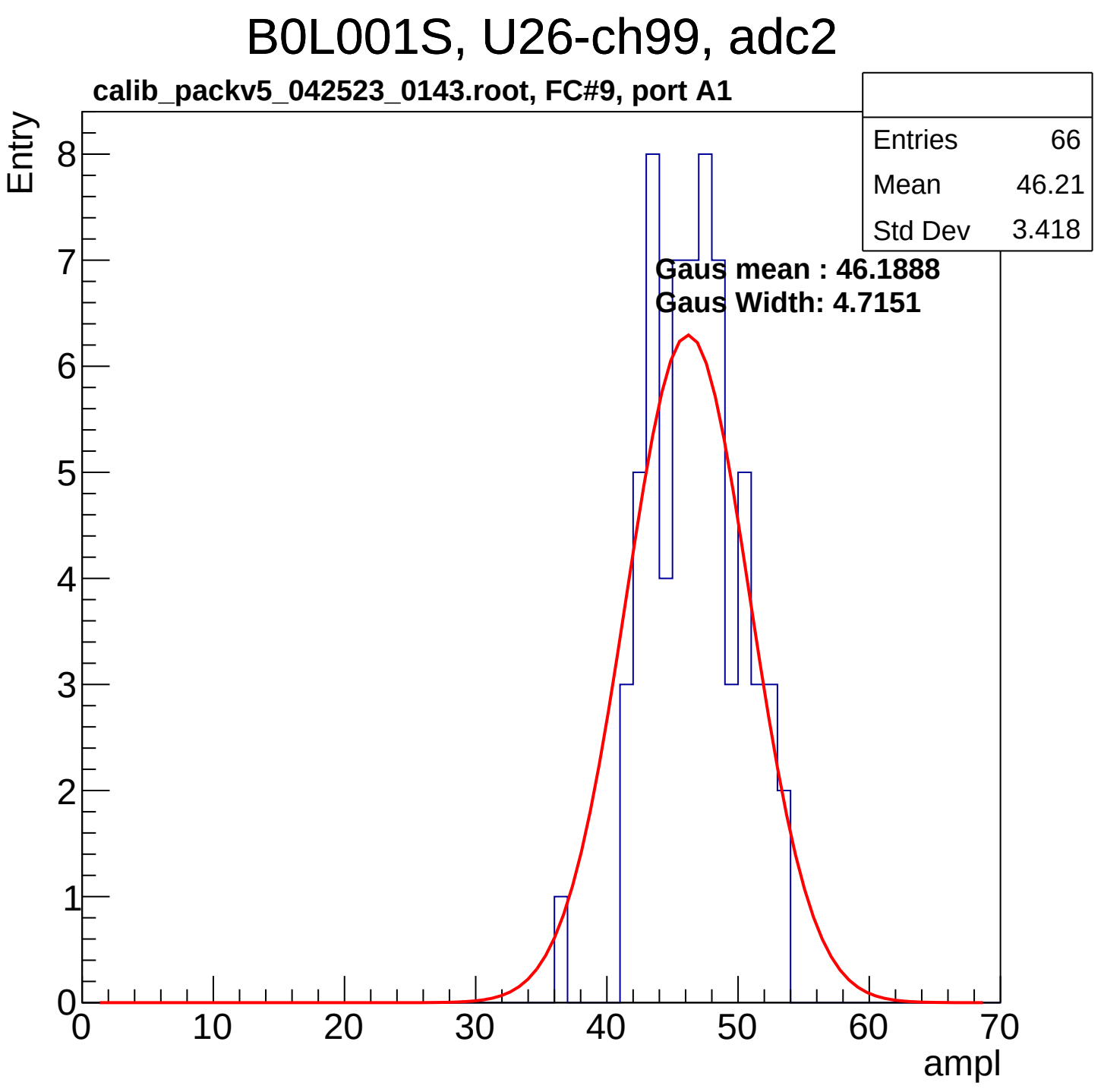
B0L001S, U26-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 46.21
Std Dev 3.418
Gaus mean : 46.1888
Gaus Width: 4.7151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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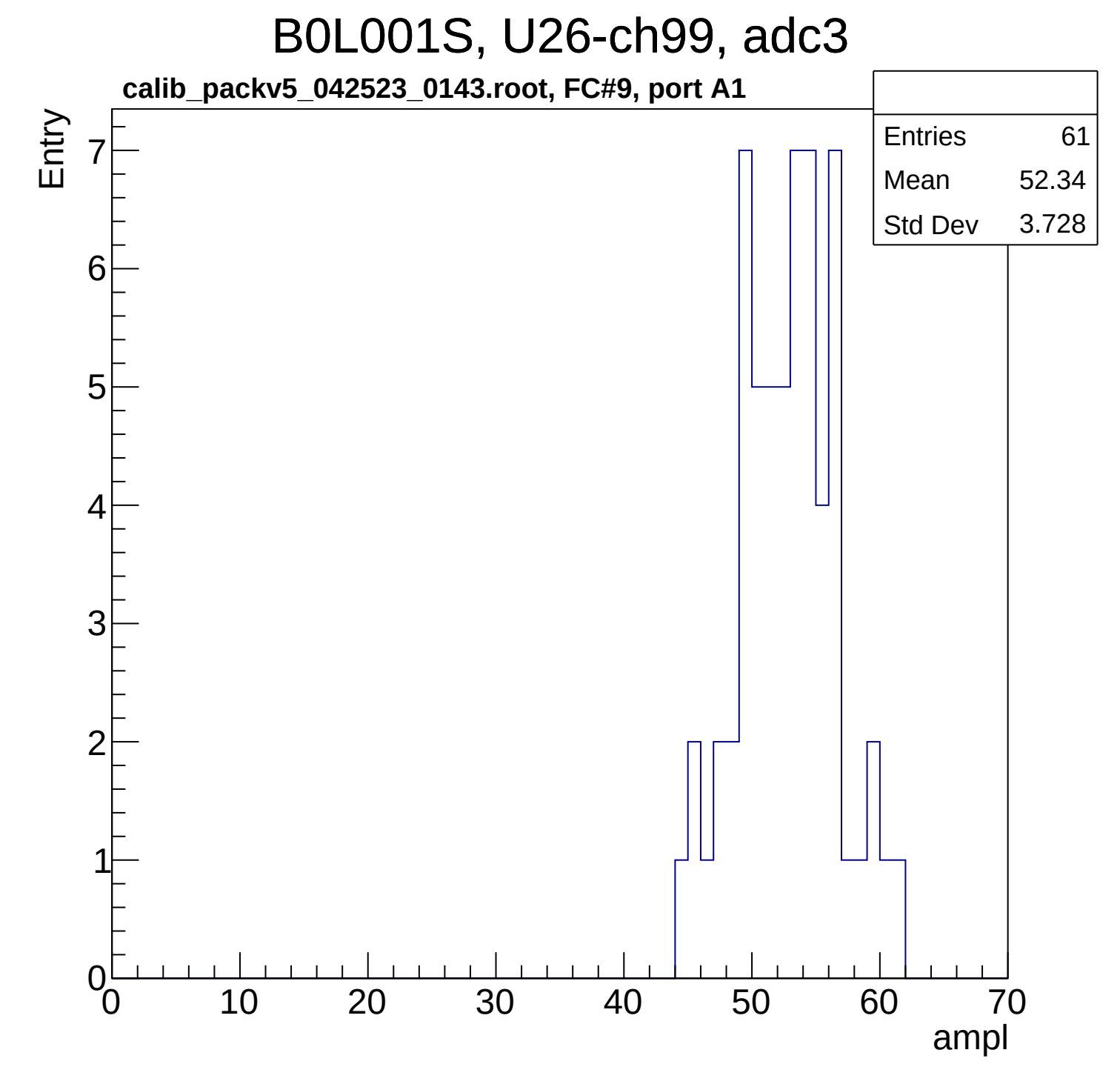
B0L001S, U26-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 52.34
Std Dev 3.728
Entry
ampl
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1
2
3
4
5
6
7
0
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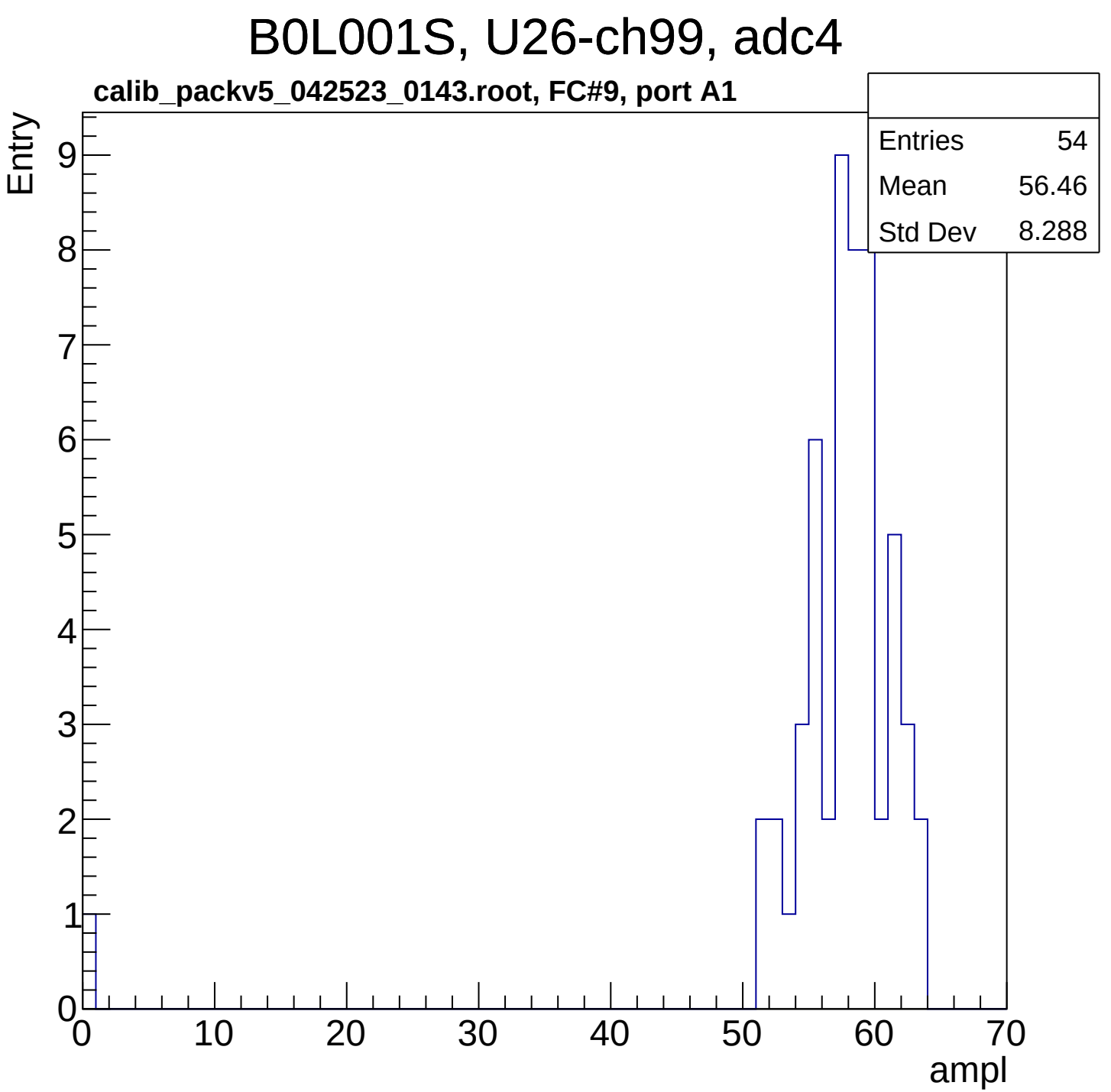
20
30
40
50
60
70


B0L001S, U26-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.46
Std Dev 8.288
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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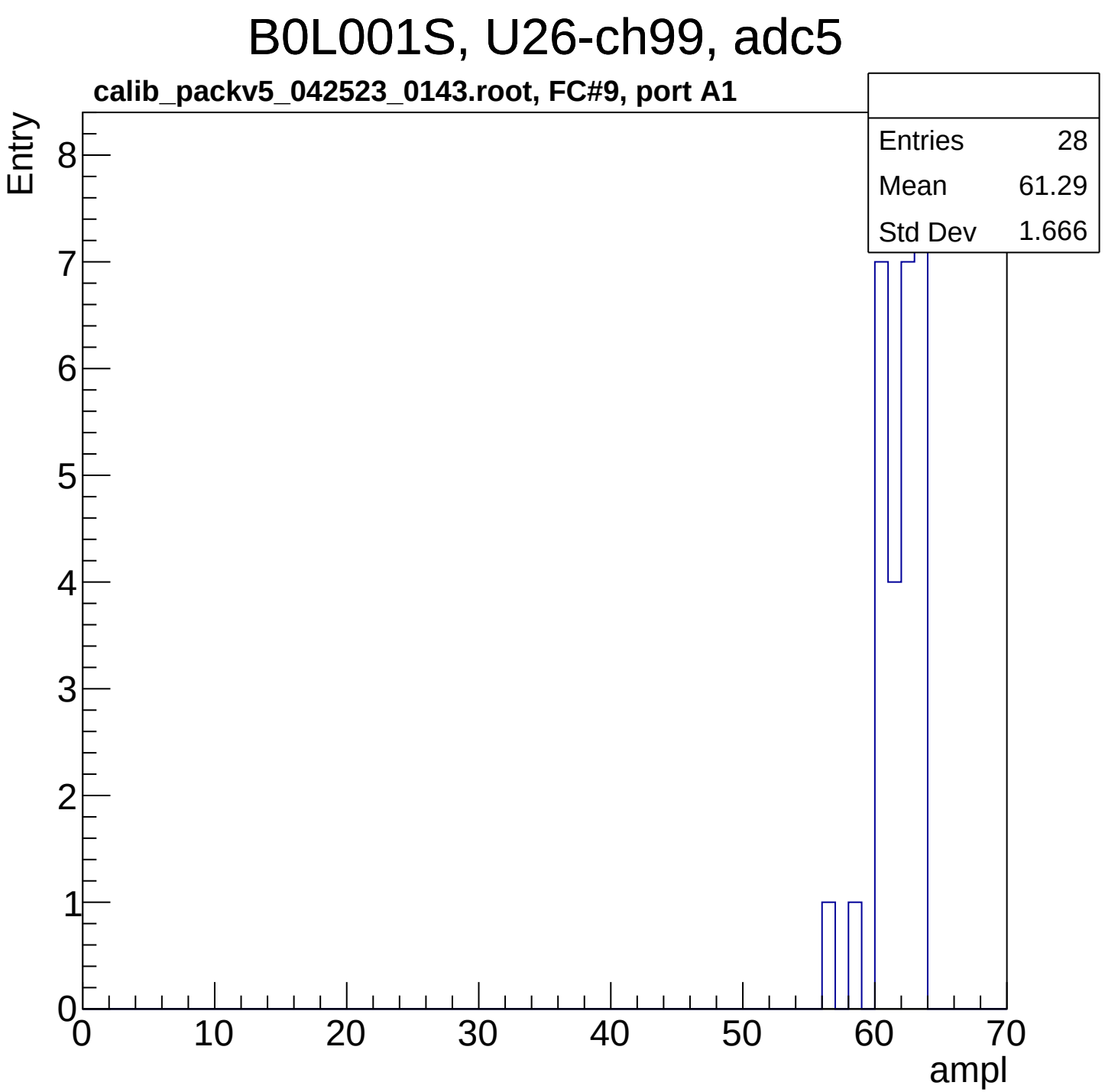
B0L001S, U26-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 61.29
Std Dev 1.666
Entry
ampl
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1
2
3
4
5
6
7
8
0
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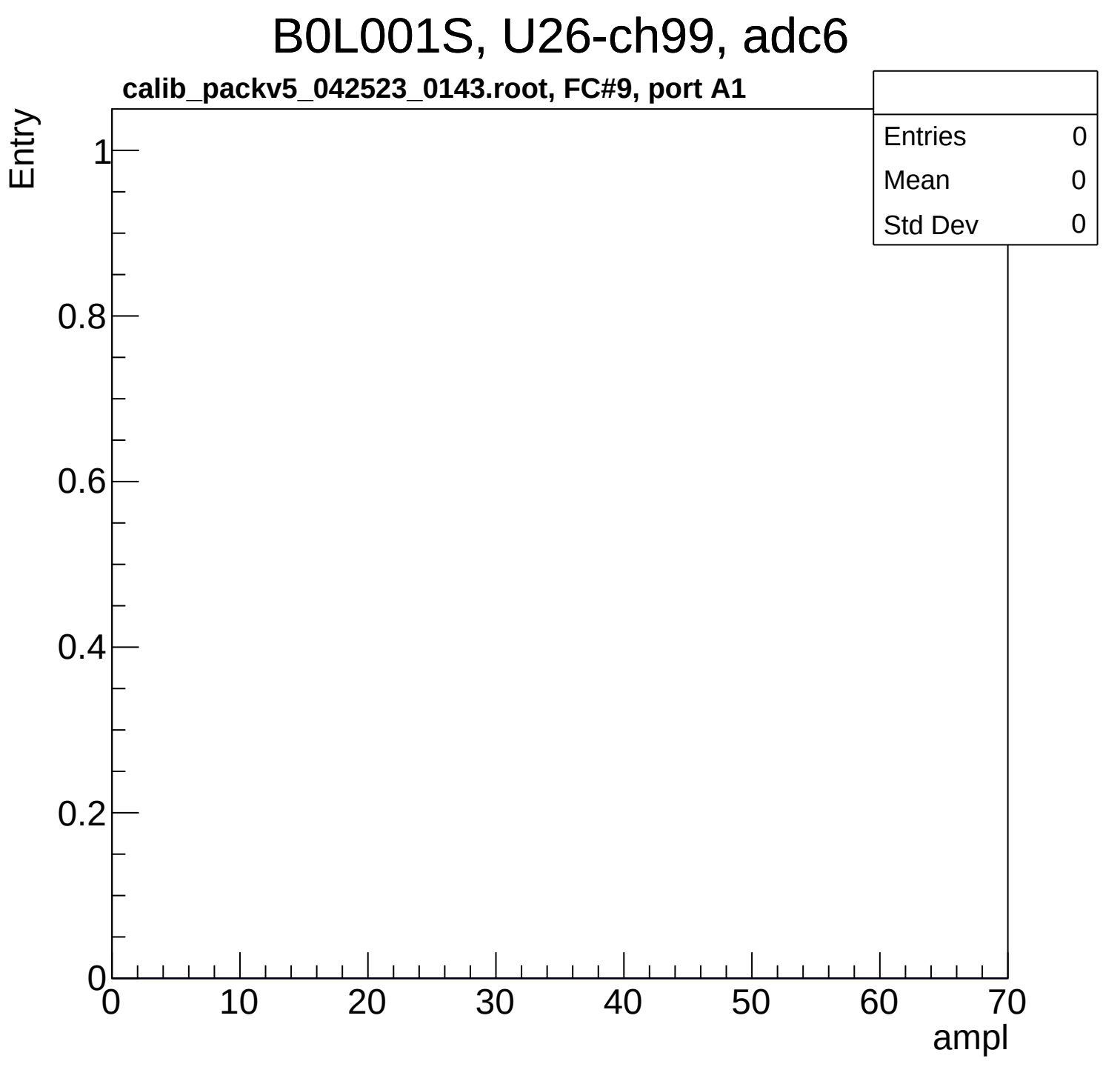
20
30
40
50
60
70


B0L001S, U26-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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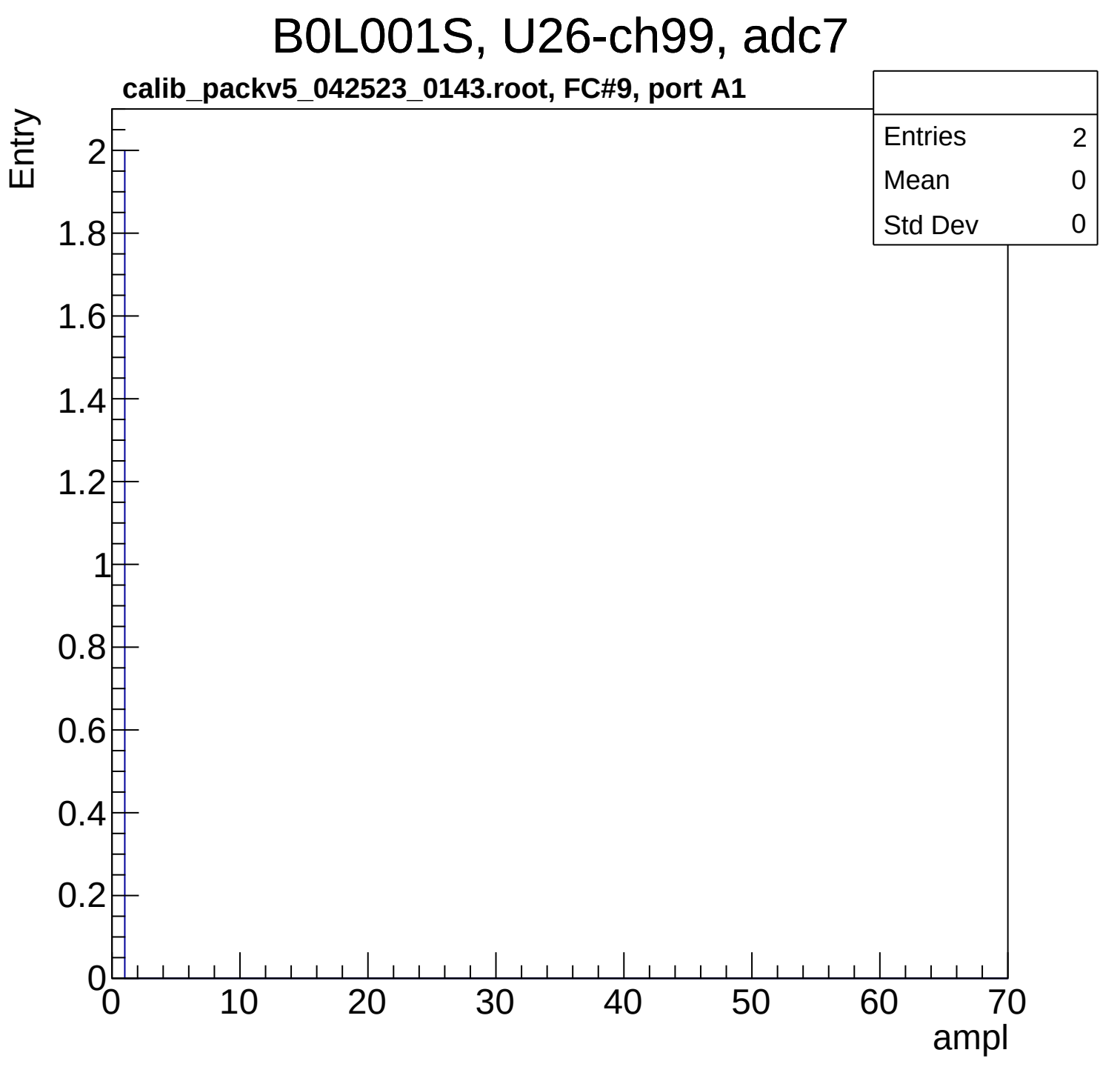
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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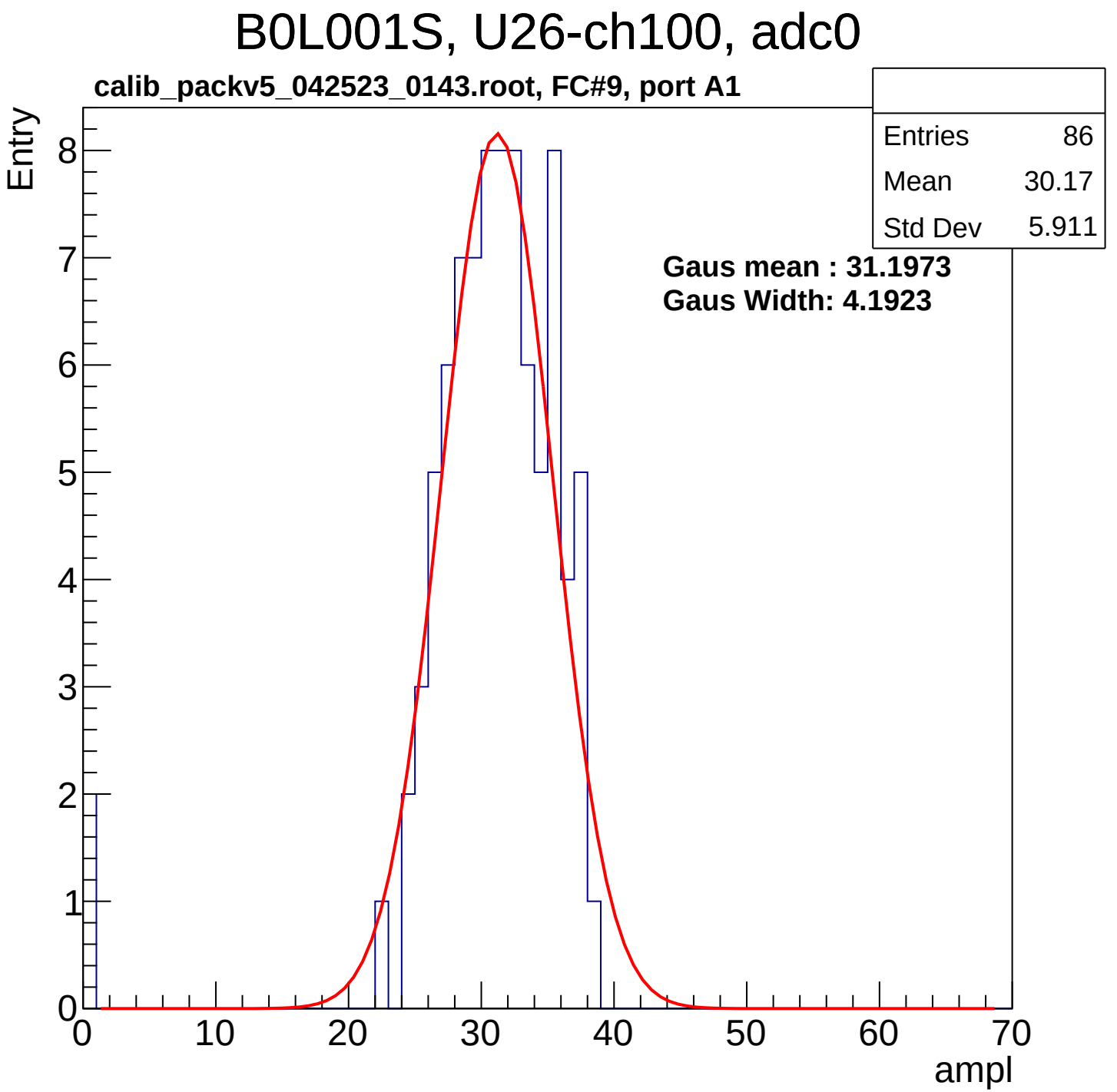
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 30.17
Std Dev 5.911
Gaus mean : 31.1973
Gaus Width: 4.1923
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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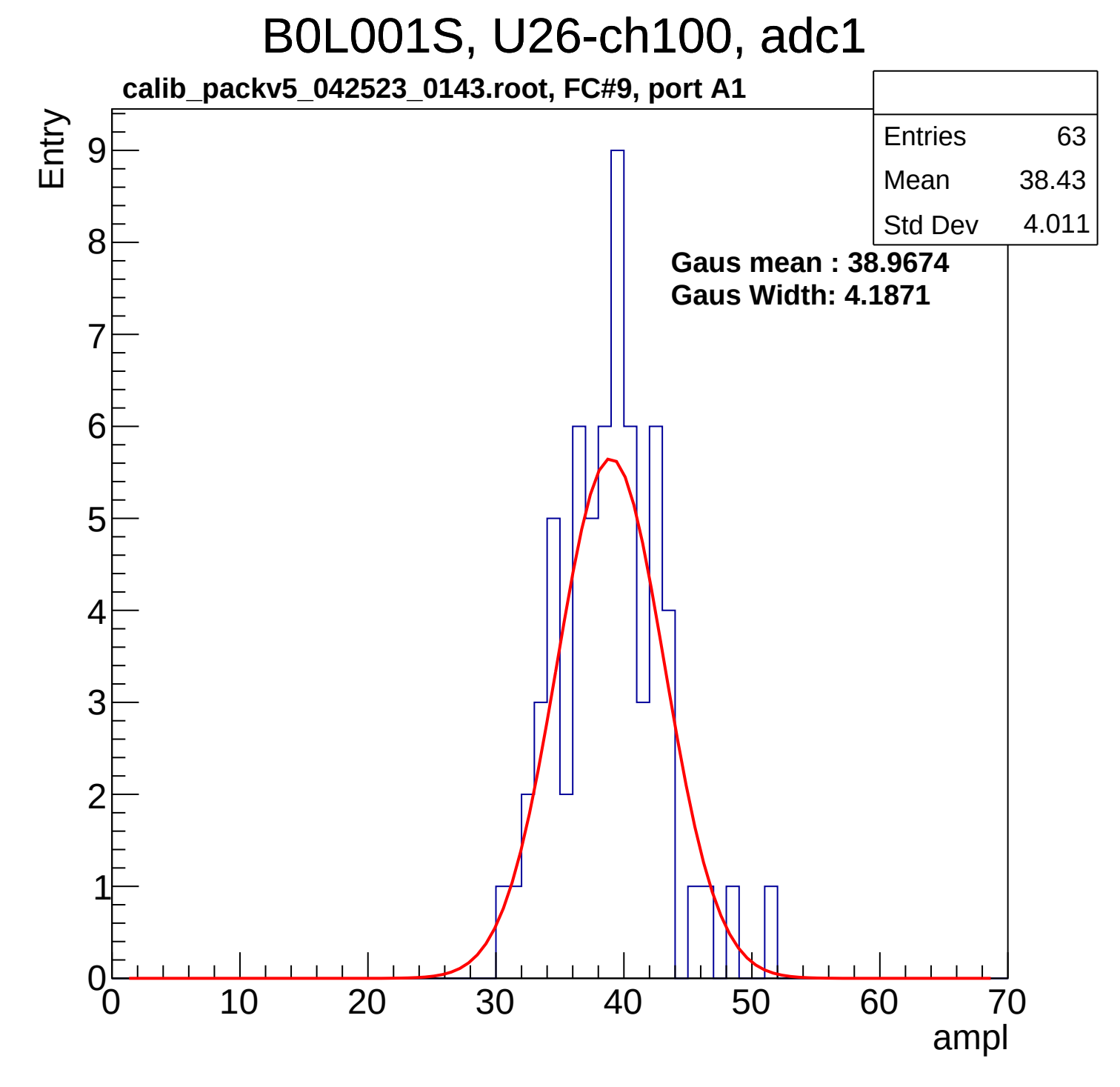
40
50
60
70


B0L001S, U26-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.43
Std Dev 4.011
Gaus mean : 38.9674
Gaus Width: 4.1871
Entry
ampl
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2
3
4
5
6
7
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9
0
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20
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60
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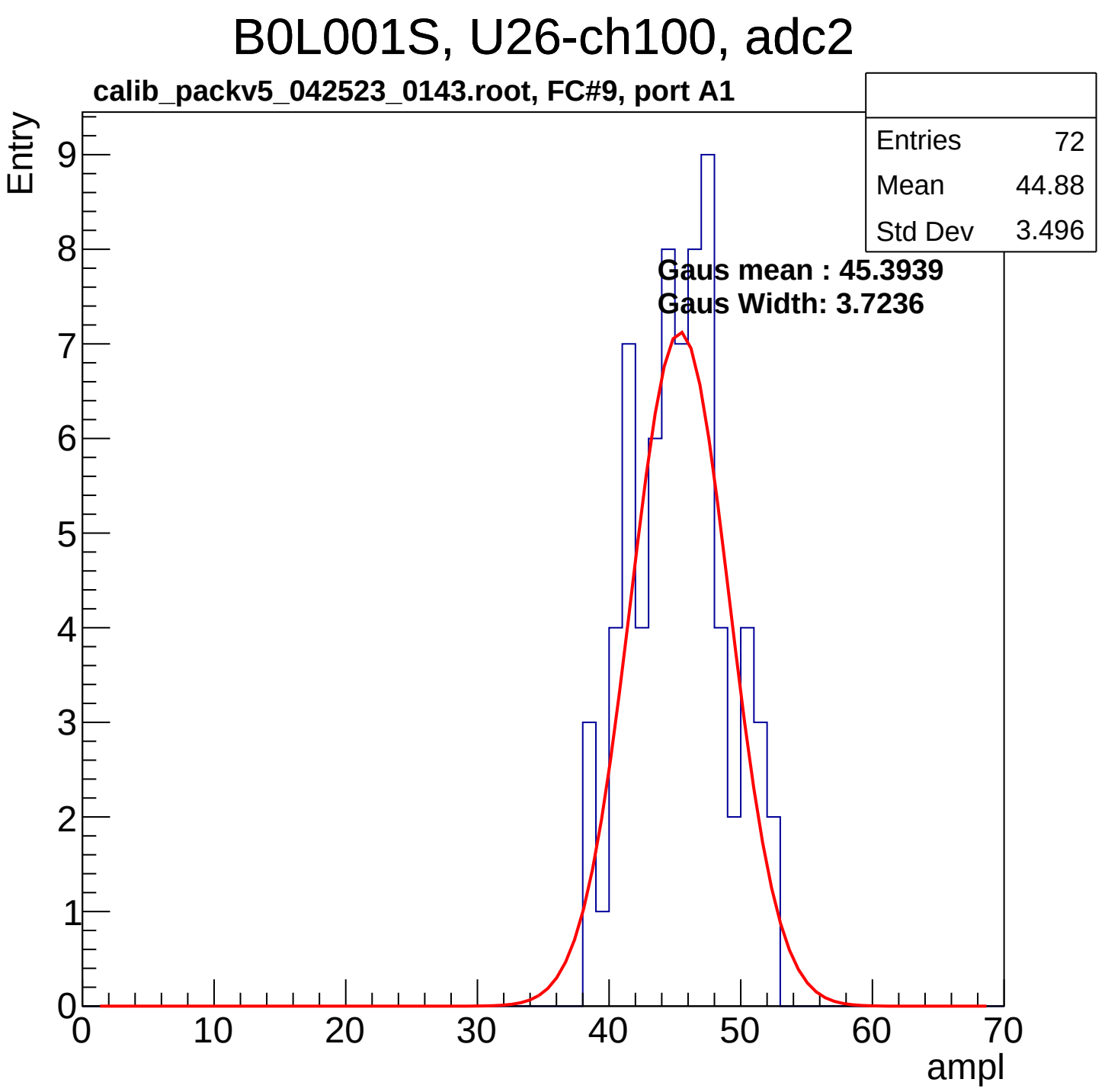
B0L001S, U26-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.88
Std Dev 3.496
Gaus mean : 45.3939
Gaus Width: 3.7236
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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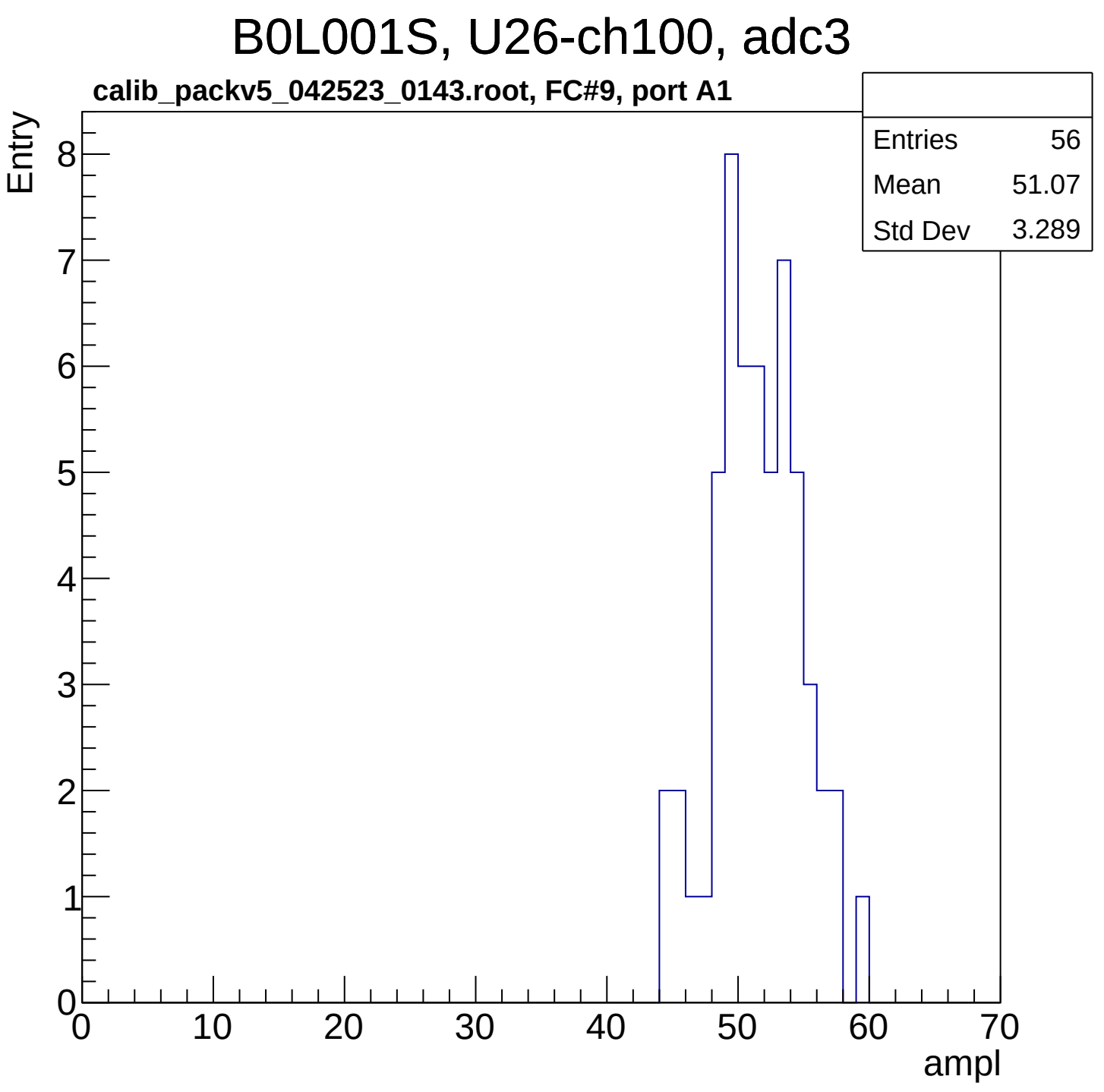
B0L001S, U26-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 51.07
Std Dev 3.289
Entry
ampl
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3
4
5
6
7
8
0
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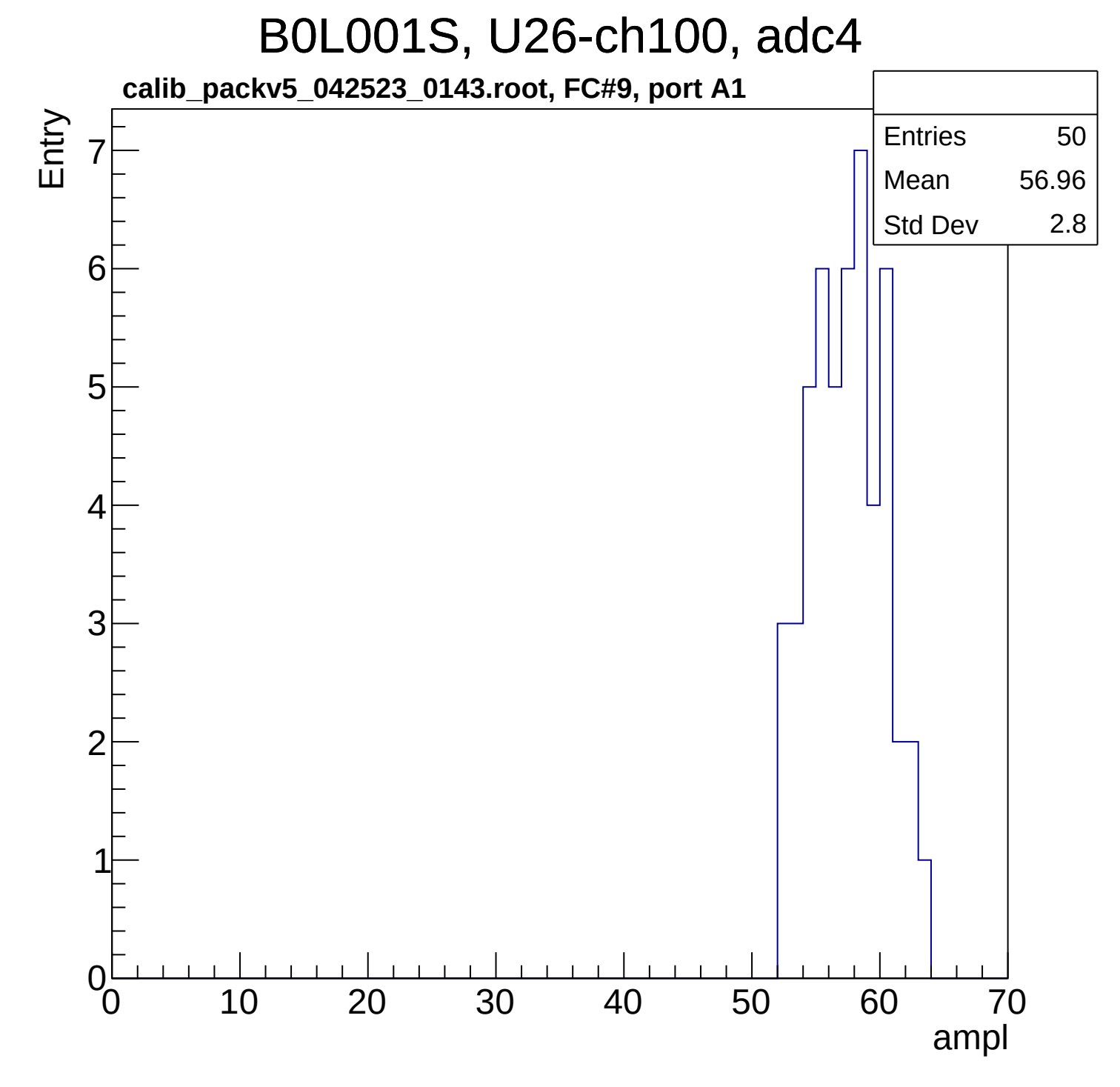
B0L001S, U26-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.96
Std Dev 2.8
Entry
ampl
0
1
2
3
4
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0
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20
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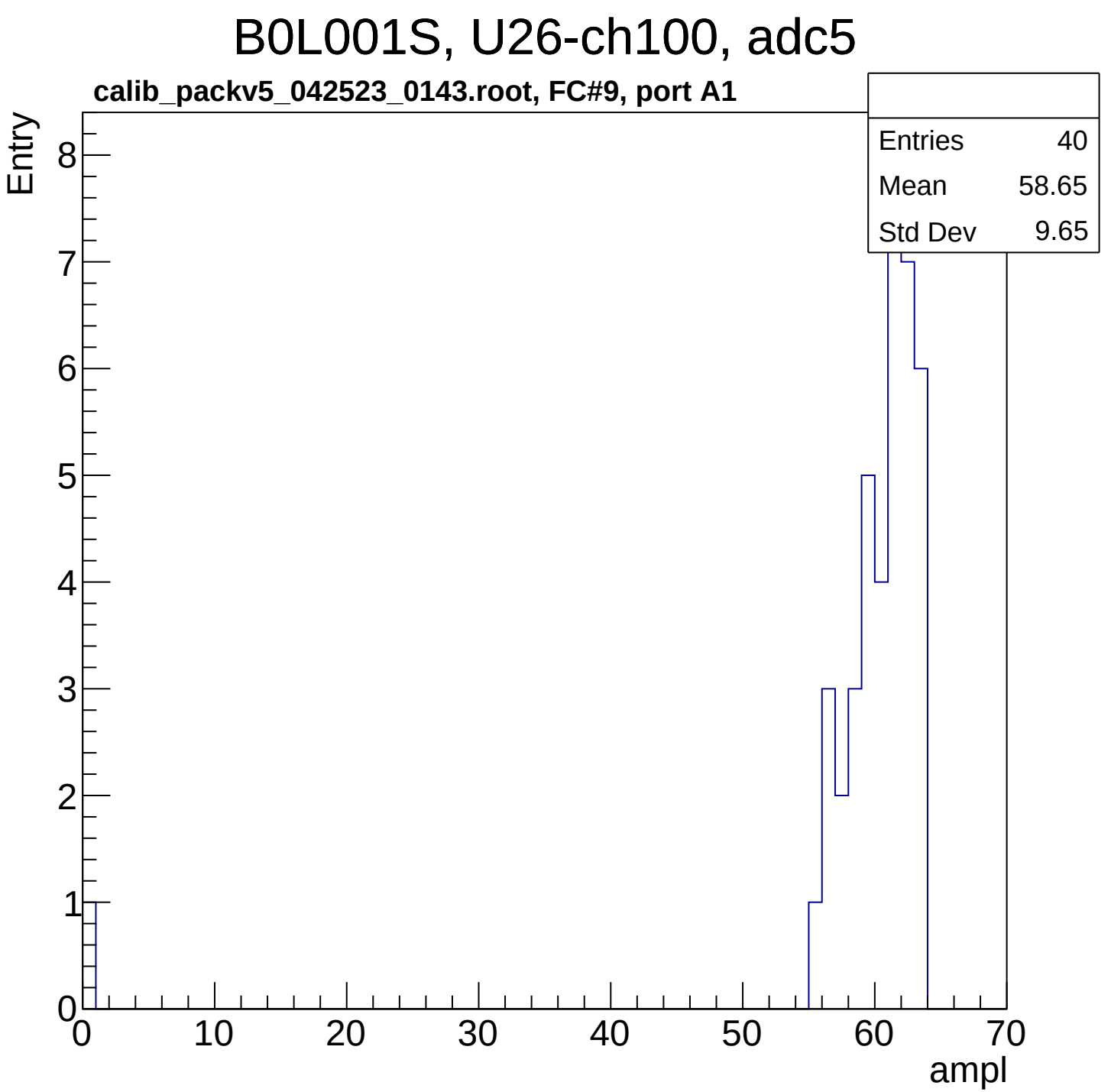
40
50
60
70


B0L001S, U26-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.65
Std Dev 9.65
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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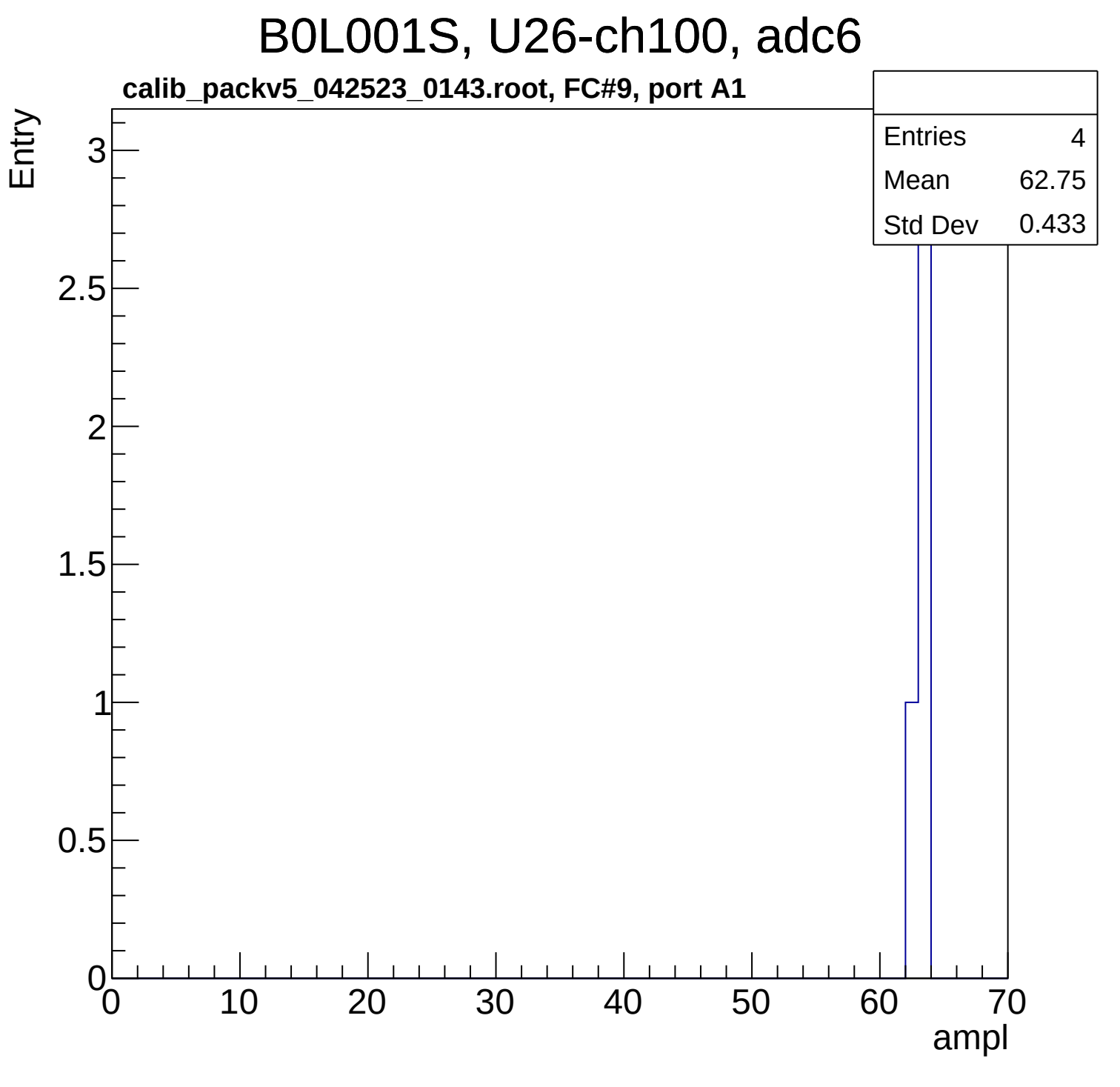
B0L001S, U26-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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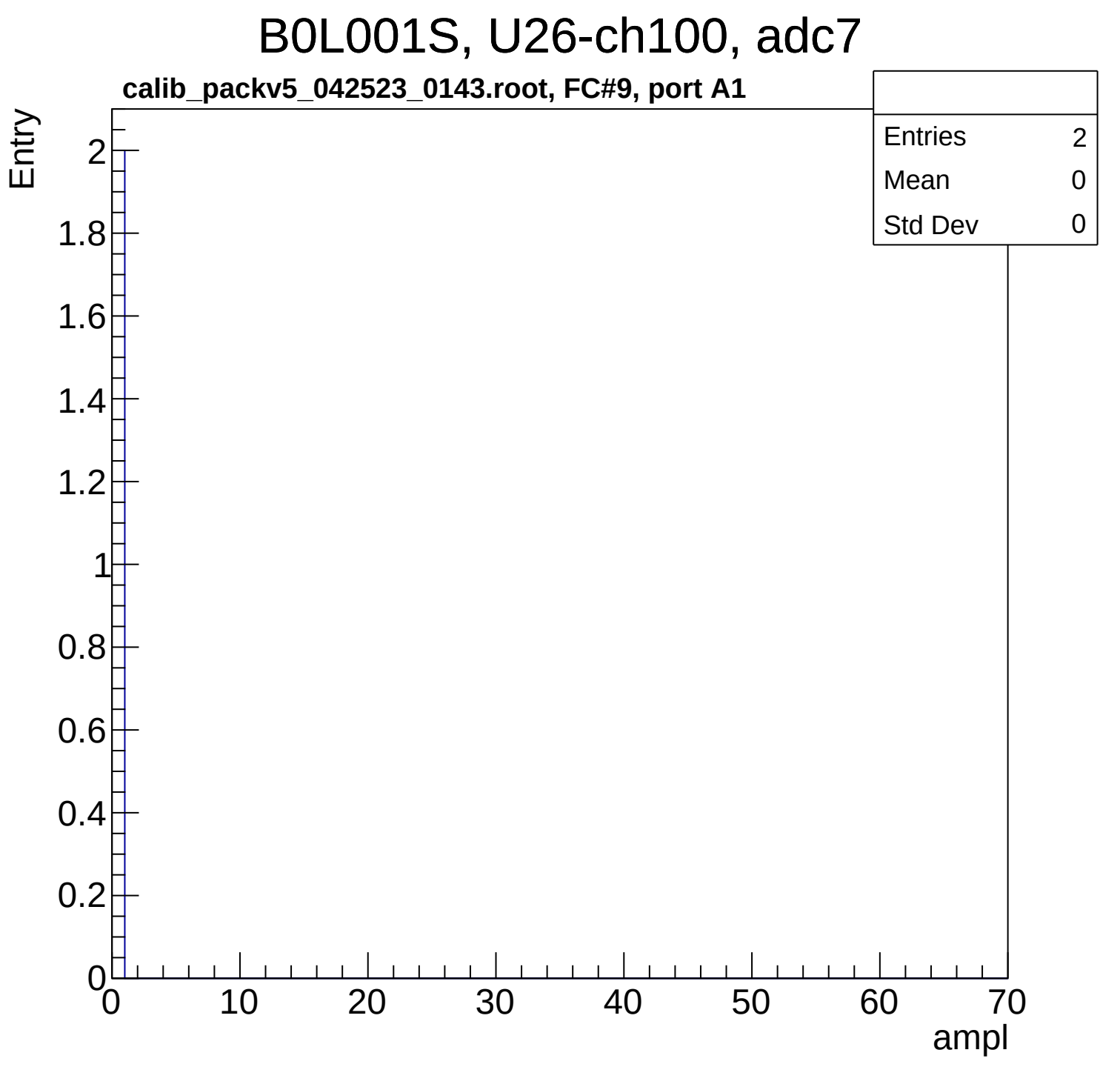
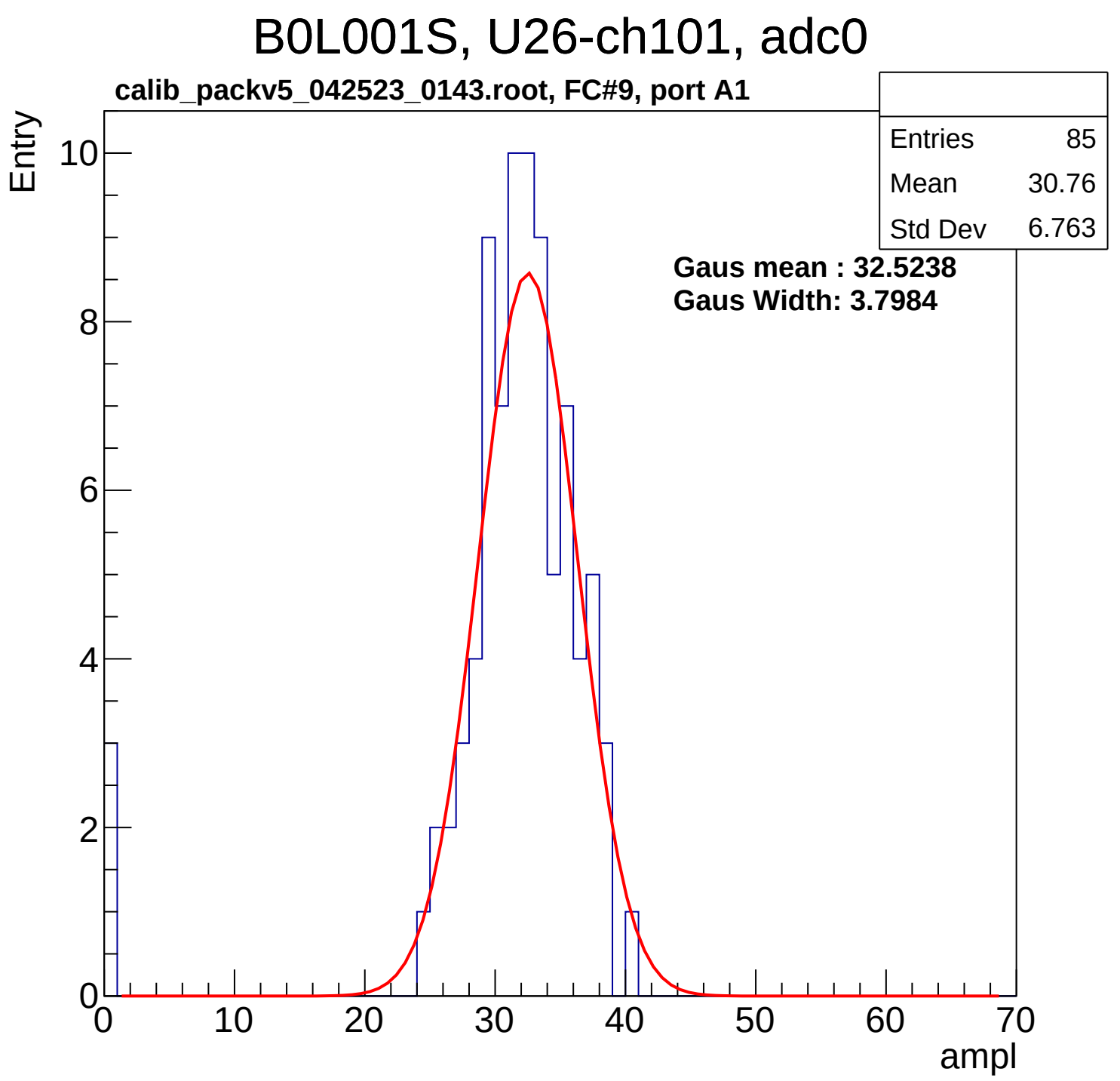
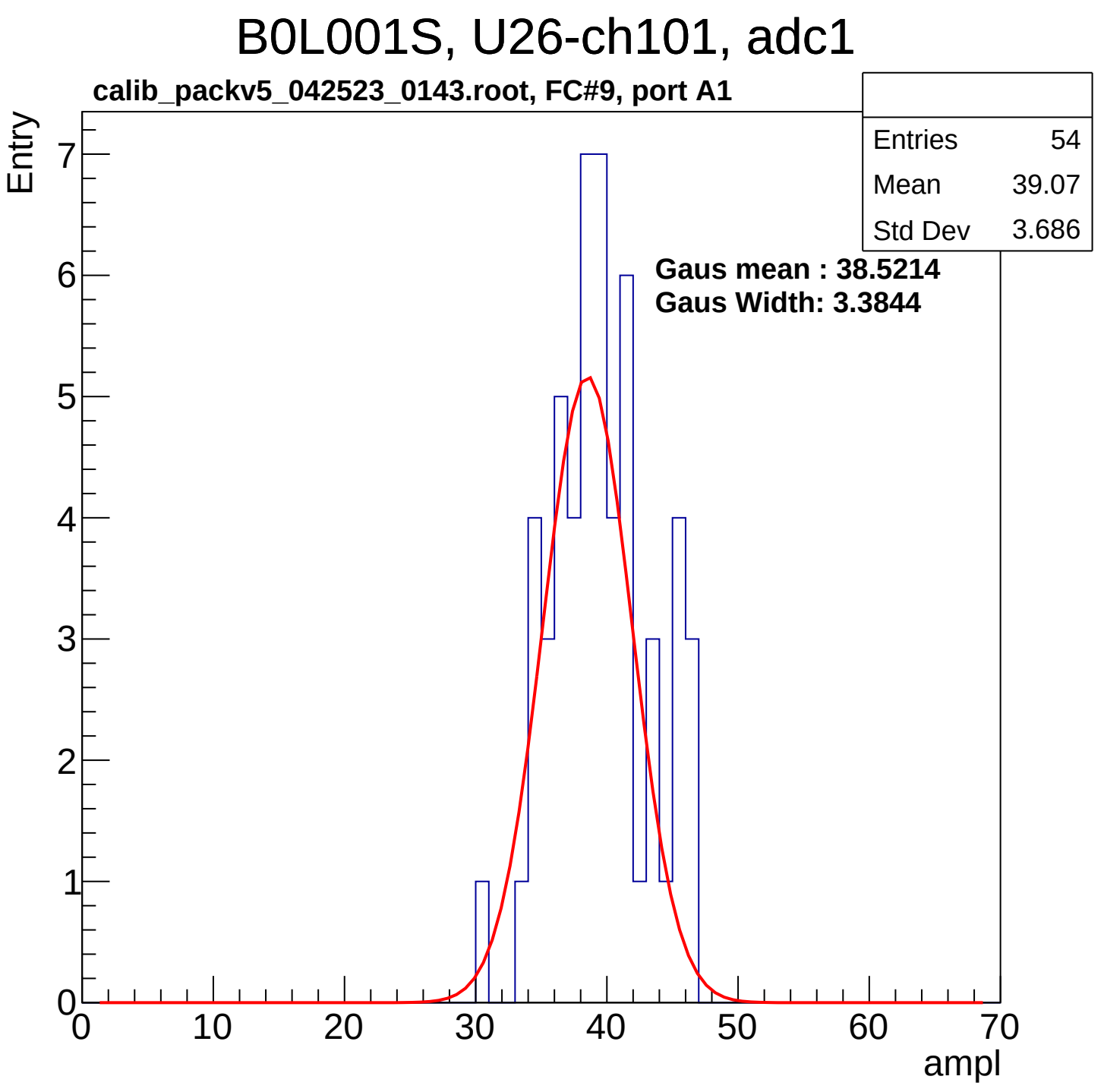
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U26-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 30.76
Std Dev 6.763
Gaus mean : 32.5238
Gaus Width: 3.7984
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 39.07
Std Dev 3.686
Gaus mean : 38.5214
Gaus Width: 3.3844
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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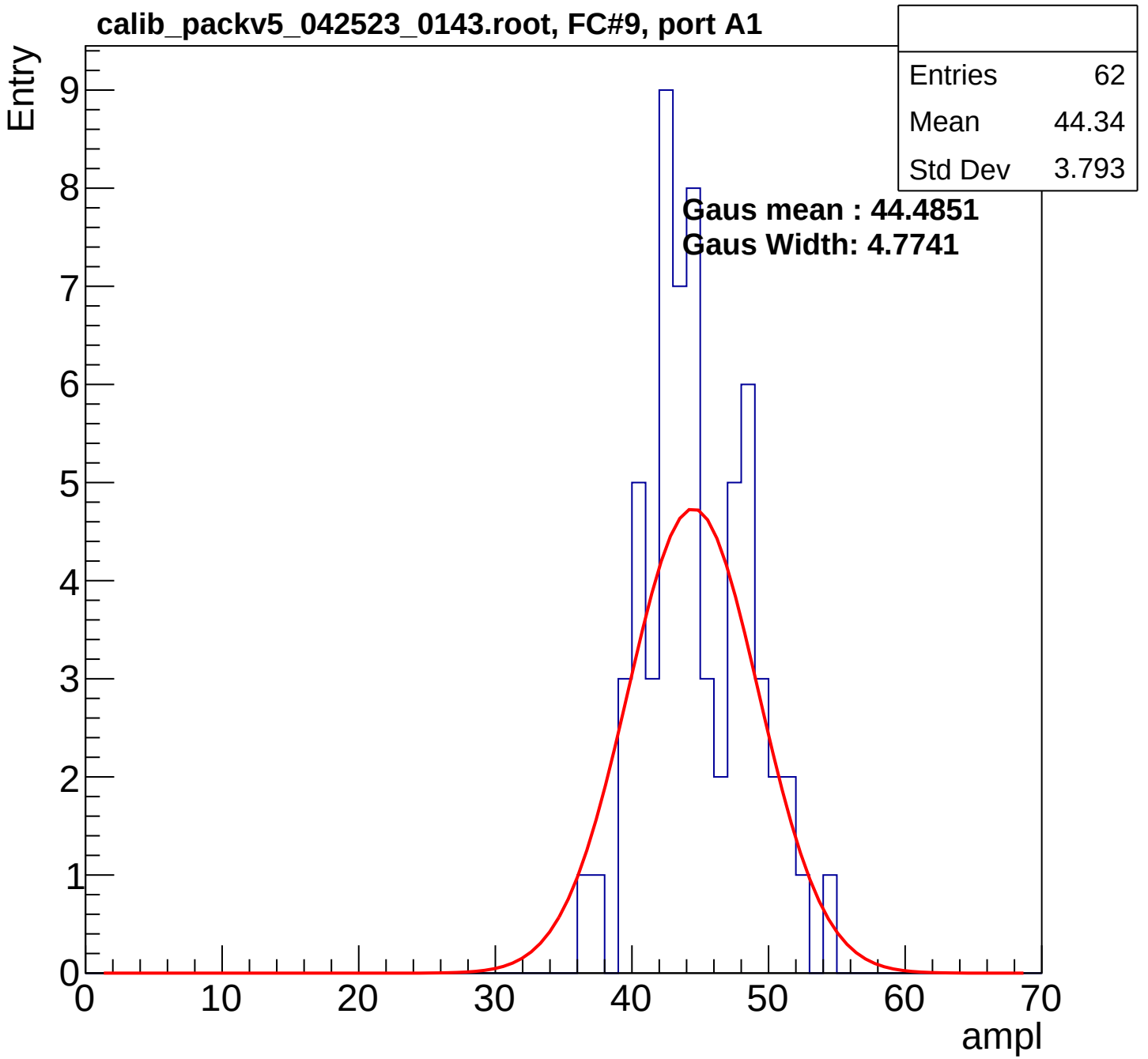
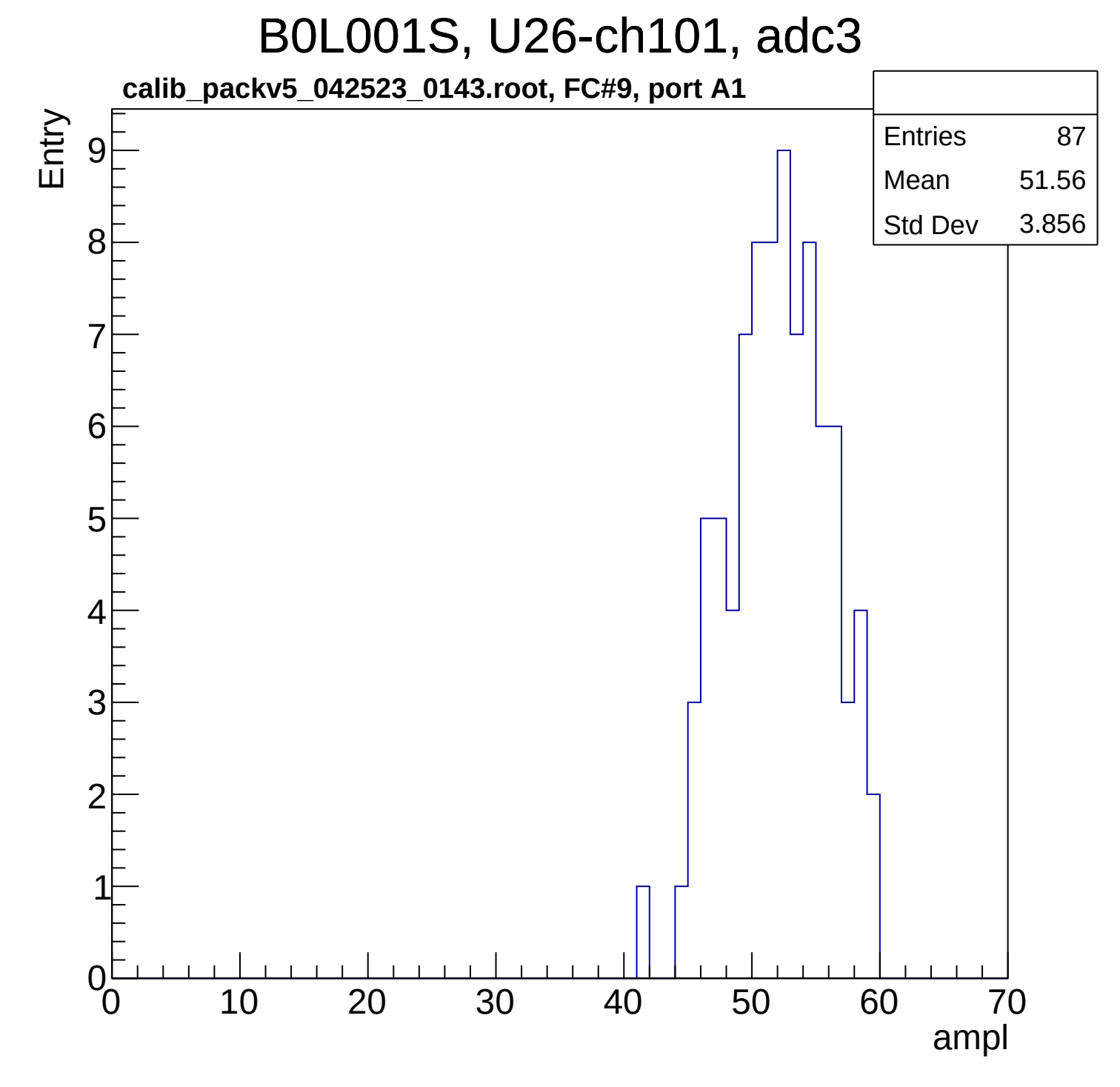
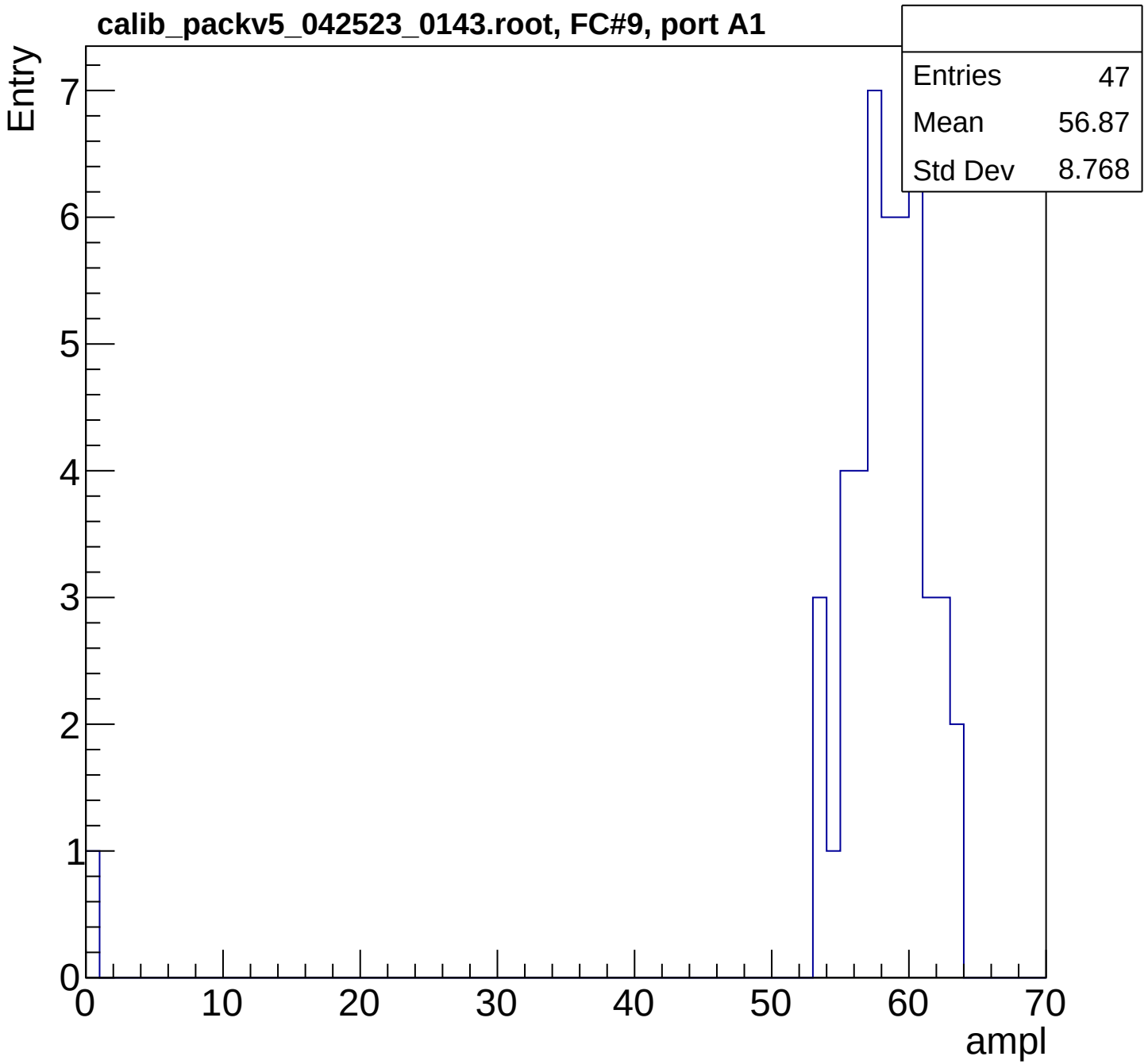
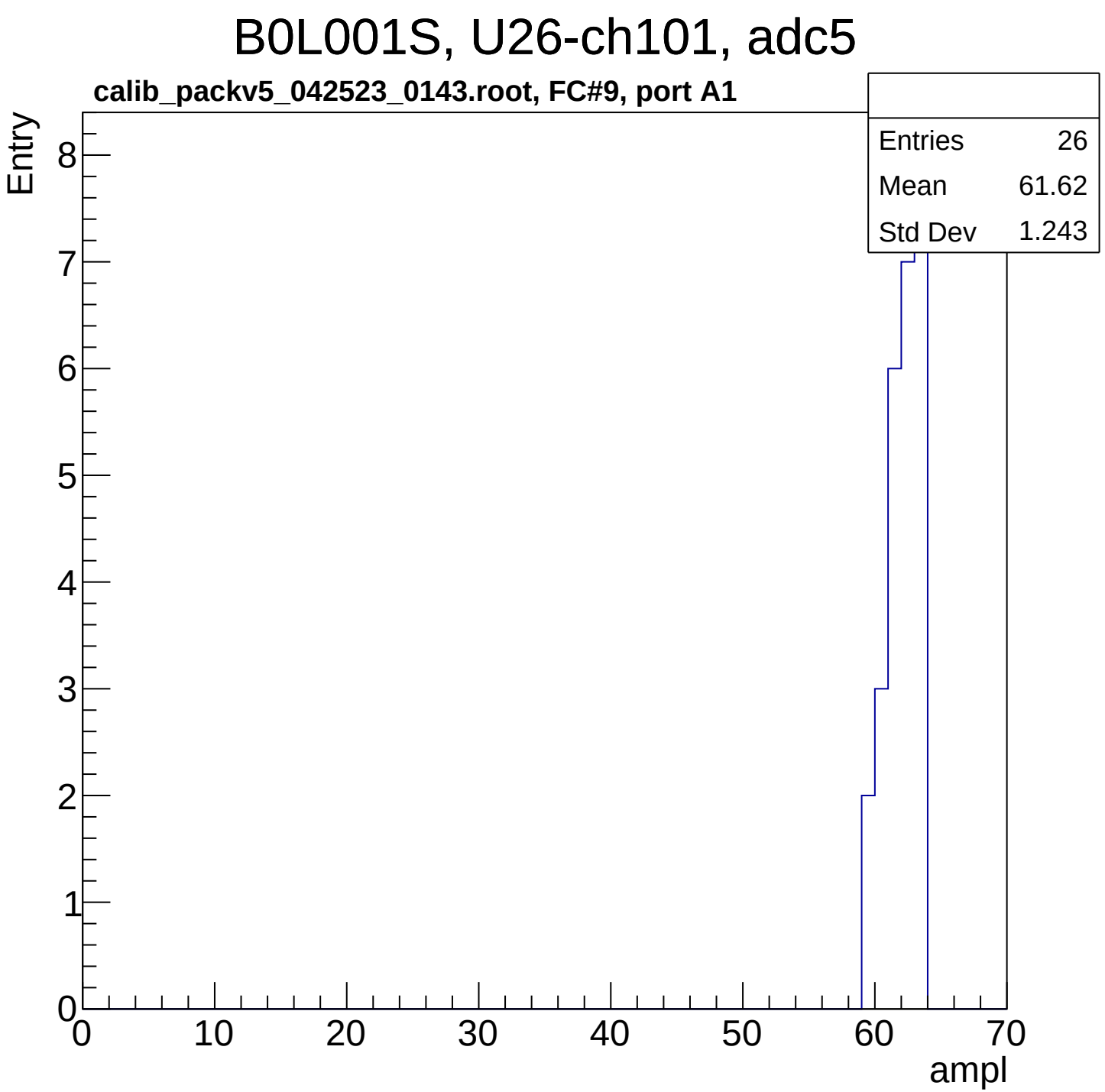
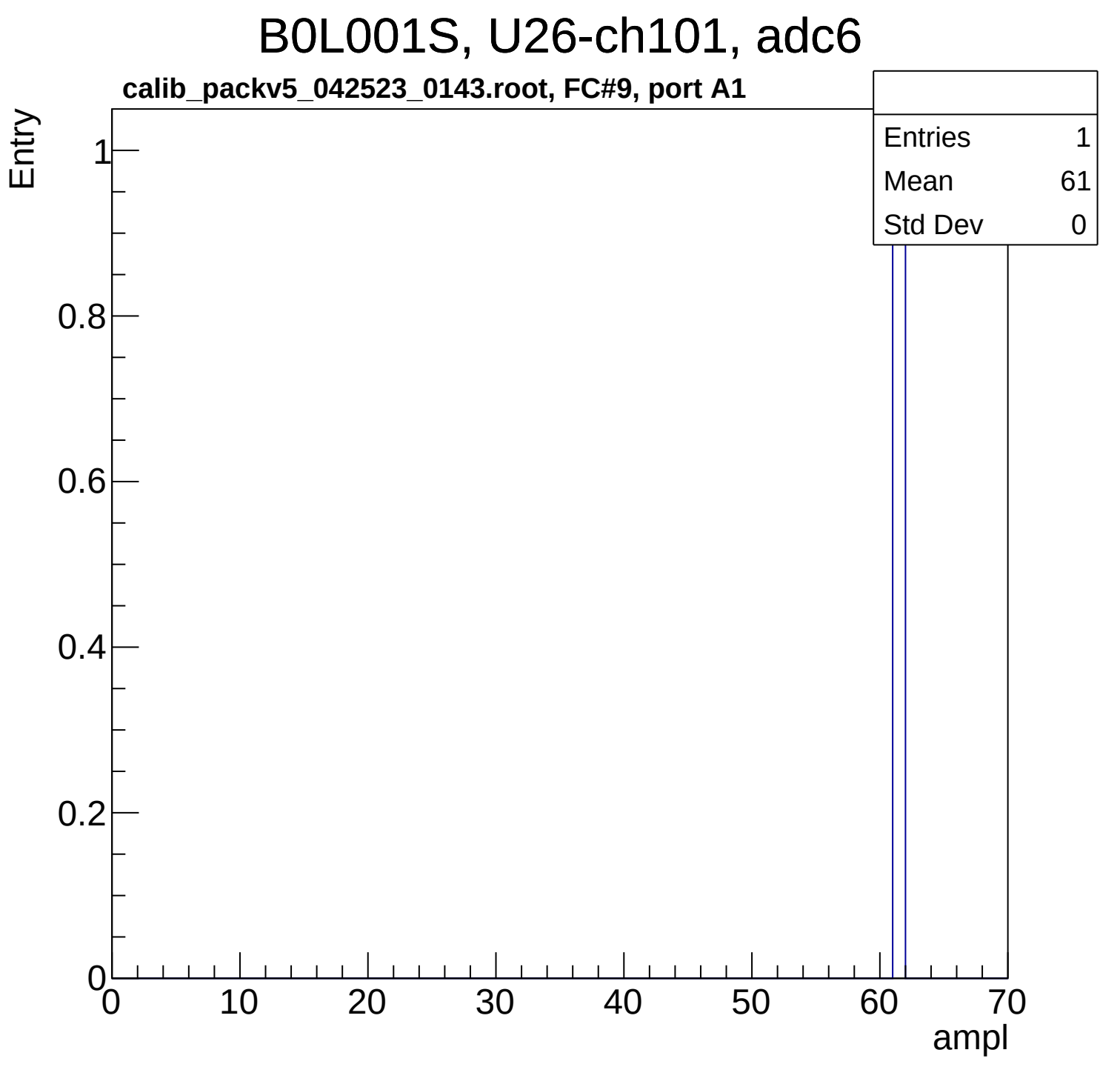
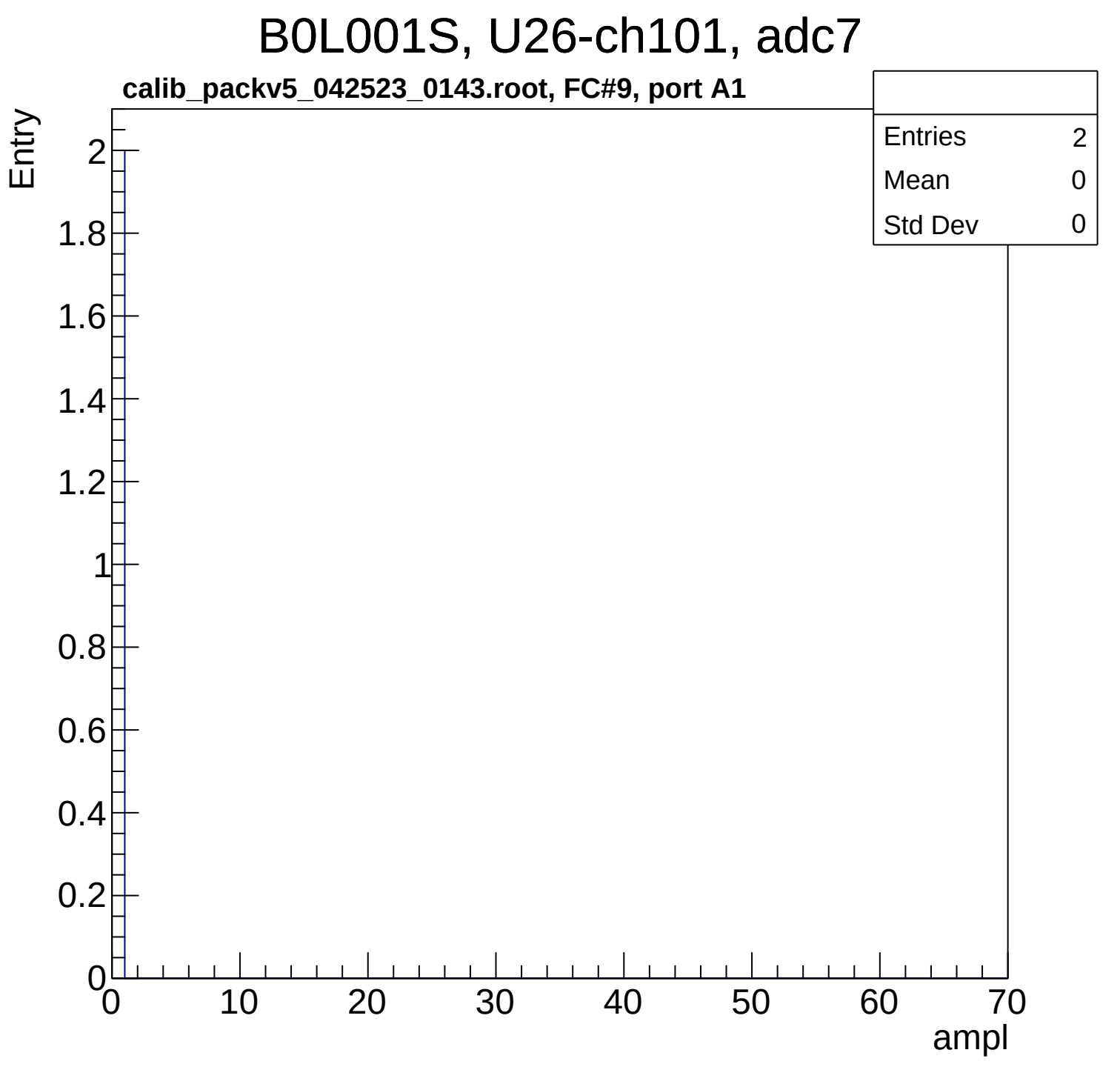

B0L001S, U26-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.34
Std Dev 3.793
Gaus mean : 44.4851
Gaus Width: 4.7741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

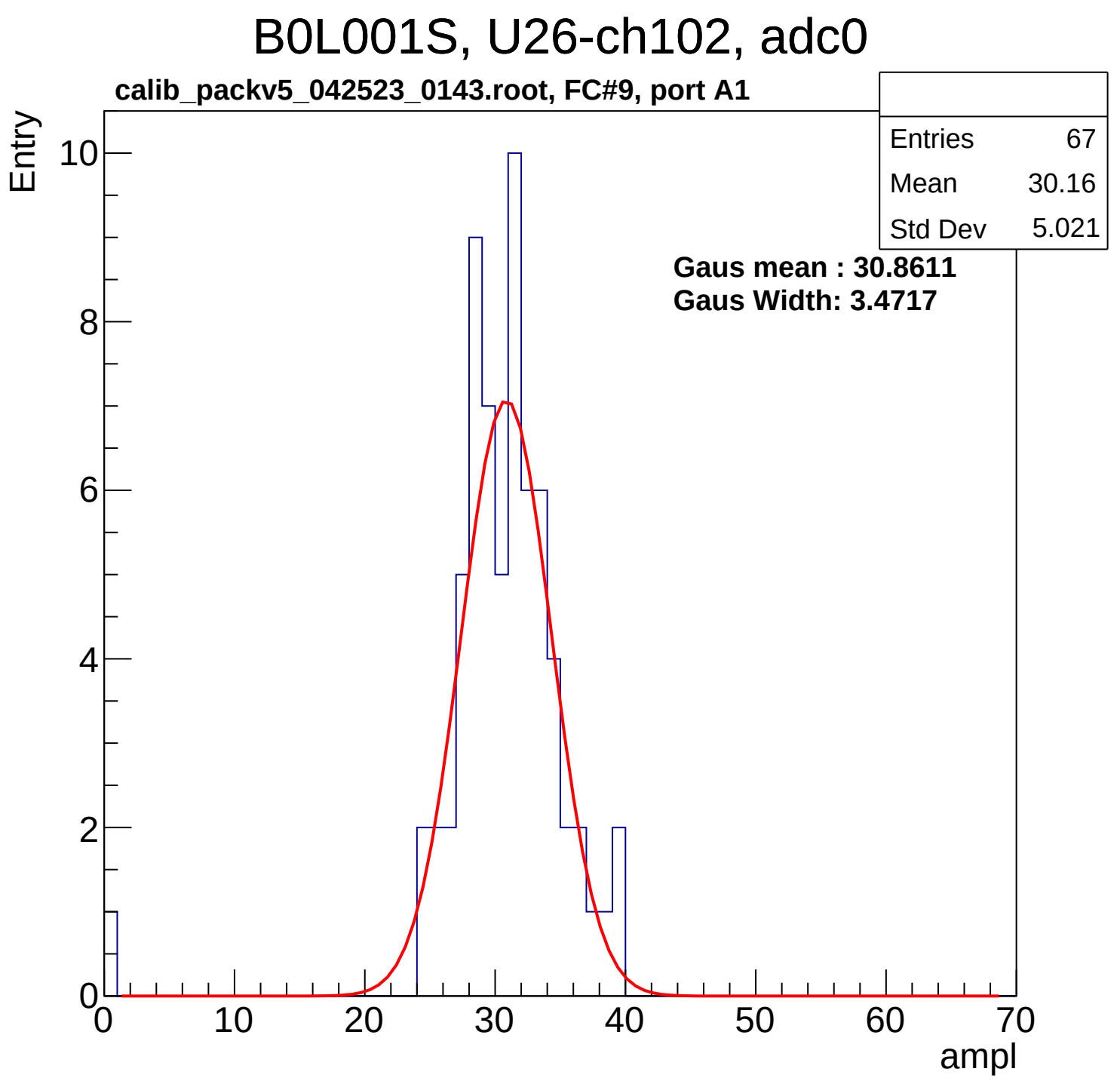

B0L001S, U26-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 51.56
Std Dev 3.856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

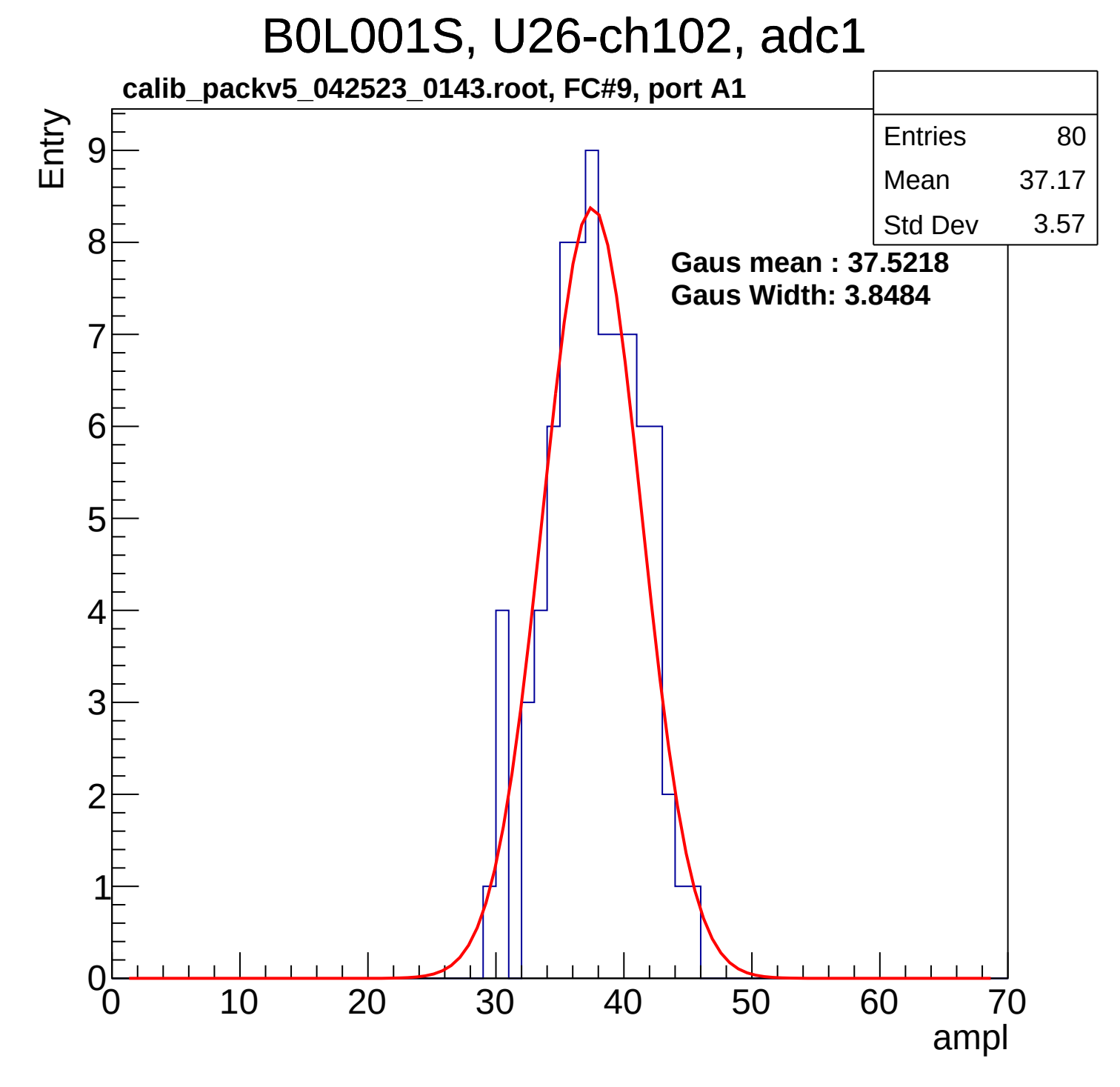

B0L001S, U26-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.87
Std Dev 8.768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

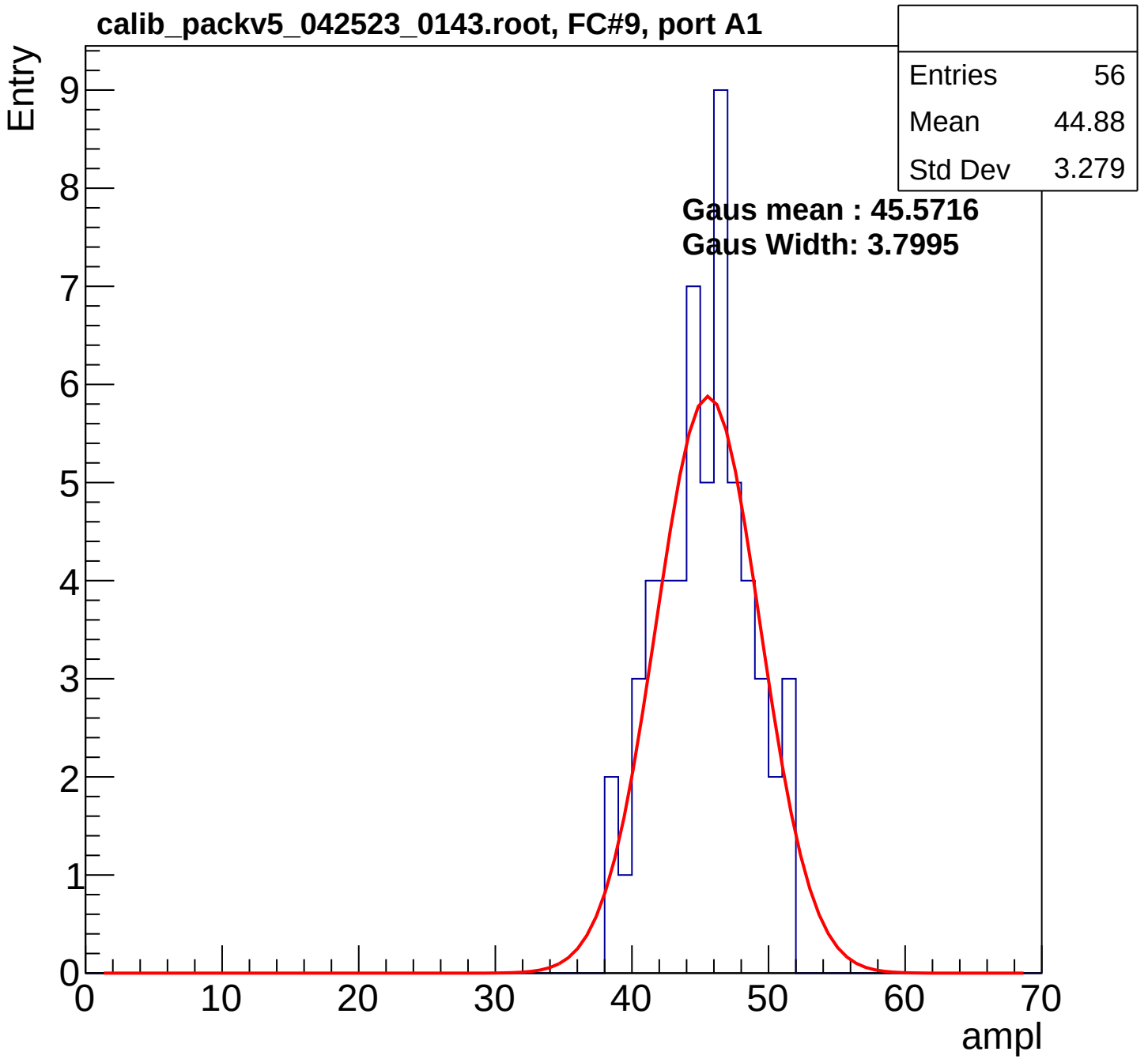

B0L001S, U26-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 61.62
Std Dev 1.243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.16
Std Dev 5.021
Gaus mean : 30.8611
Gaus Width: 3.4717
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.17
Std Dev 3.57
Gaus mean : 37.5218
Gaus Width: 3.8484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.88
Std Dev 3.279
Gaus mean : 45.5716
Gaus Width: 3.7995
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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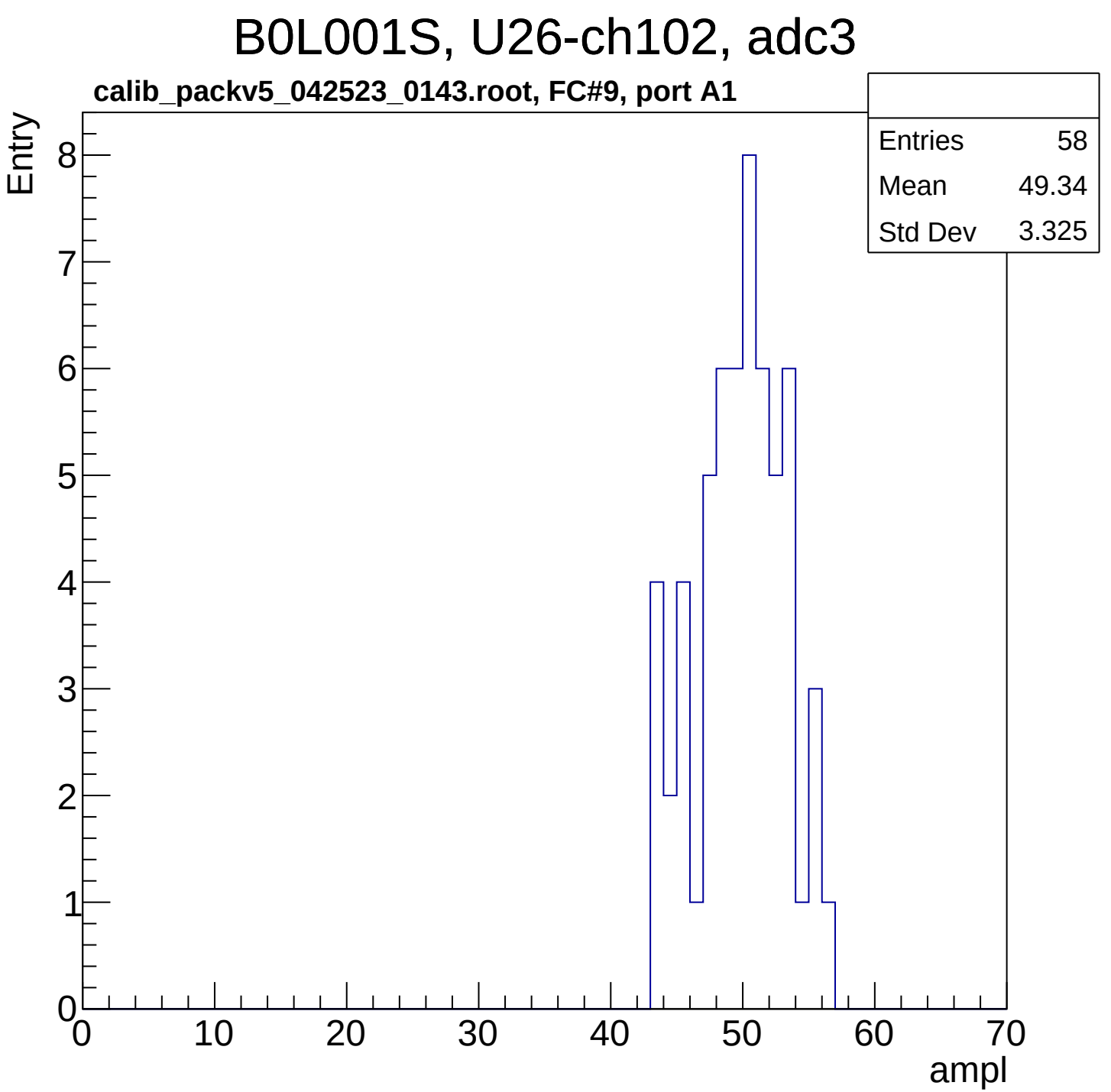
B0L001S, U26-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.34
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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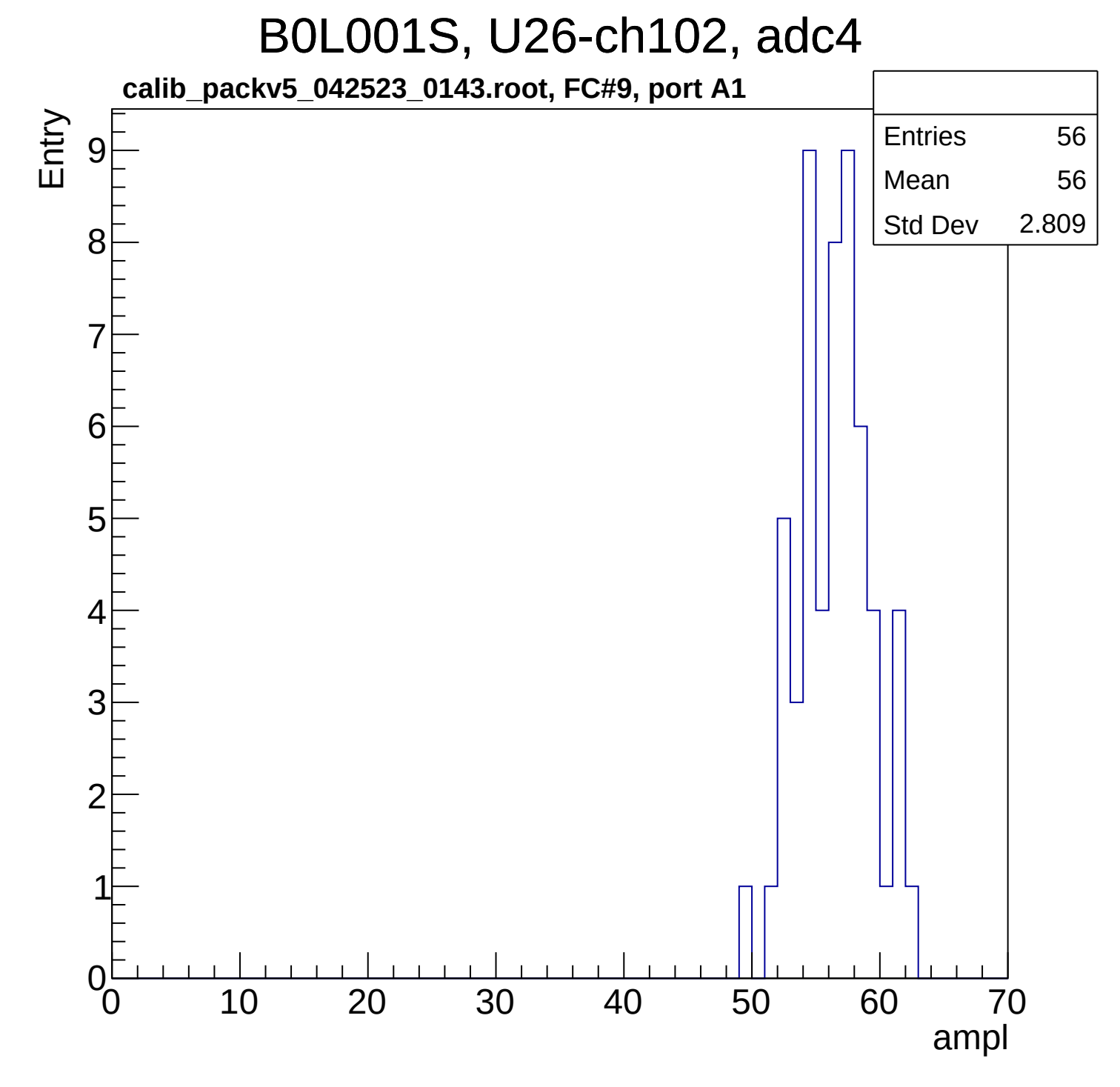
B0L001S, U26-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56
Std Dev 2.809
Entry
ampl
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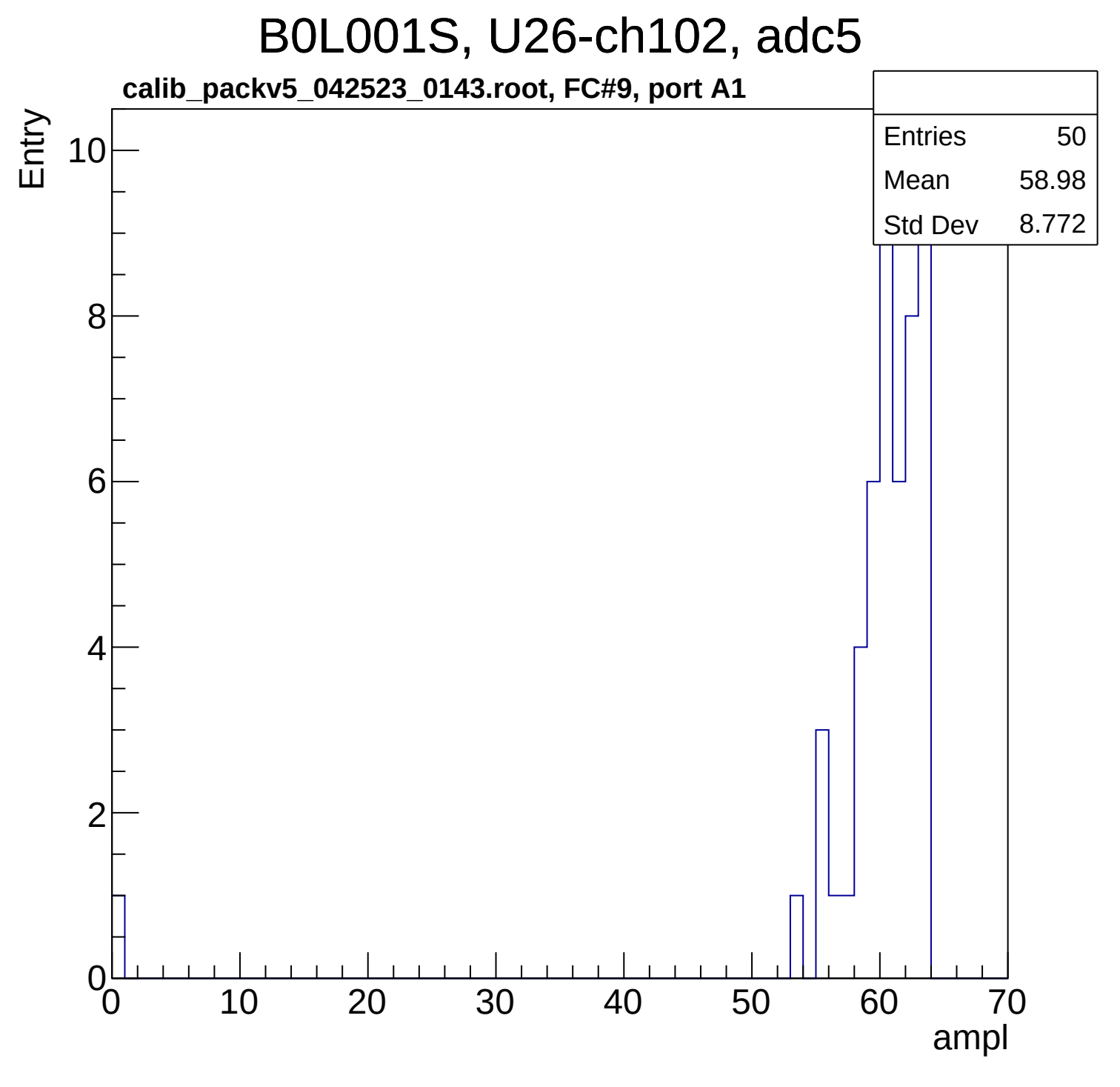
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58.98
Std Dev 8.772
Entry
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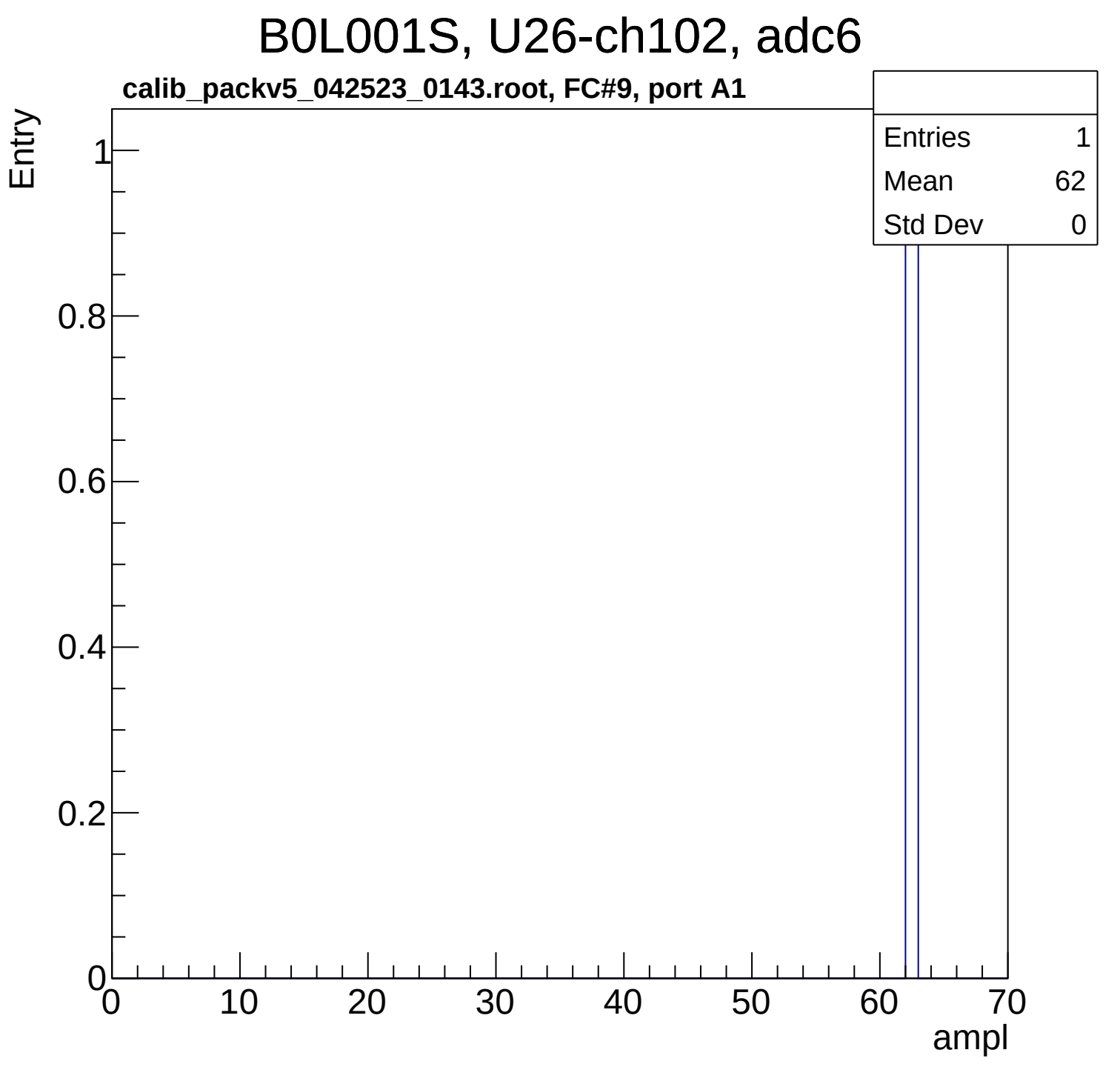
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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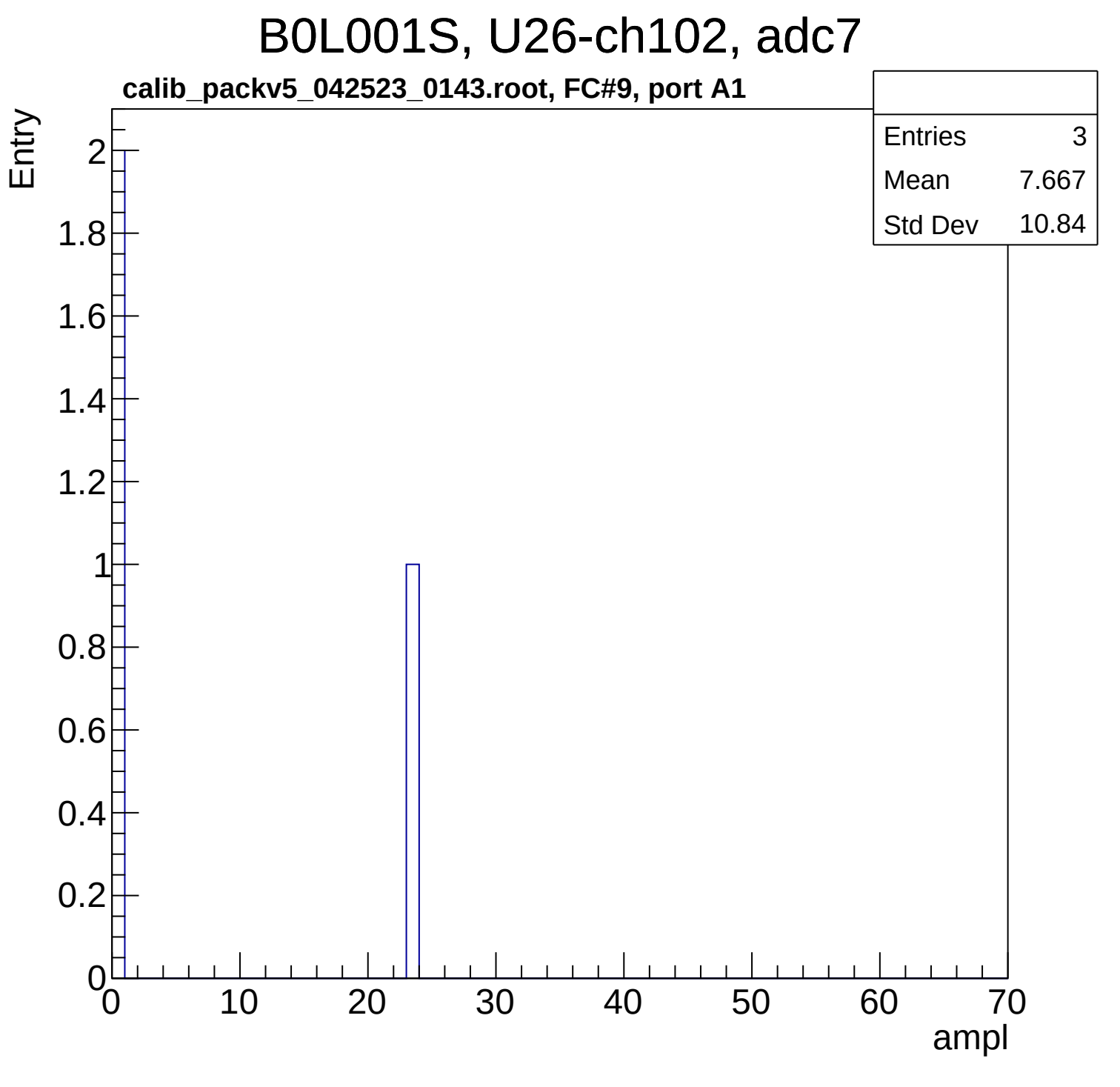
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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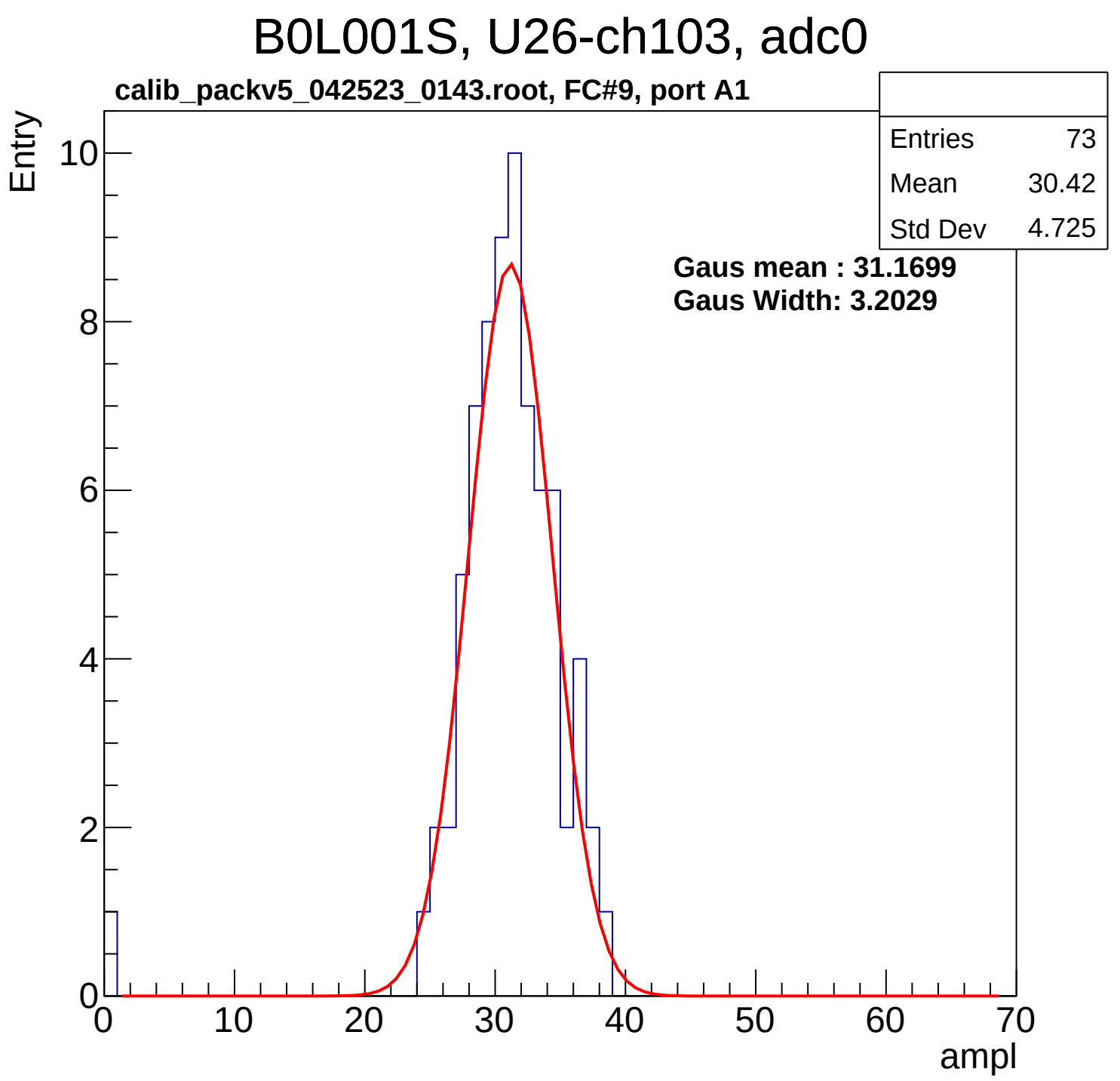
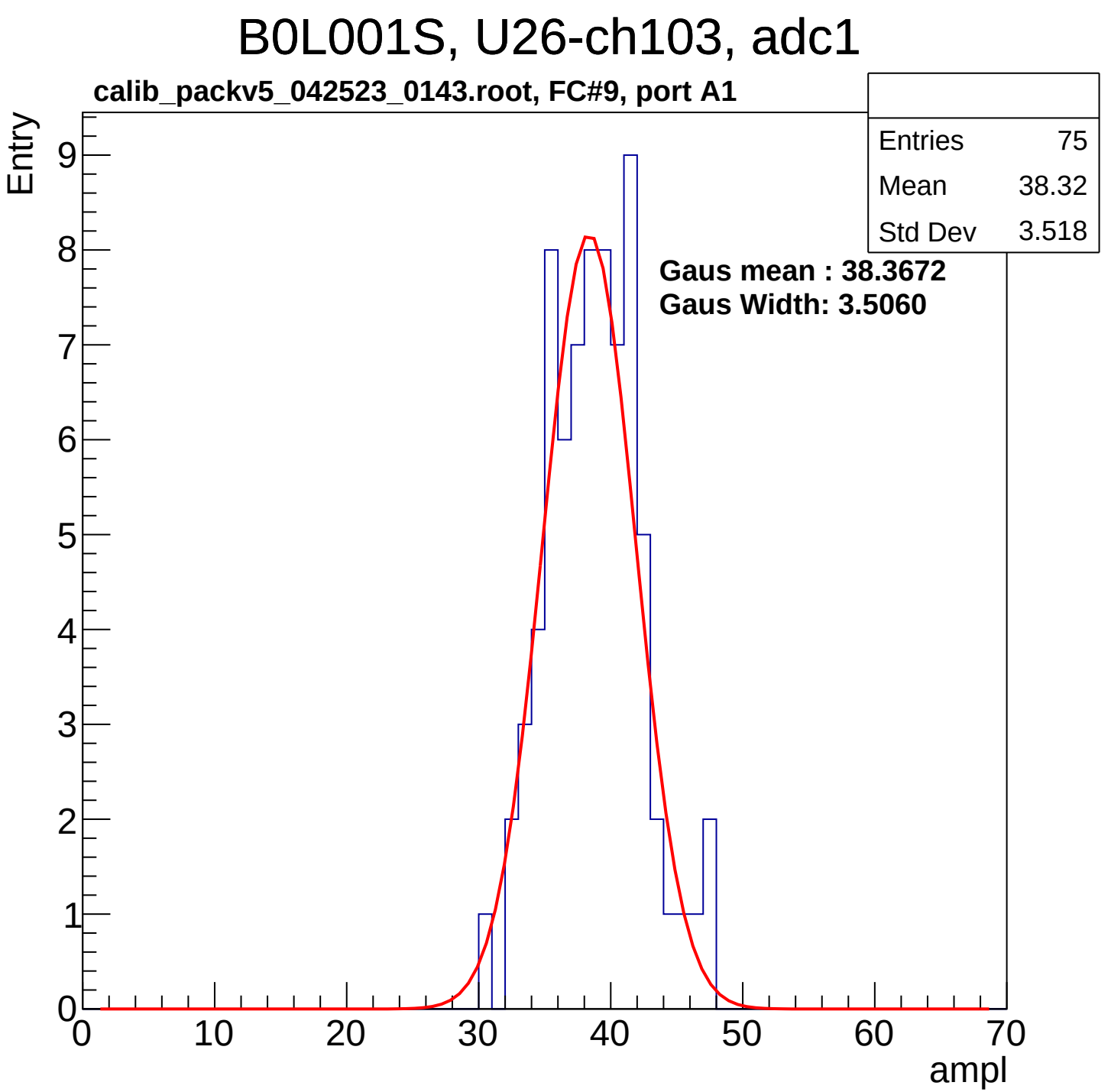
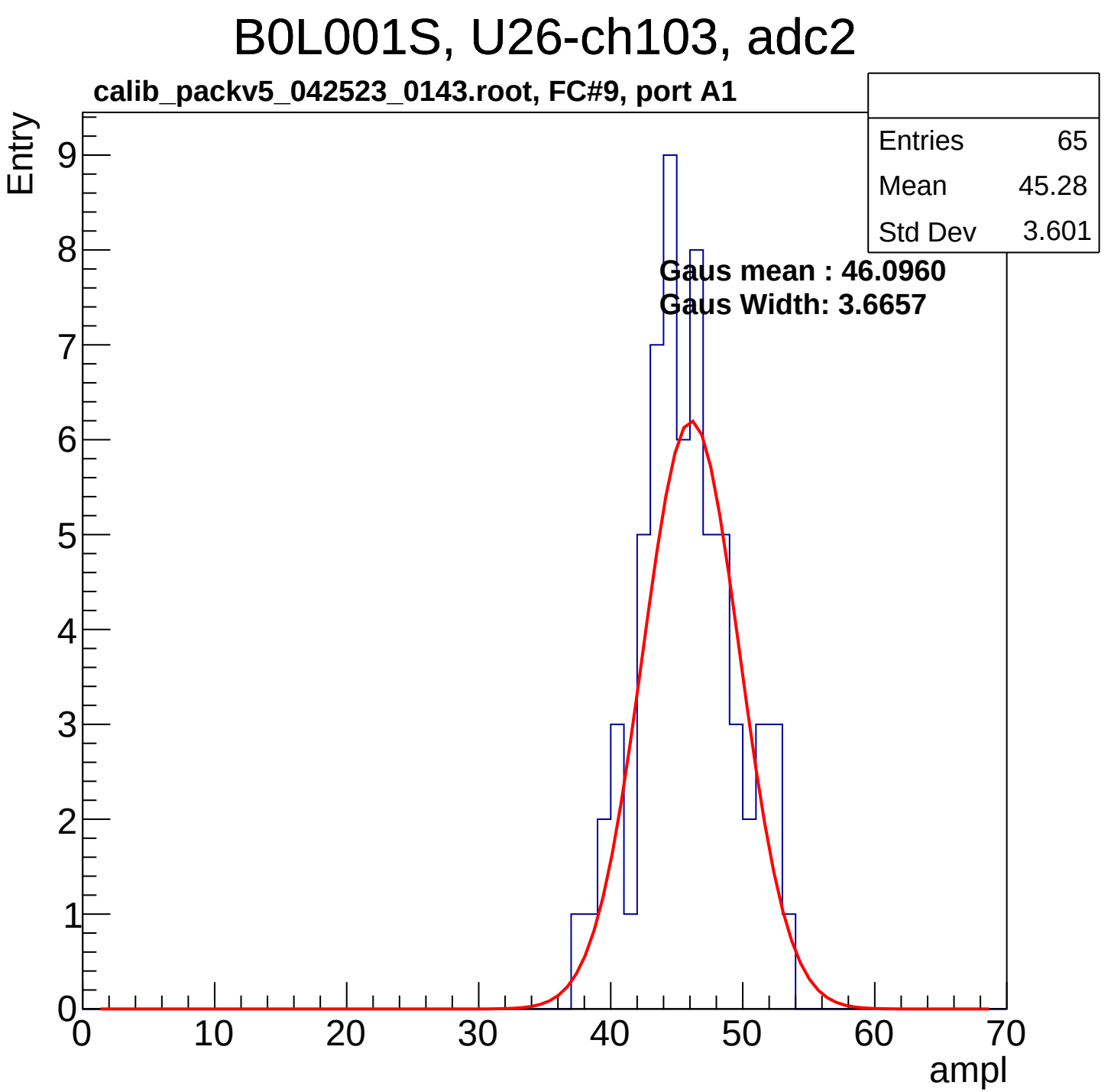

B0L001S, U26-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.42
Std Dev 4.725
Gaus mean : 31.1699
Gaus Width: 3.2029
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.32
Std Dev 3.518
Gaus mean : 38.3672
Gaus Width: 3.5060
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.28
Std Dev 3.601
Gaus mean : 46.0960
Gaus Width: 3.6657
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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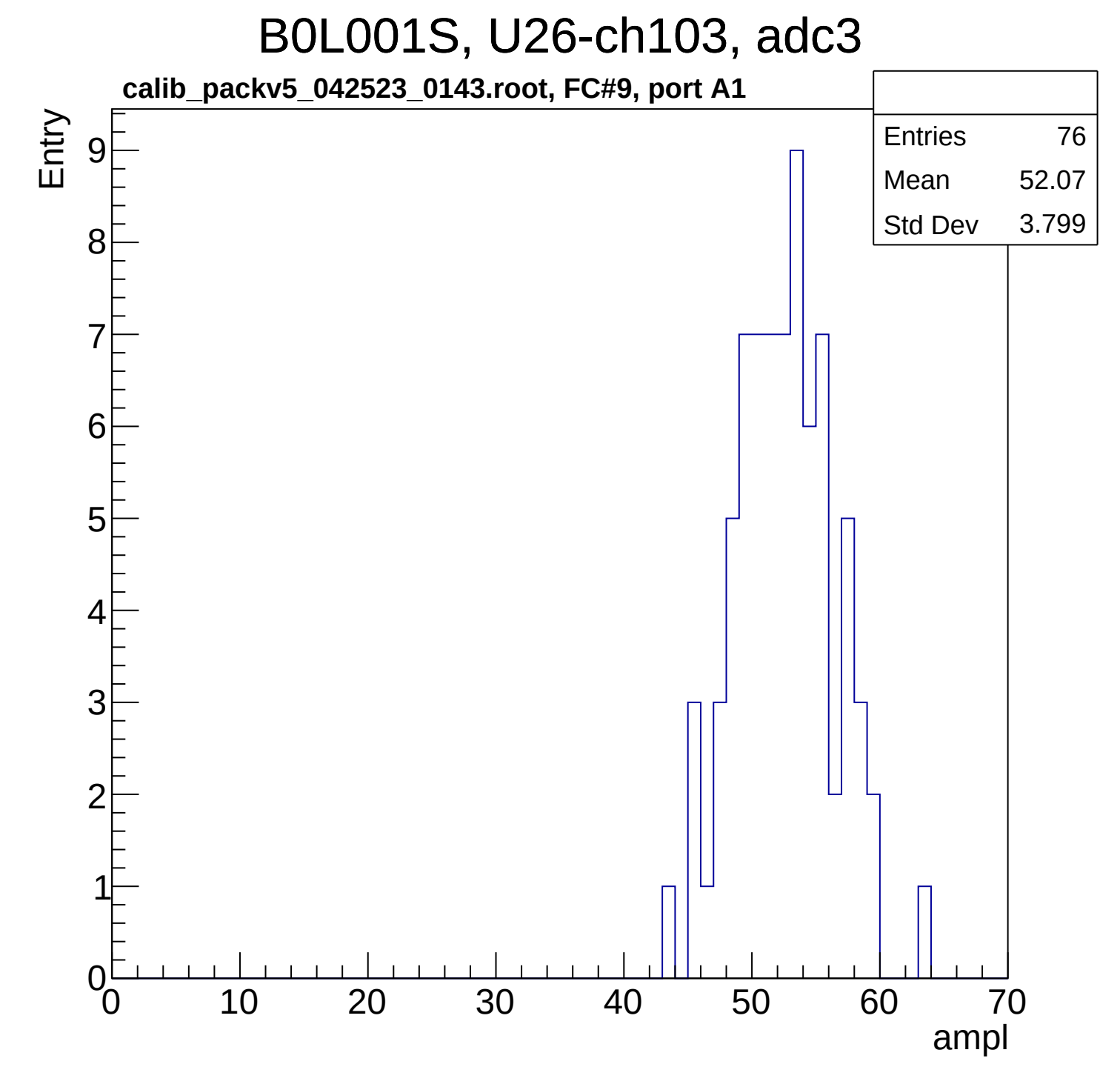
B0L001S, U26-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 52.07
Std Dev 3.799
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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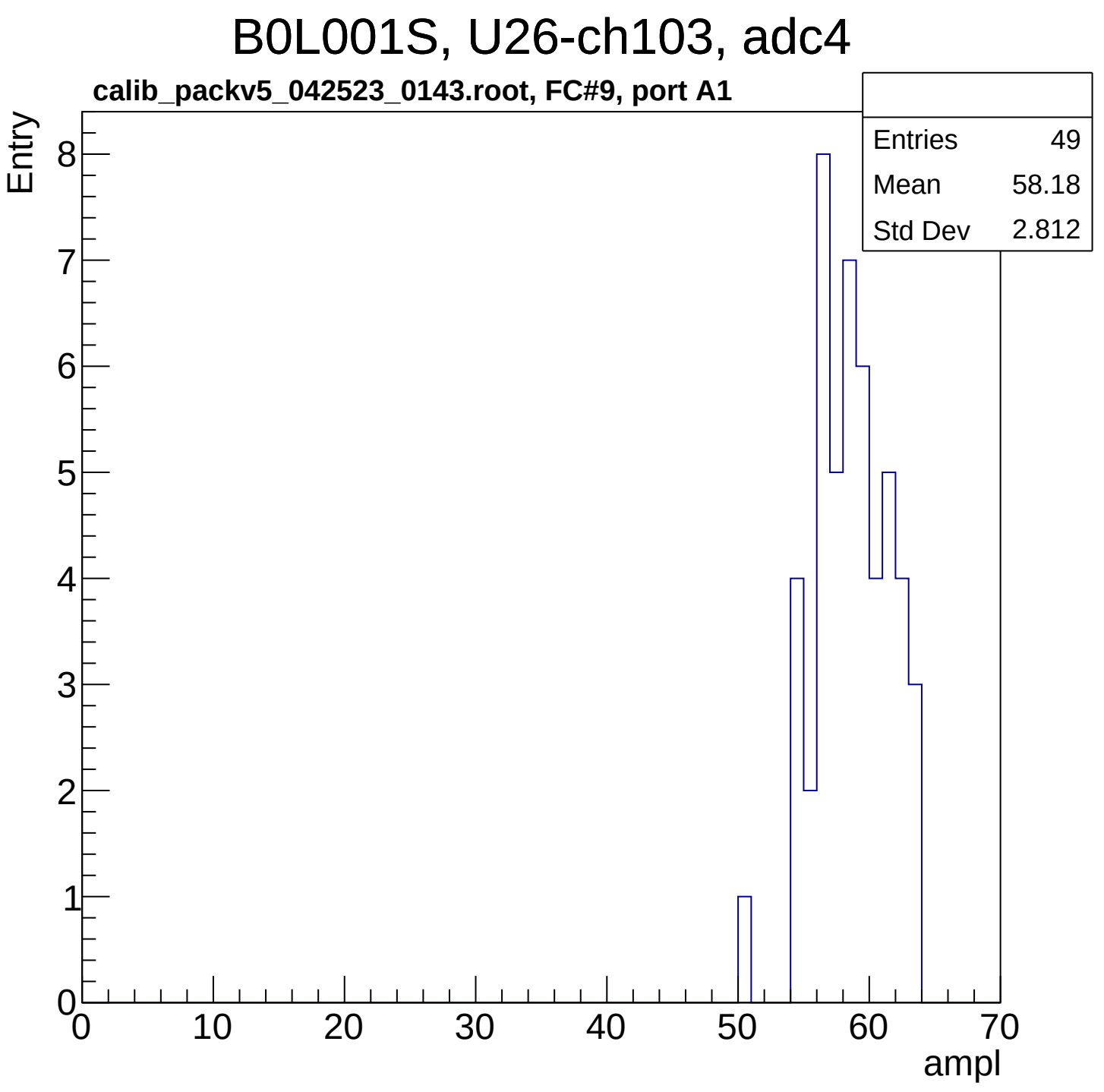
B0L001S, U26-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.18
Std Dev 2.812
Entry
ampl
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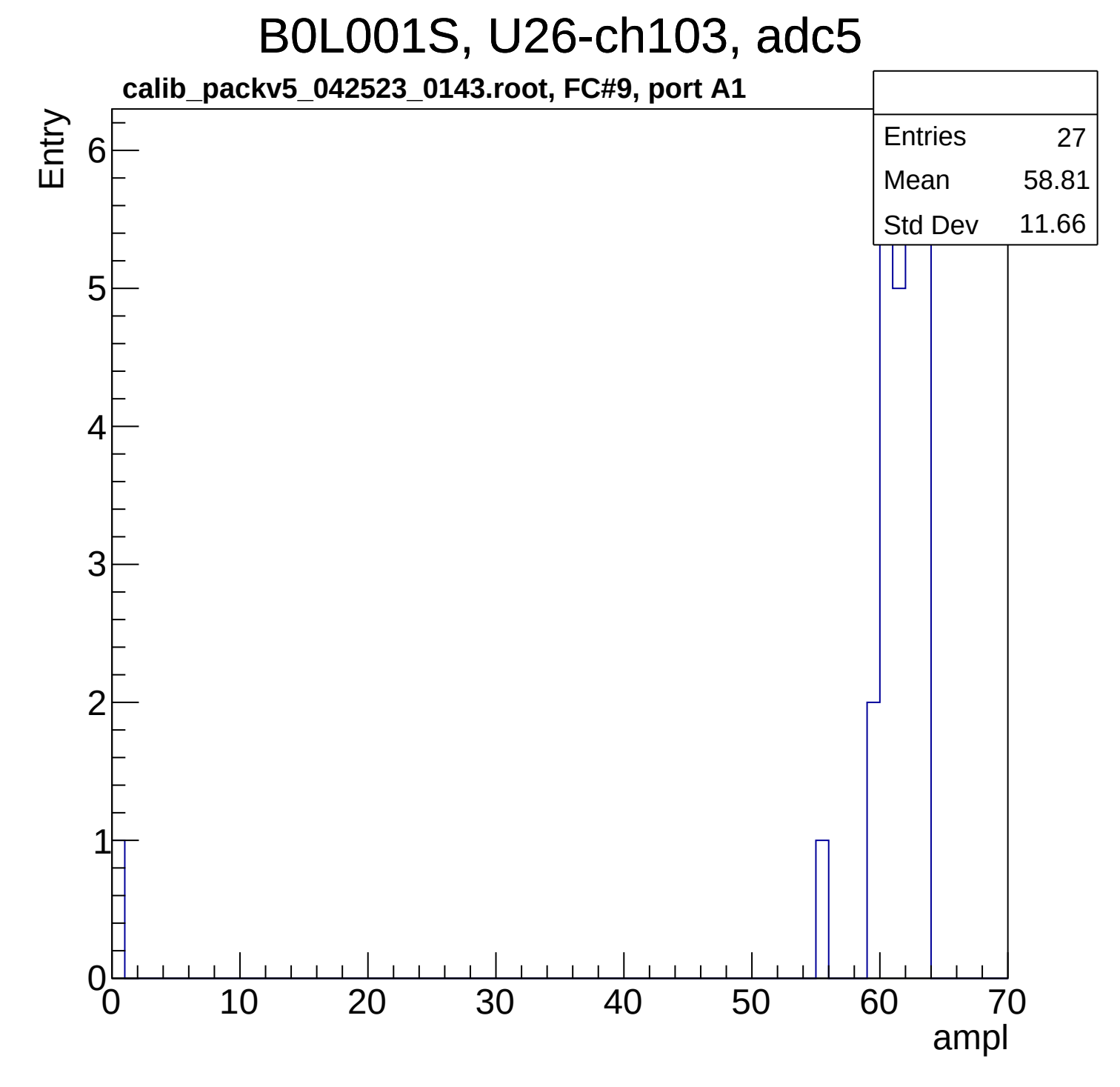
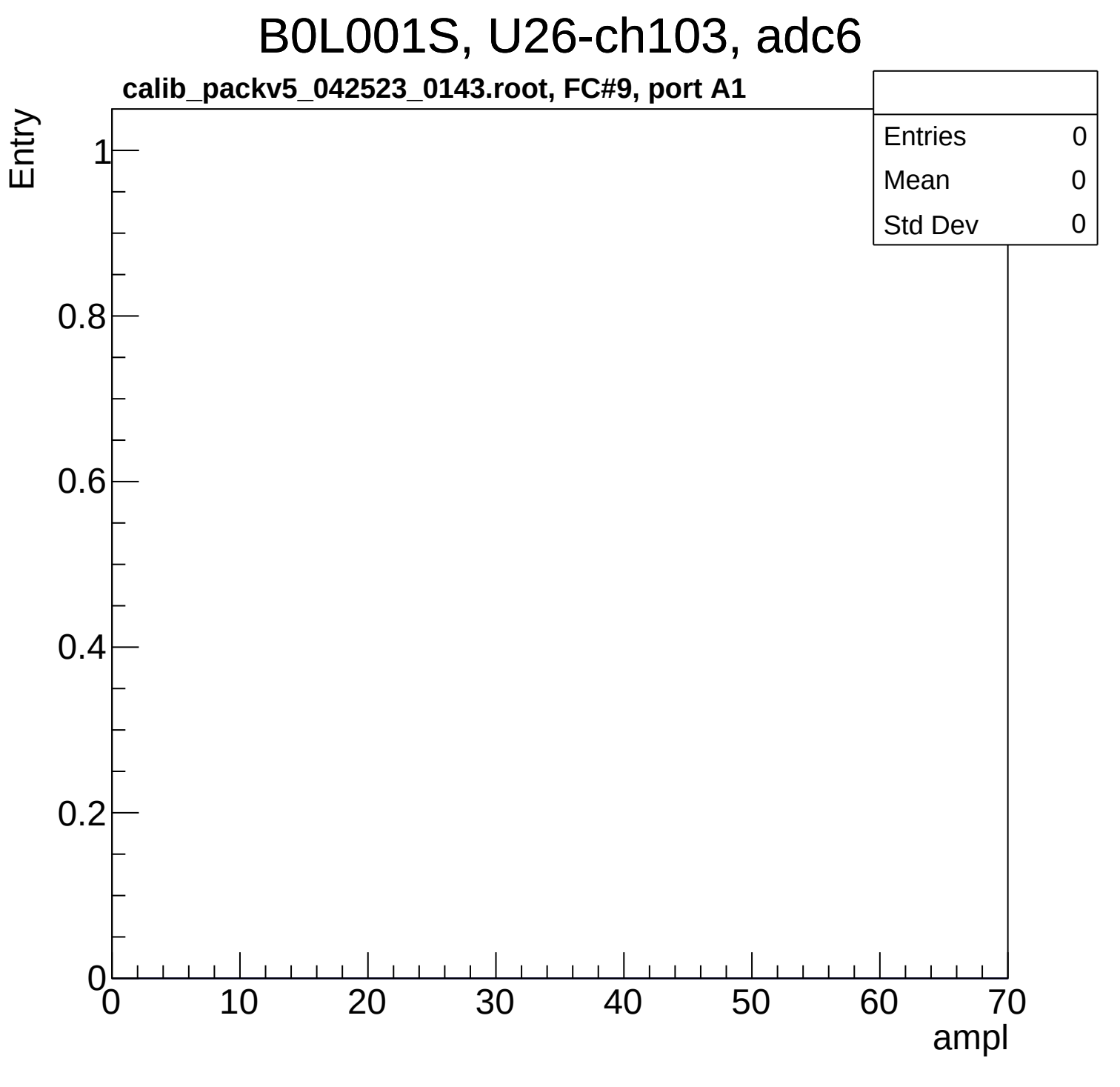
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.81
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U26-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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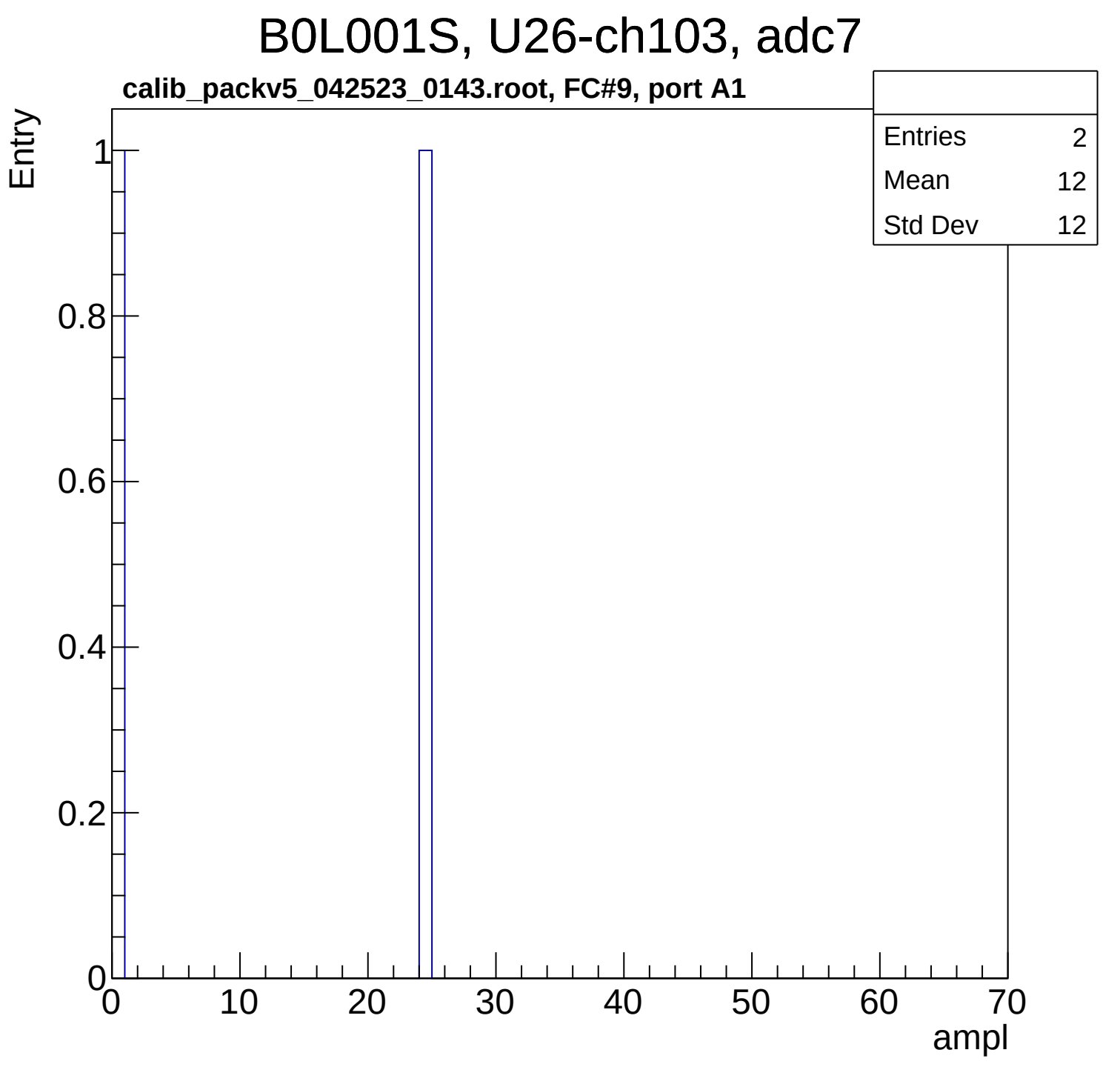
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
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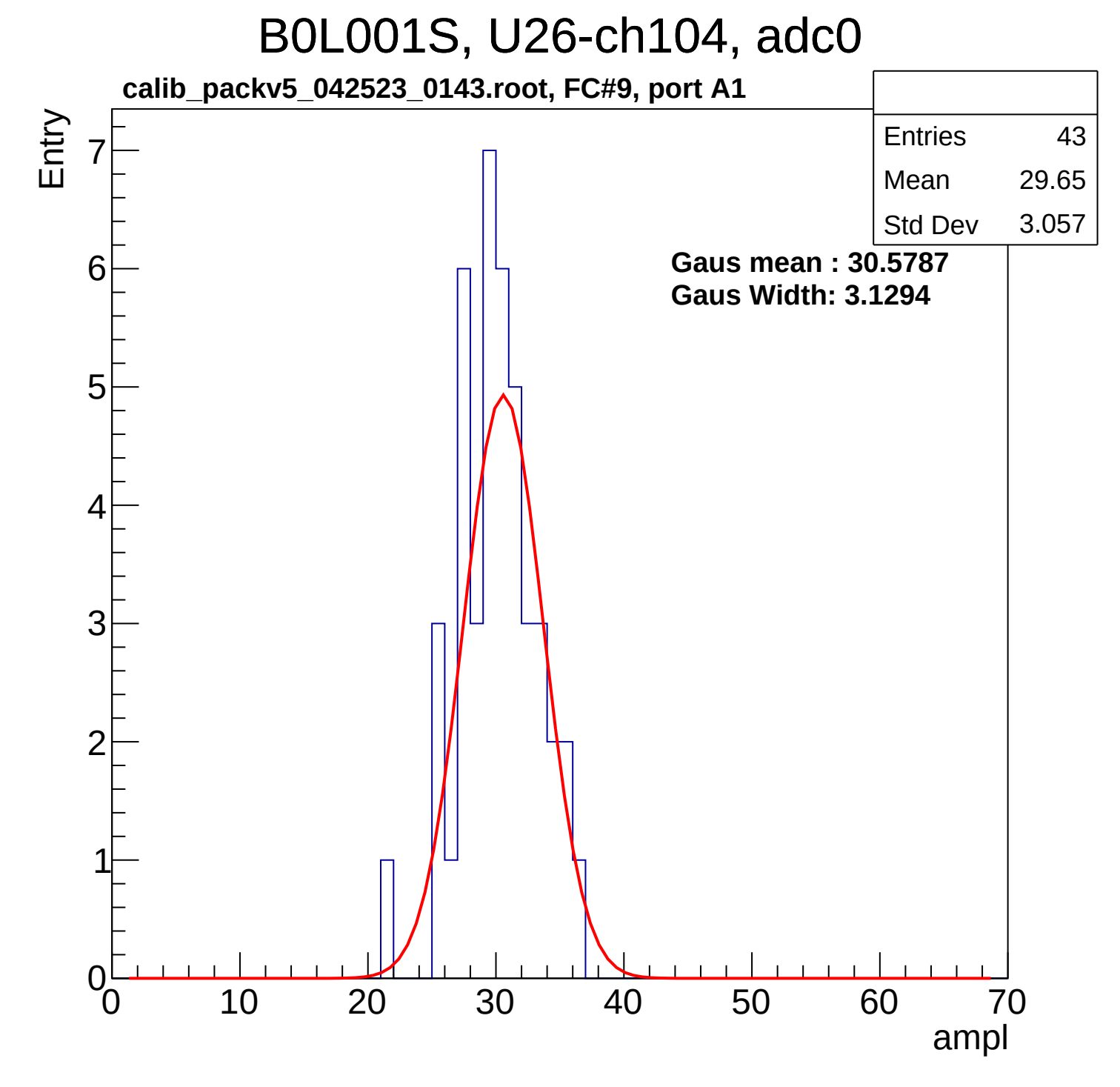
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 29.65
Std Dev 3.057
Gaus mean : 30.5787
Gaus Width: 3.1294
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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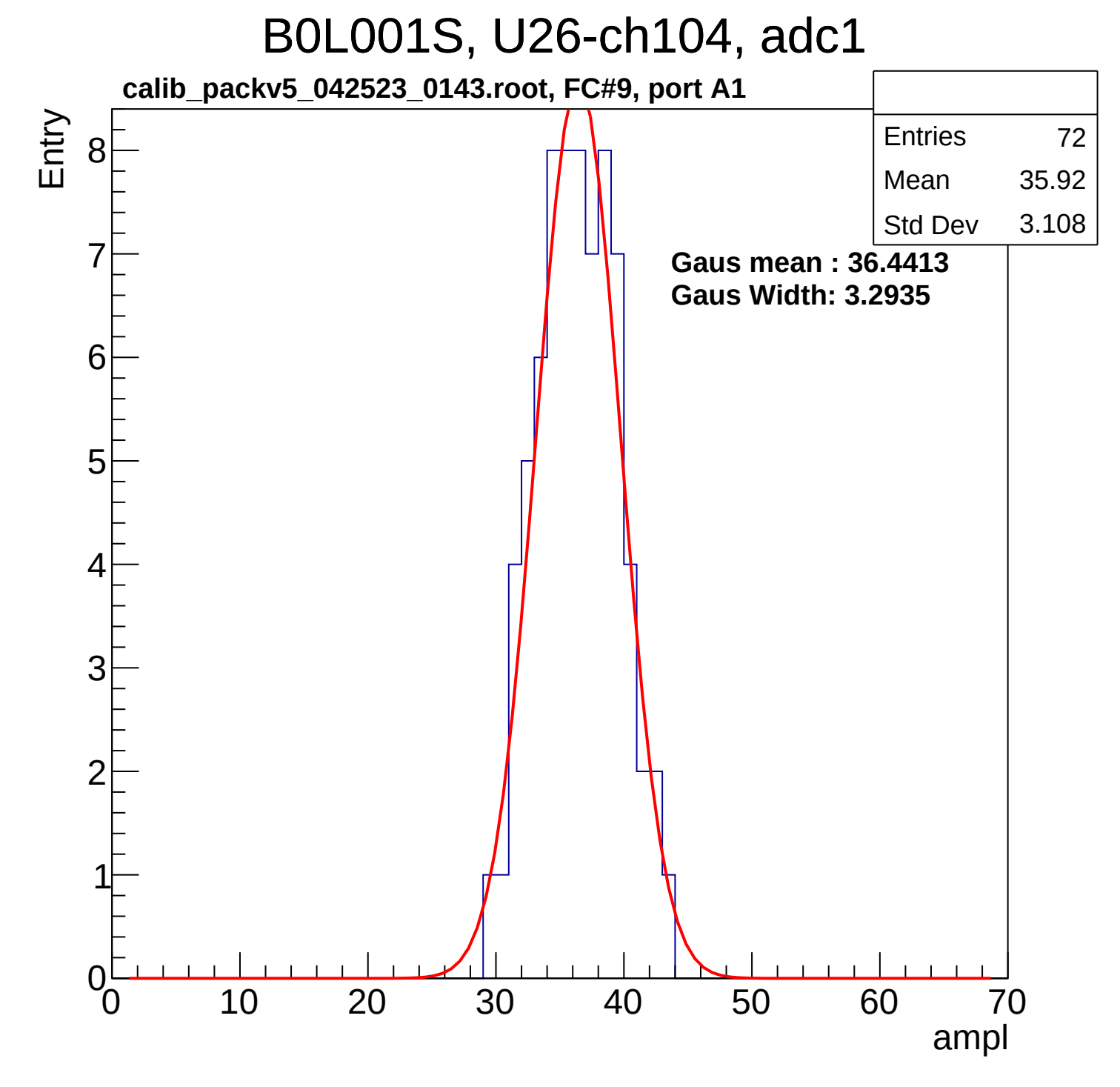
B0L001S, U26-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 35.92
Std Dev 3.108
Gaus mean : 36.4413
Gaus Width: 3.2935
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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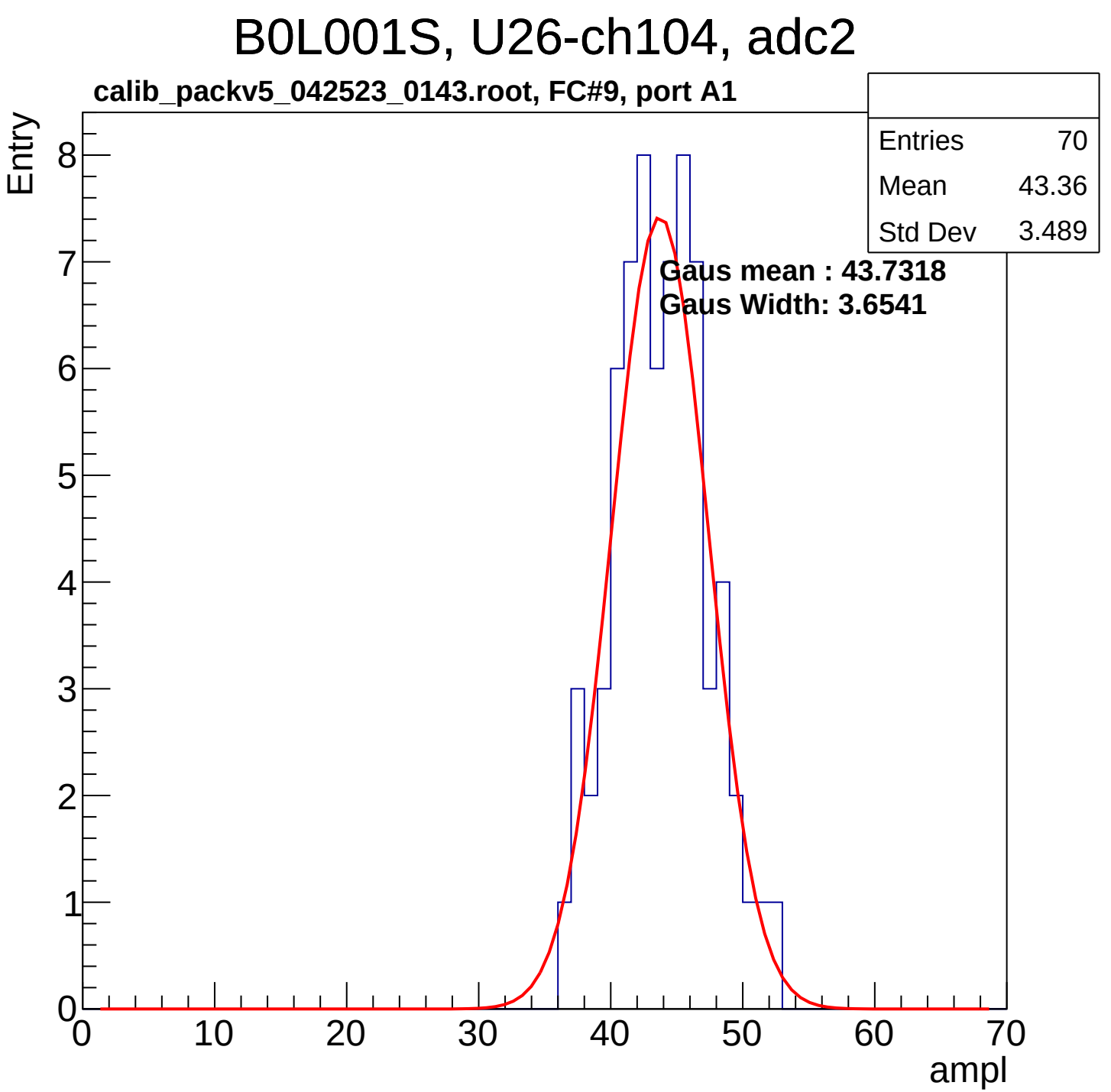
B0L001S, U26-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.36
Std Dev 3.489
Gaus mean : 43.7318
Gaus Width: 3.6541
Entry
ampl
0
1
2
3
4
5
6
7
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0
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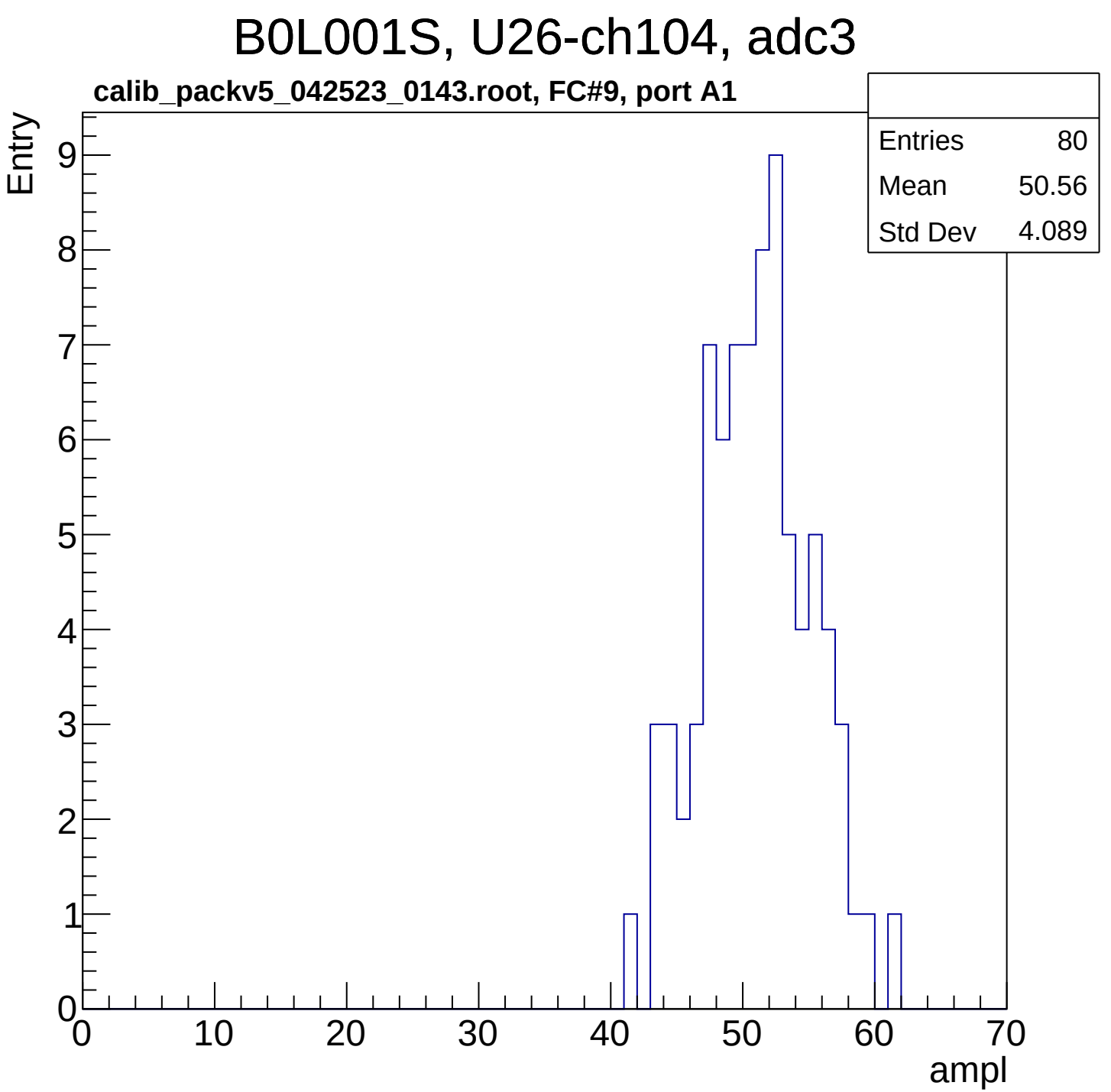
B0L001S, U26-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 50.56
Std Dev 4.089
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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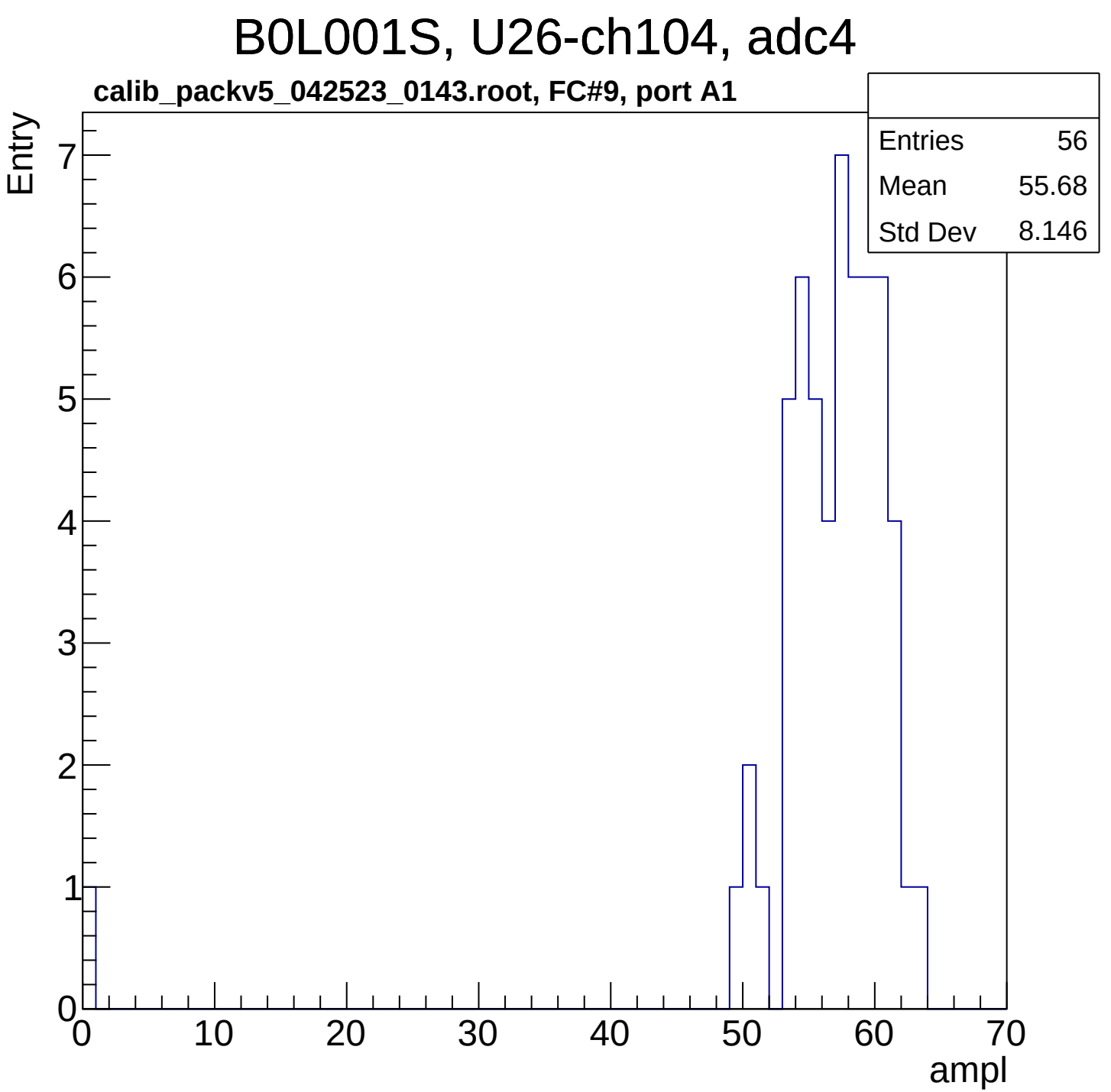
B0L001S, U26-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.68
Std Dev 8.146
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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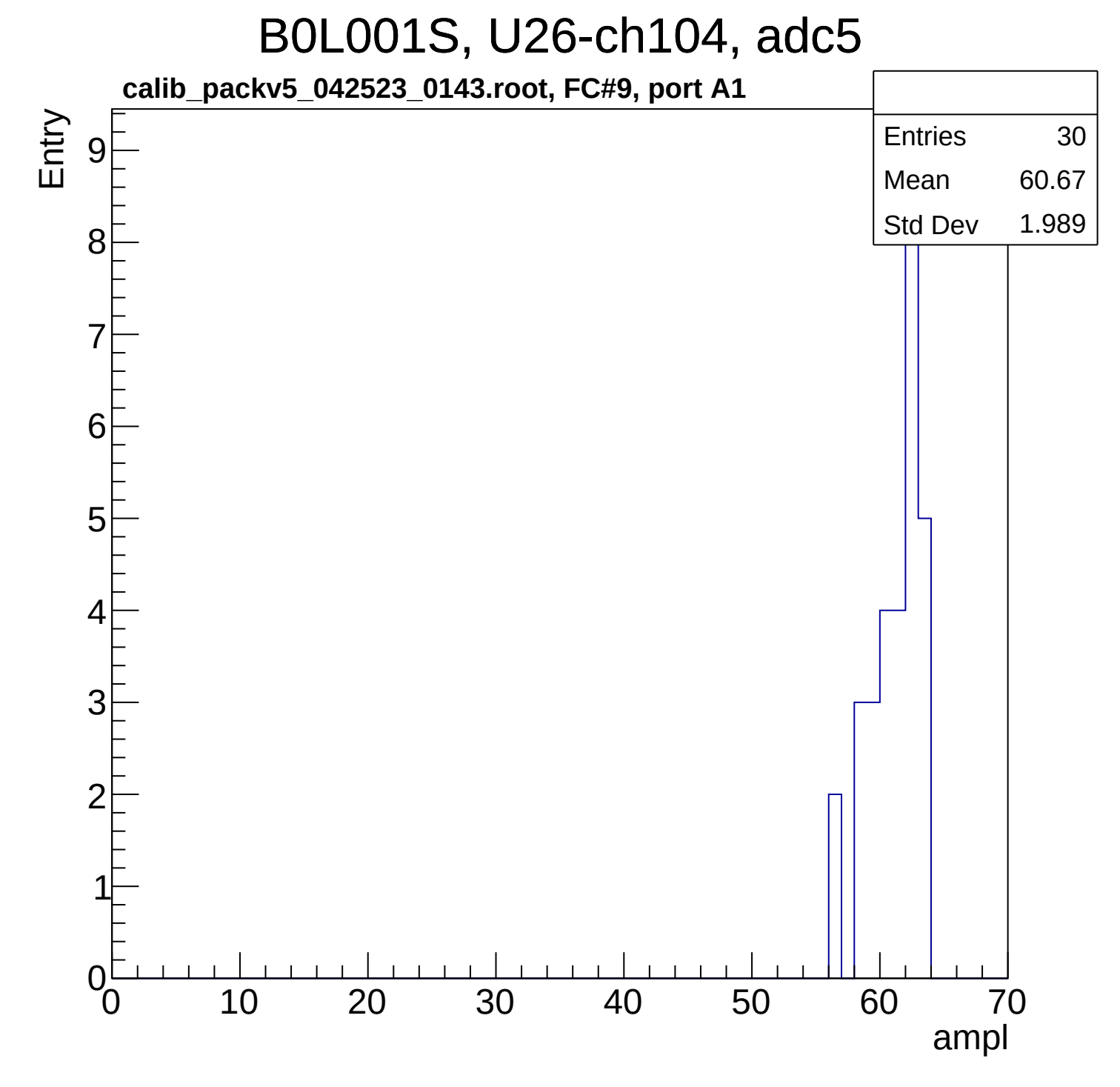
60
70


B0L001S, U26-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.67
Std Dev 1.989
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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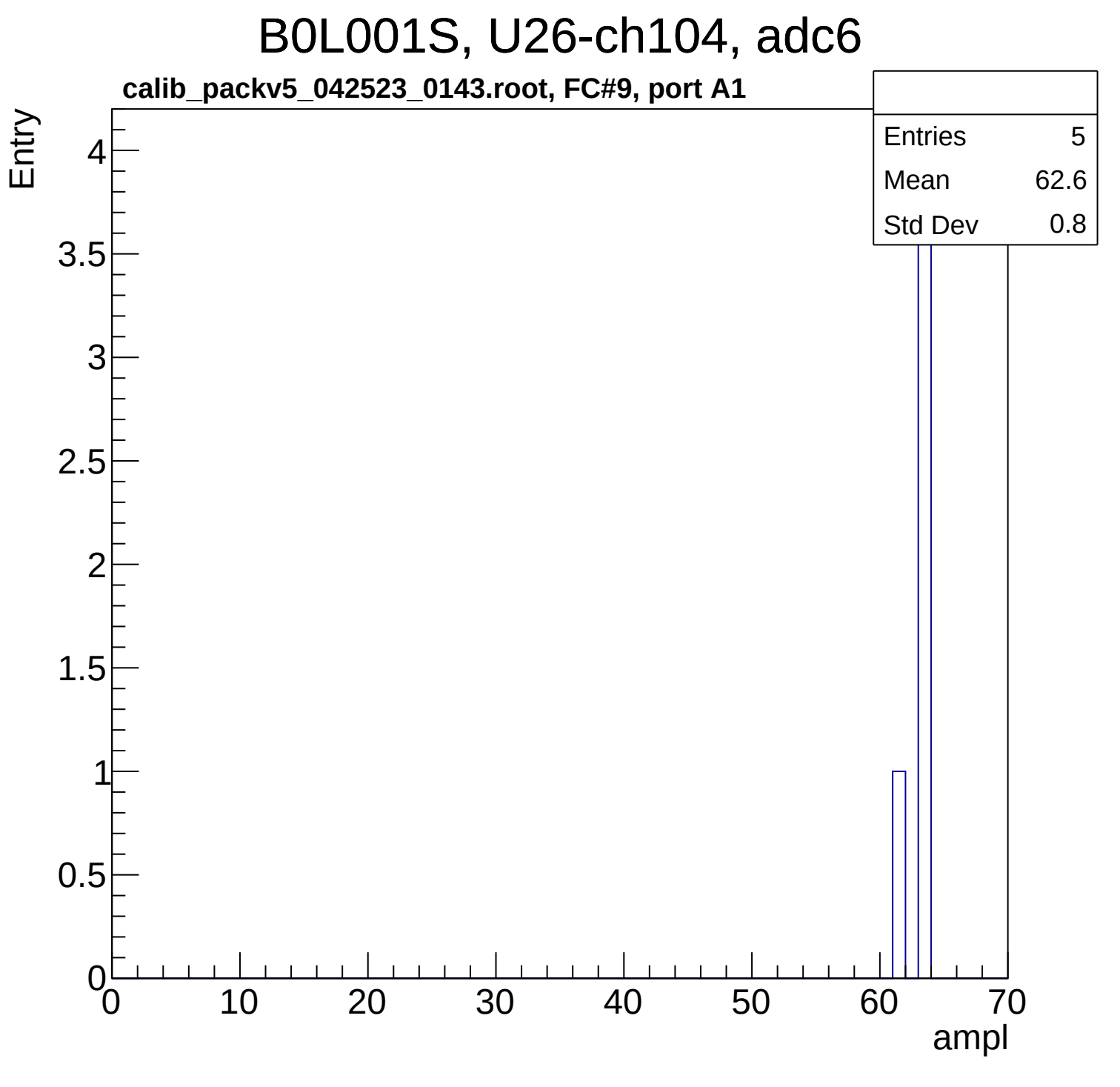
B0L001S, U26-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.8
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
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60
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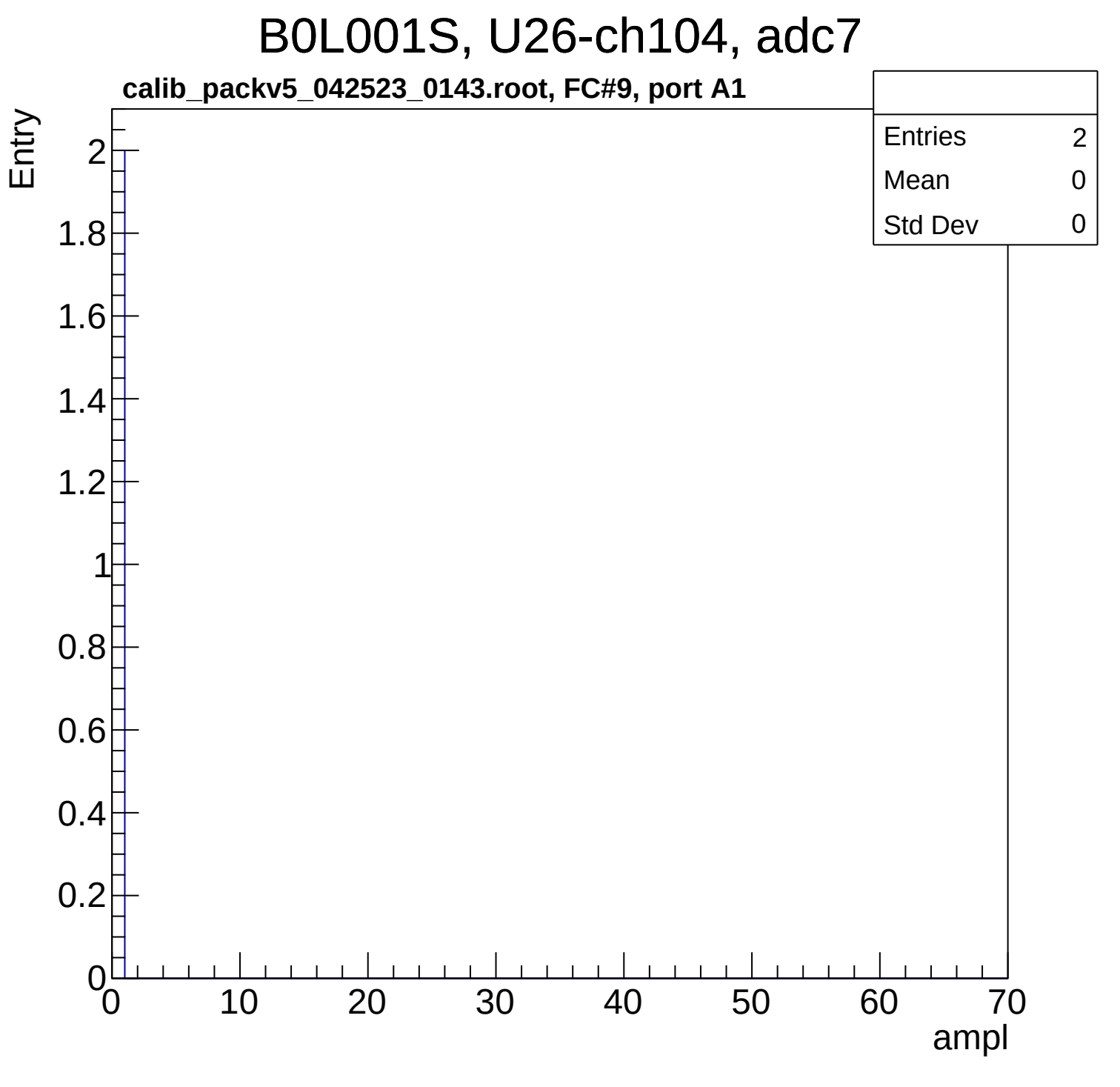
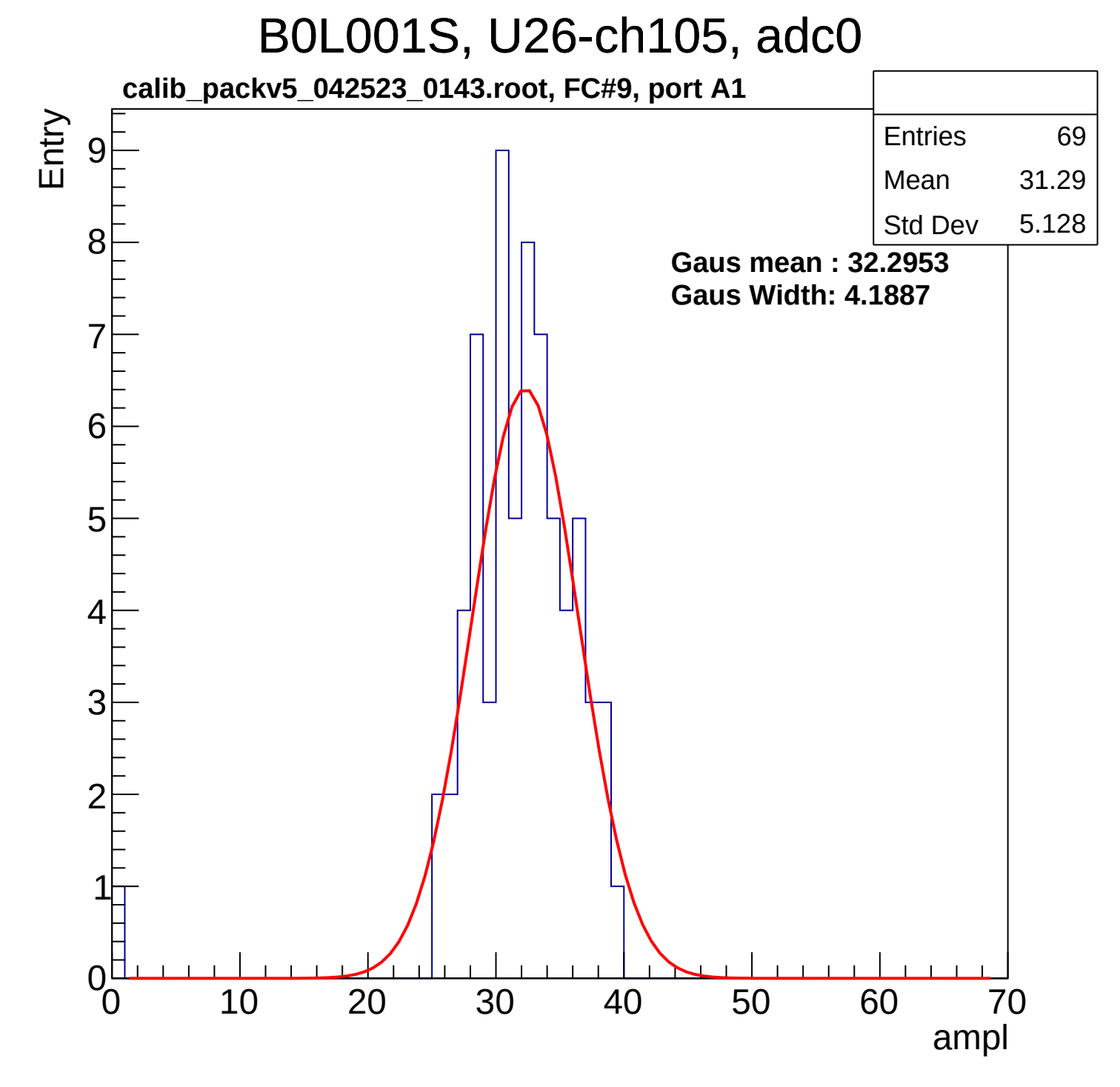

B0L001S, U26-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31.29
Std Dev 5.128
Gaus mean : 32.2953
Gaus Width: 4.1887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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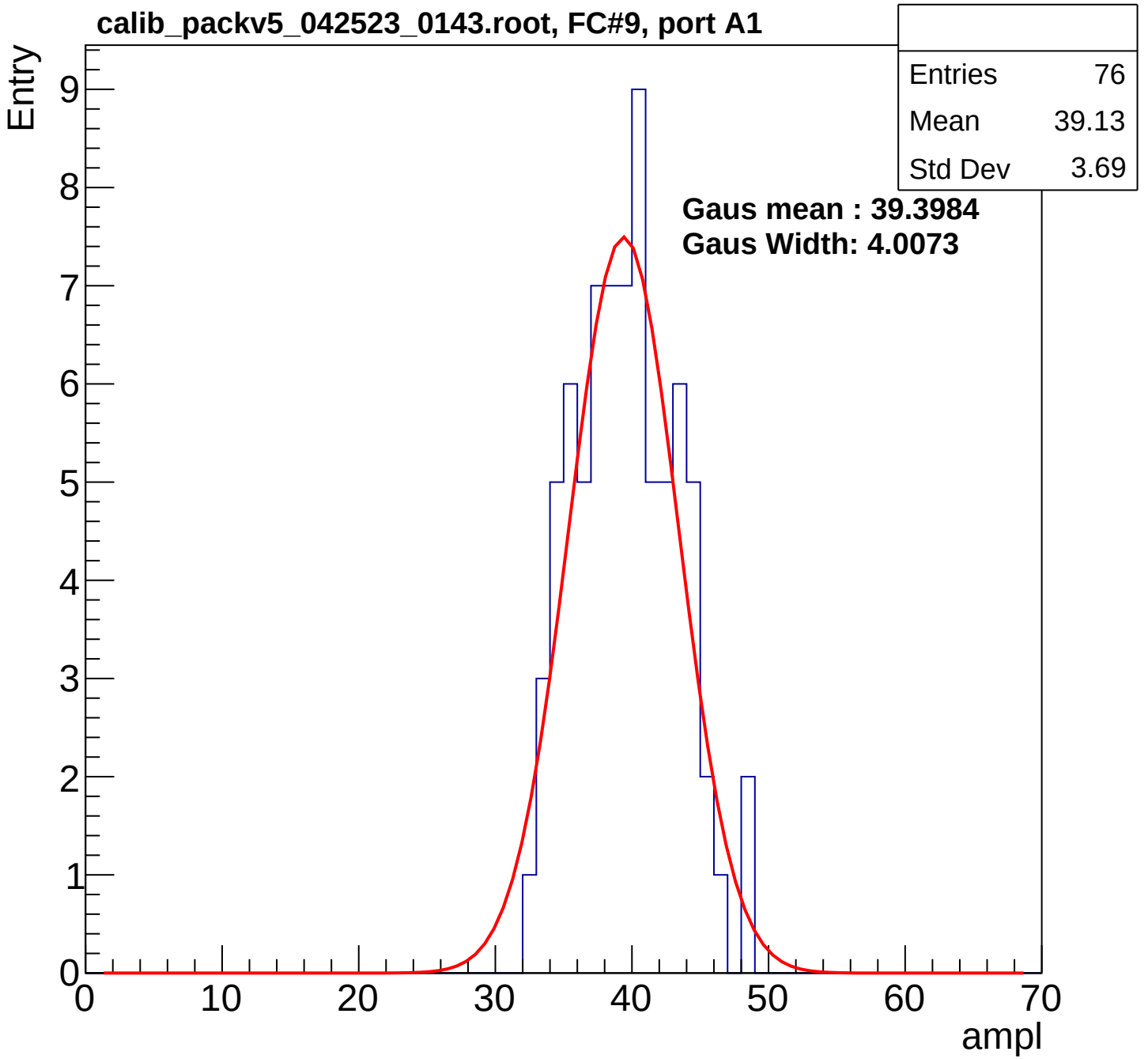
B0L001S, U26-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 39.13
Std Dev 3.69
Gaus mean : 39.3984
Gaus Width: 4.0073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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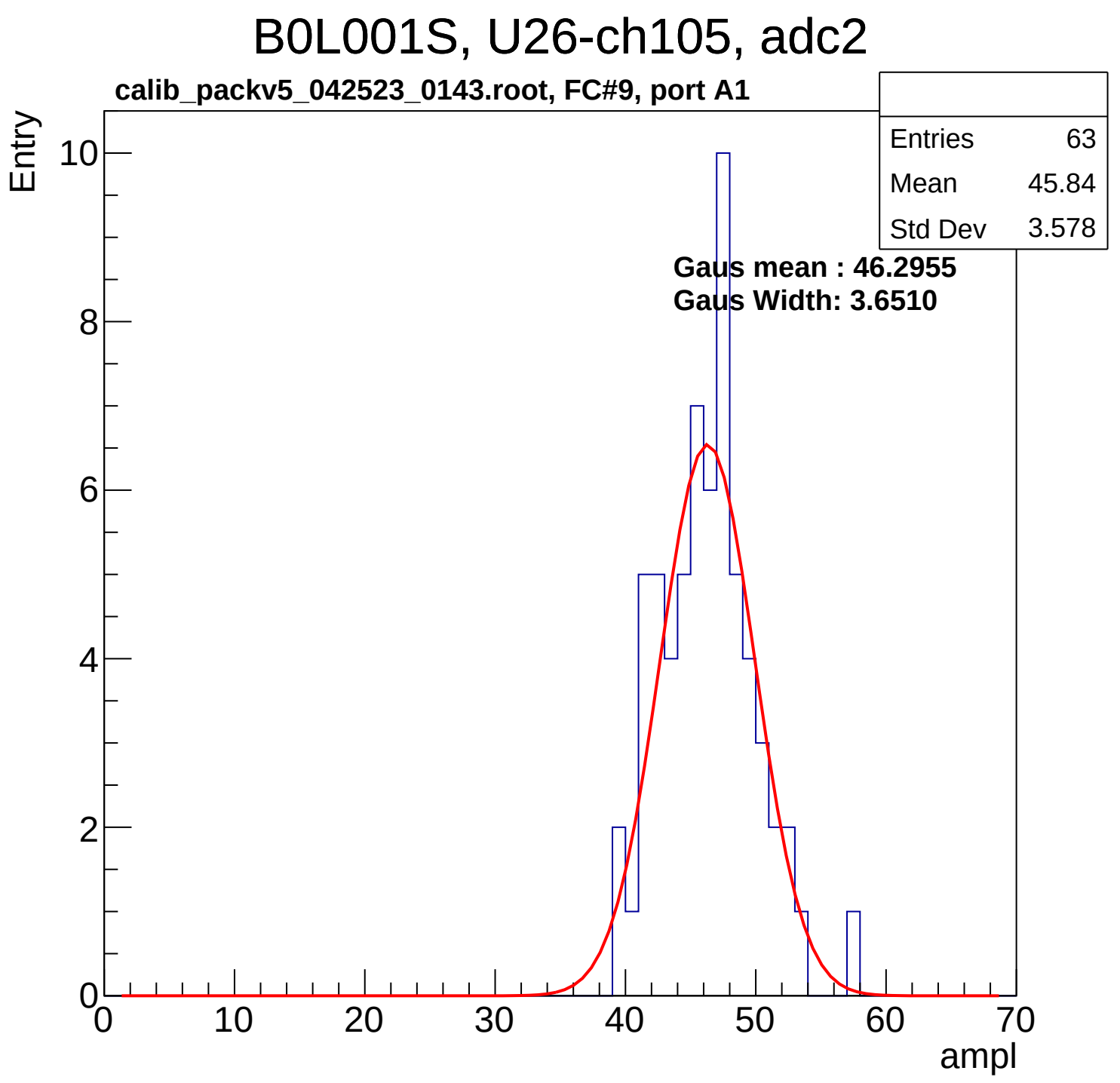
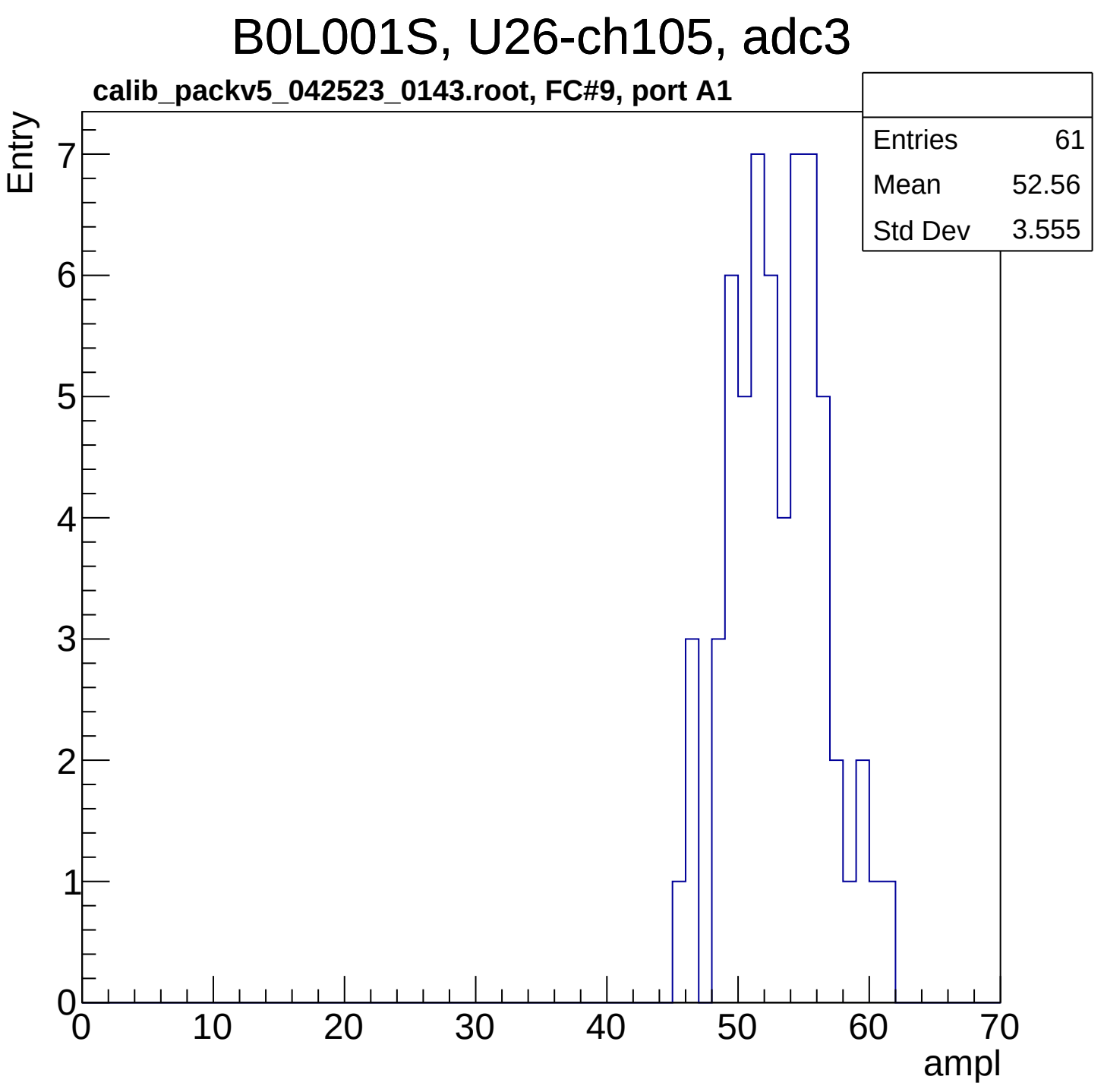
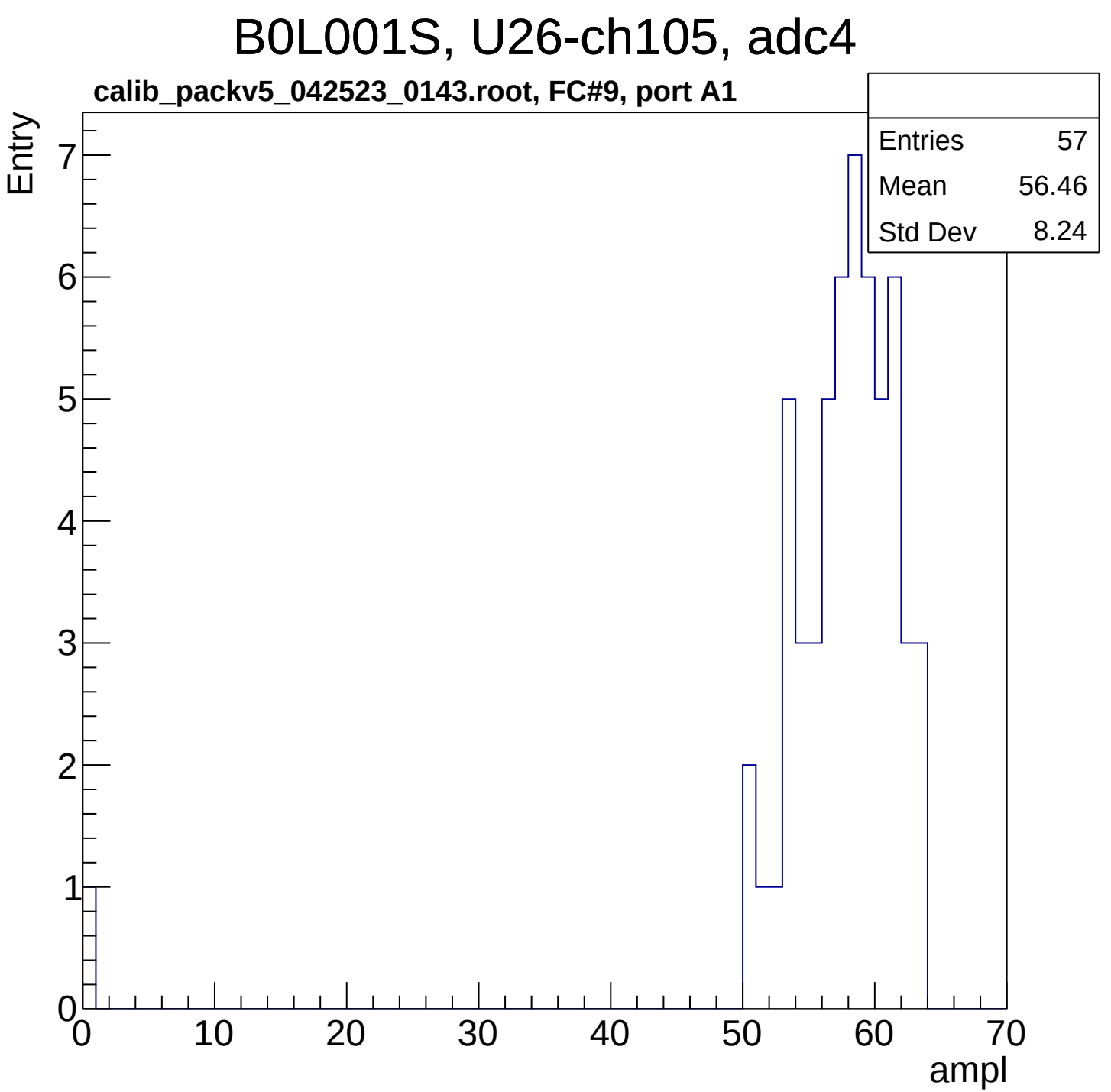

B0L001S, U26-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 45.84
Std Dev 3.578
Gaus mean : 46.2955
Gaus Width: 3.6510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 52.56
Std Dev 3.555
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.46
Std Dev 8.24
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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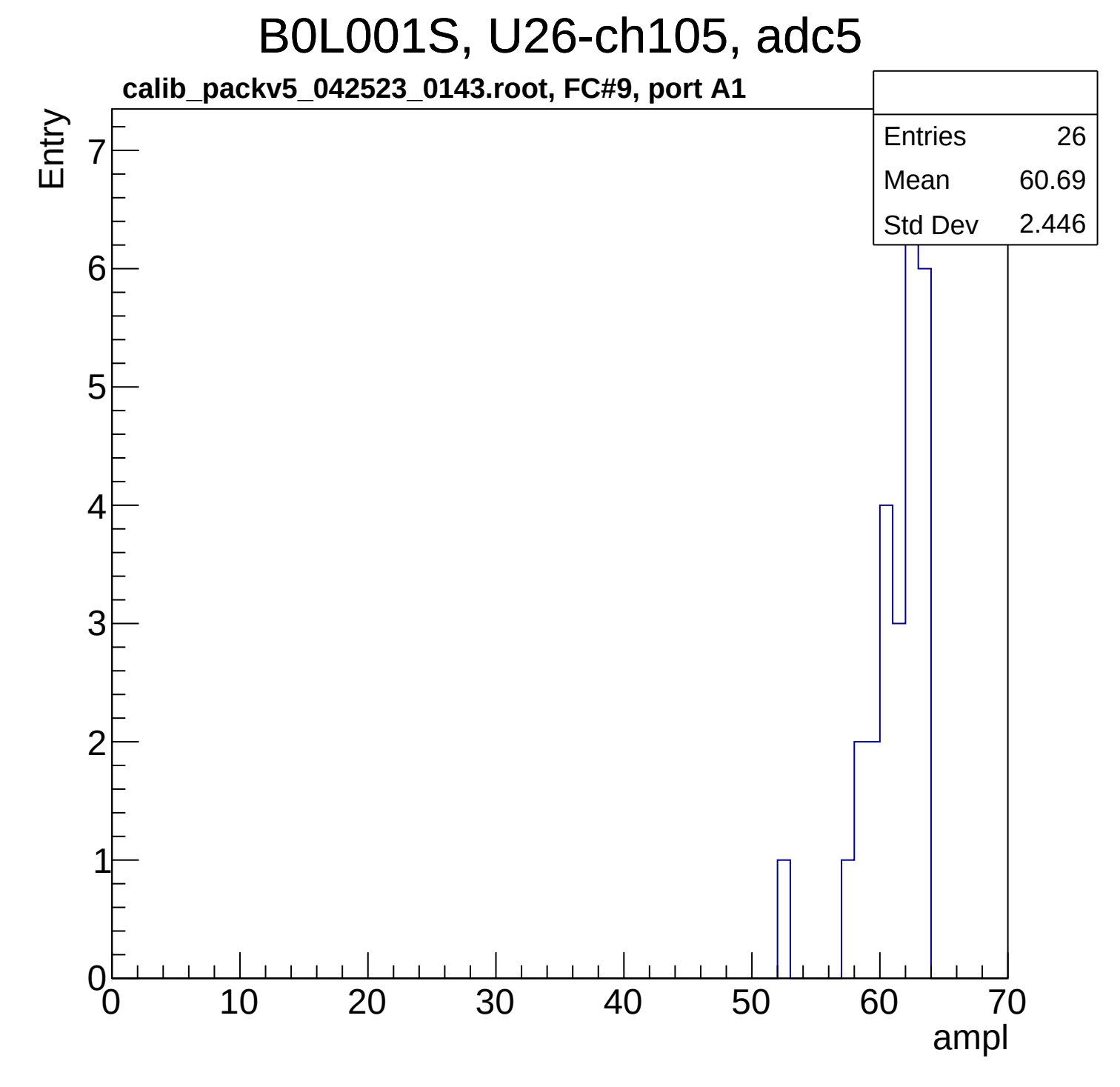
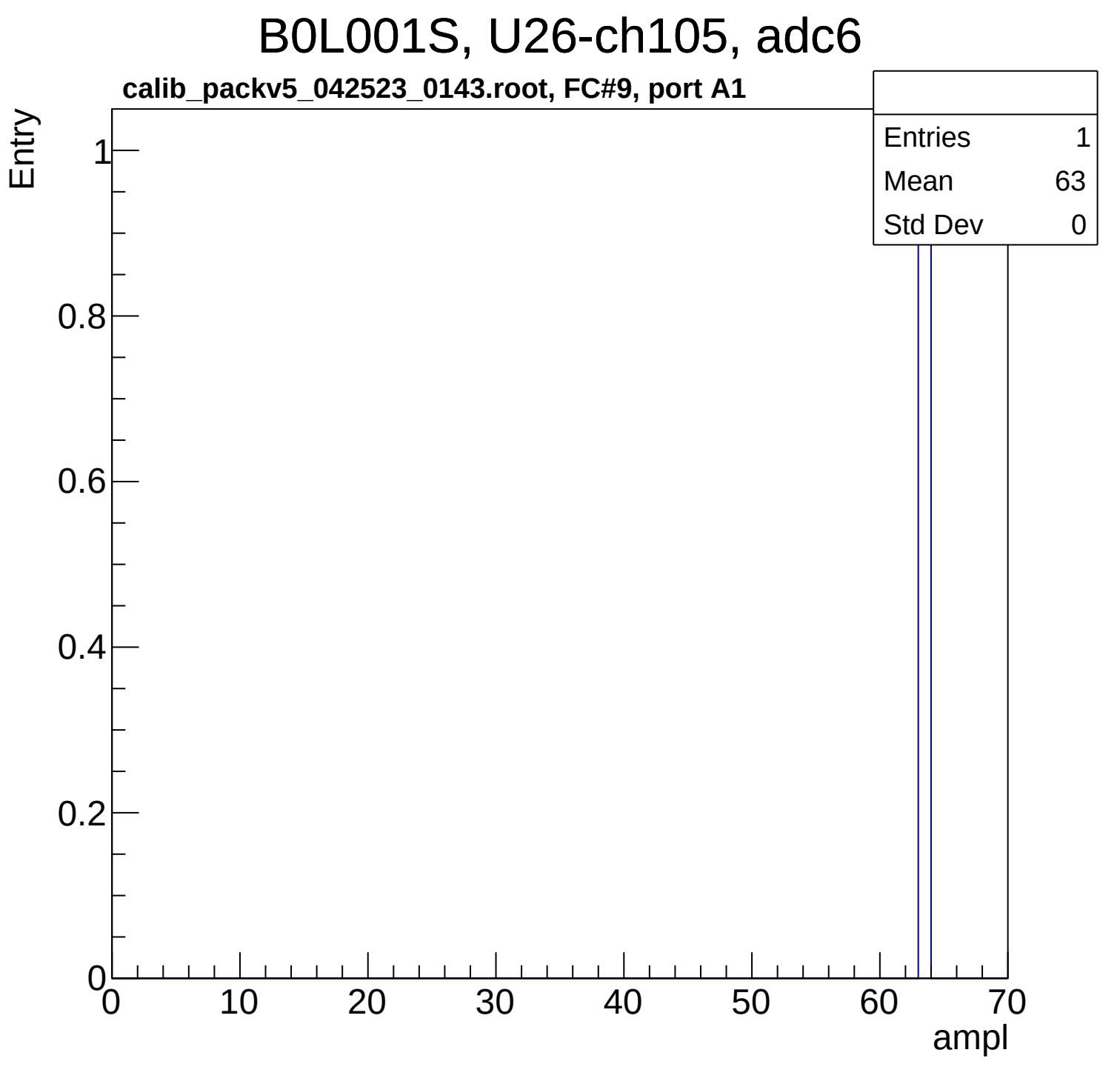
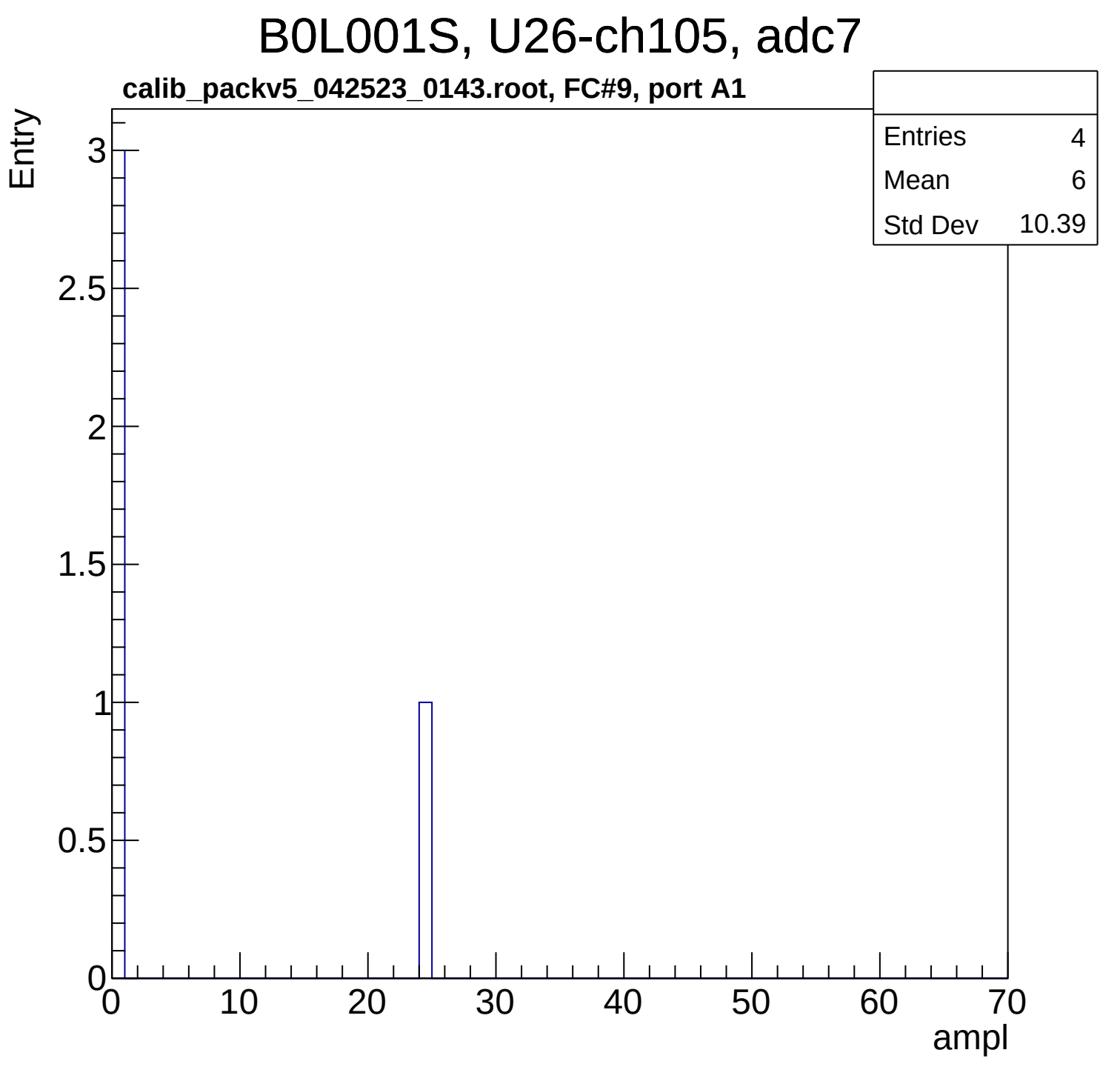

B0L001S, U26-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.69
Std Dev 2.446
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 6
Std Dev 10.39
Entry
ampl
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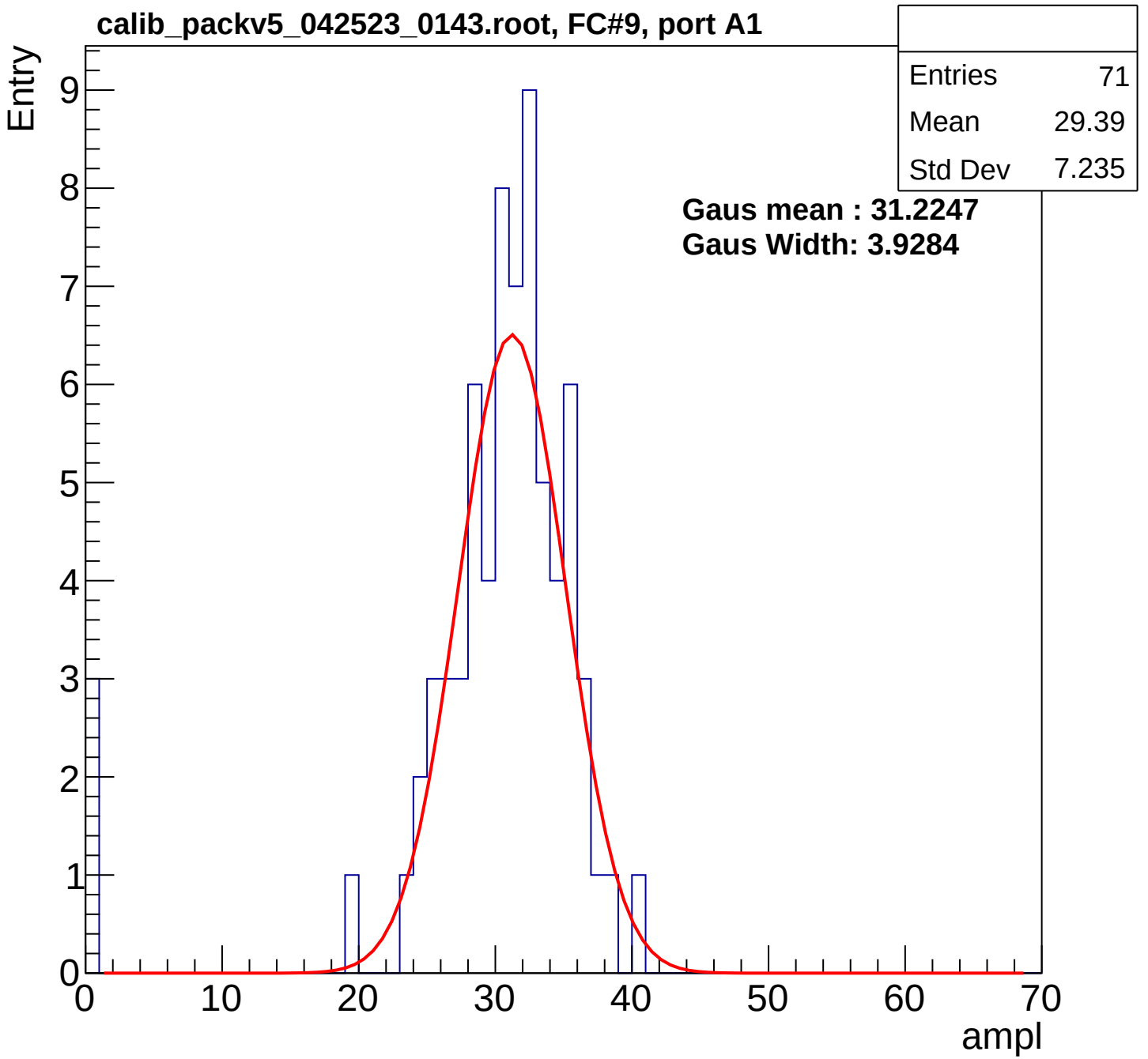
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U26-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.39
Std Dev 7.235
Gaus mean : 31.2247
Gaus Width: 3.9284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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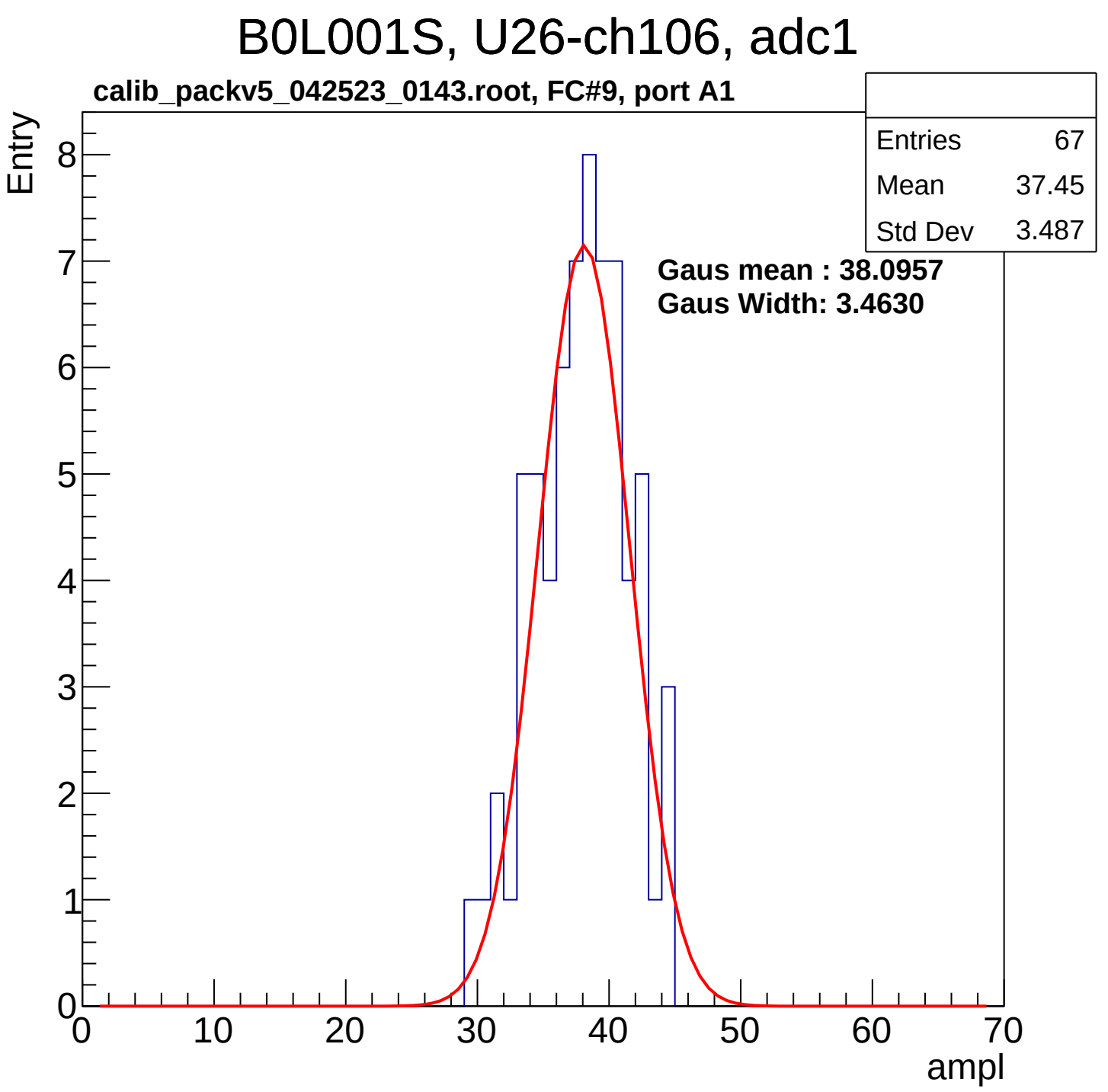
B0L001S, U26-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.45
Std Dev 3.487
Gaus mean : 38.0957
Gaus Width: 3.4630
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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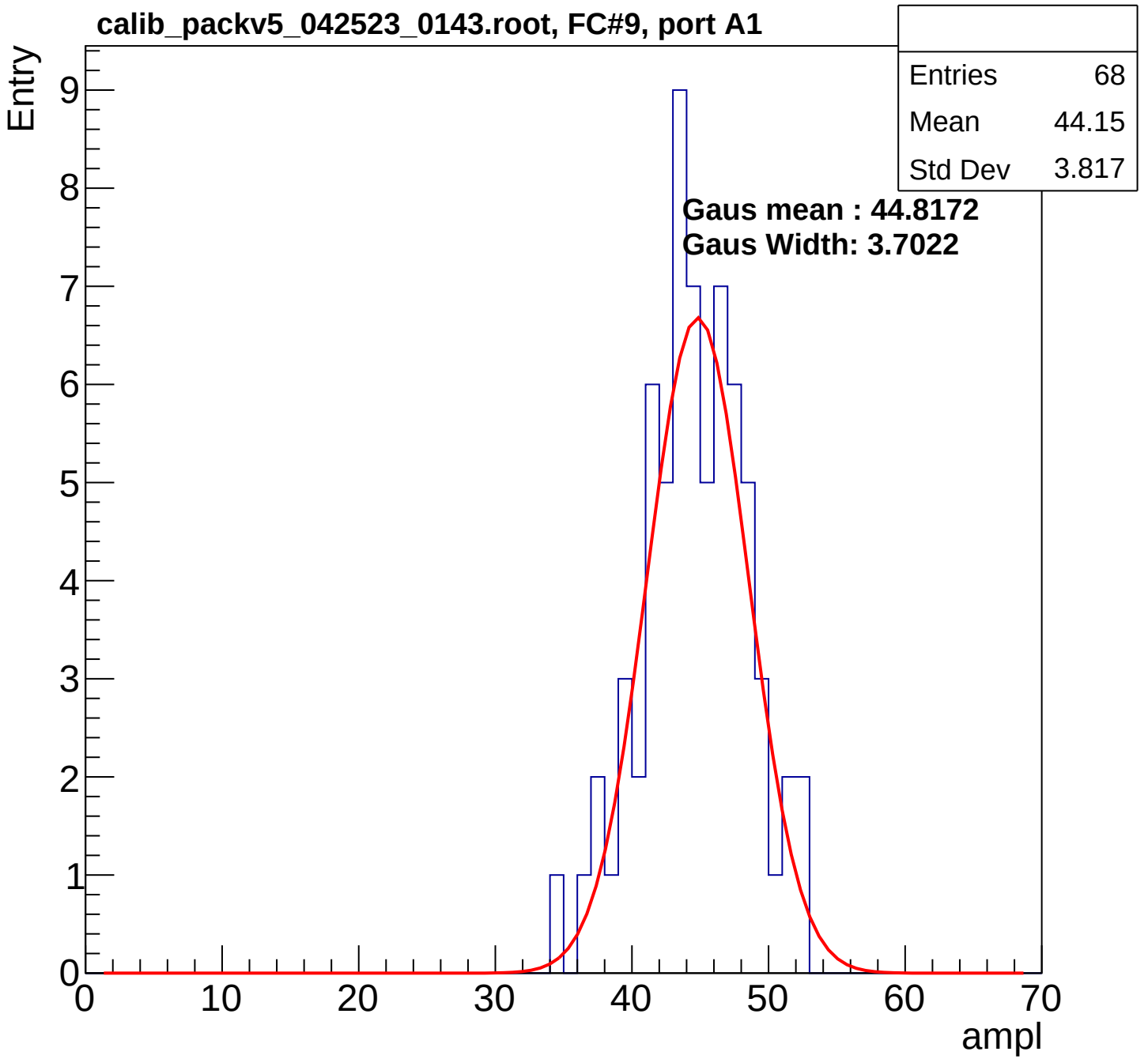
B0L001S, U26-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.15
Std Dev 3.817
Gaus mean : 44.8172
Gaus Width: 3.7022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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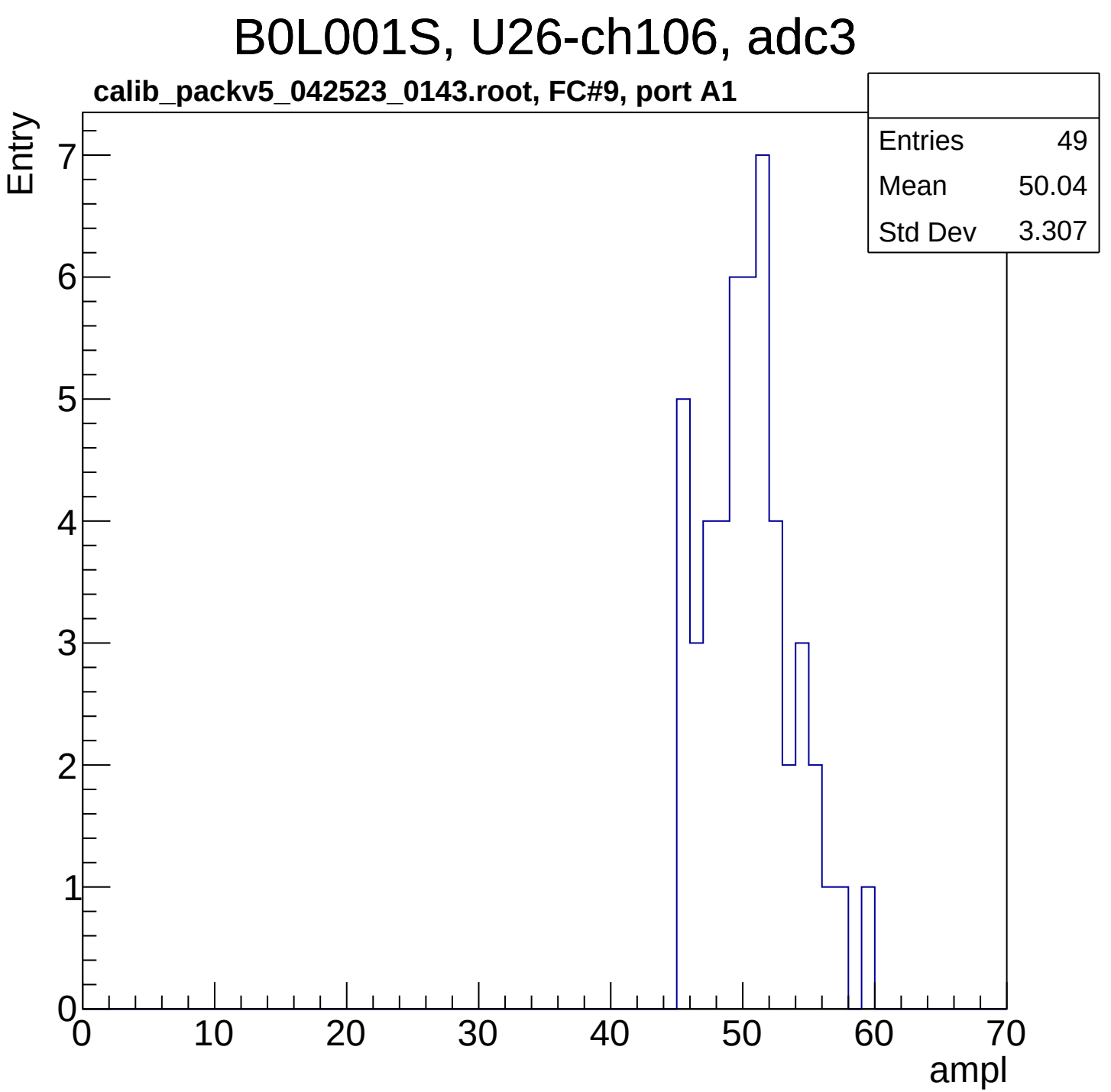
B0L001S, U26-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 50.04
Std Dev 3.307
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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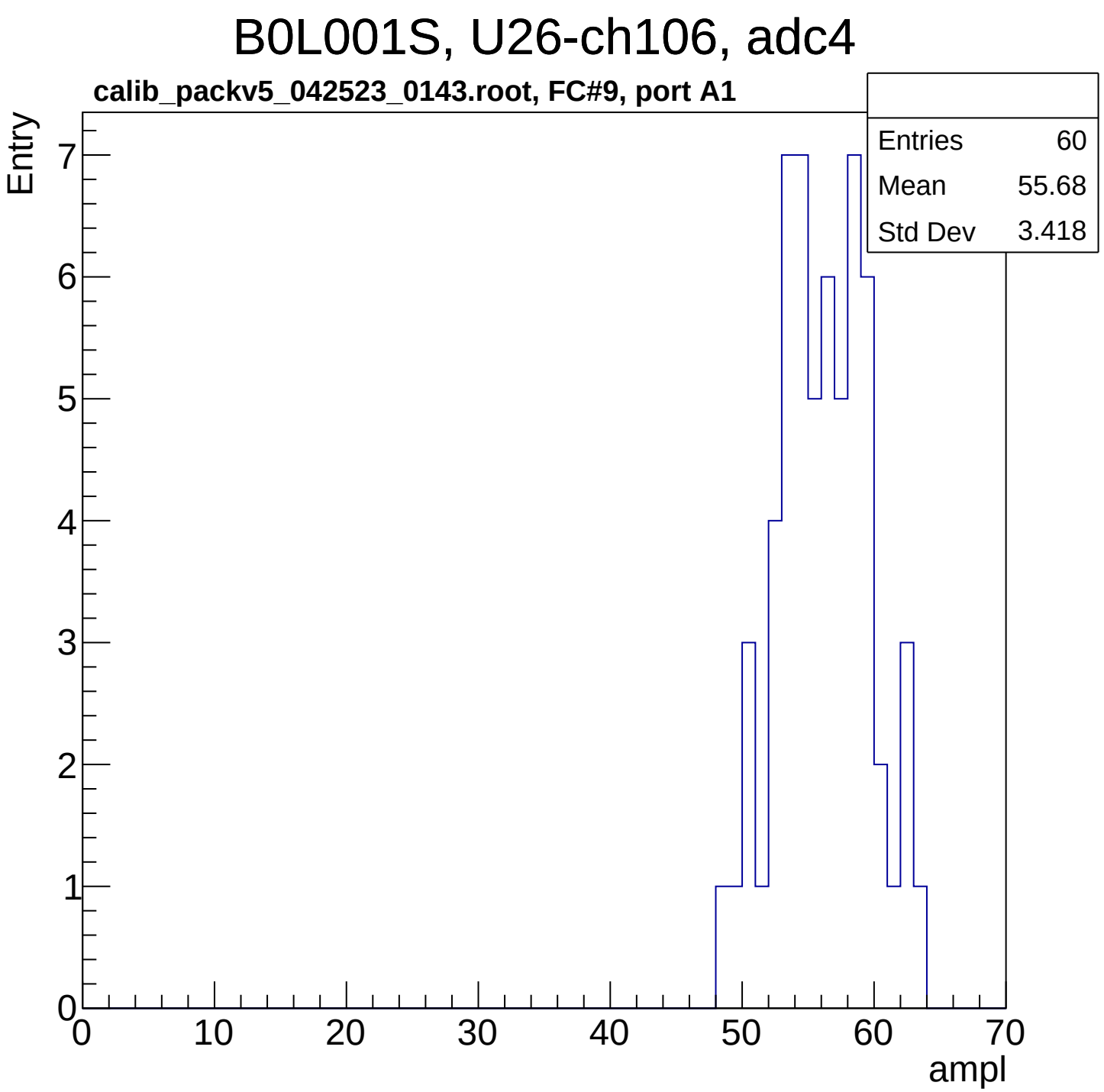
B0L001S, U26-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.68
Std Dev 3.418
Entry
ampl
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1
2
3
4
5
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0
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20
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40
50
60
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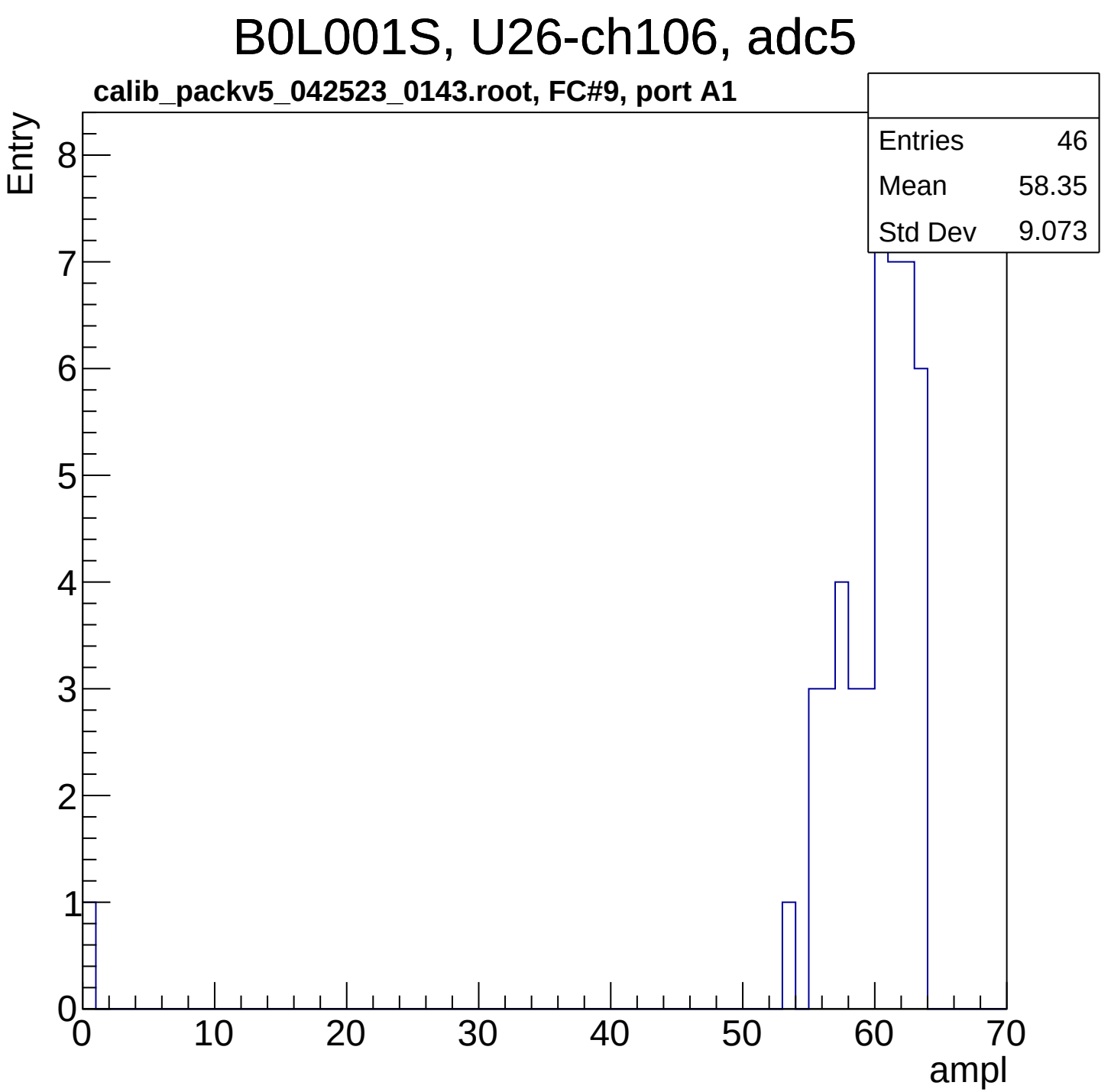
B0L001S, U26-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.35
Std Dev 9.073
Entry
ampl
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1
2
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4
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6
7
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0
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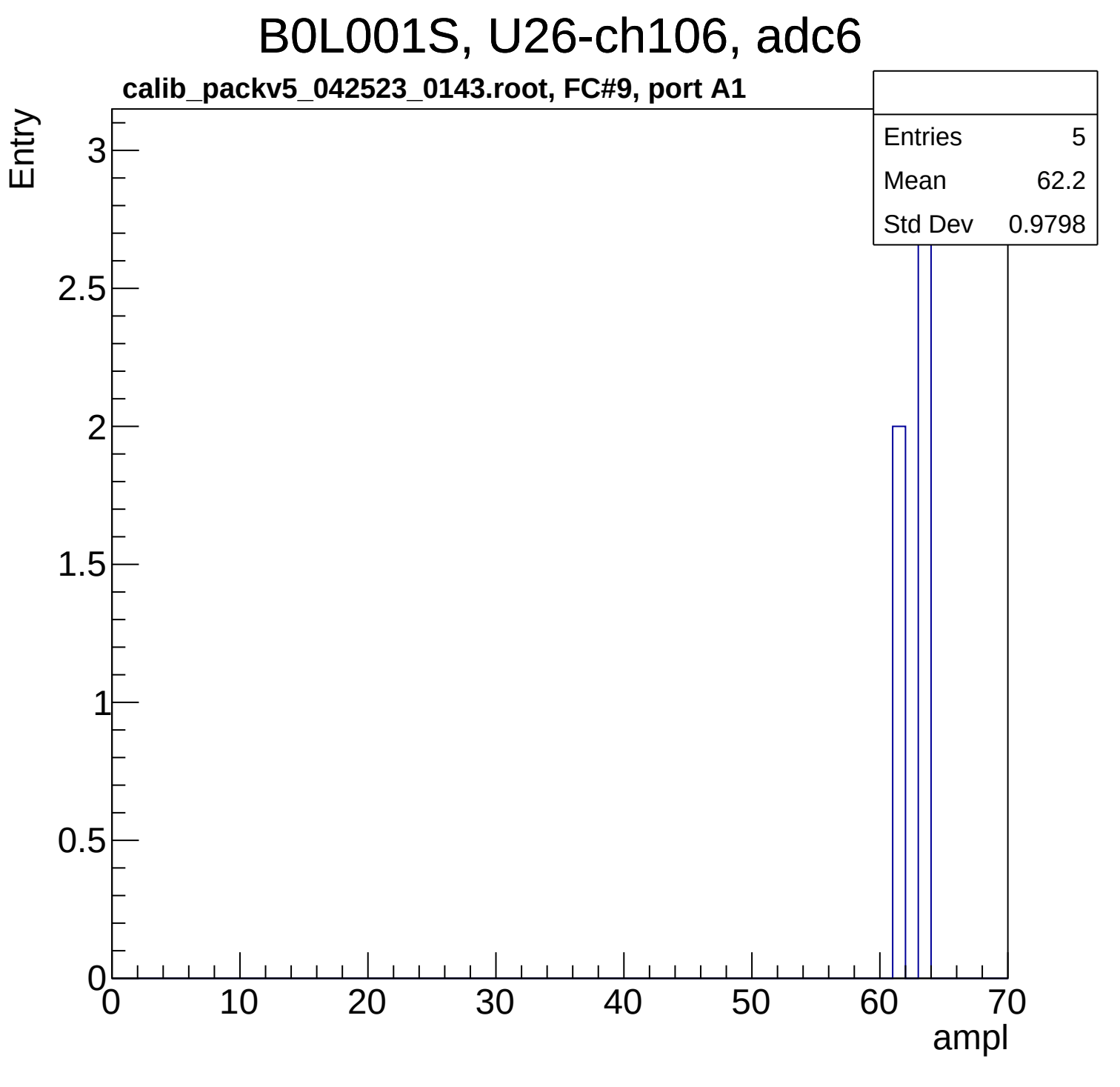
B0L001S, U26-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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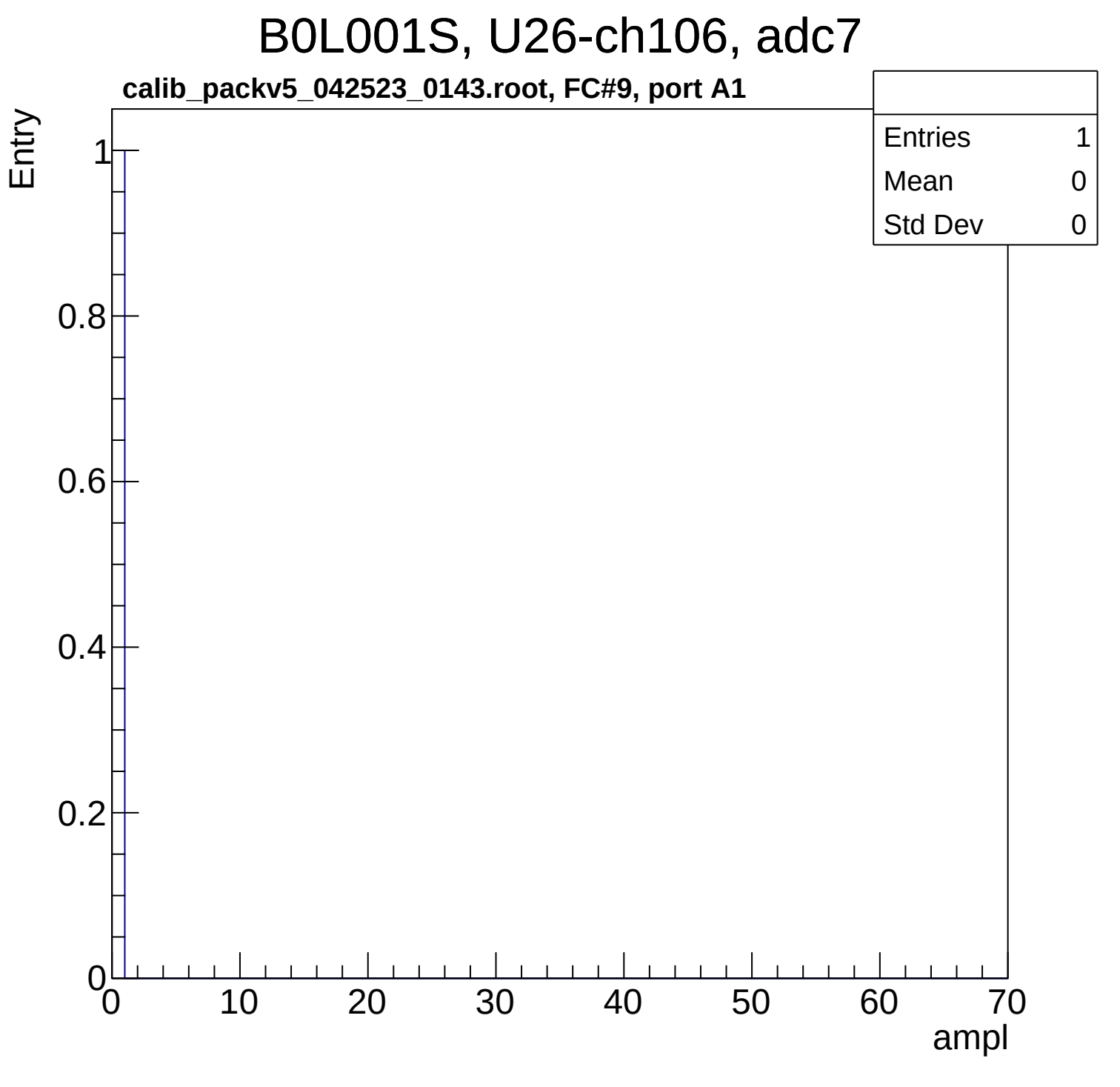
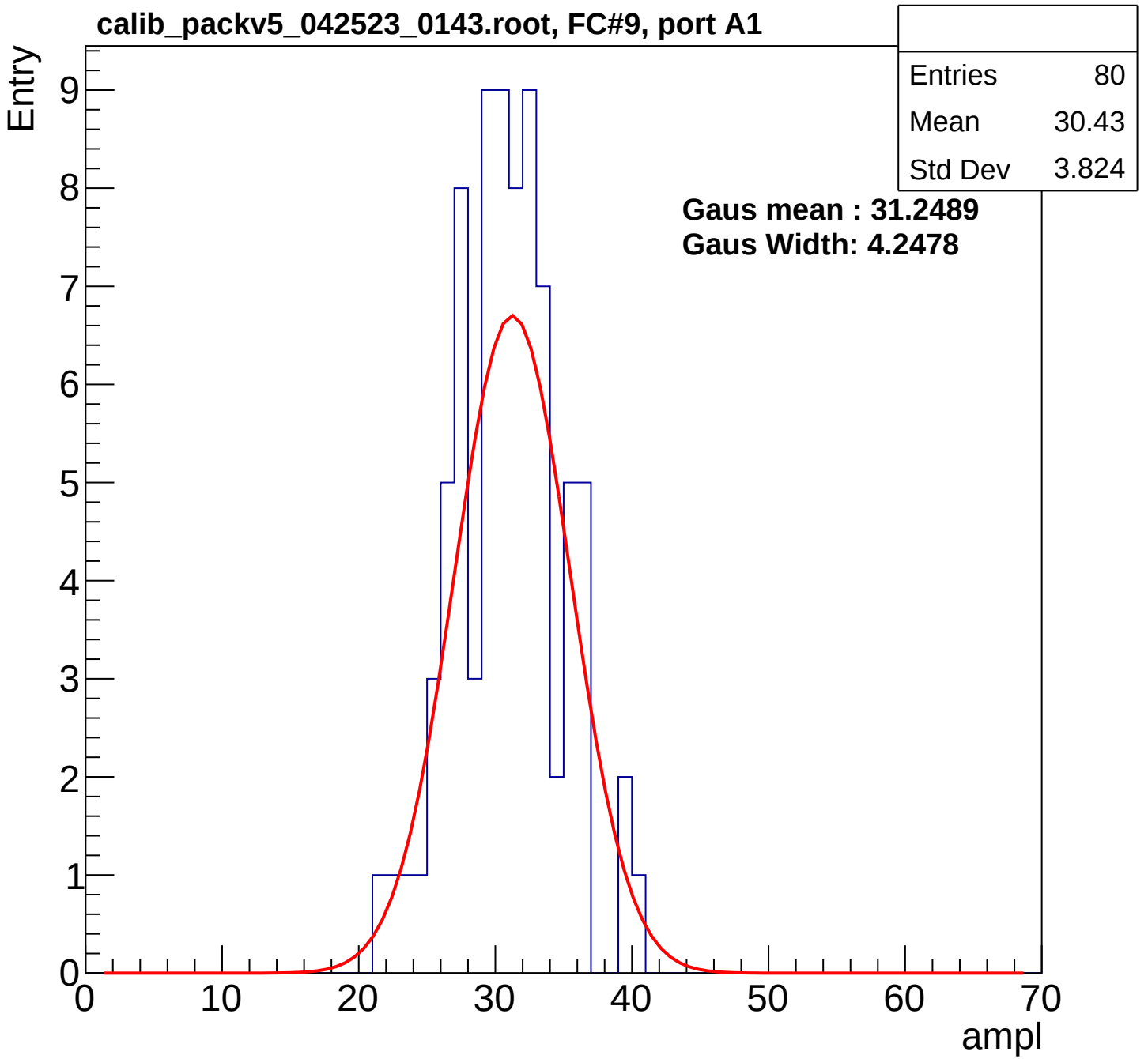
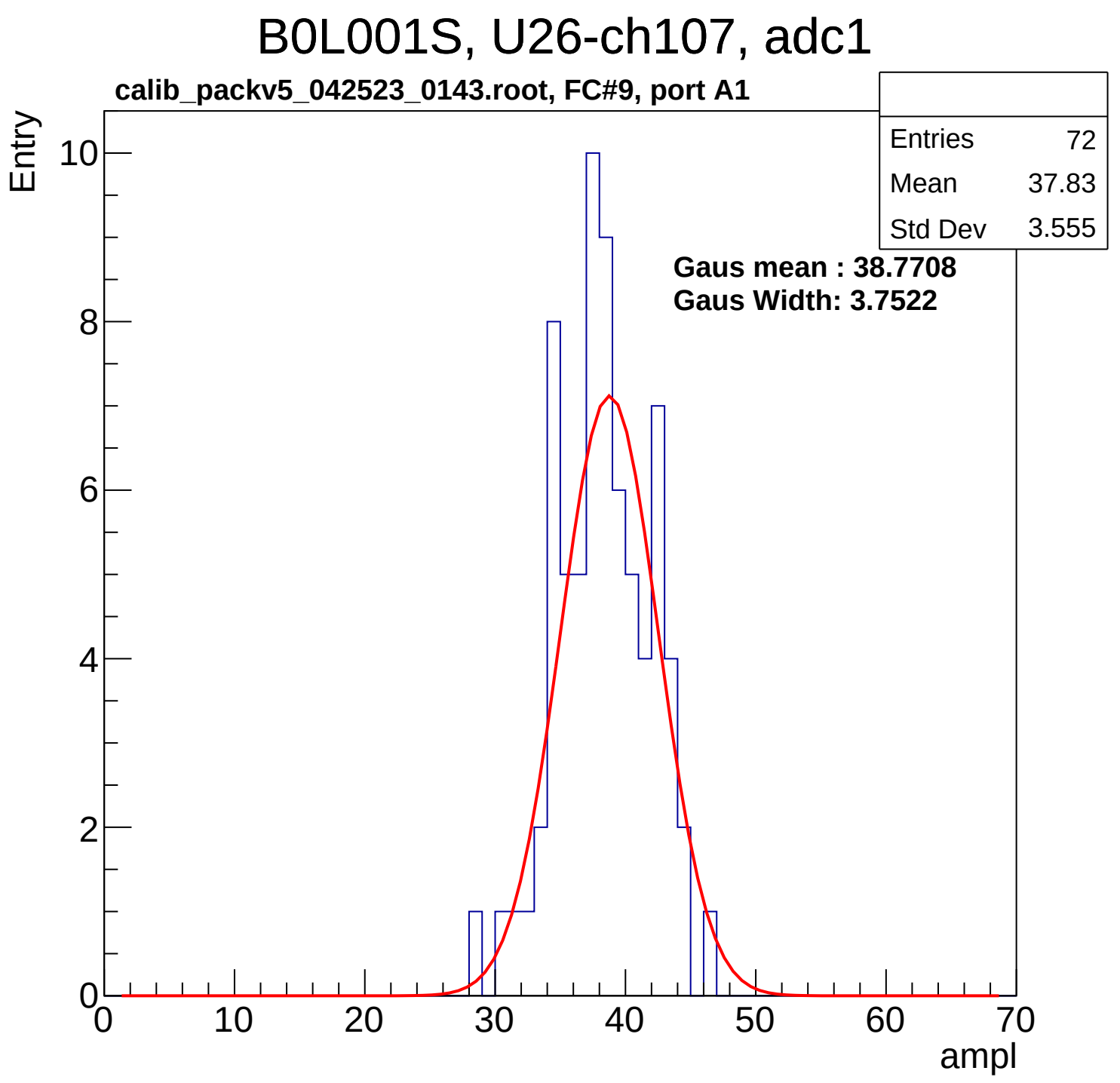
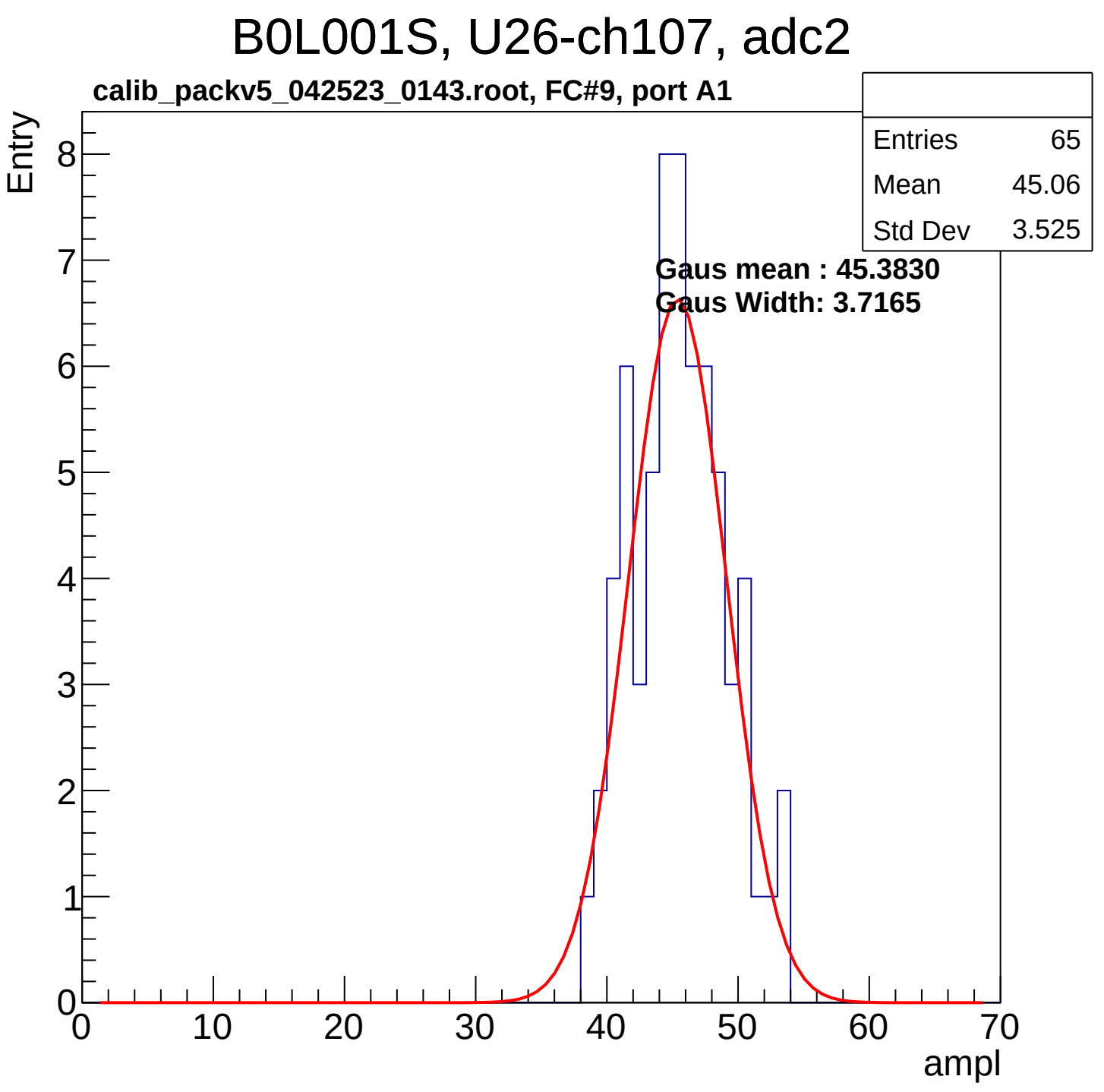
40
50
60
70


B0L001S, U26-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.43
Std Dev 3.824
Gaus mean : 31.2489
Gaus Width: 4.2478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.83
Std Dev 3.555
Gaus mean : 38.7708
Gaus Width: 3.7522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.06
Std Dev 3.525
Gaus mean : 45.3830
Gaus Width: 3.7165
Entry
ampl
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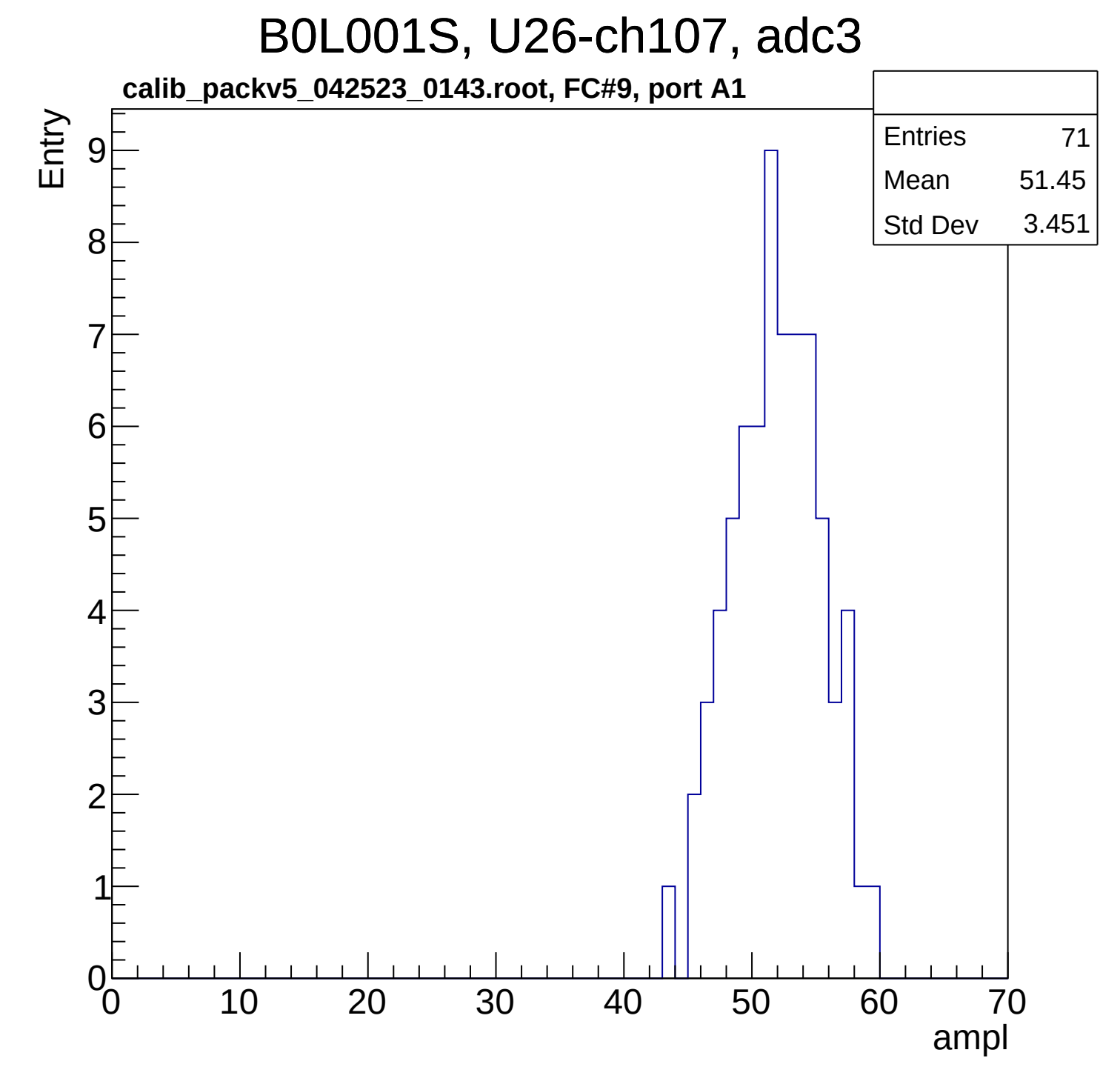
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.45
Std Dev 3.451
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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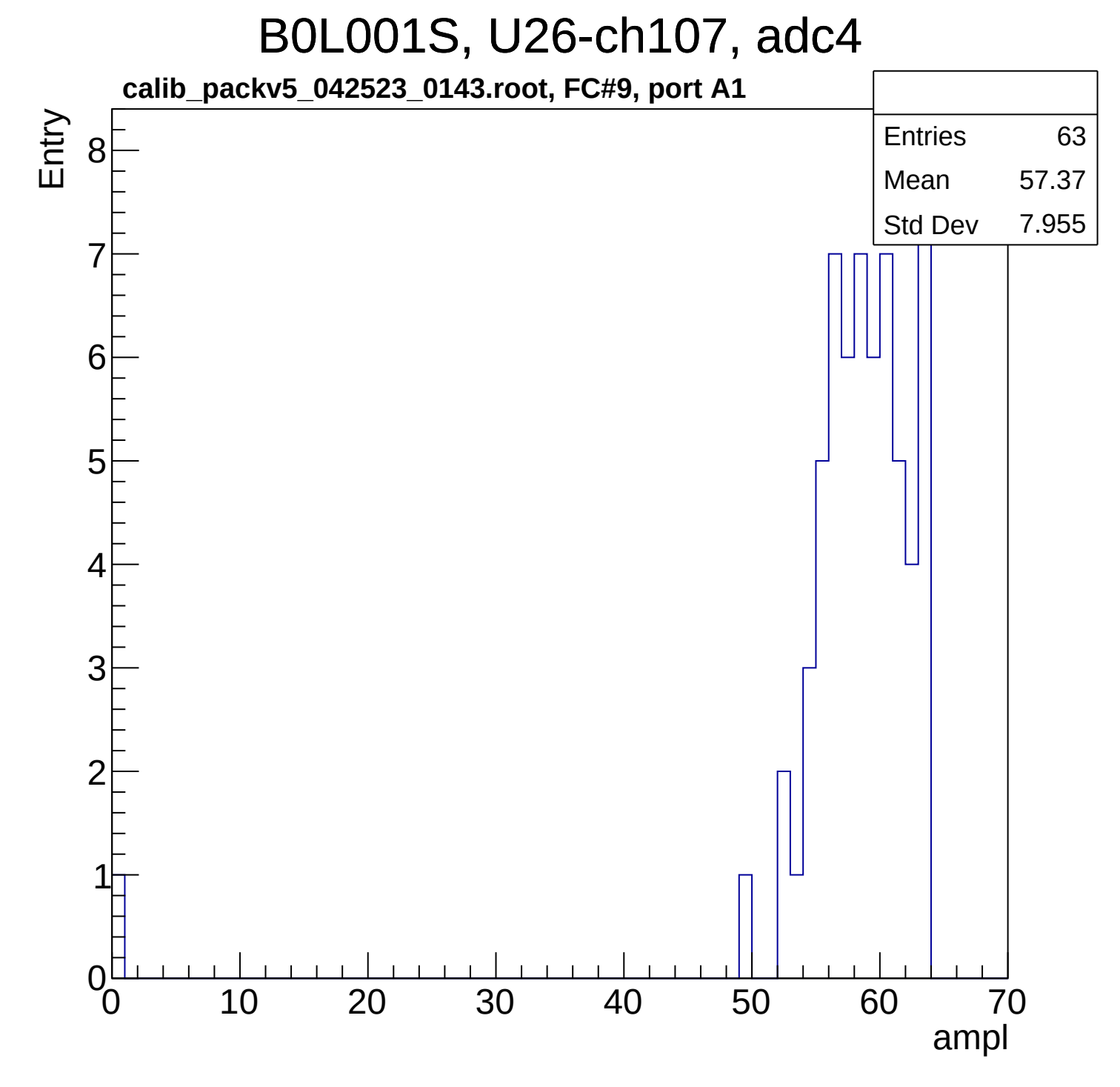
B0L001S, U26-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 57.37
Std Dev 7.955
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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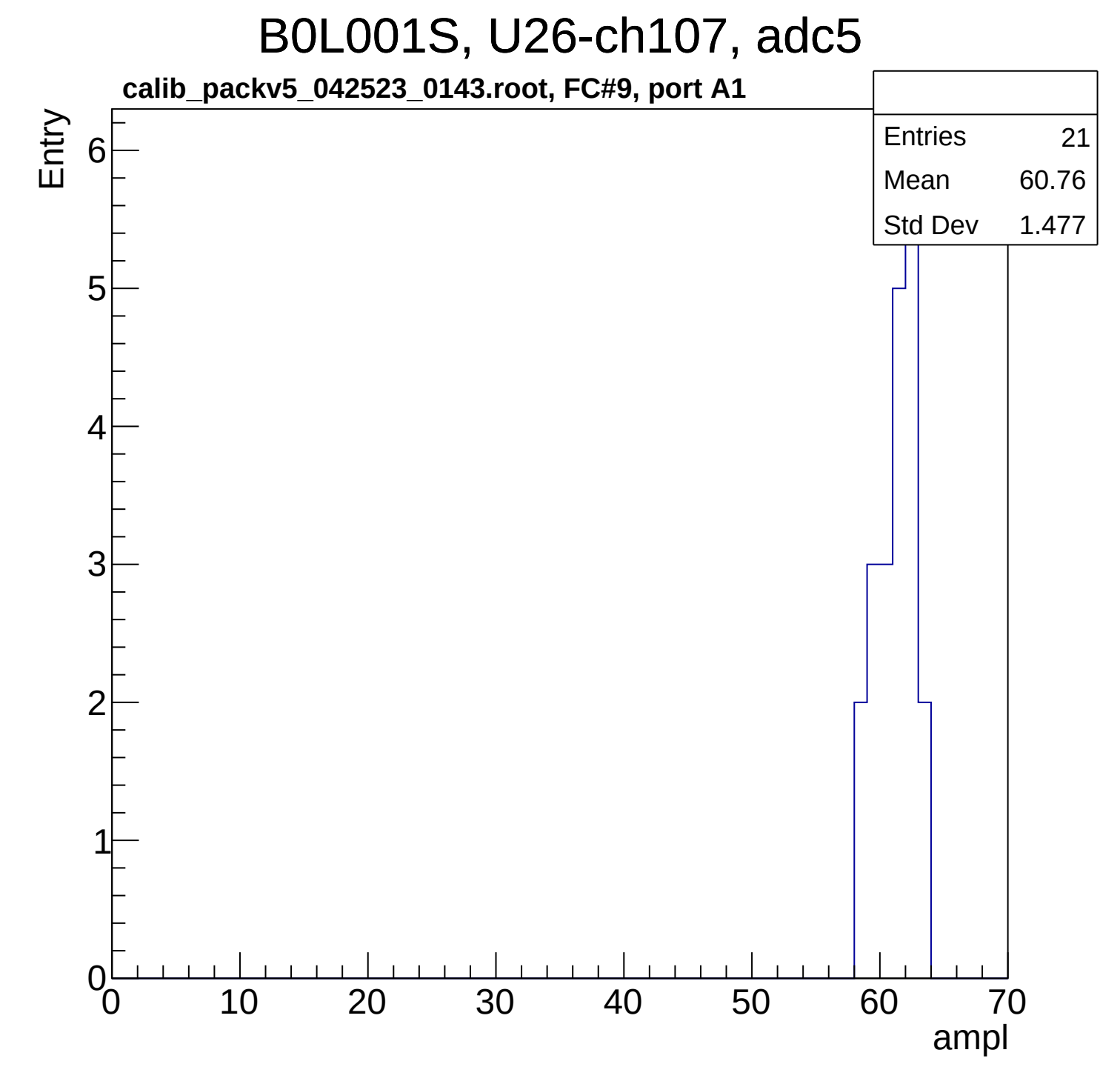
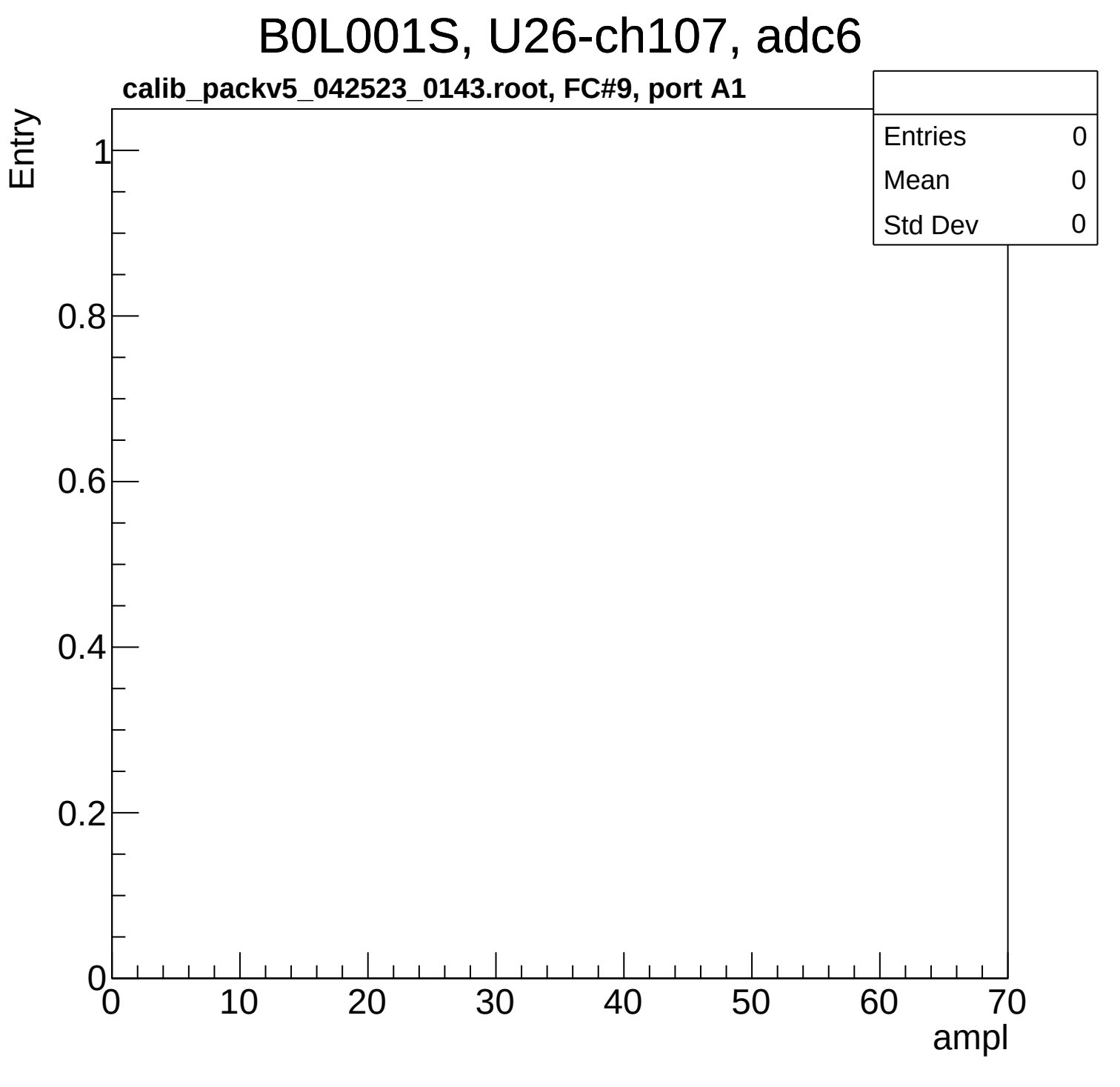

B0L001S, U26-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 60.76
Std Dev 1.477
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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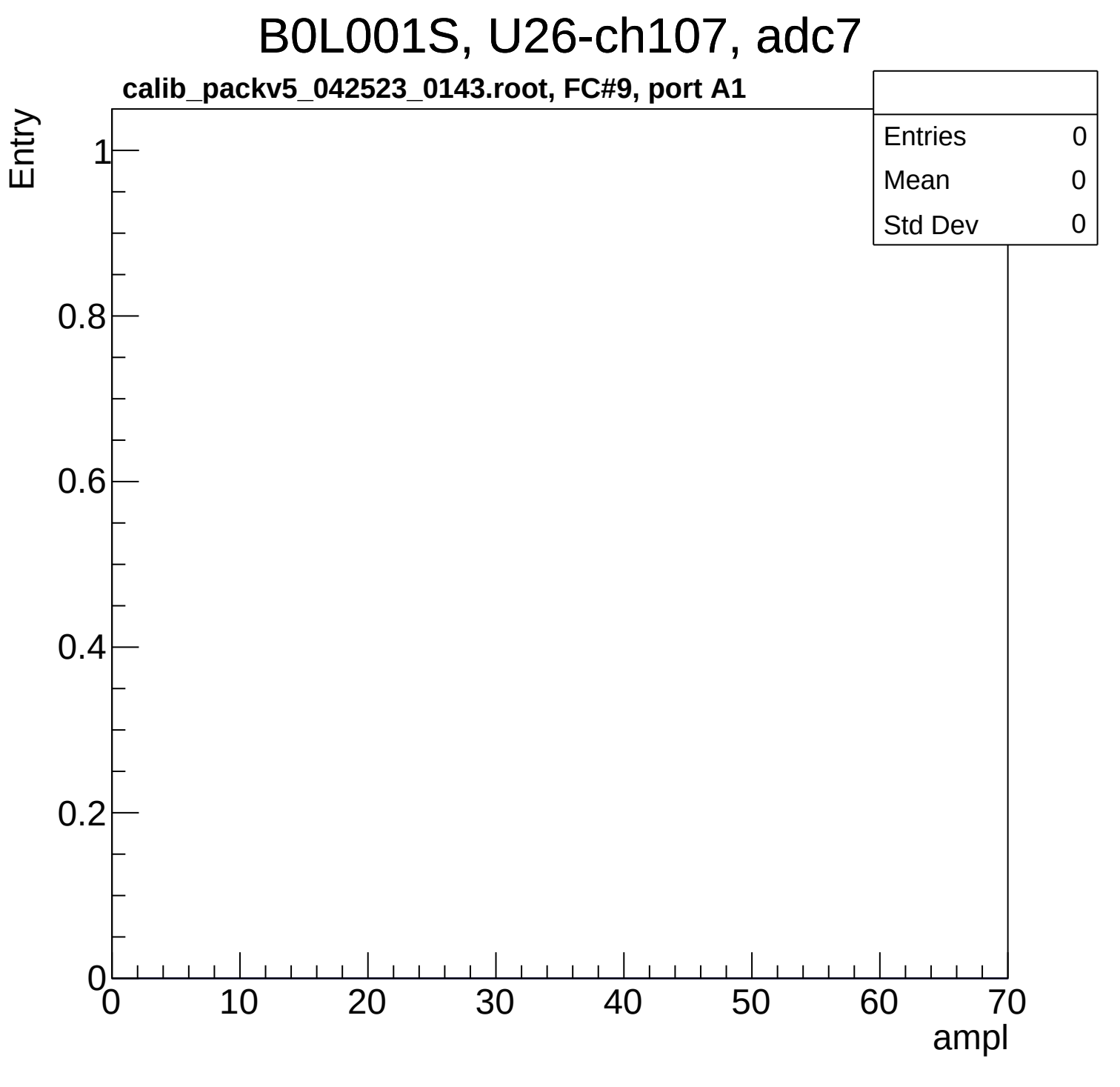
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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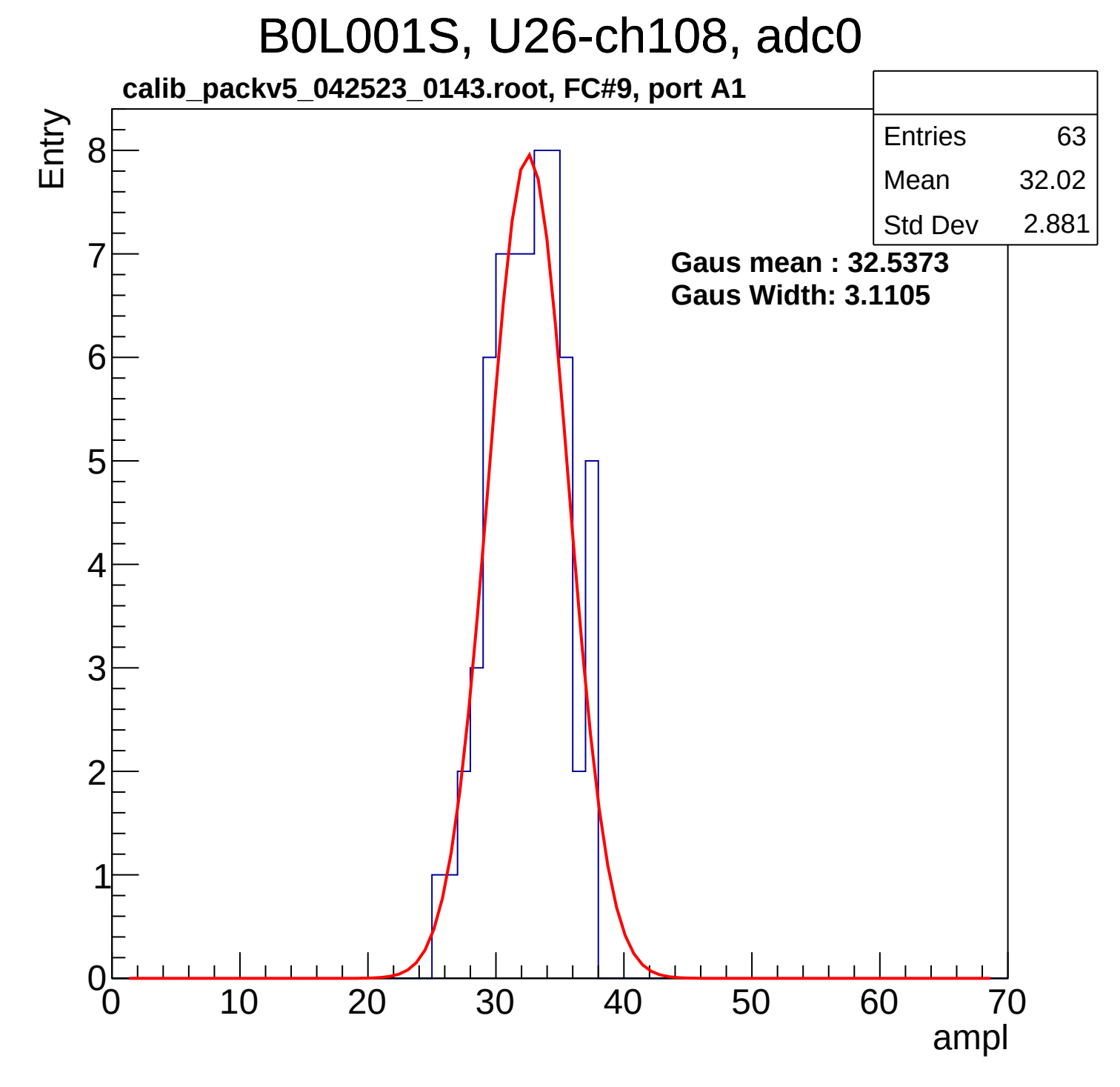
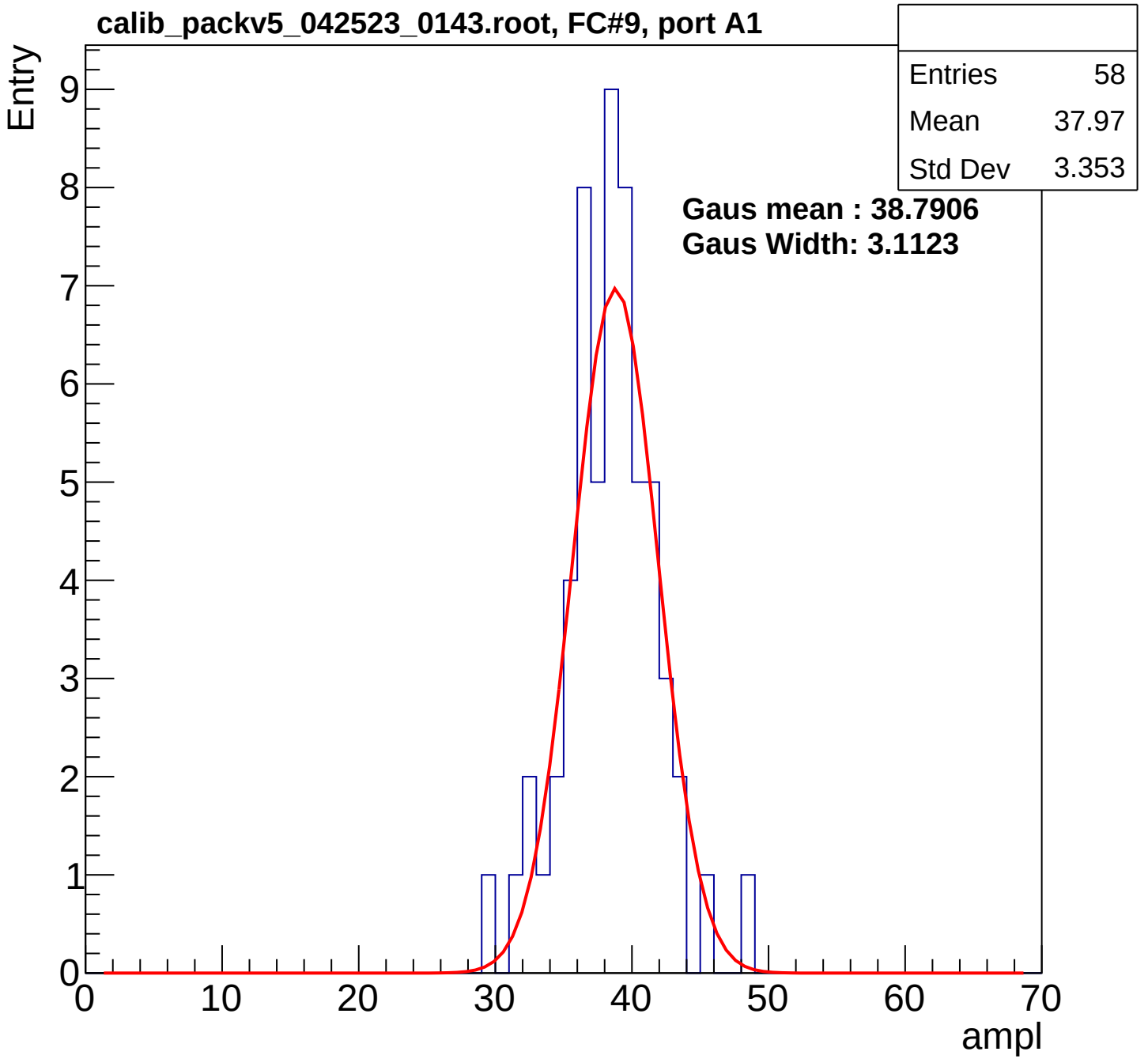
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 32.02
Std Dev 2.881
Gaus mean : 32.5373
Gaus Width: 3.1105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 37.97
Std Dev 3.353
Gaus mean : 38.7906
Gaus Width: 3.1123
Entry
ampl
0
1
2
3
4
5
6
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0
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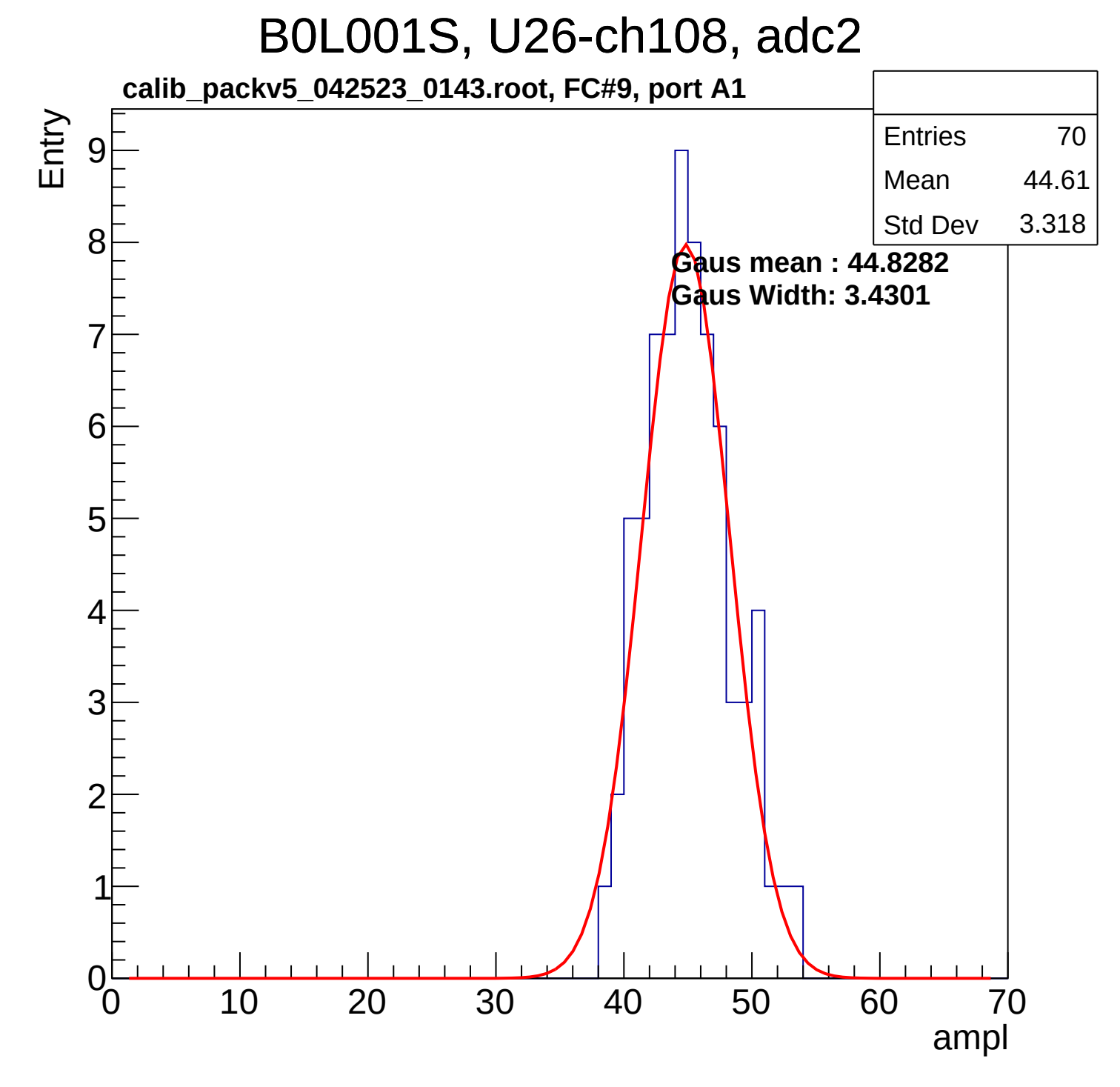
B0L001S, U26-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.61
Std Dev 3.318
Gaus mean : 44.8282
Gaus Width: 3.4301
Entry
ampl
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2
3
4
5
6
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0
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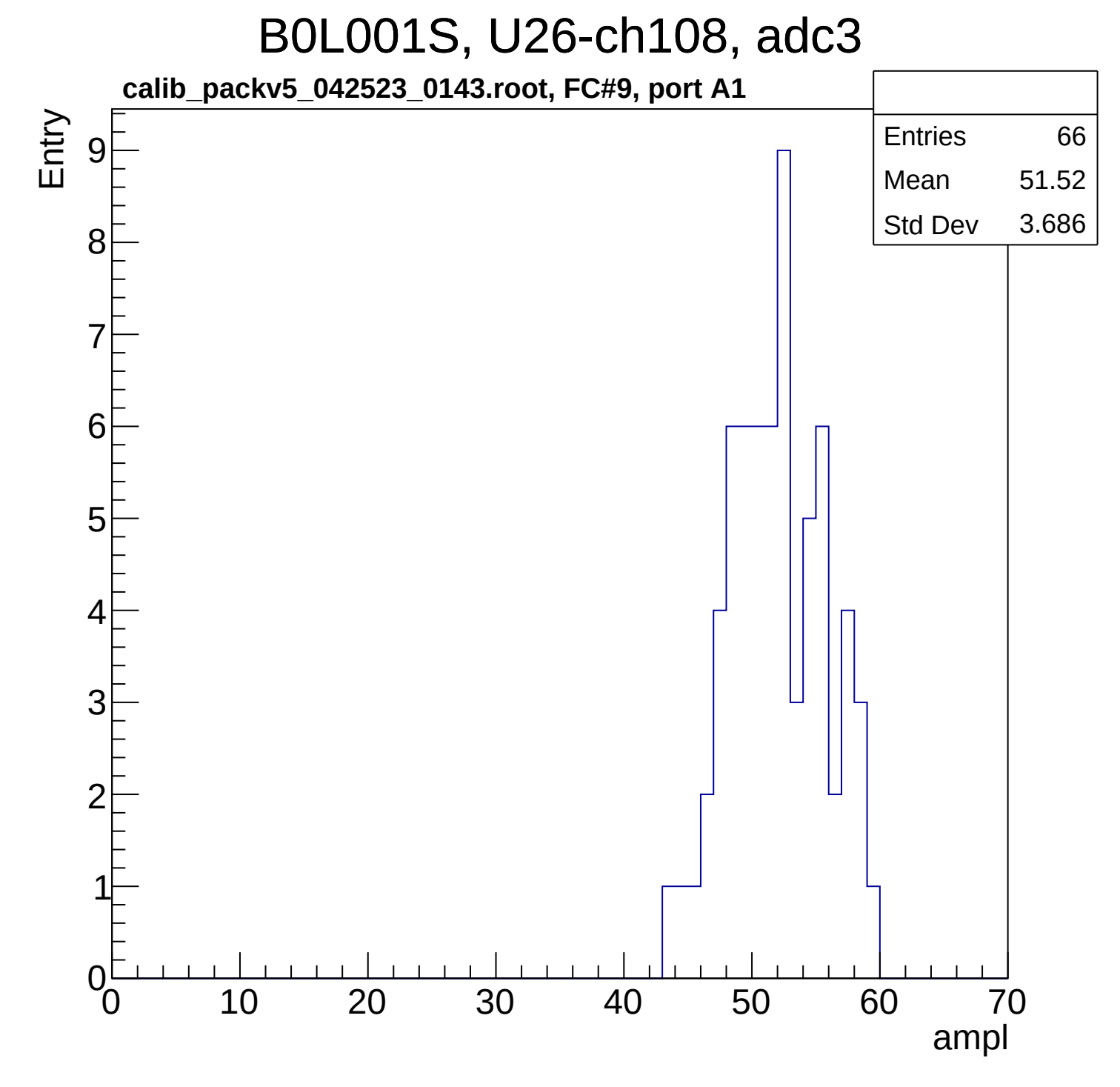
B0L001S, U26-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.52
Std Dev 3.686
Entry
ampl
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5
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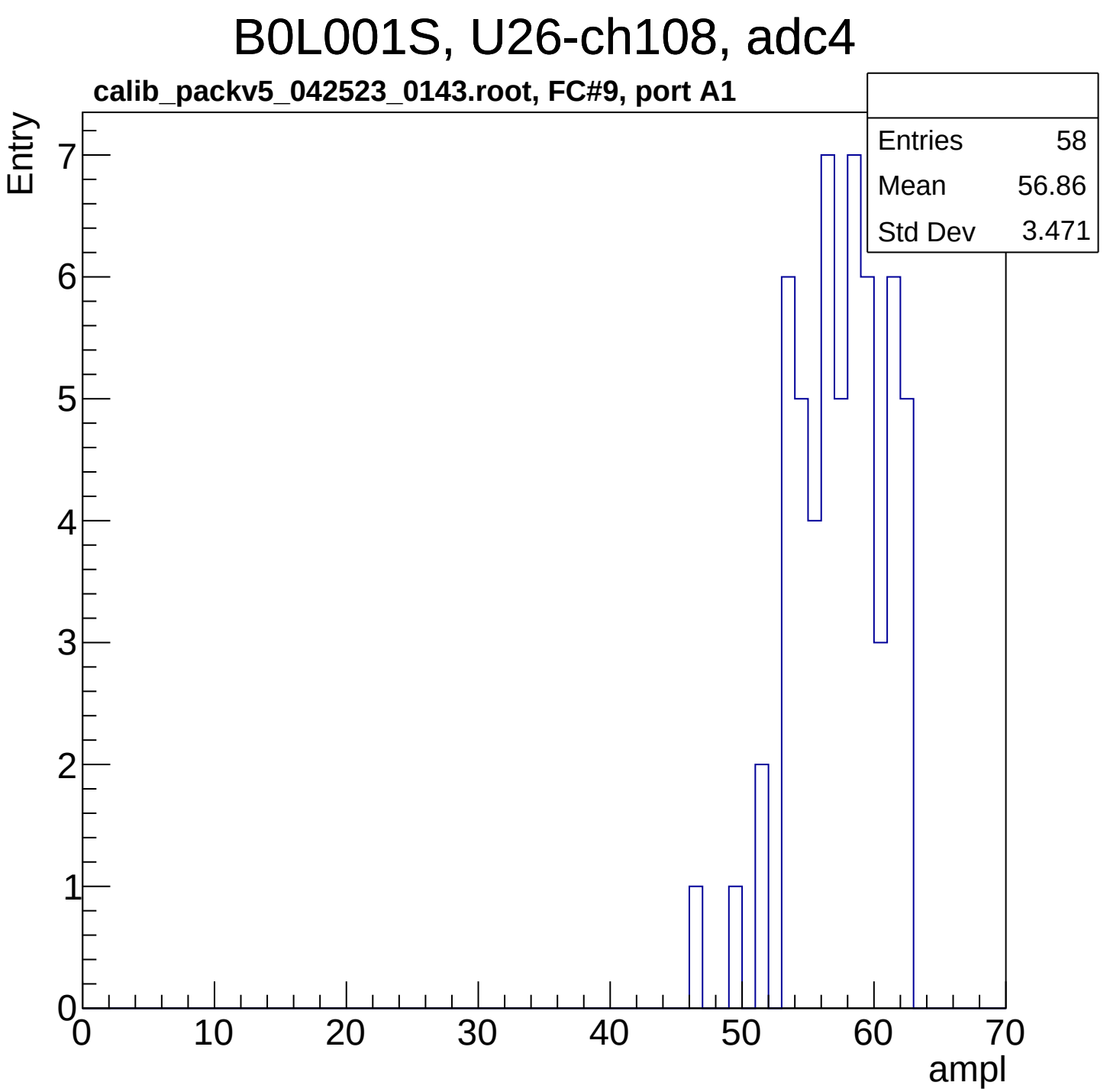
B0L001S, U26-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.86
Std Dev 3.471
Entry
ampl
0
1
2
3
4
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0
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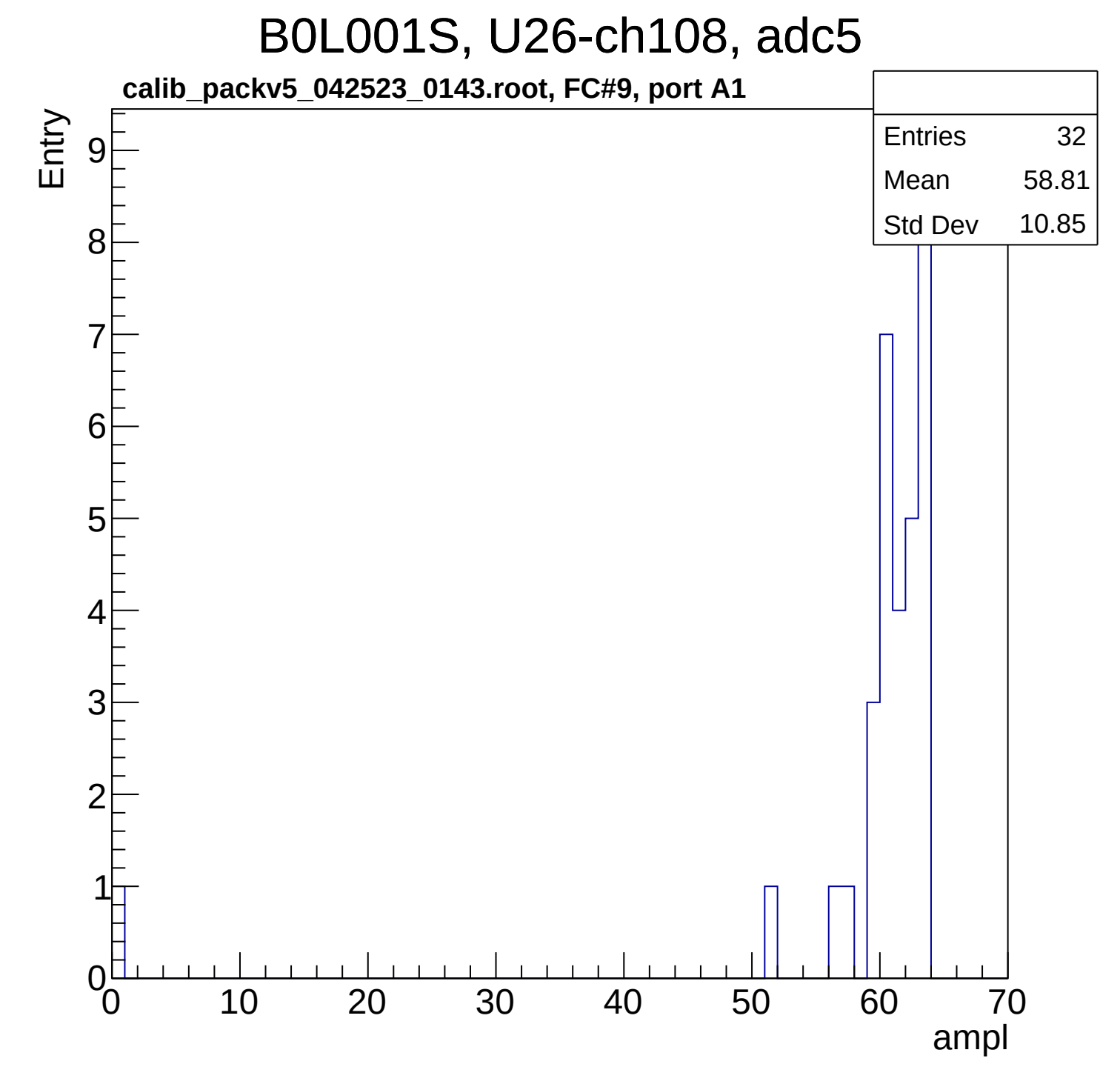
B0L001S, U26-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.81
Std Dev 10.85
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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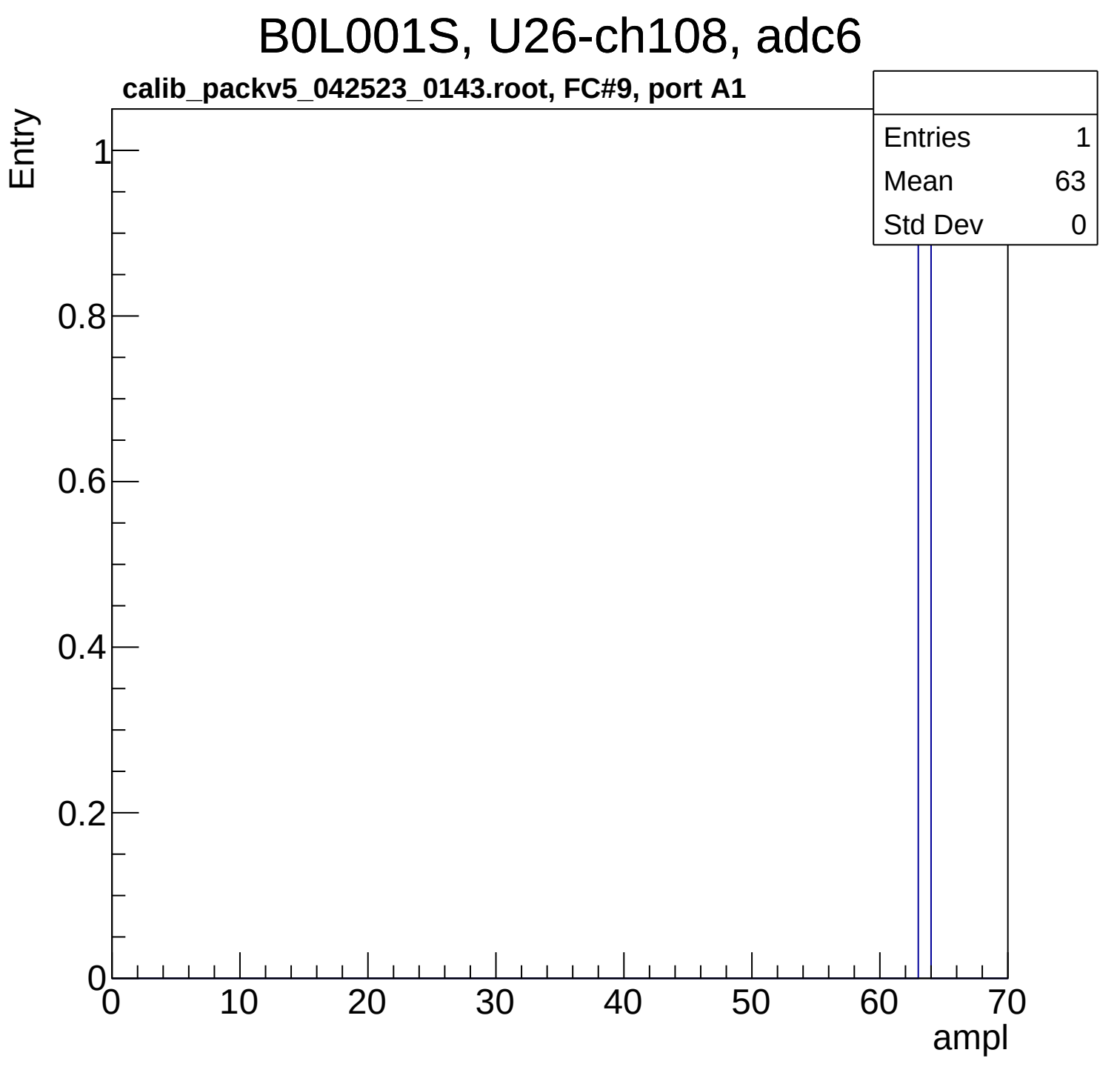
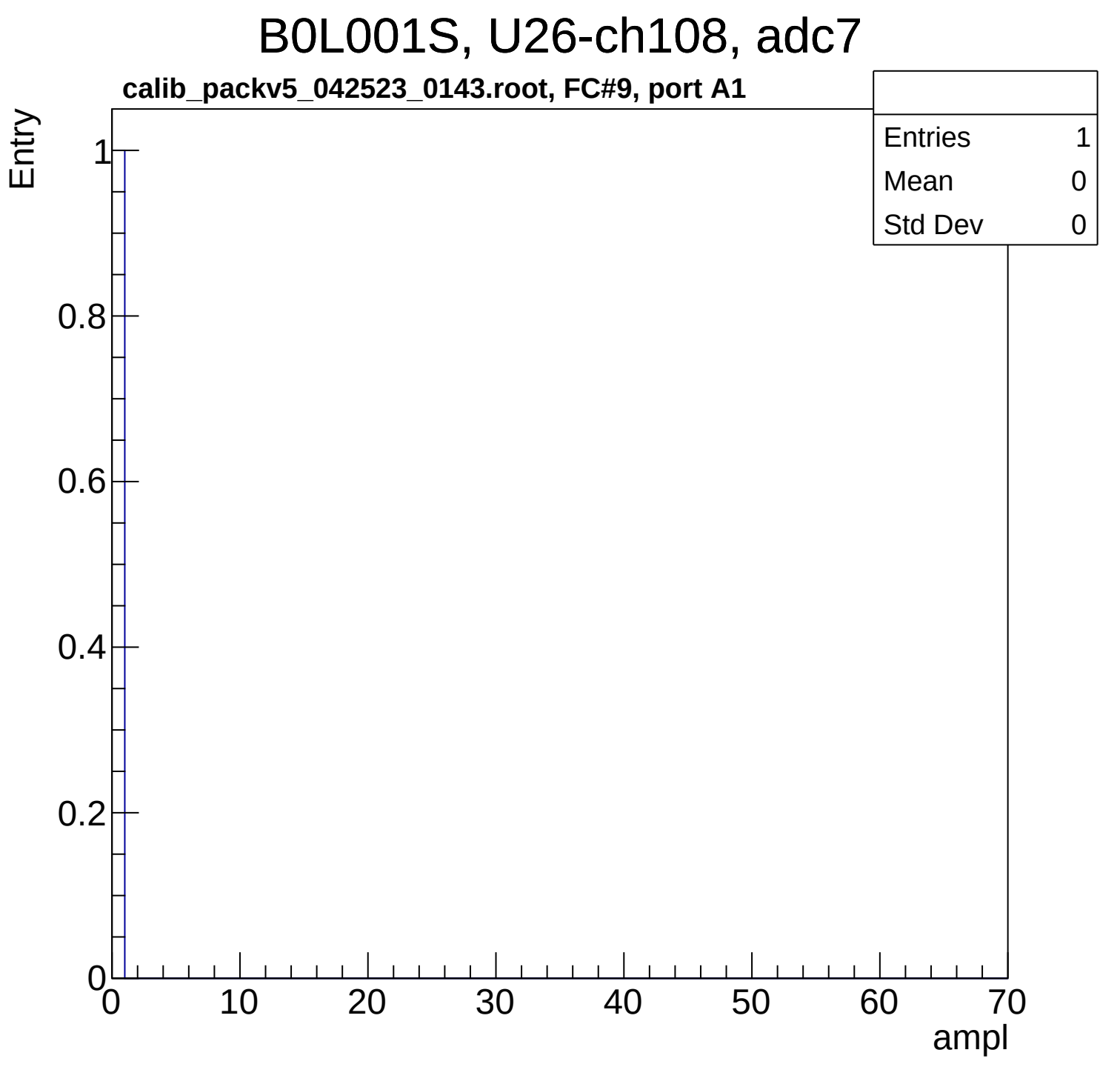
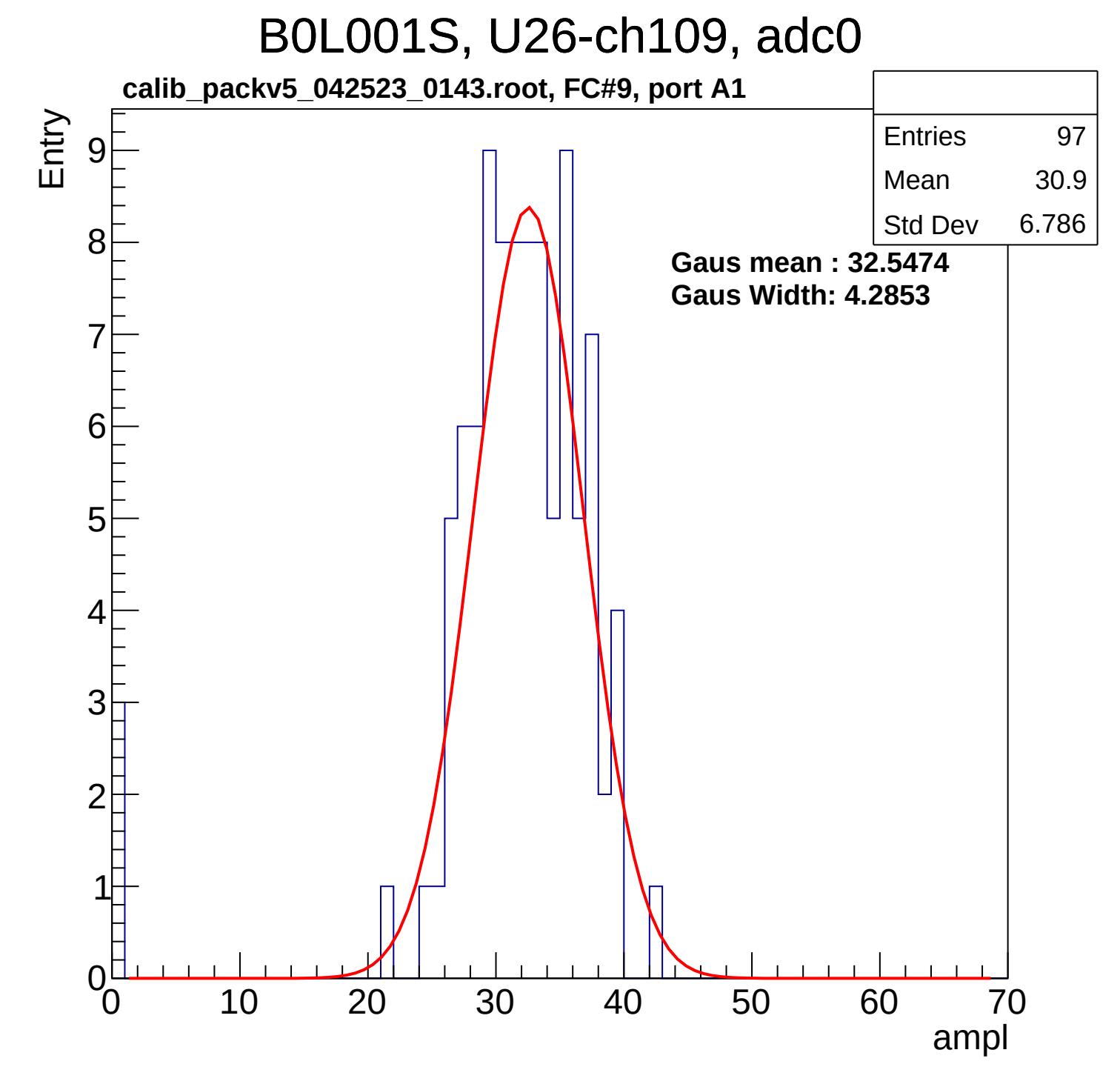

B0L001S, U26-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 97
Mean 30.9
Std Dev 6.786
Gaus mean : 32.5474
Gaus Width: 4.2853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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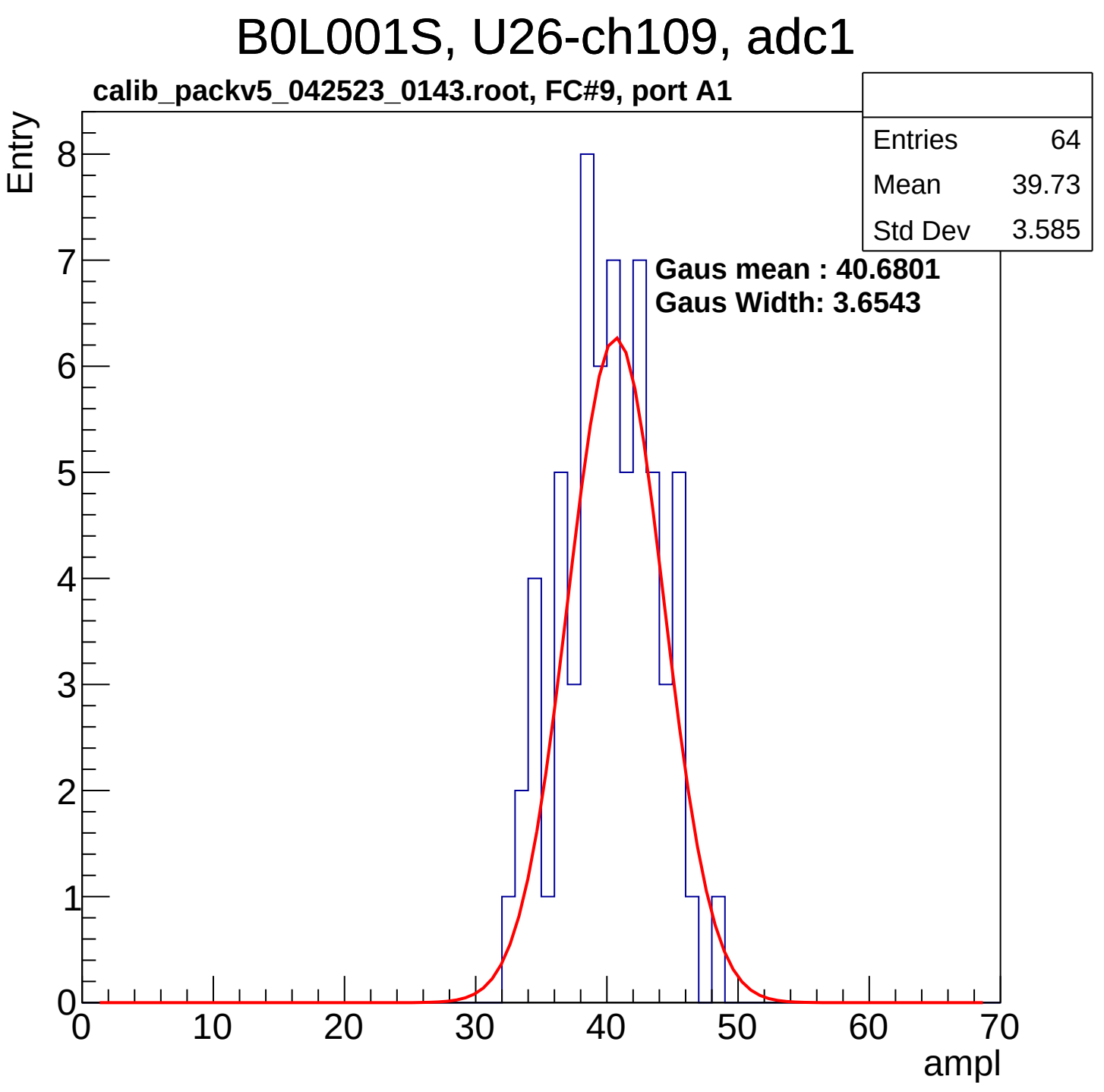
B0L001S, U26-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 39.73
Std Dev 3.585
Gaus mean : 40.6801
Gaus Width: 3.6543
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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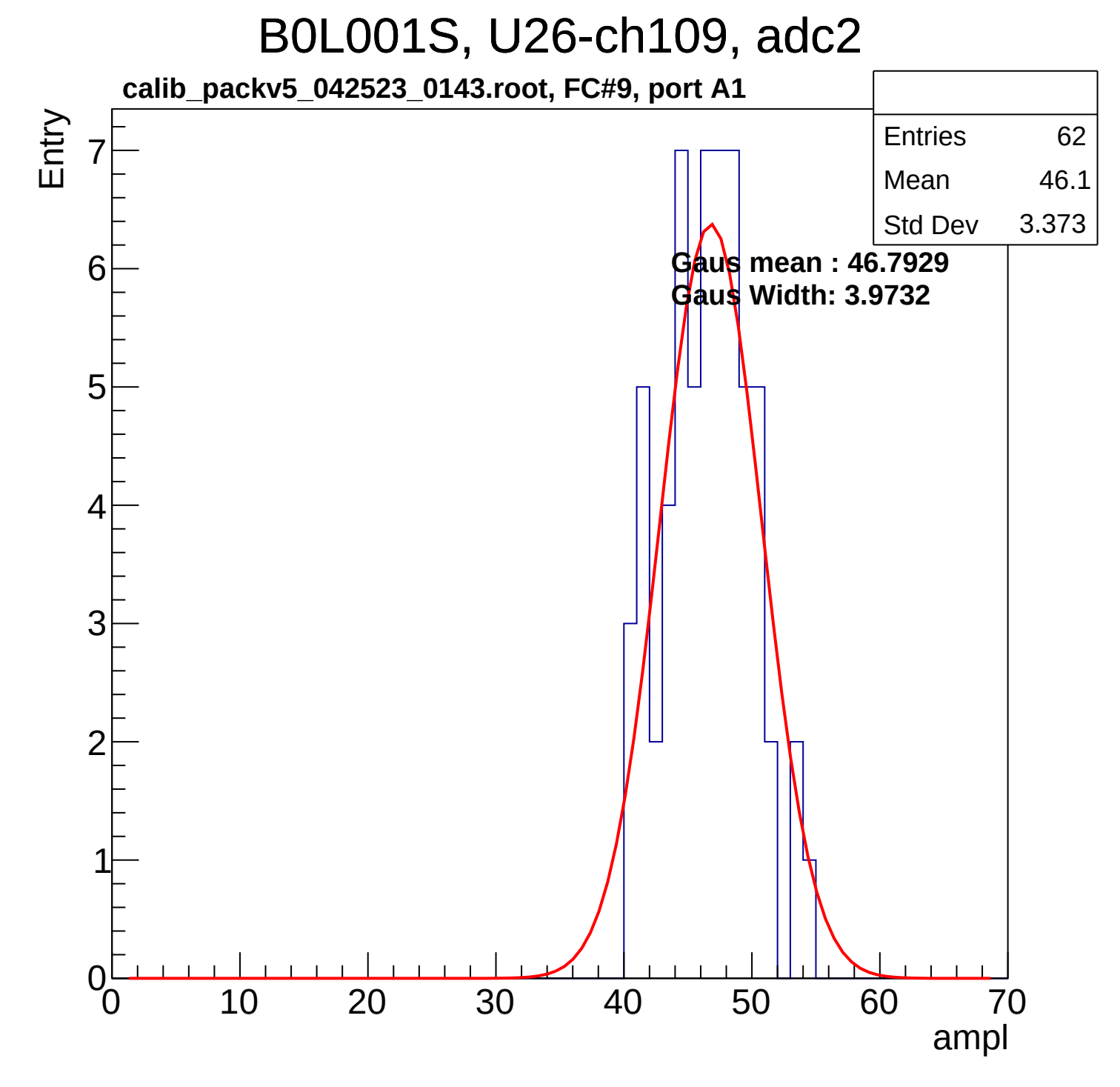
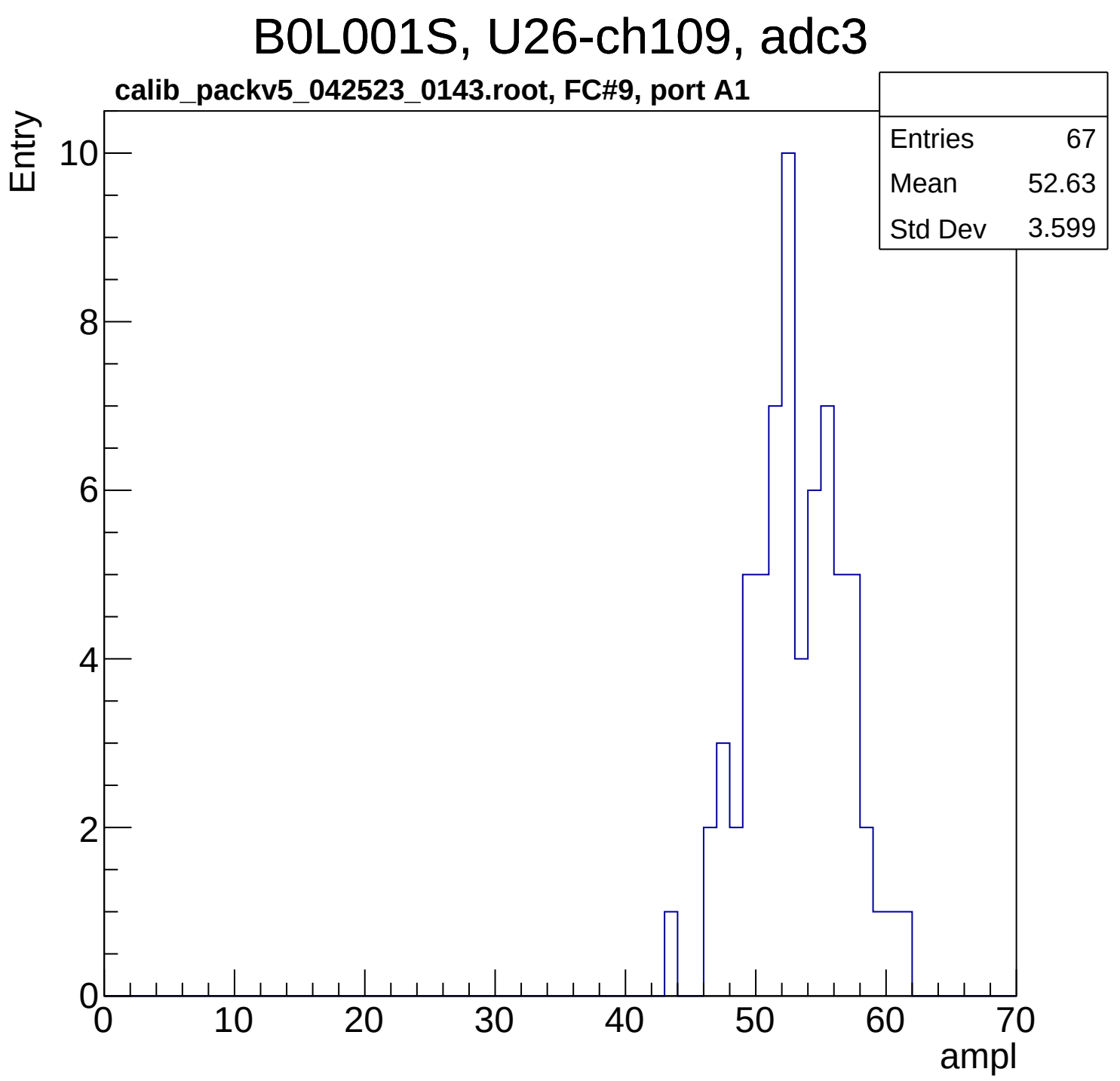
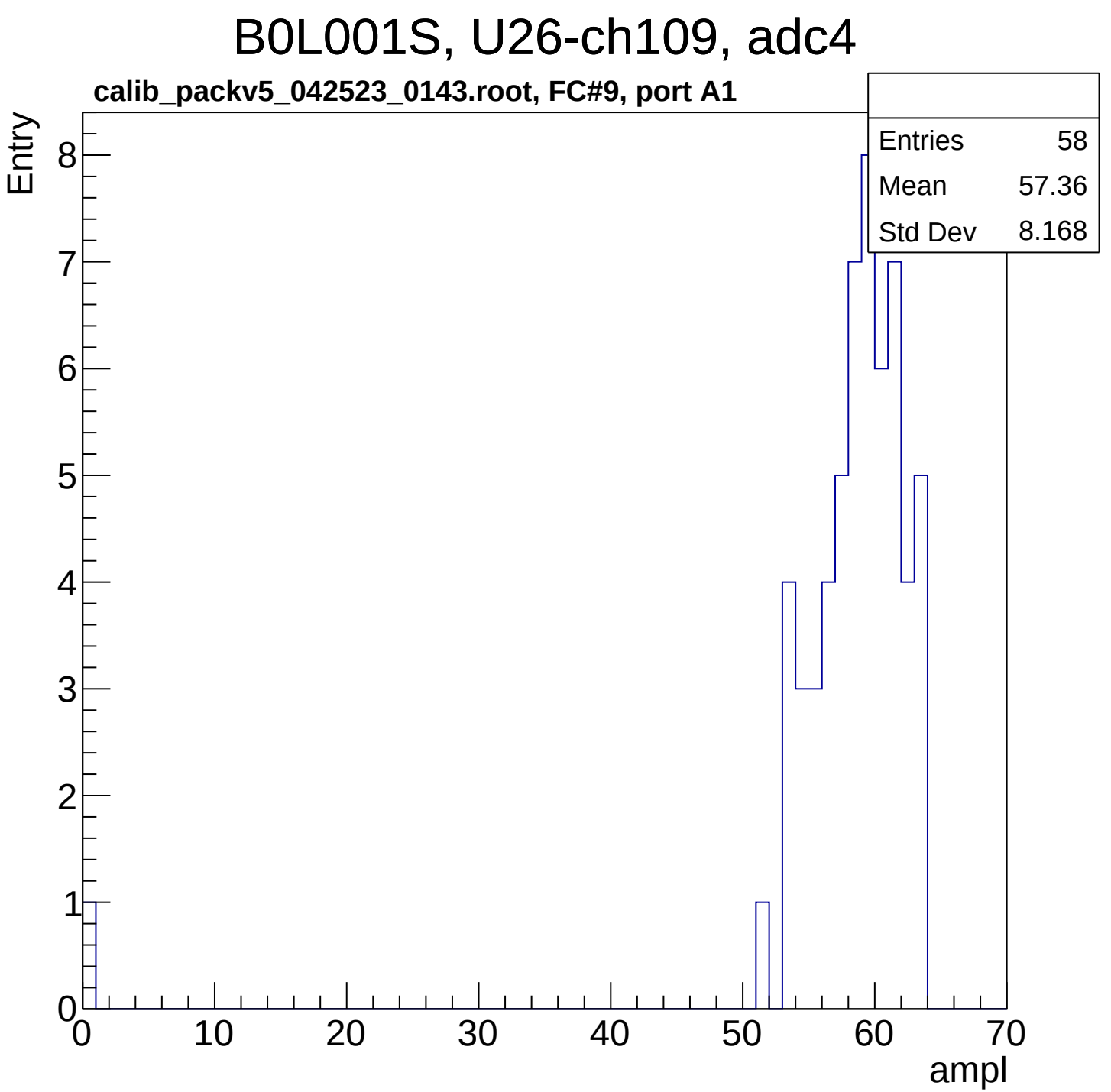

B0L001S, U26-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 46.1
Std Dev 3.373
Gaus mean : 46.7929
Gaus Width: 3.9732
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 52.63
Std Dev 3.599
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.36
Std Dev 8.168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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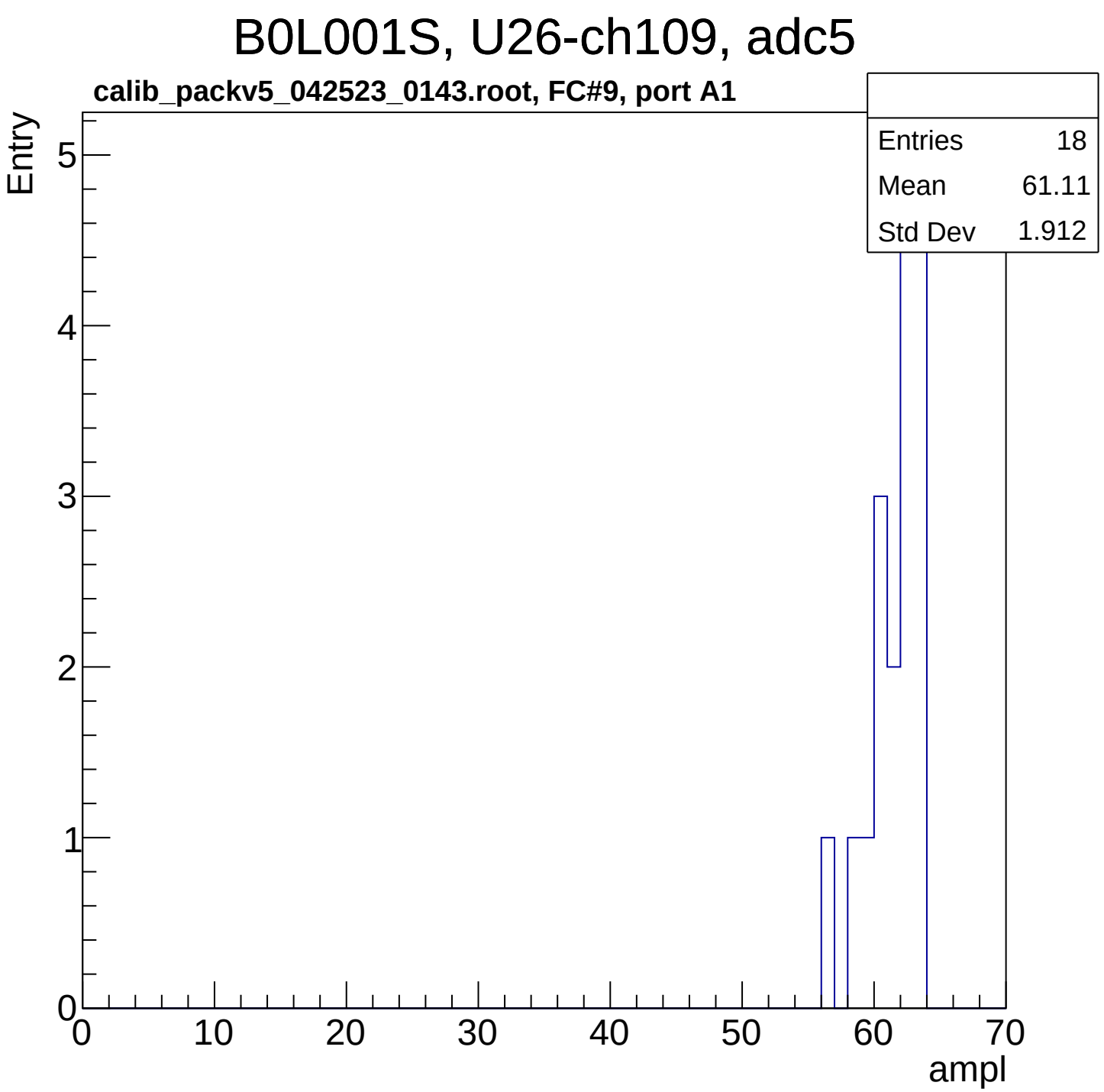
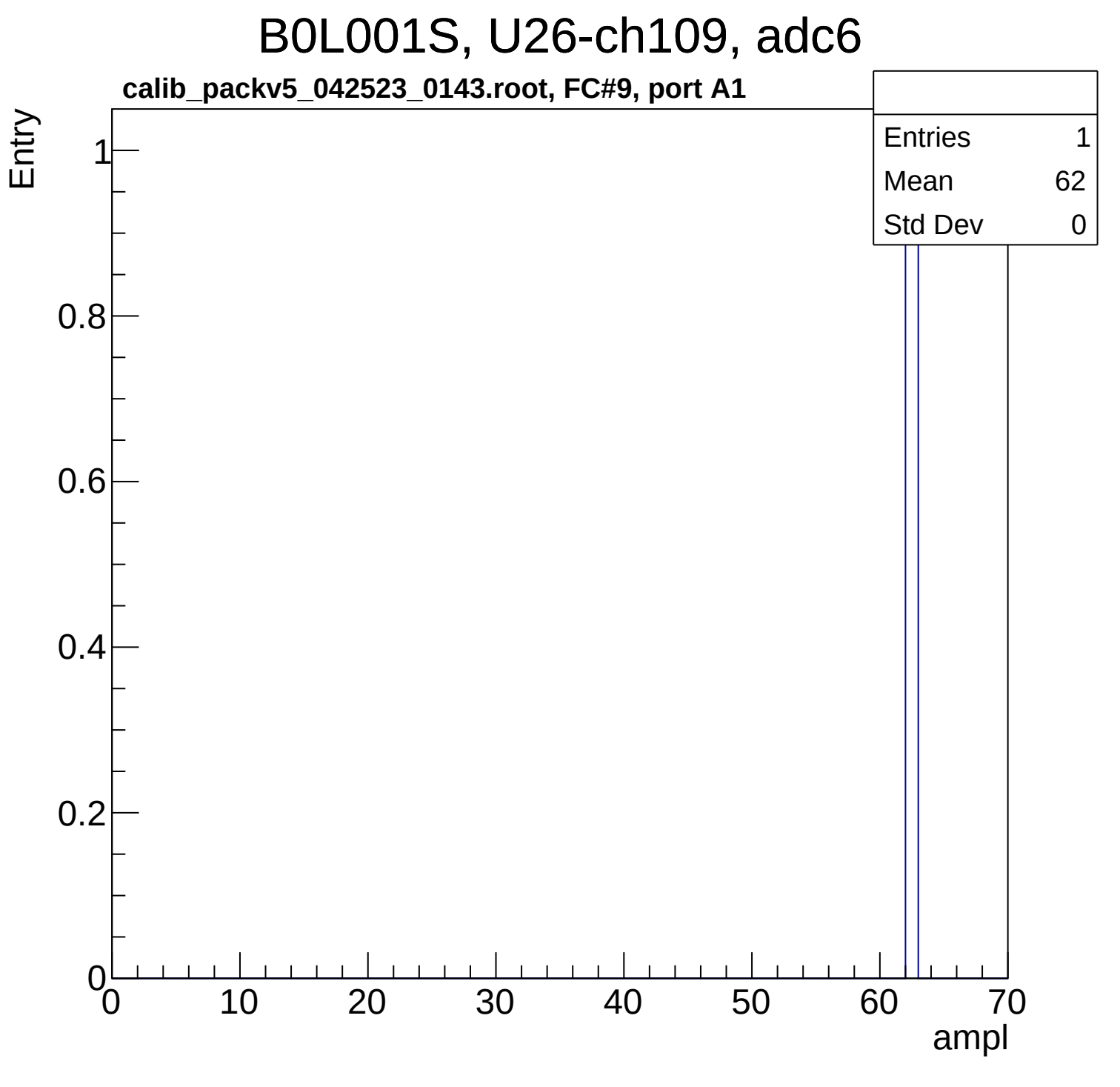
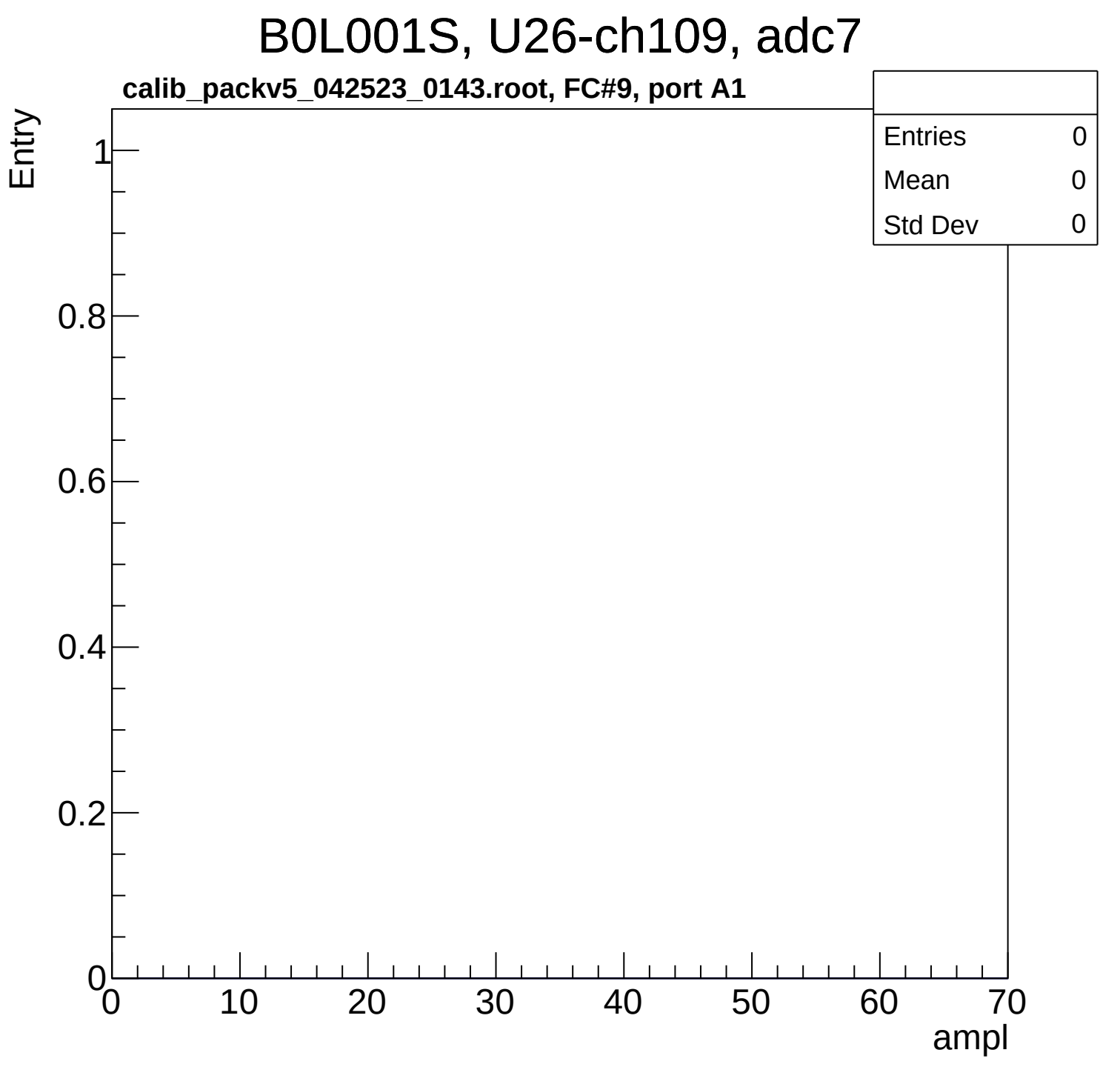

B0L001S, U26-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 61.11
Std Dev 1.912
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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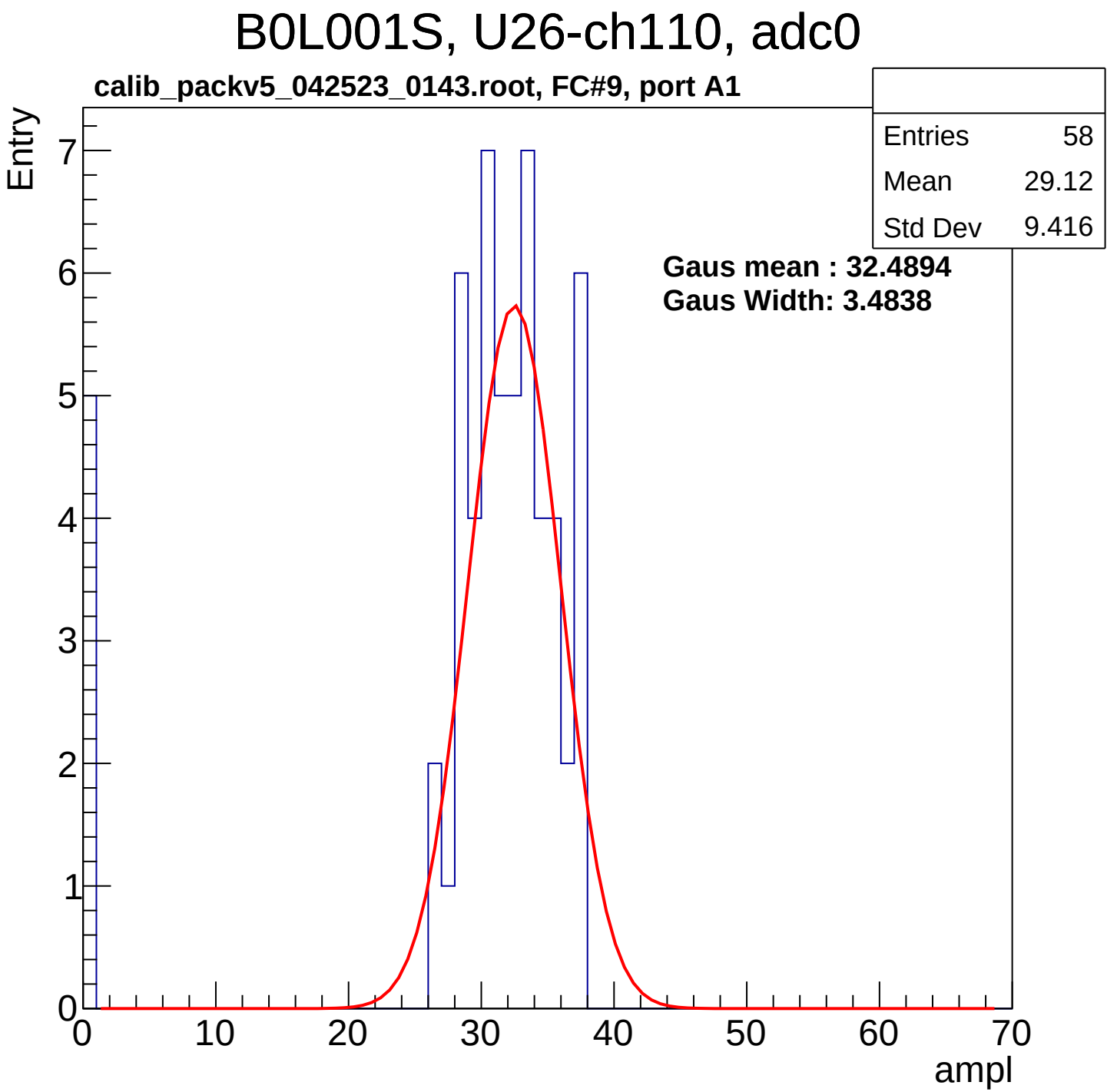
50
60
70
ampl


B0L001S, U26-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 29.12
Std Dev 9.416
Gaus mean : 32.4894
Gaus Width: 3.4838
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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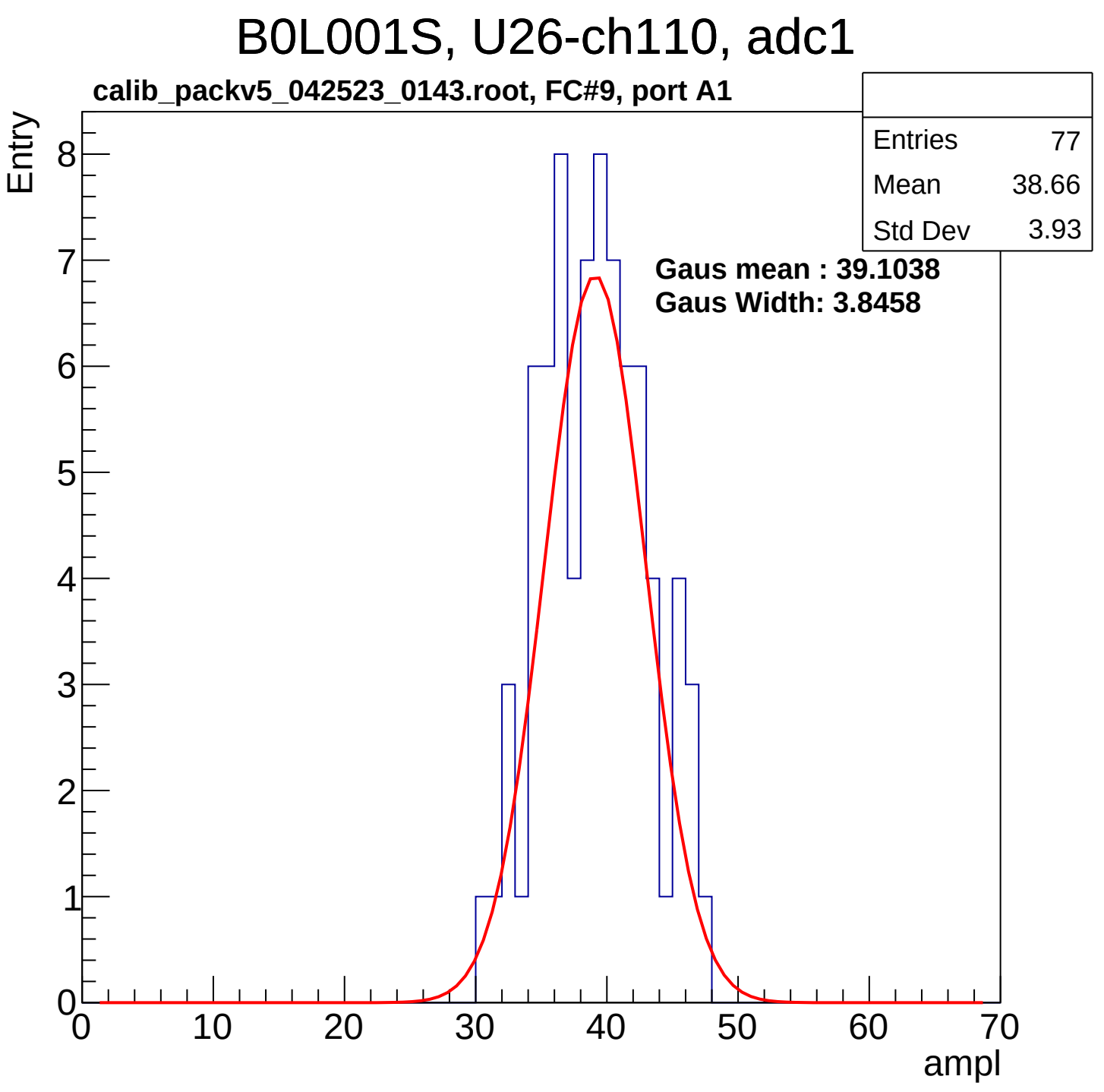
B0L001S, U26-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.66
Std Dev 3.93
Gaus mean : 39.1038
Gaus Width: 3.8458
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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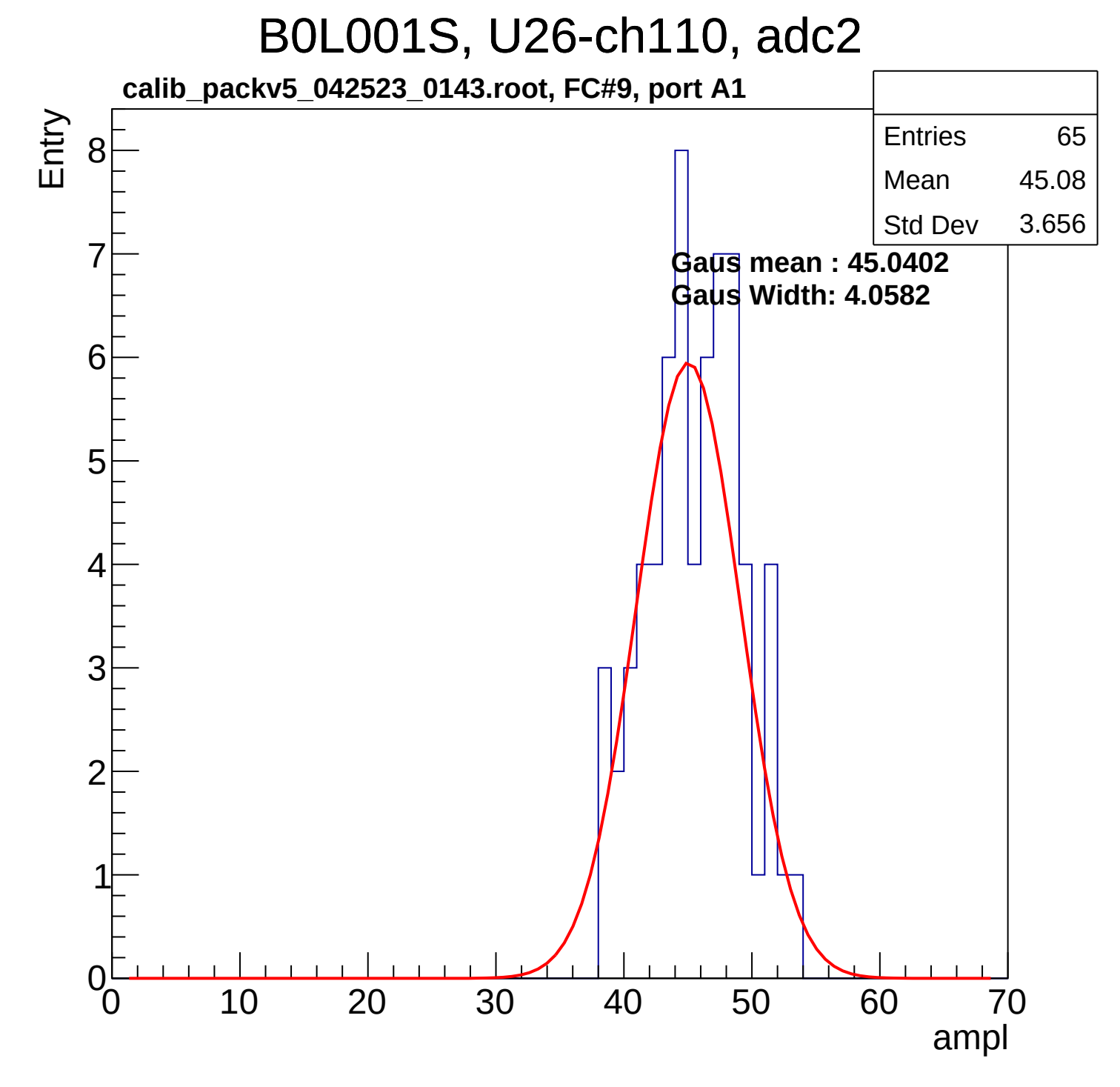
B0L001S, U26-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.08
Std Dev 3.656
Gaus mean : 45.0402
Gaus Width: 4.0582
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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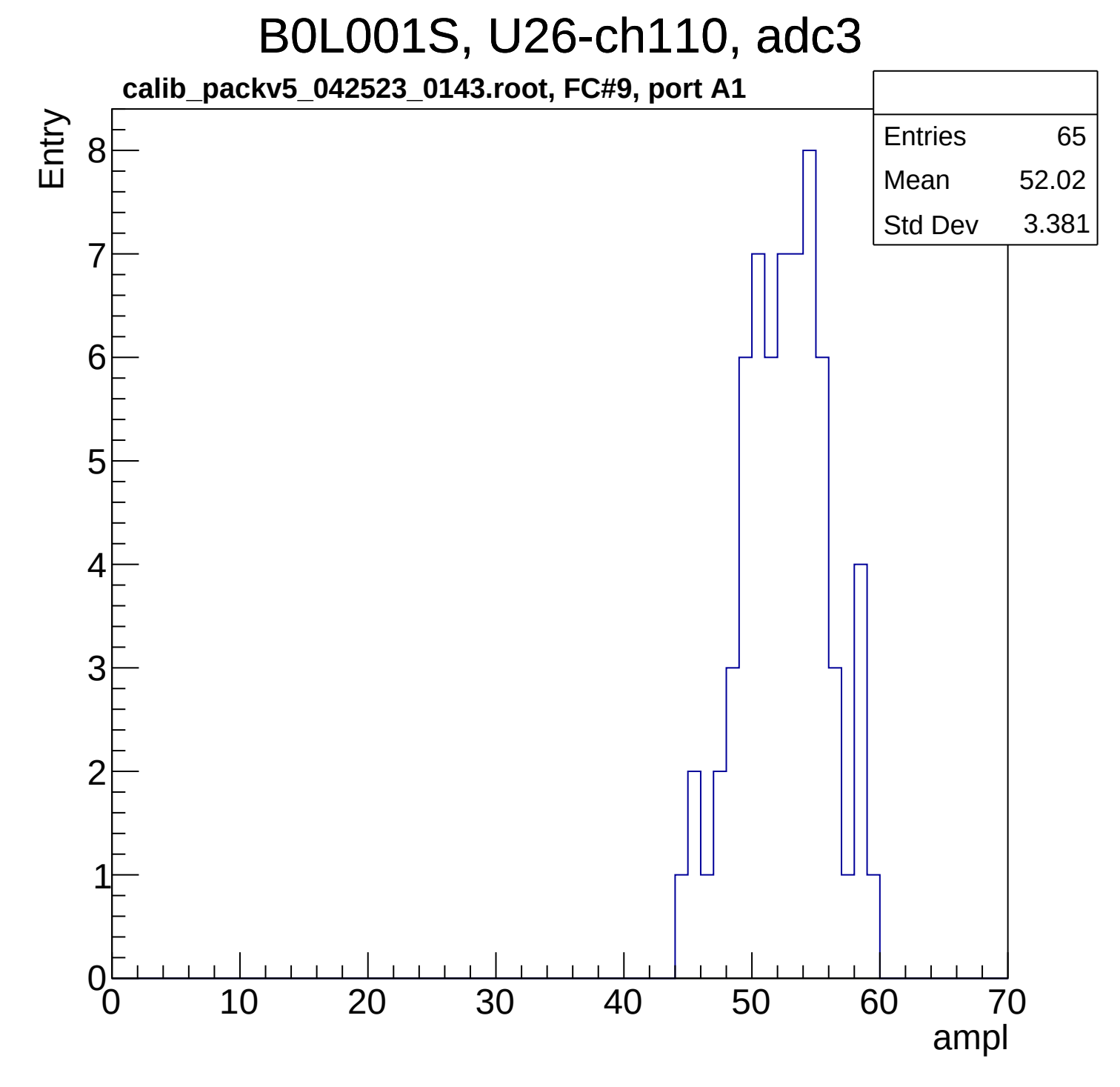
B0L001S, U26-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 52.02
Std Dev 3.381
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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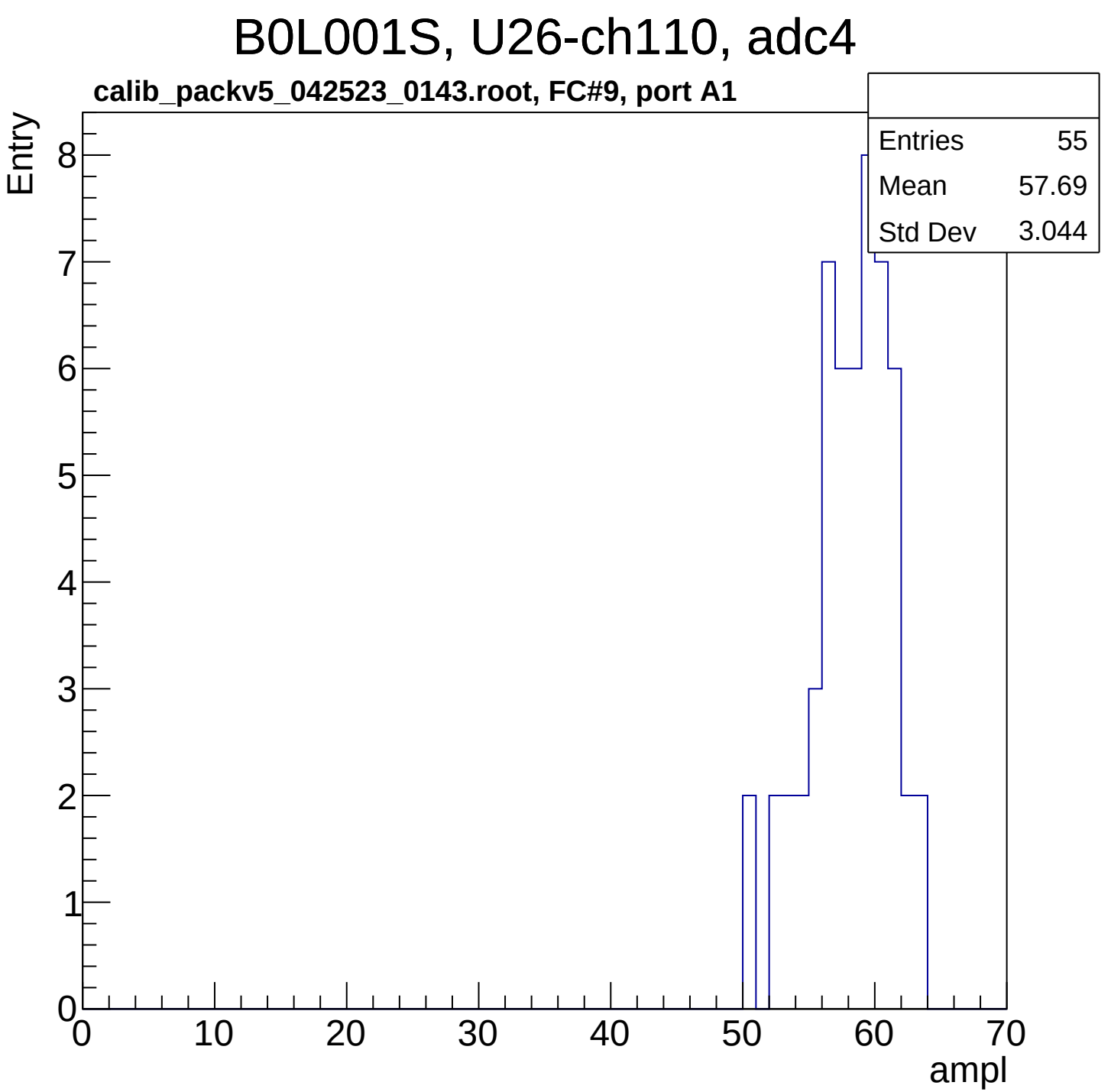
B0L001S, U26-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.69
Std Dev 3.044
Entry
ampl
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2
3
4
5
6
7
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0
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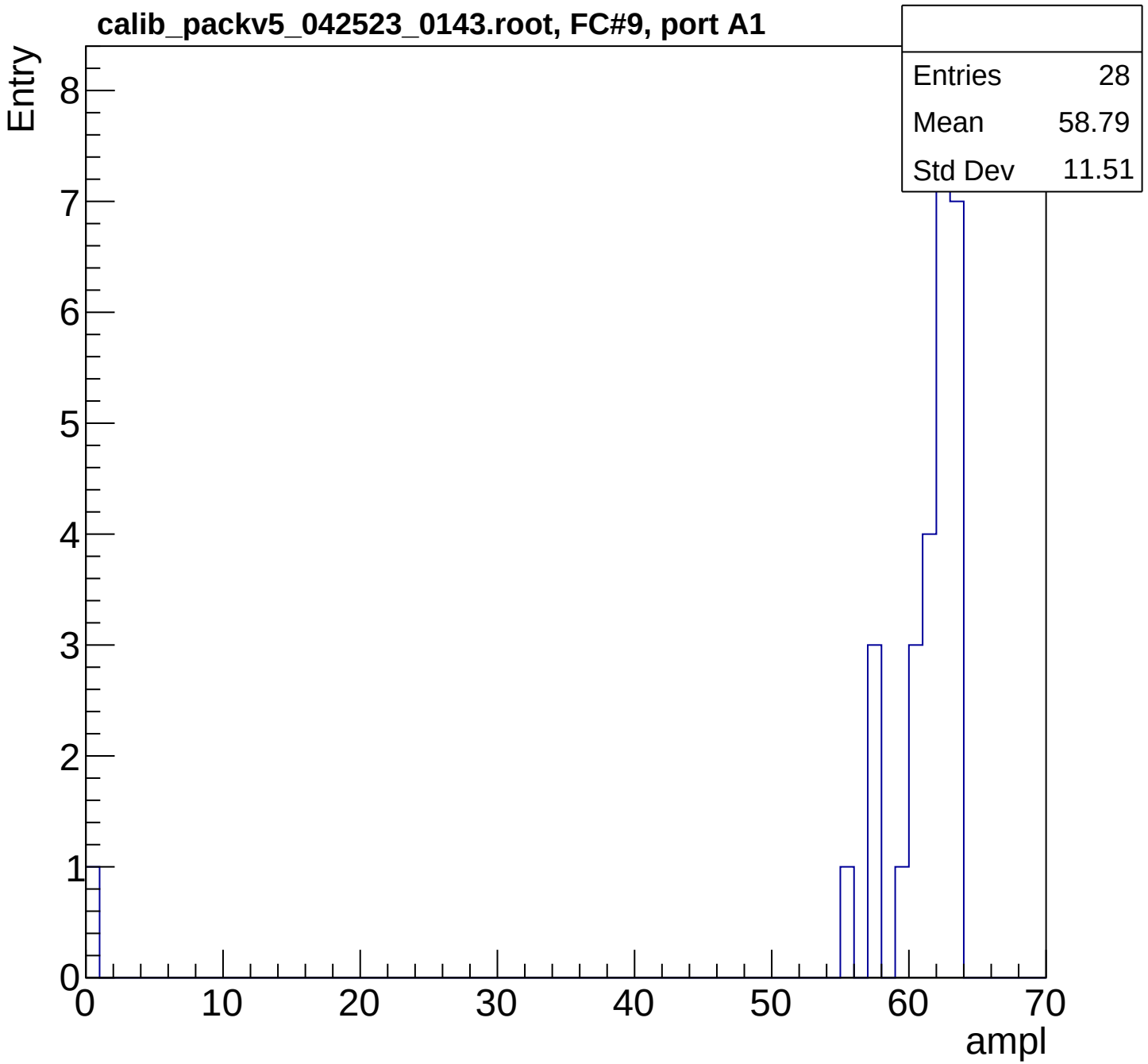
B0L001S, U26-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.79
Std Dev 11.51
Entry
ampl
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2
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0
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60
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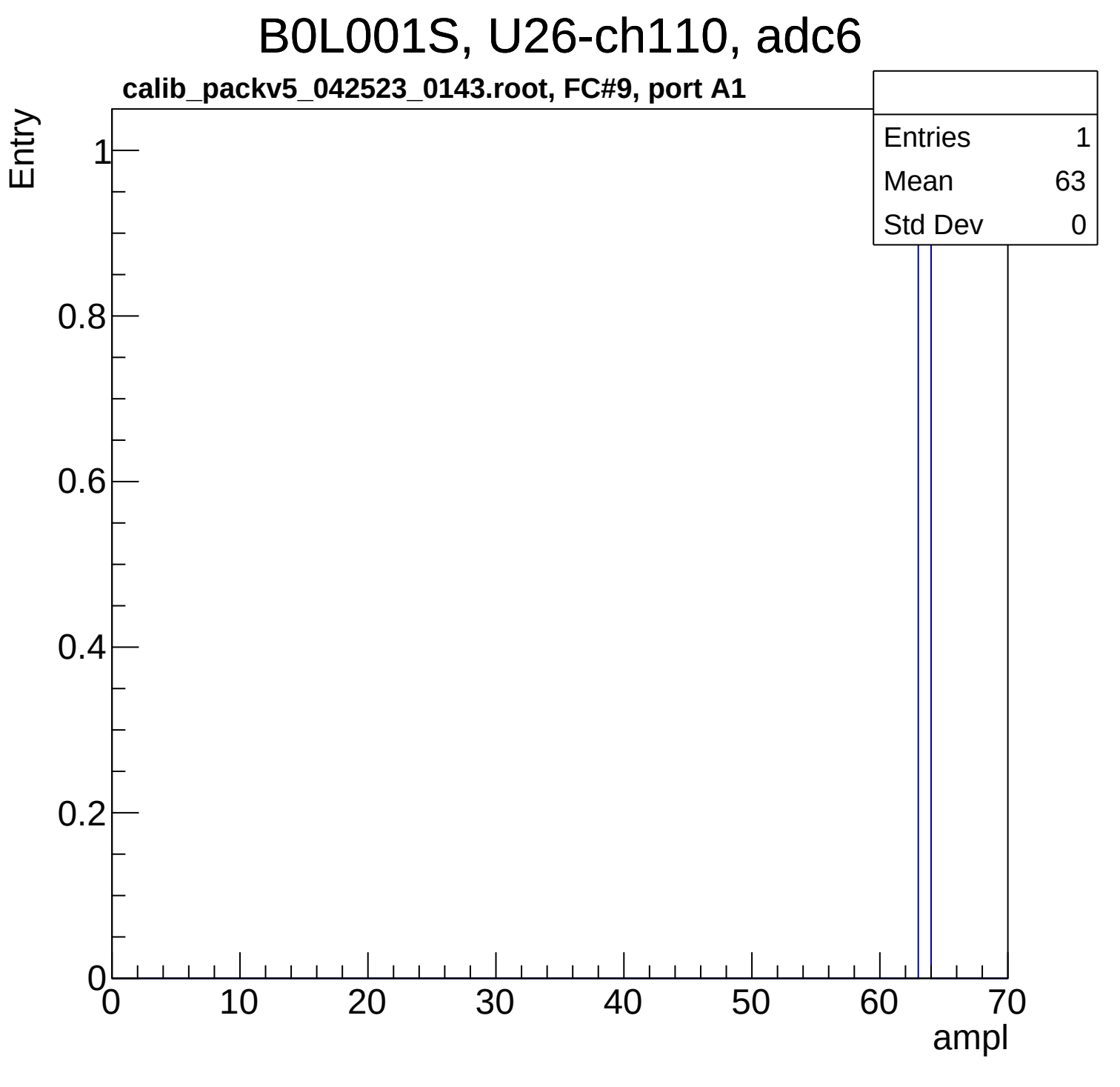
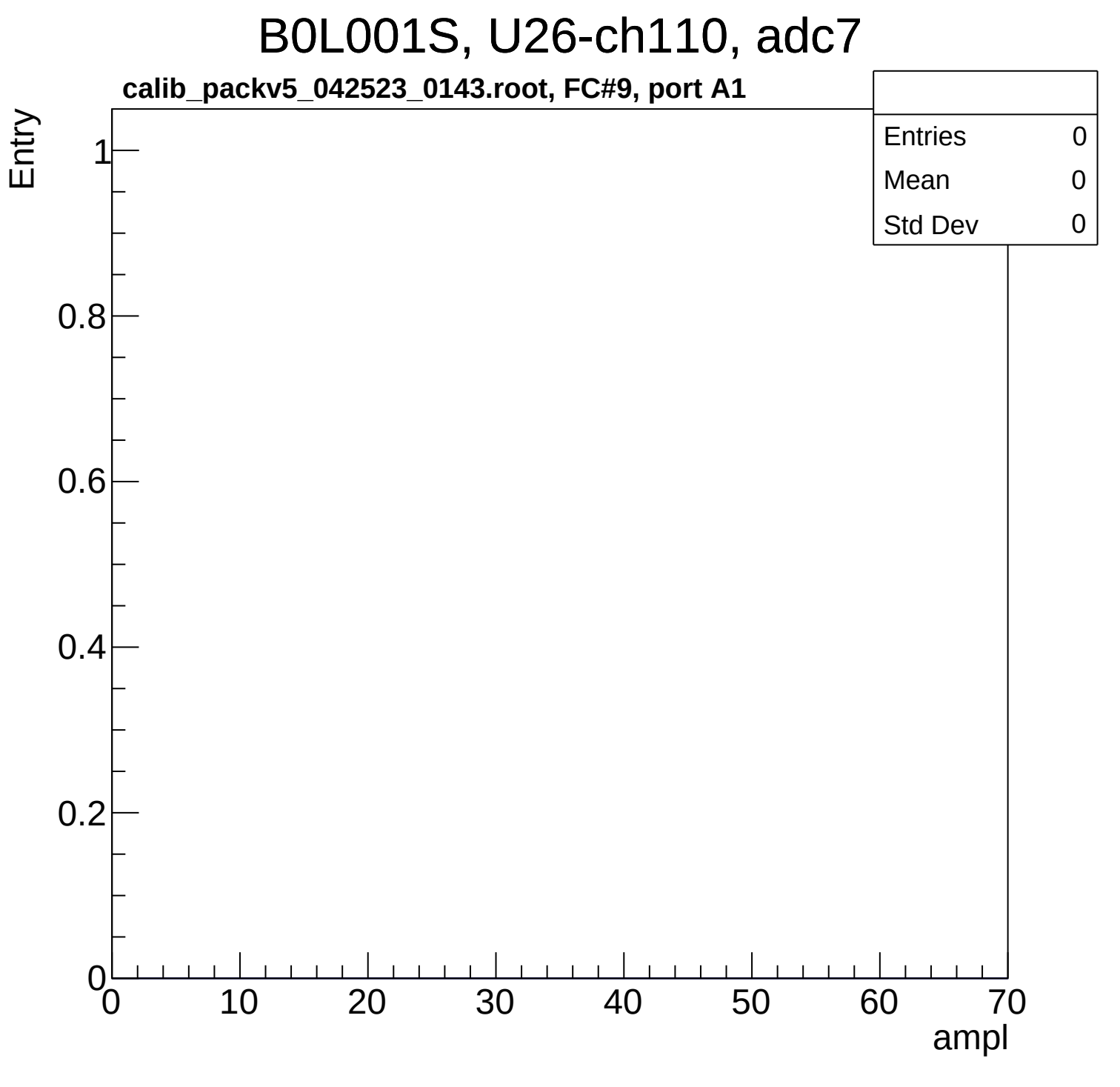
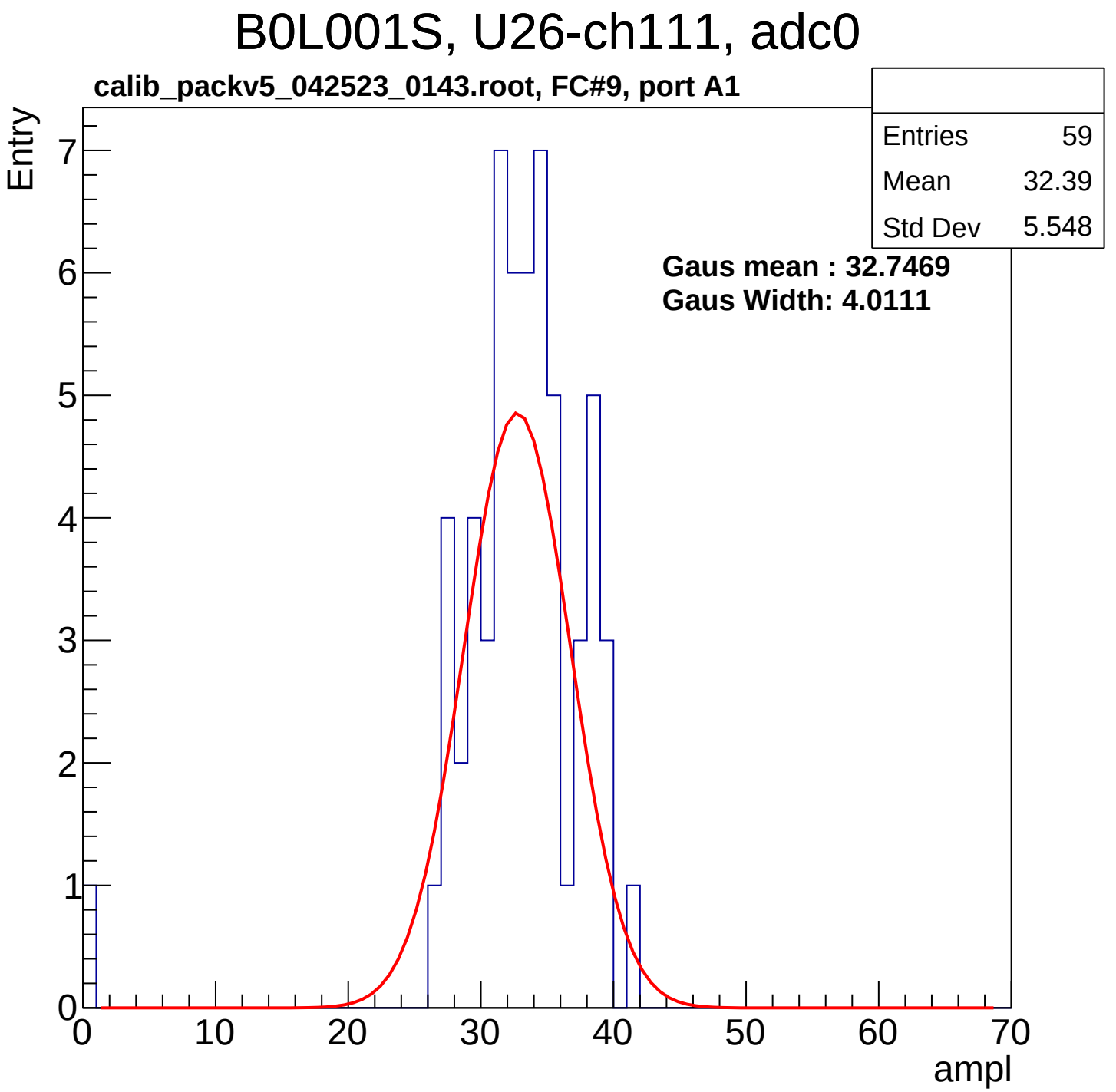

B0L001S, U26-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 32.39
Std Dev 5.548
Gaus mean : 32.7469
Gaus Width: 4.0111
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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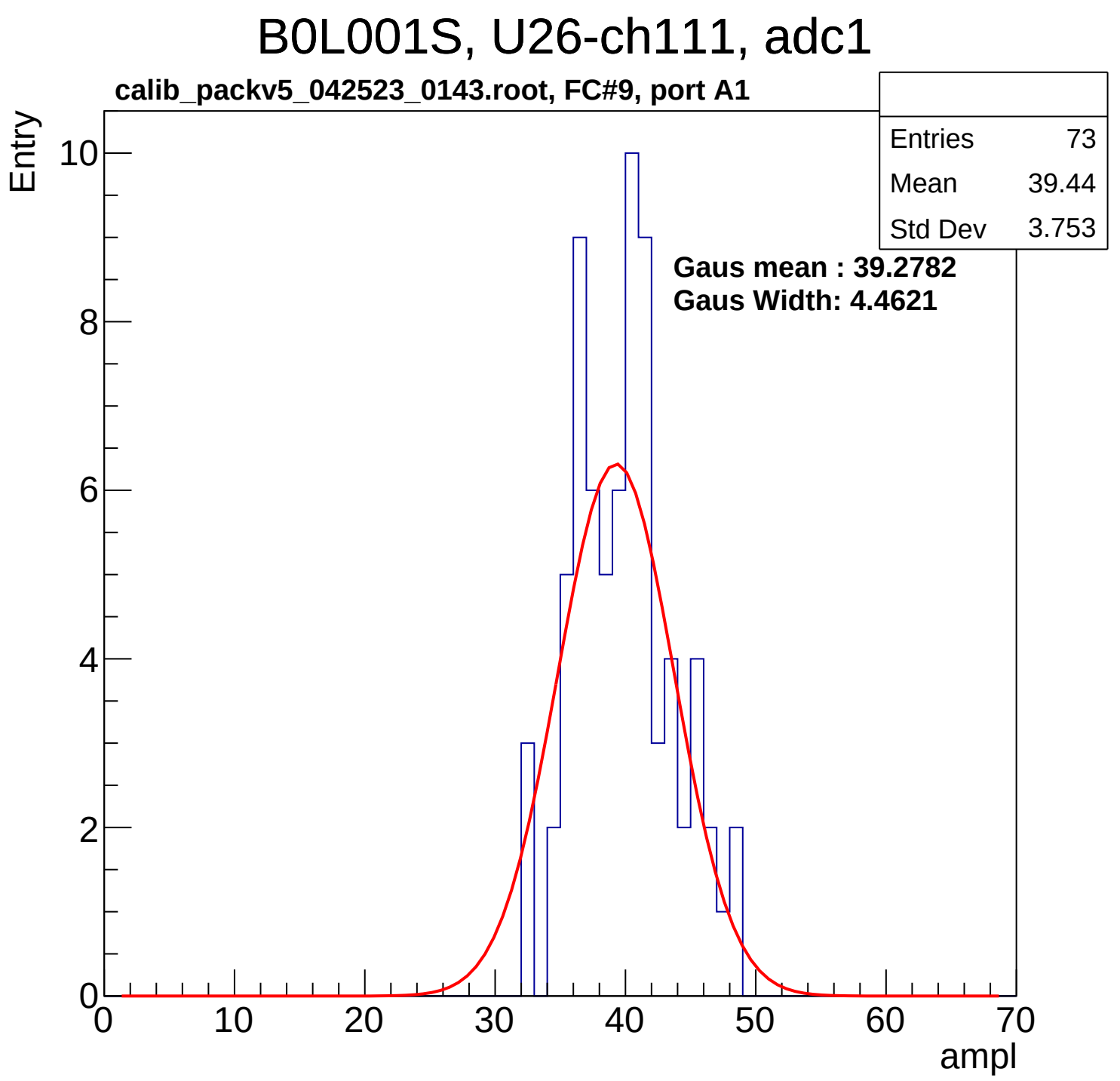
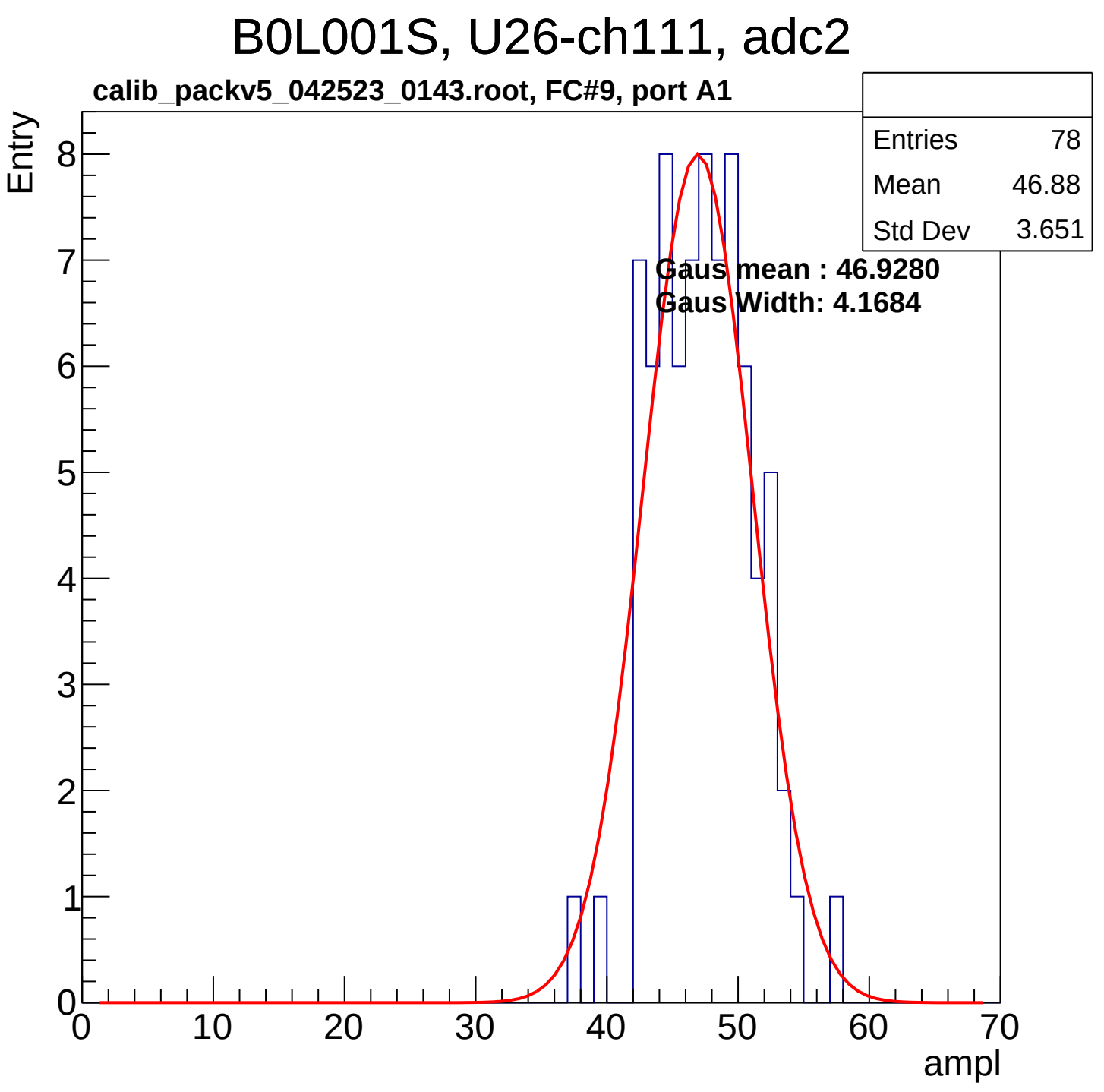

B0L001S, U26-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 39.44
Std Dev 3.753
Gaus mean : 39.2782
Gaus Width: 4.4621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 46.88
Std Dev 3.651
Gaus mean : 46.9280
Gaus Width: 4.1684
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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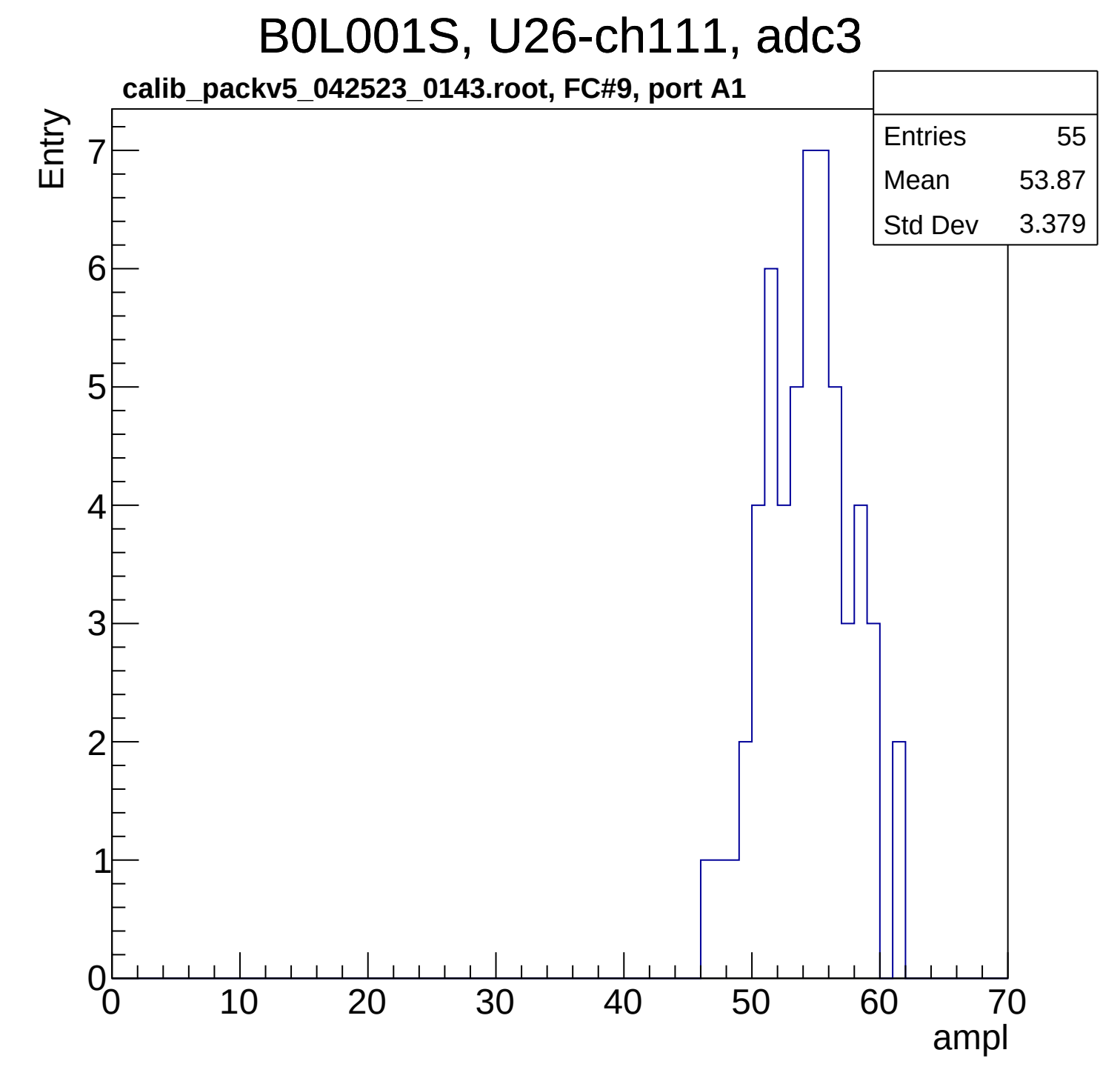
B0L001S, U26-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 53.87
Std Dev 3.379
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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60
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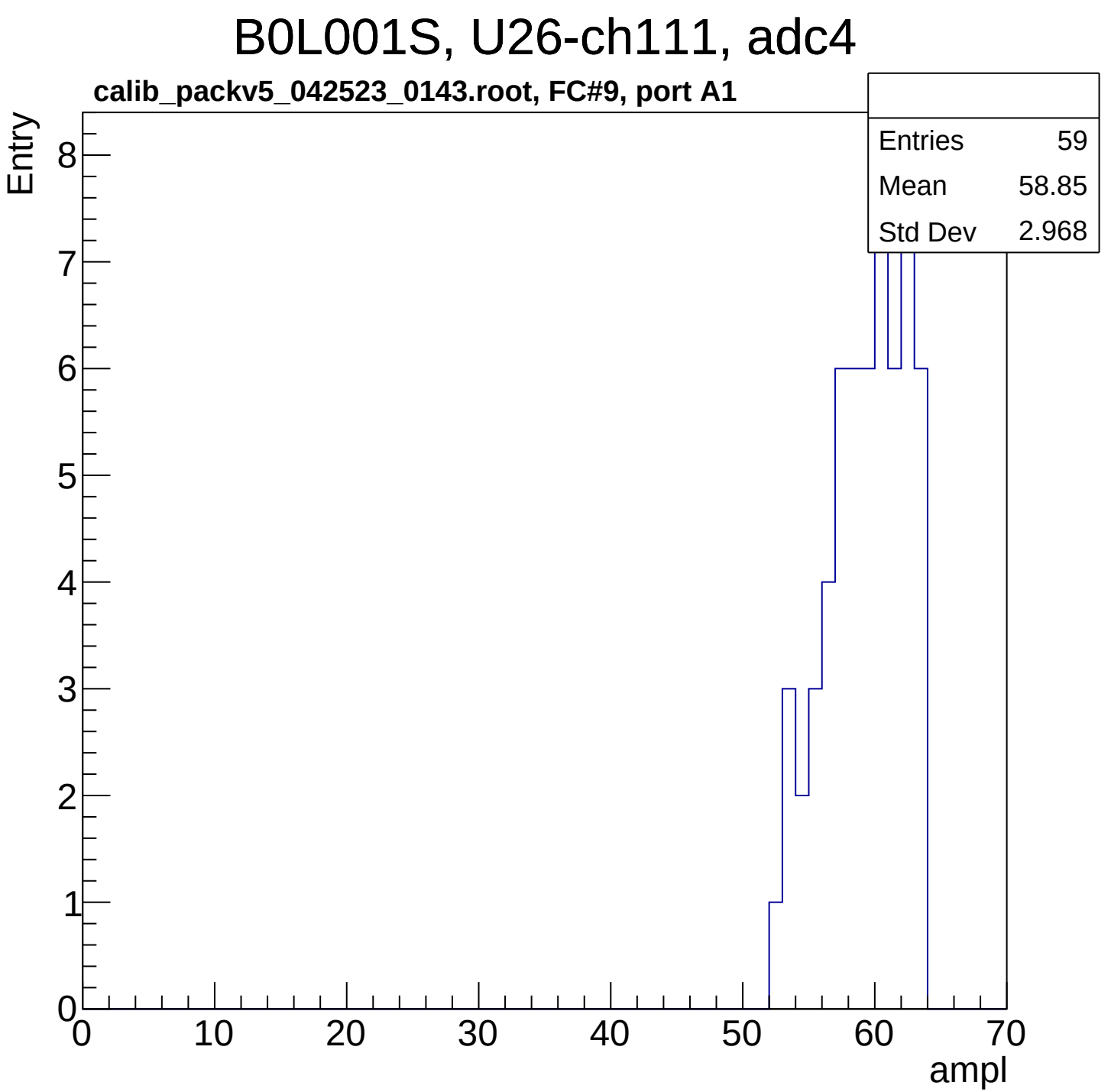
B0L001S, U26-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.85
Std Dev 2.968
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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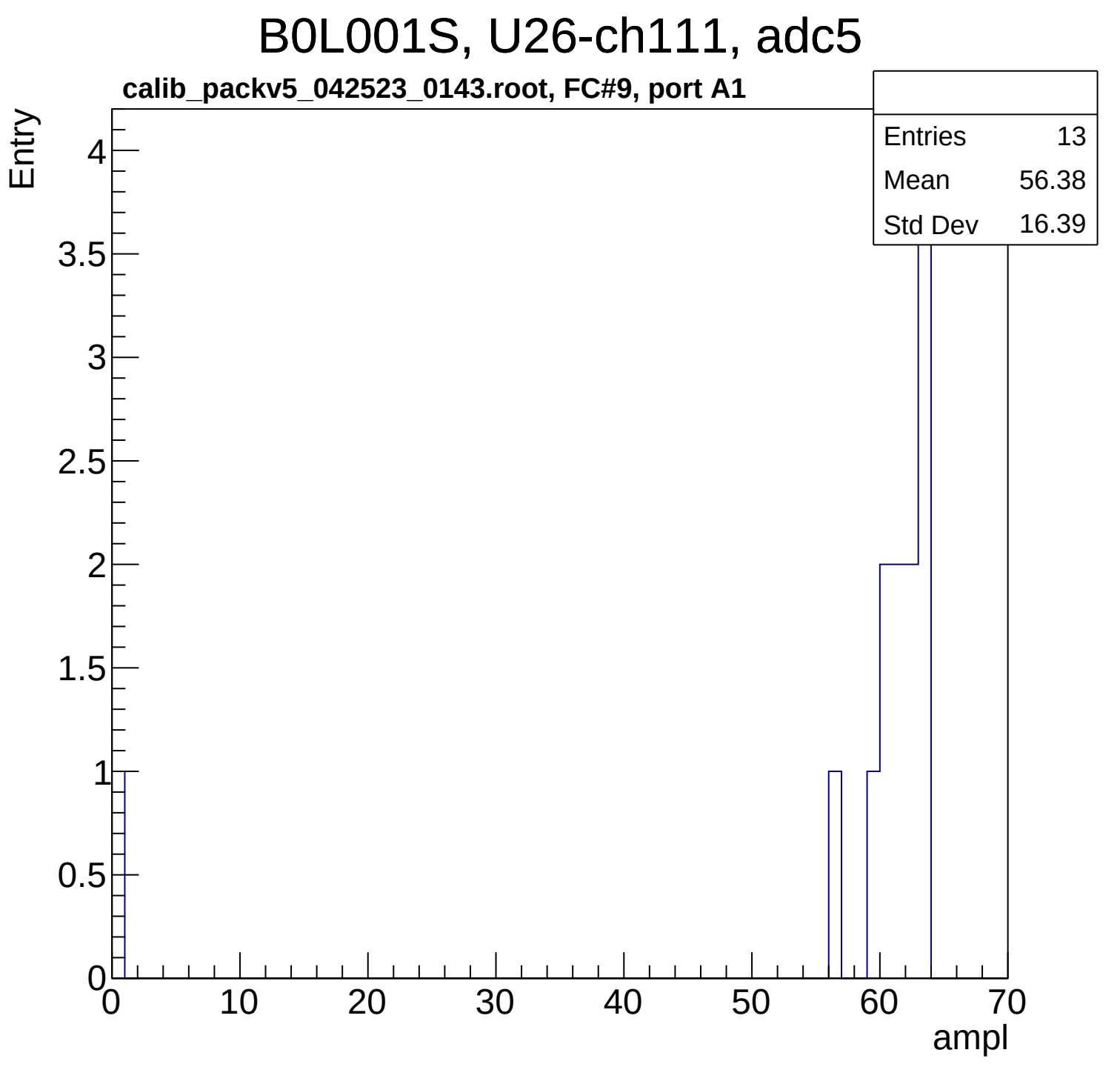
B0L001S, U26-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 56.38
Std Dev 16.39
Entry
ampl
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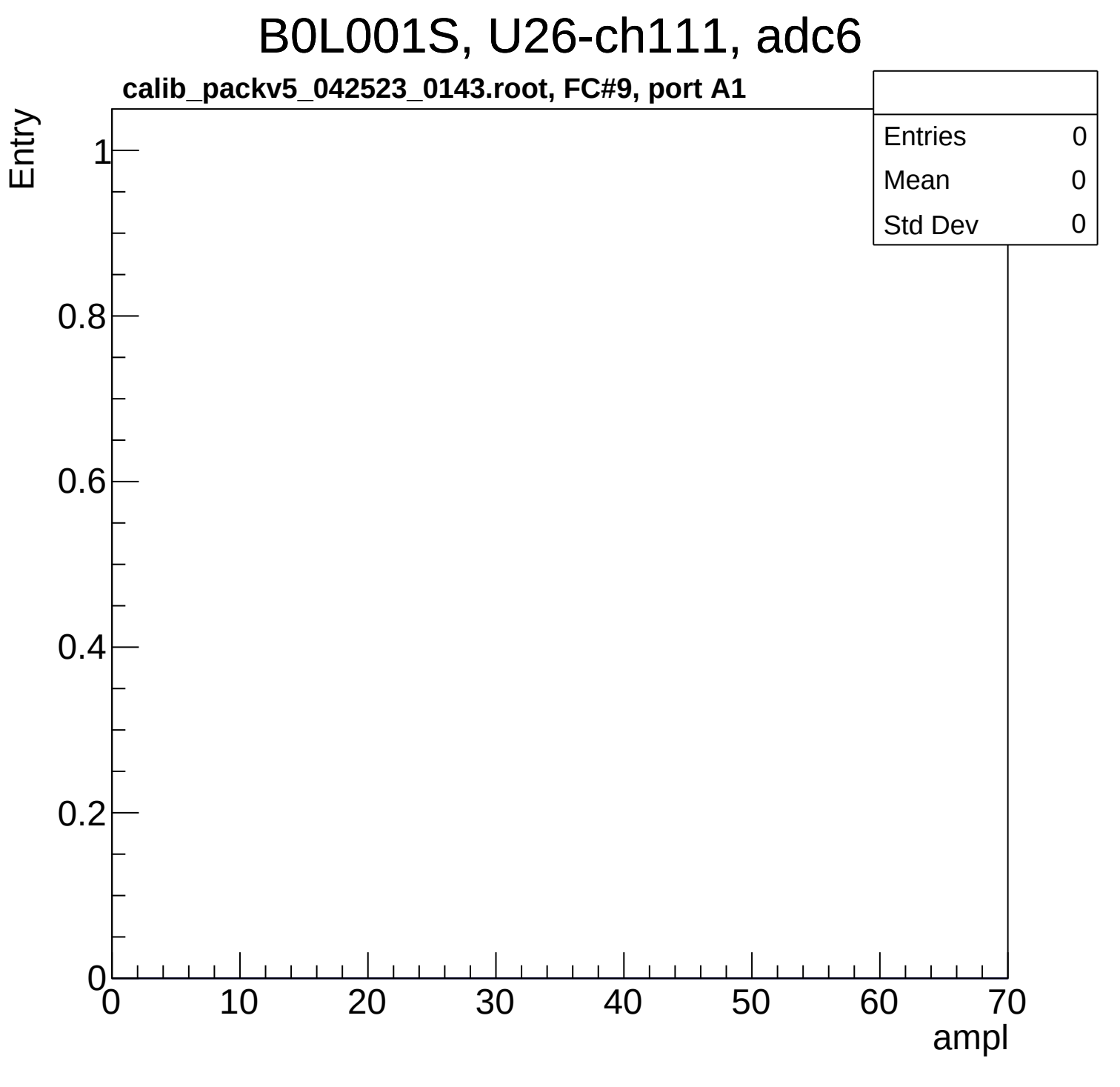
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U26-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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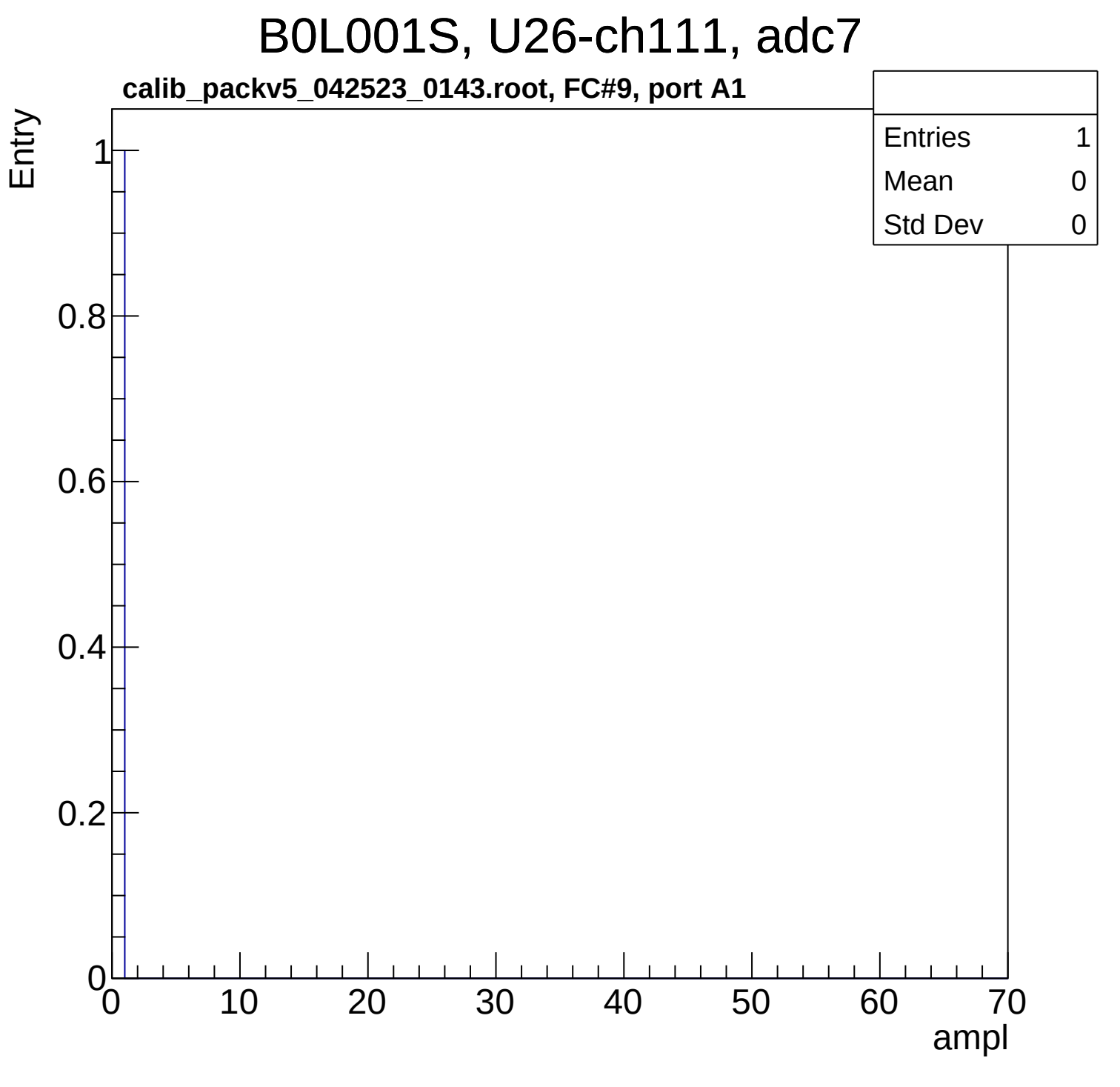
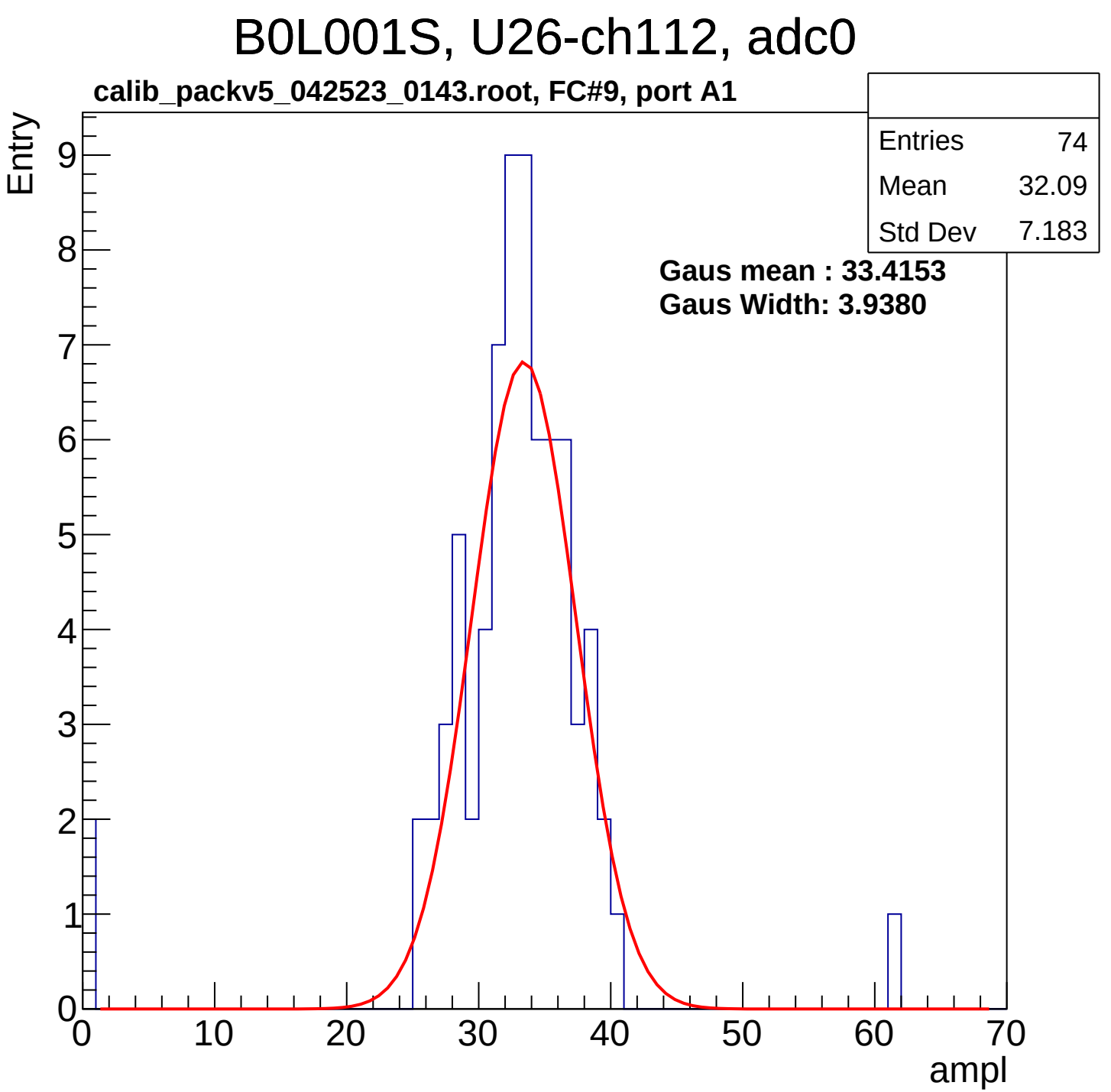
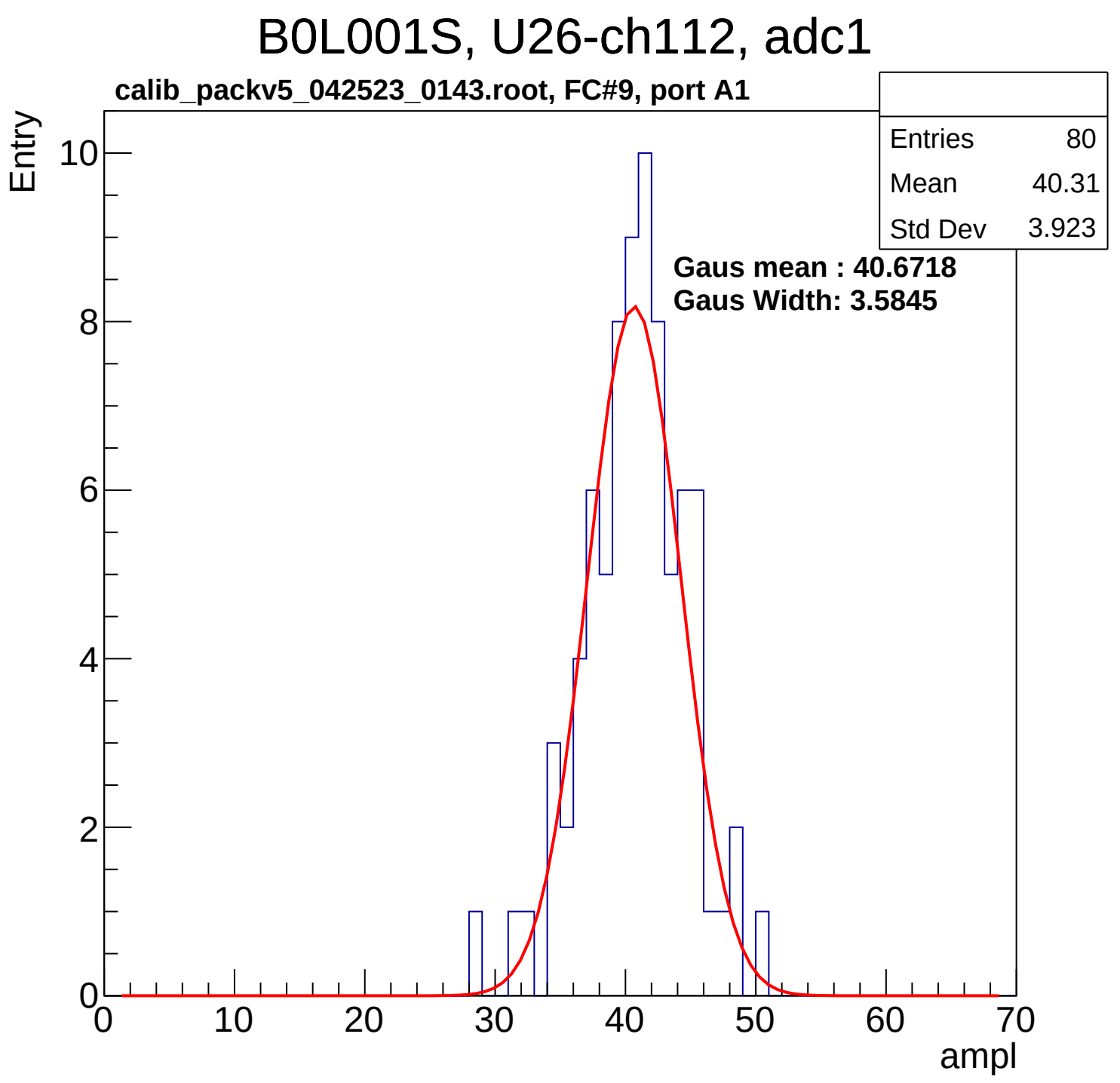
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 32.09
Std Dev 7.183
Gaus mean : 33.4153
Gaus Width: 3.9380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 40.31
Std Dev 3.923
Gaus mean : 40.6718
Gaus Width: 3.5845
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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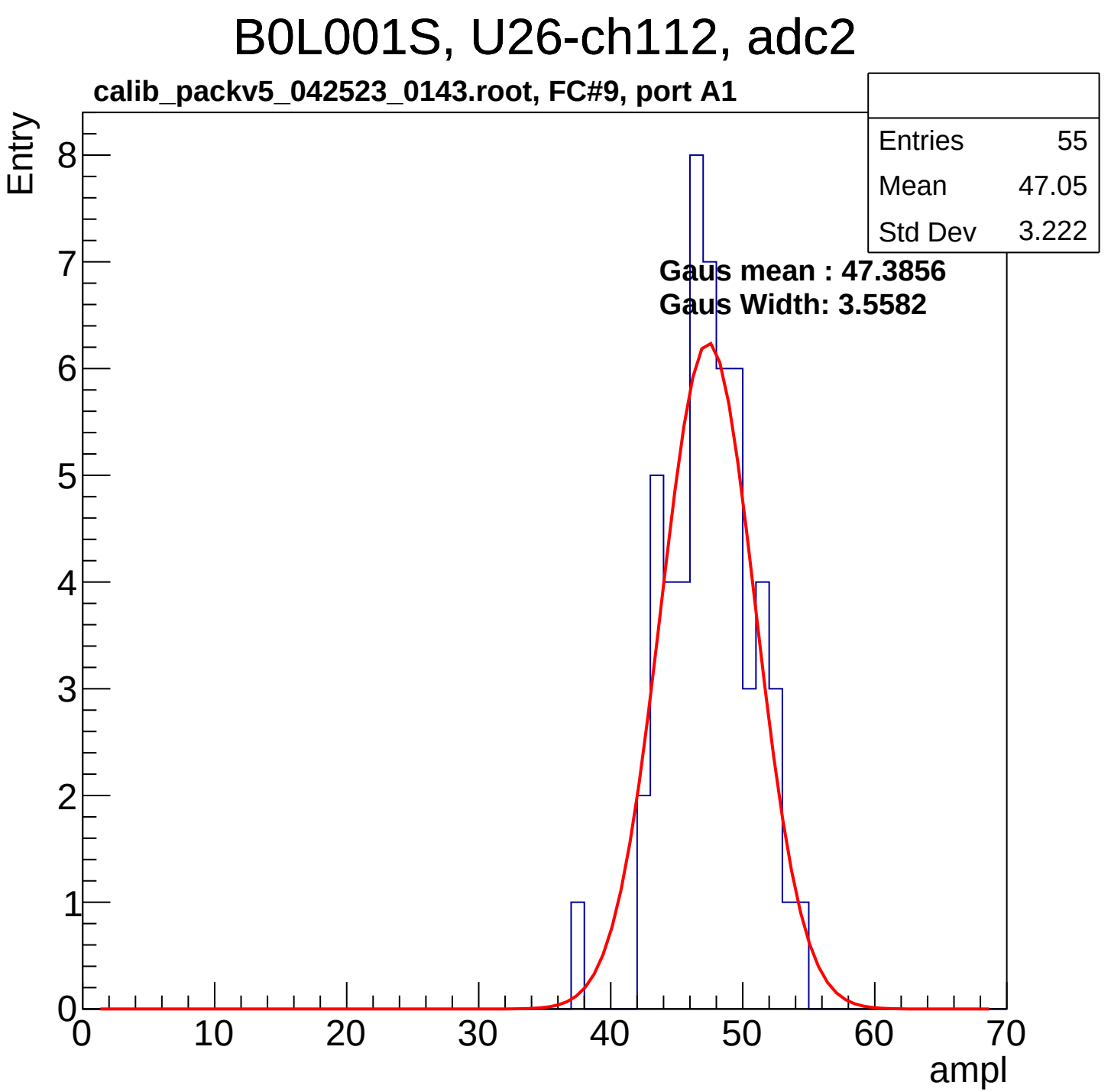
B0L001S, U26-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 47.05
Std Dev 3.222
Gaus mean : 47.3856
Gaus Width: 3.5582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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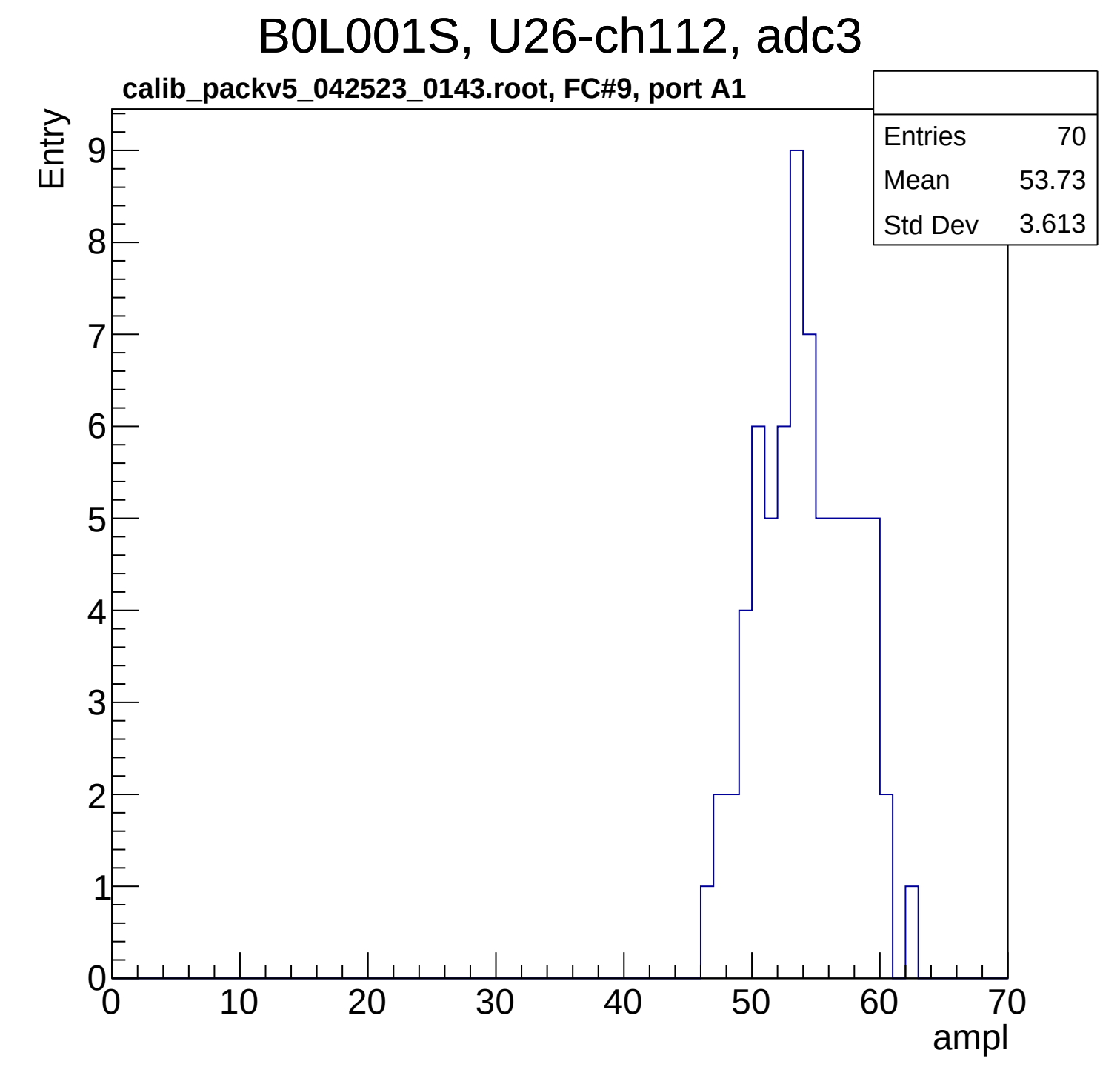
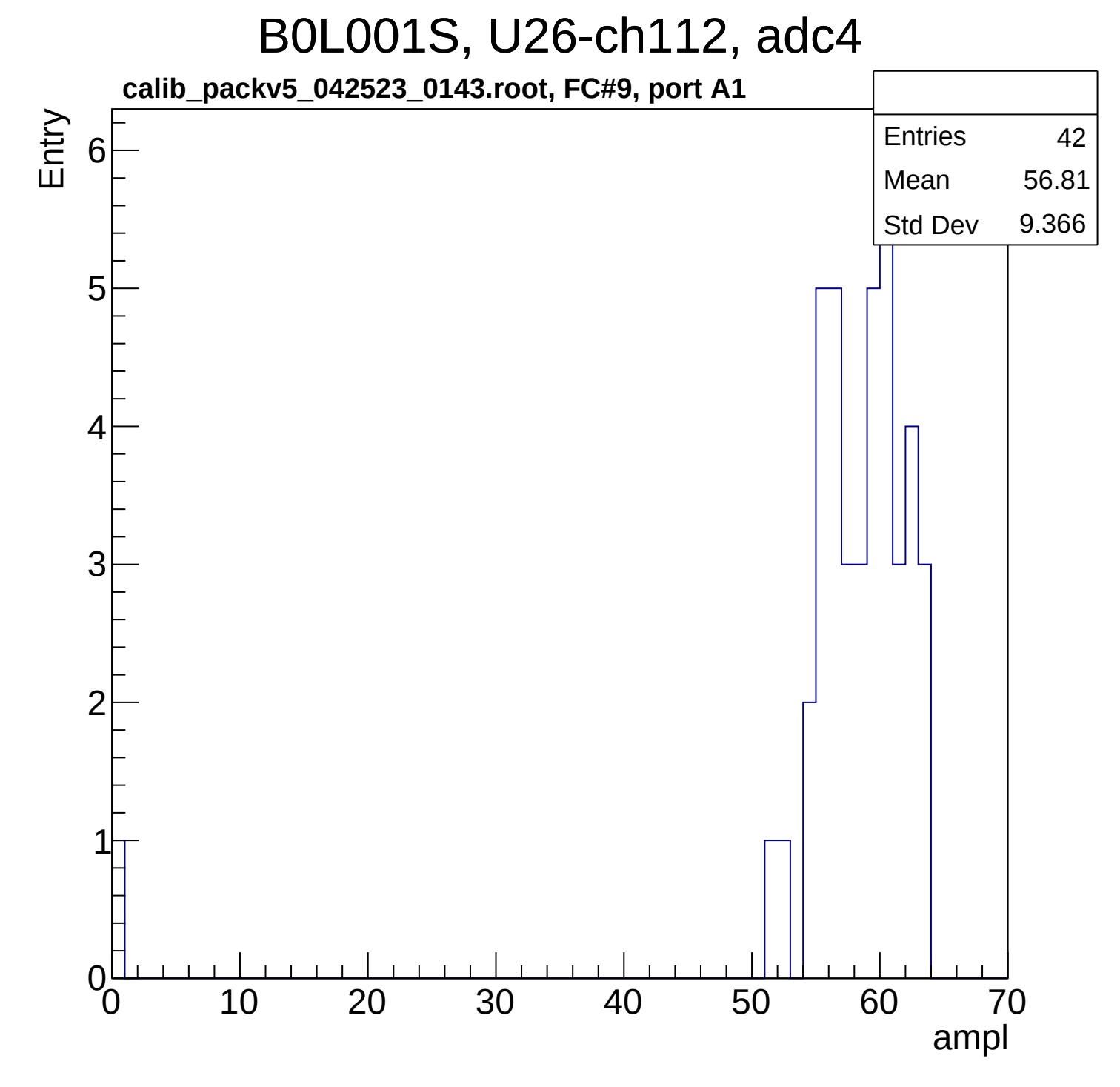
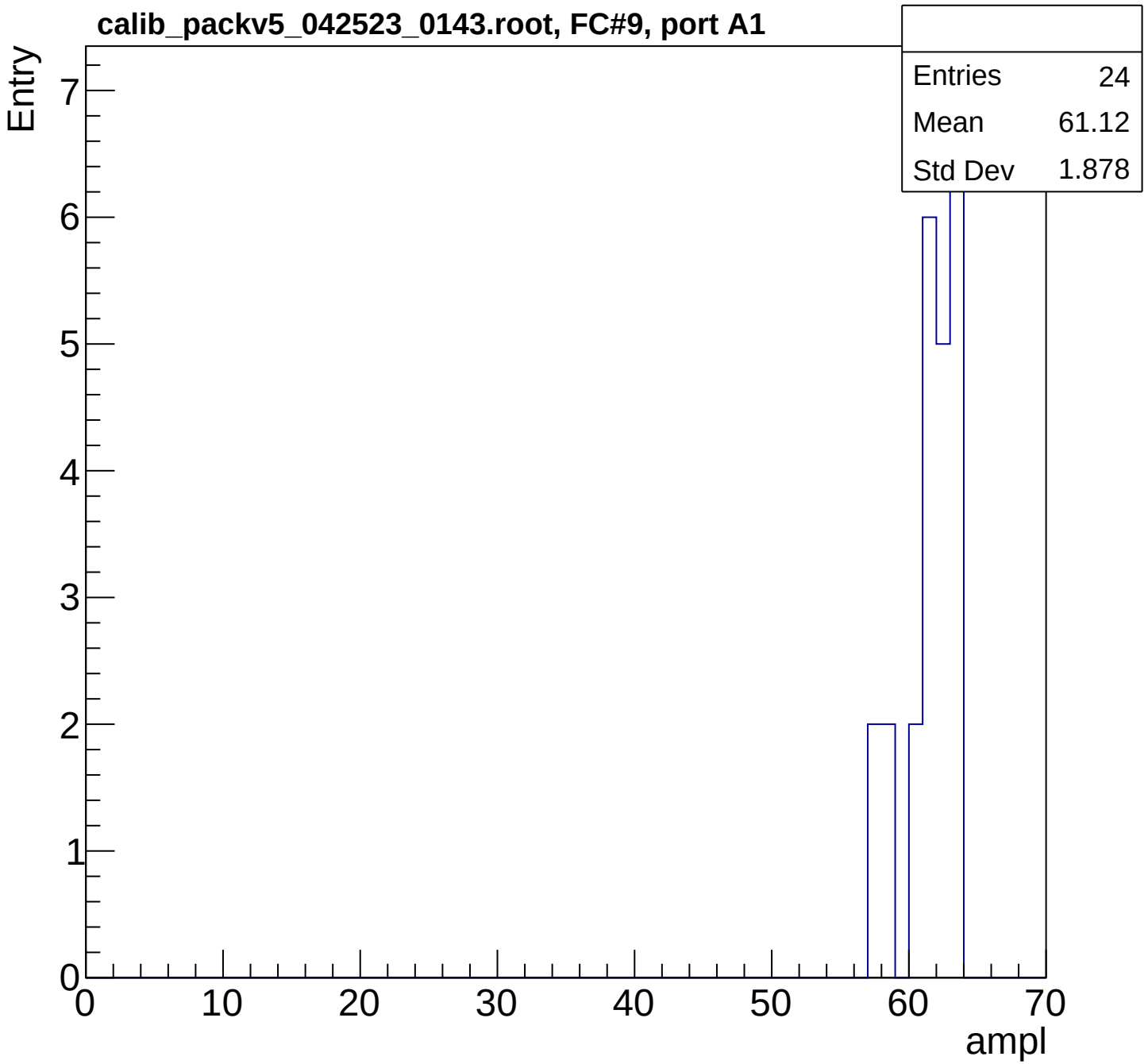

B0L001S, U26-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 53.73
Std Dev 3.613
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 56.81
Std Dev 9.366
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 61.12
Std Dev 1.878
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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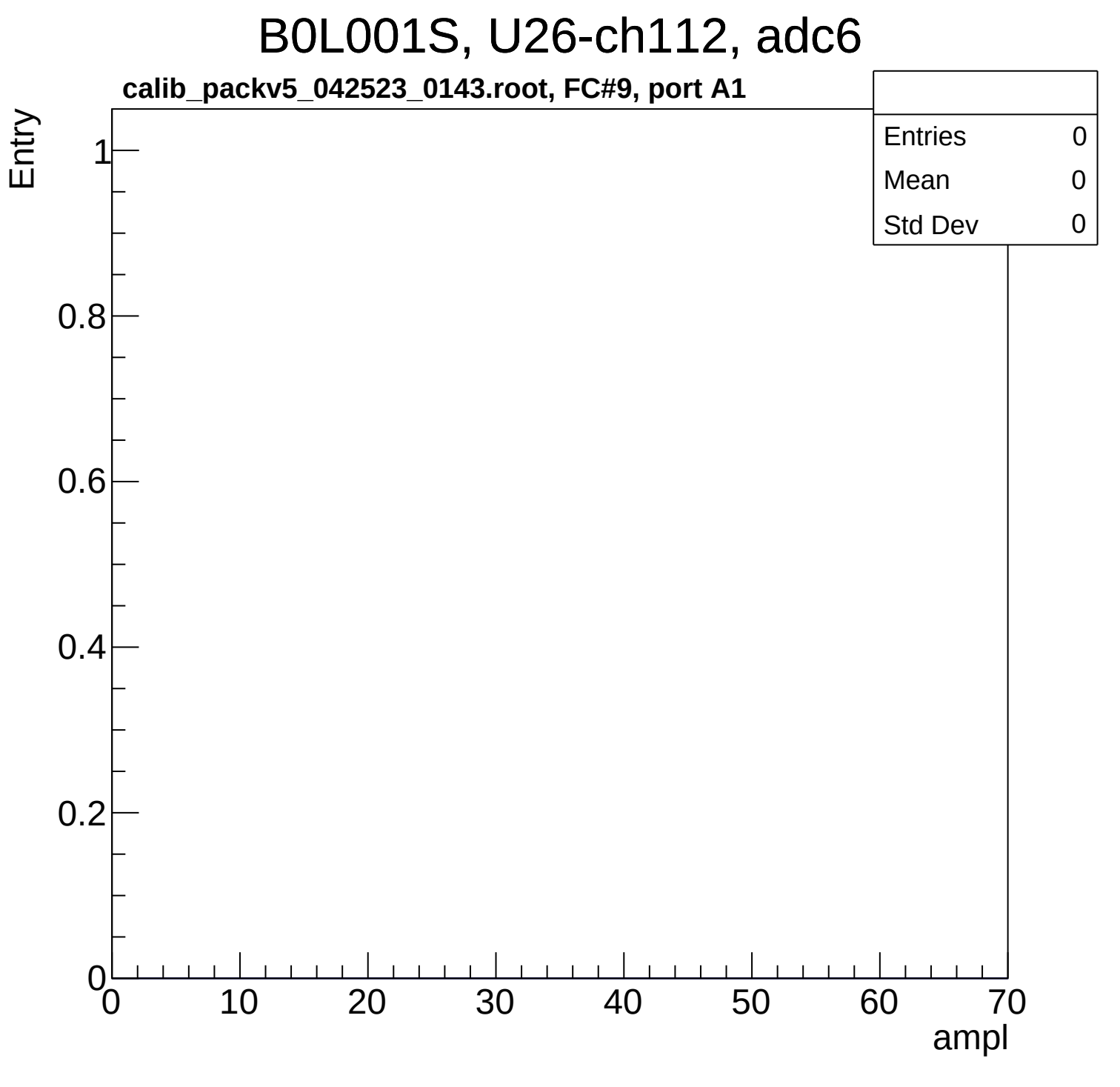
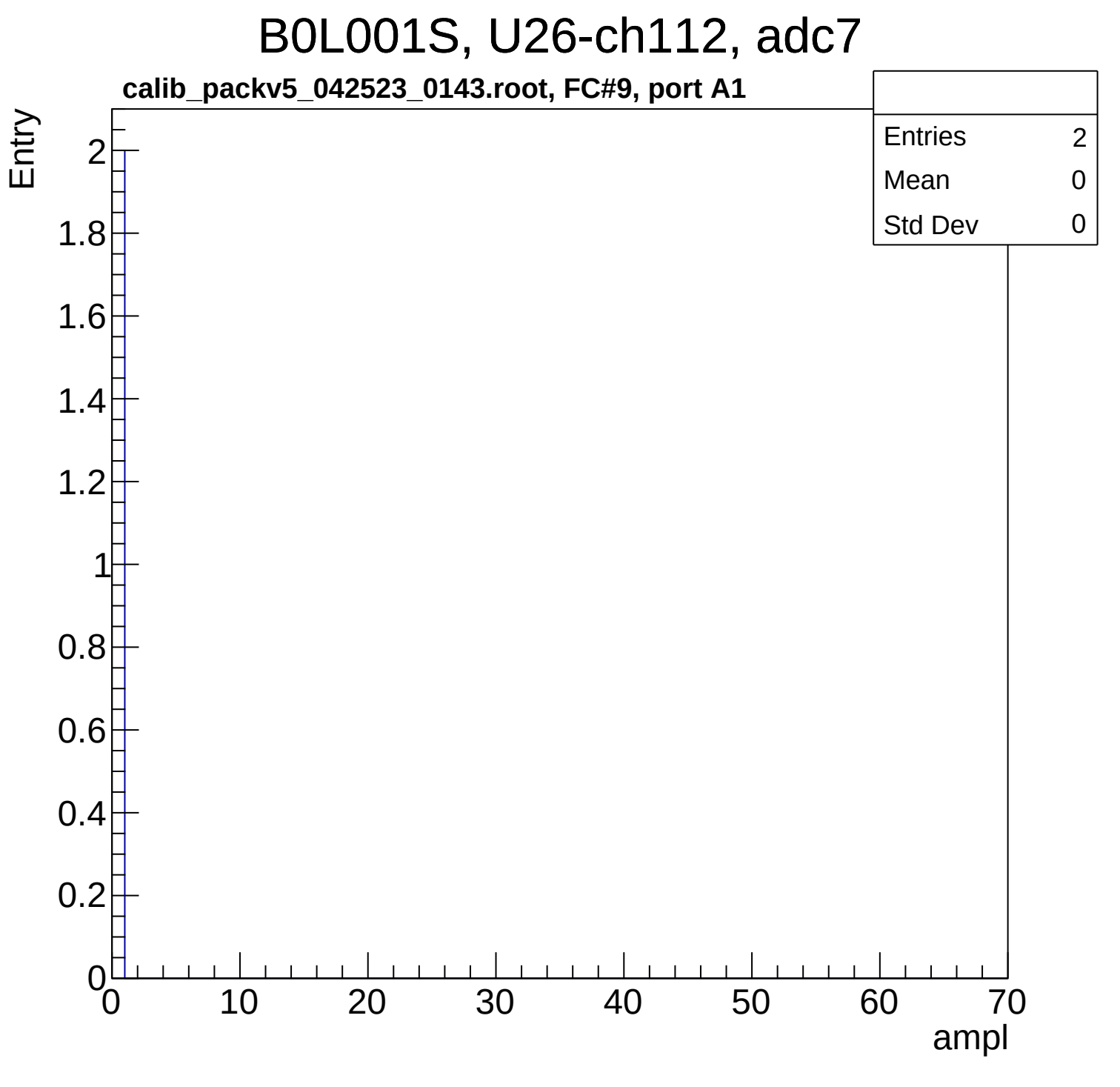
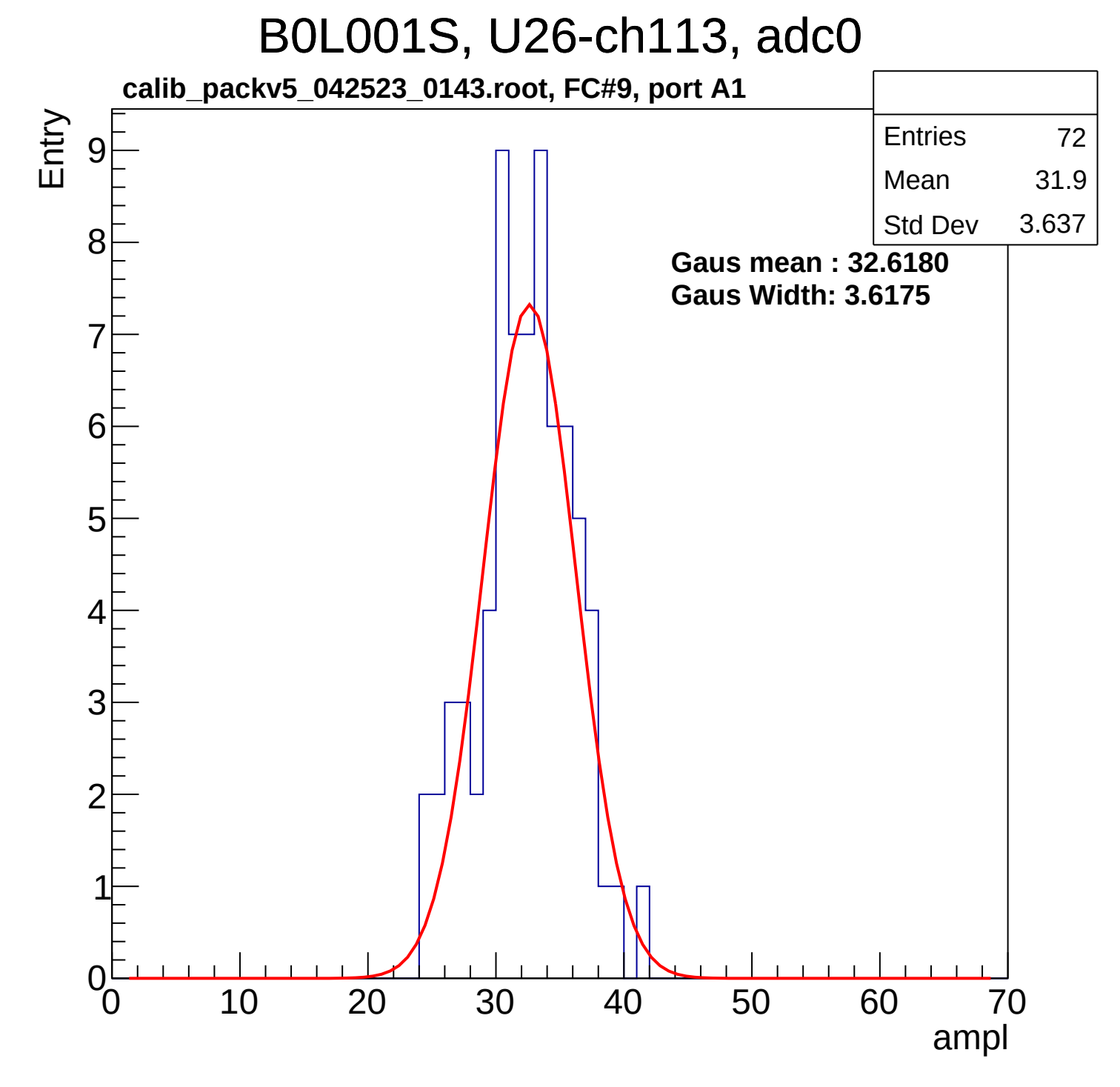

B0L001S, U26-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 31.9
Std Dev 3.637
Gaus mean : 32.6180
Gaus Width: 3.6175
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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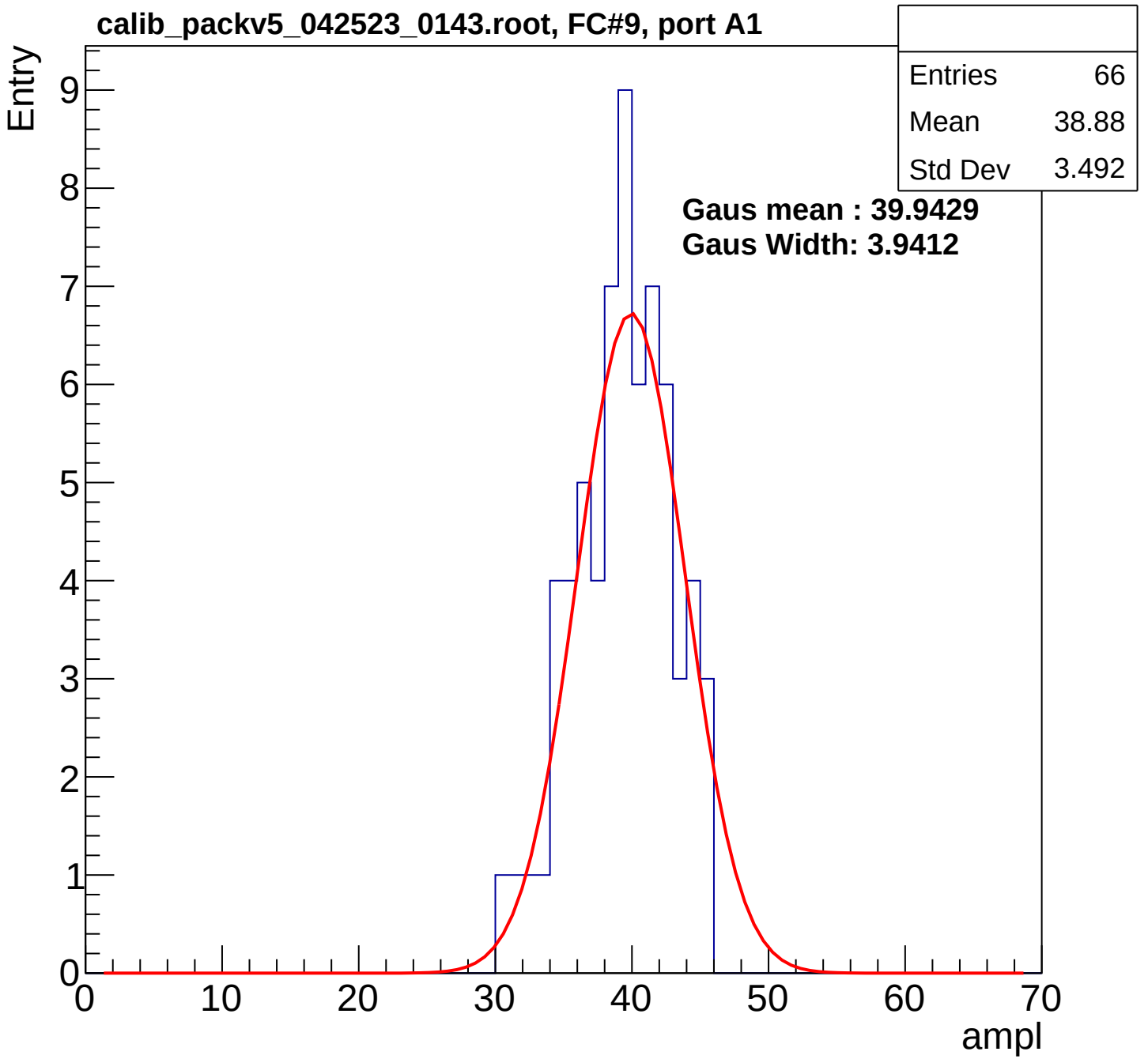
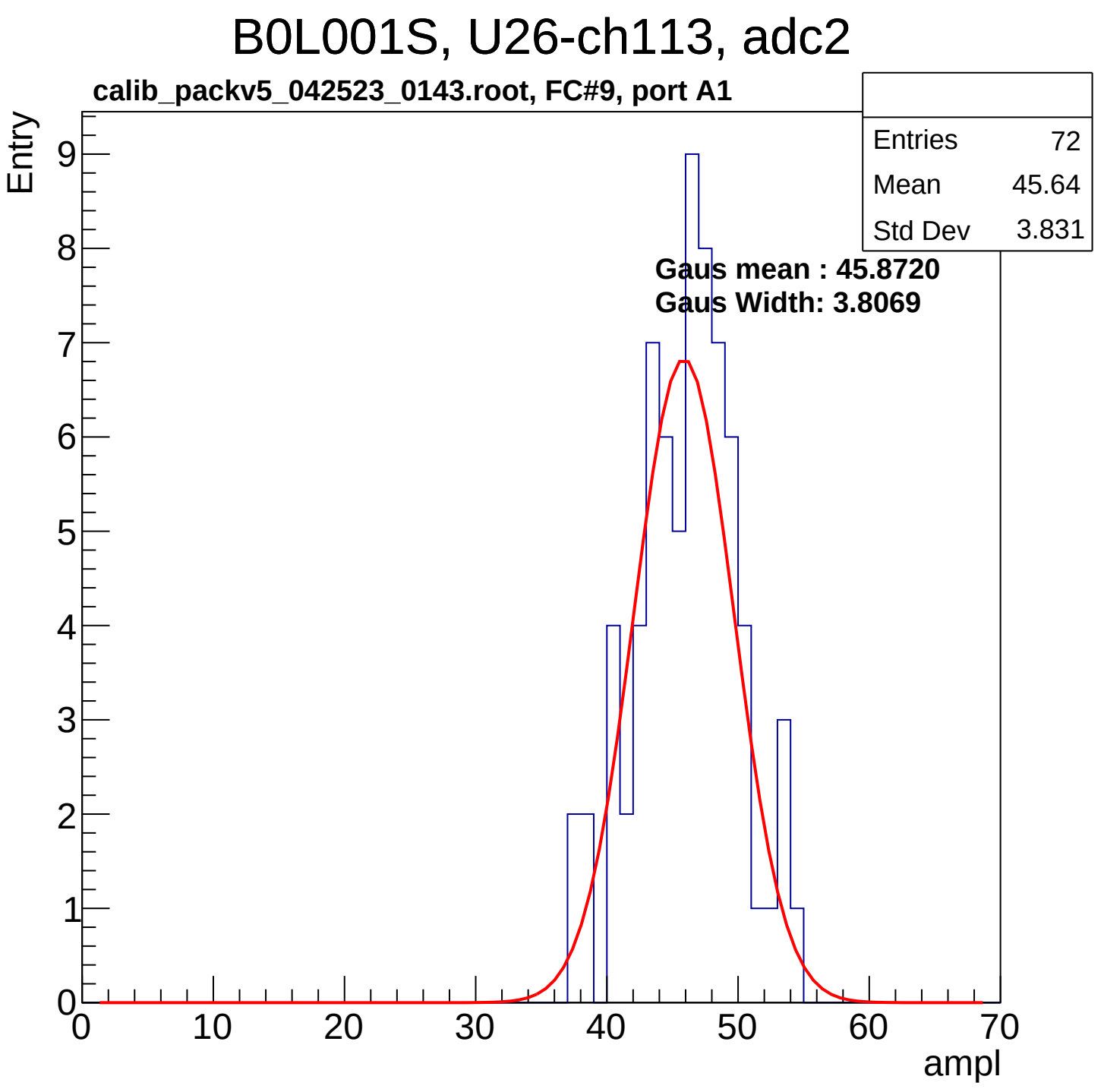
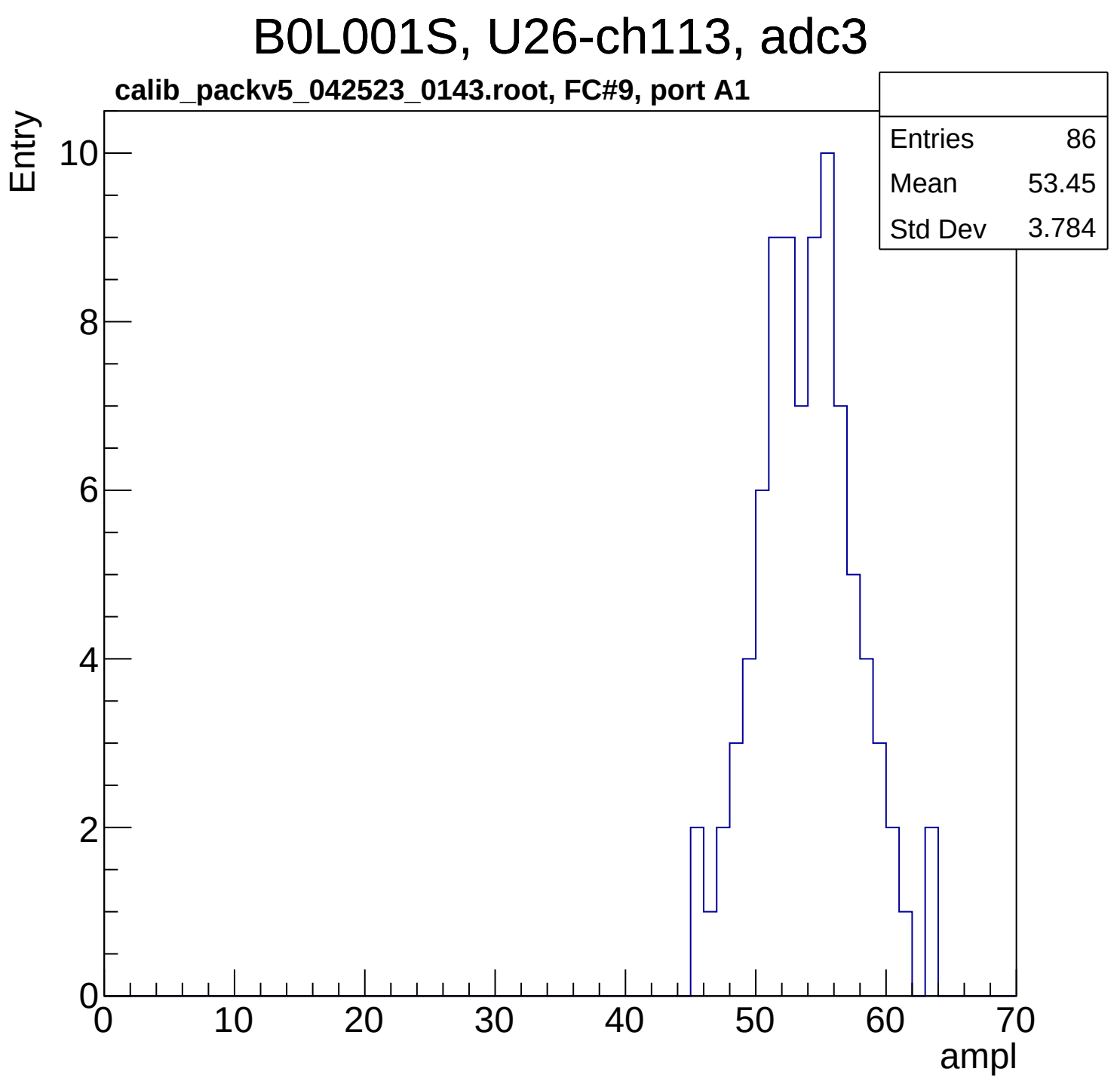
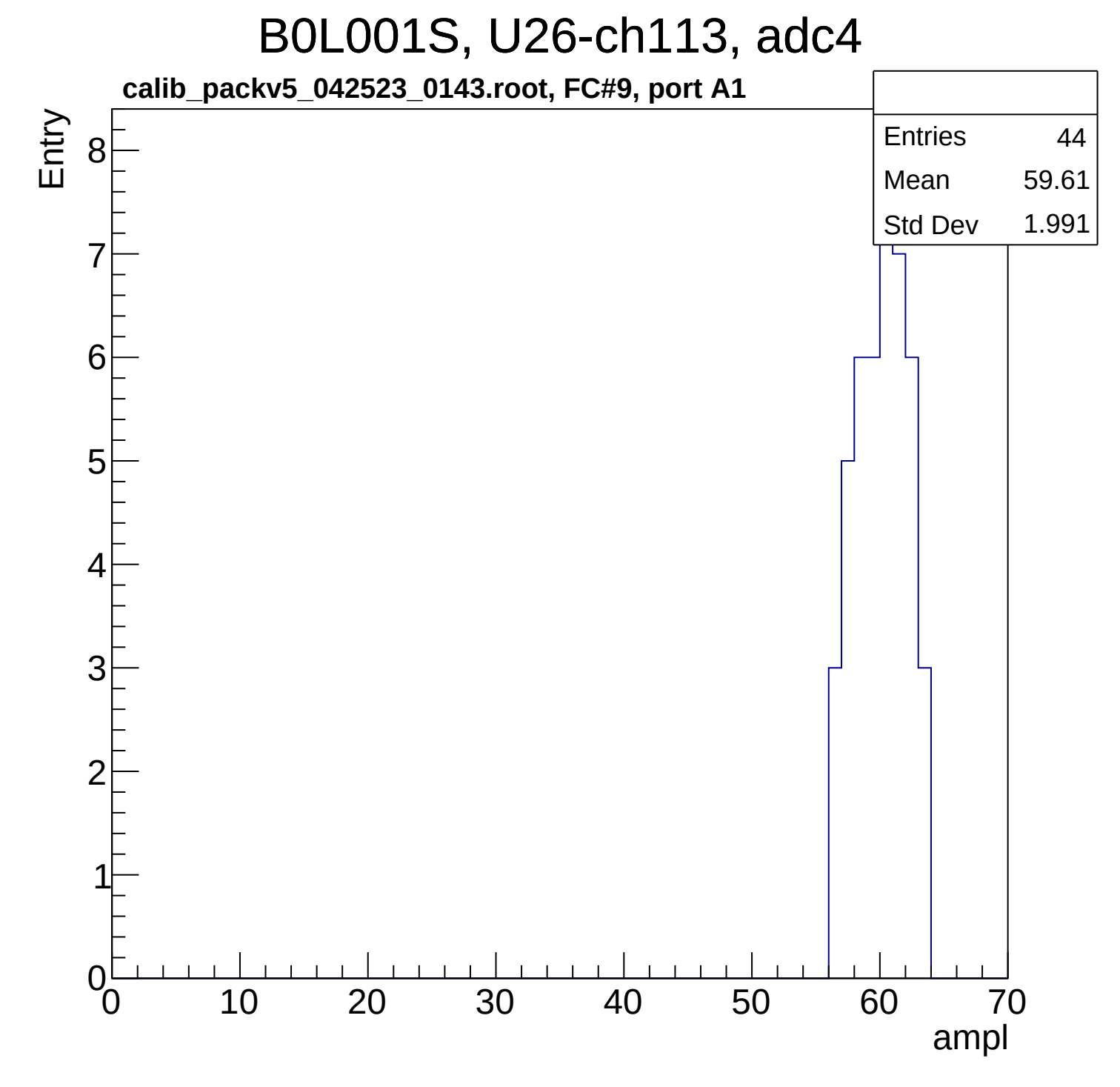

B0L001S, U26-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.88
Std Dev 3.492
Gaus mean : 39.9429
Gaus Width: 3.9412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 45.64
Std Dev 3.831
Gaus mean : 45.8720
Gaus Width: 3.8069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 53.45
Std Dev 3.784
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.61
Std Dev 1.991
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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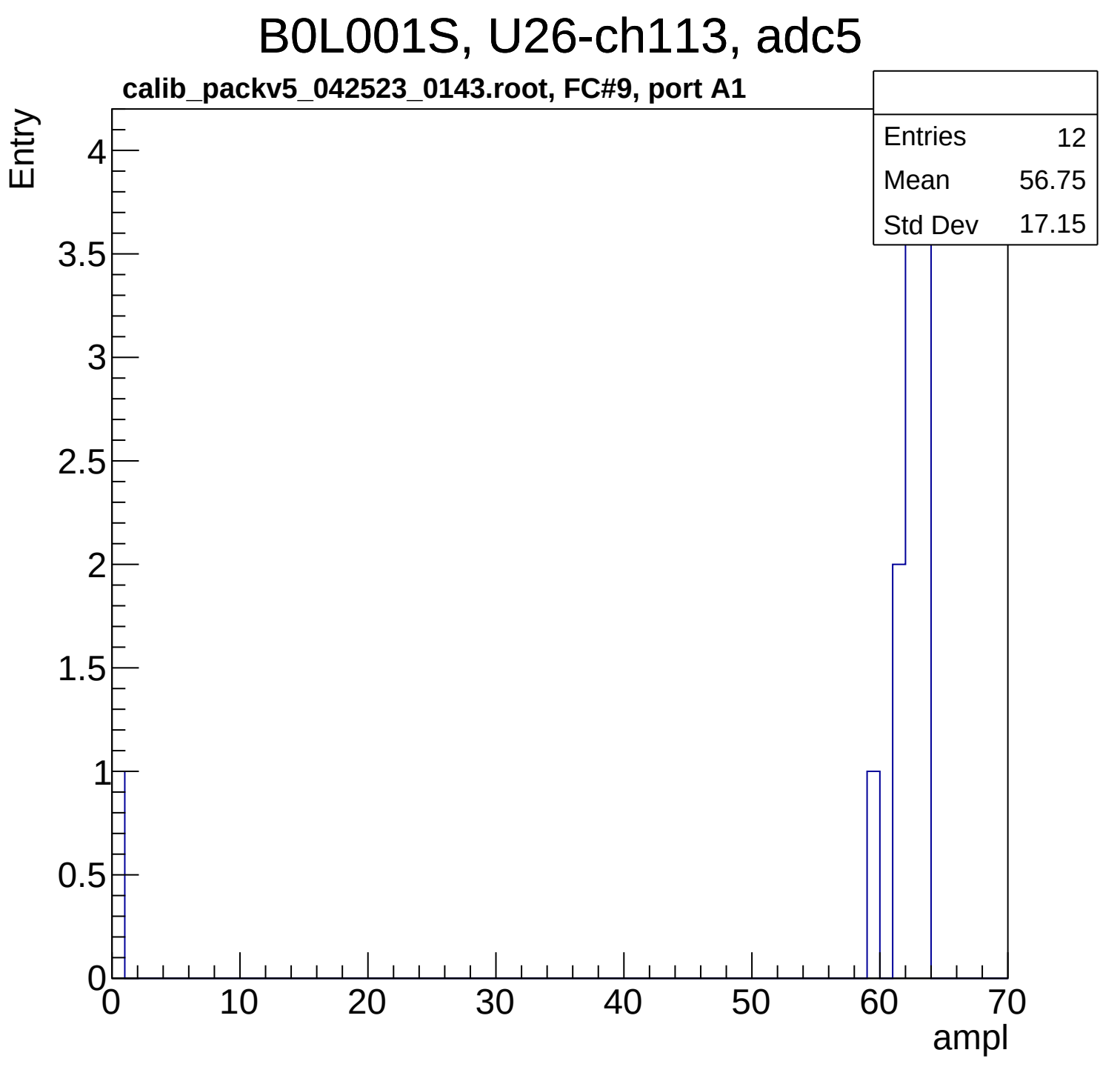
B0L001S, U26-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.75
Std Dev 17.15
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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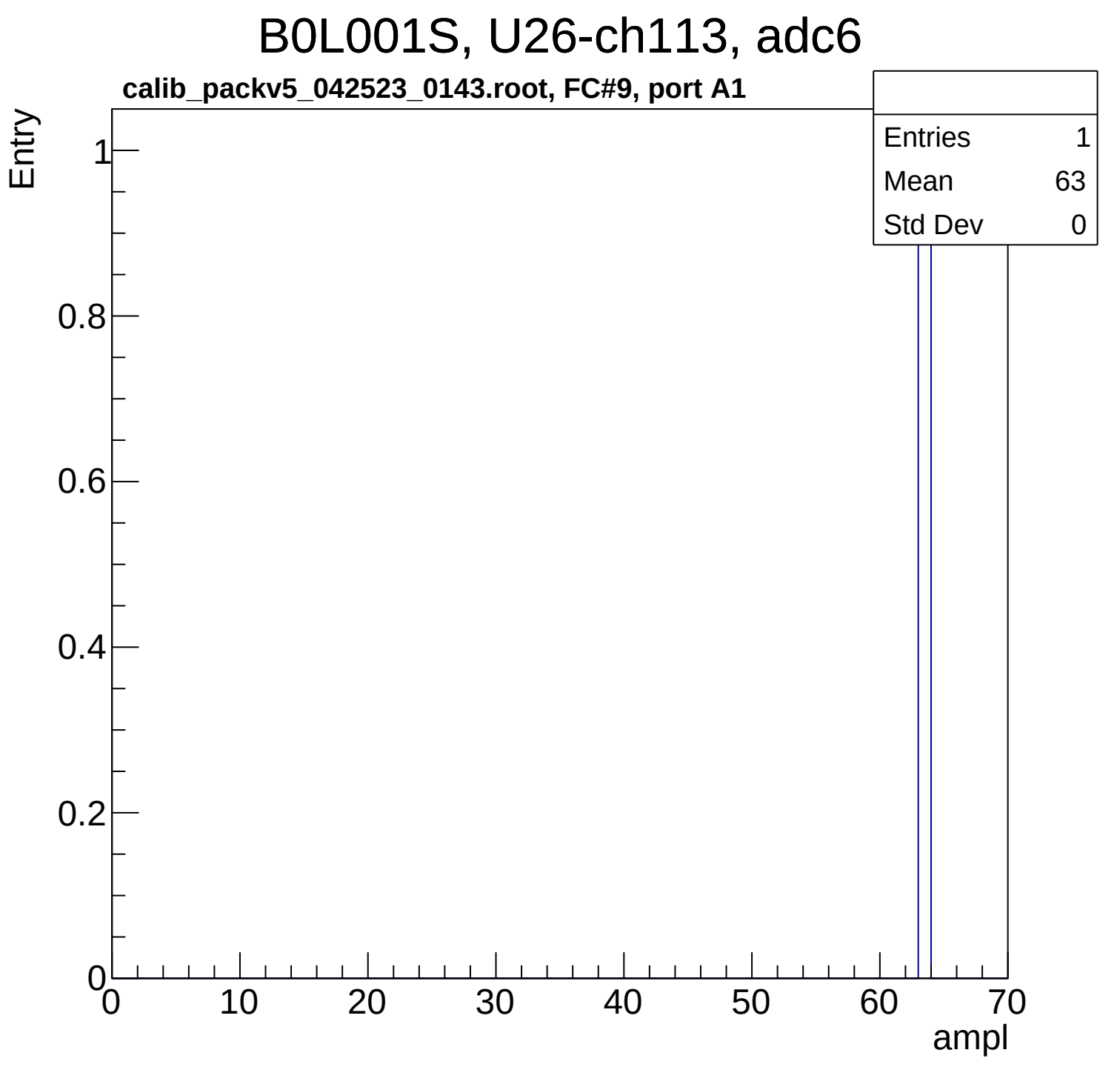
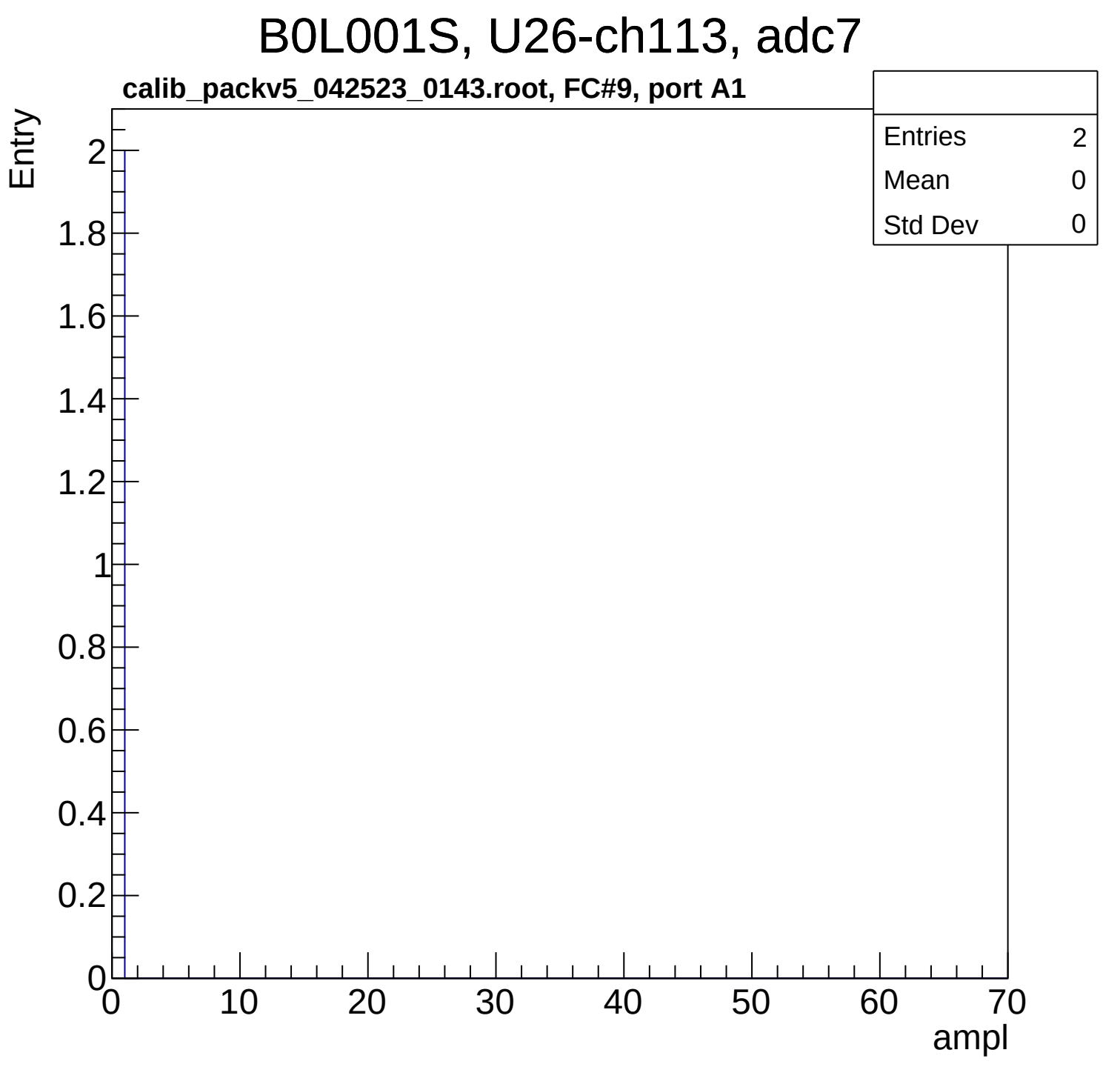
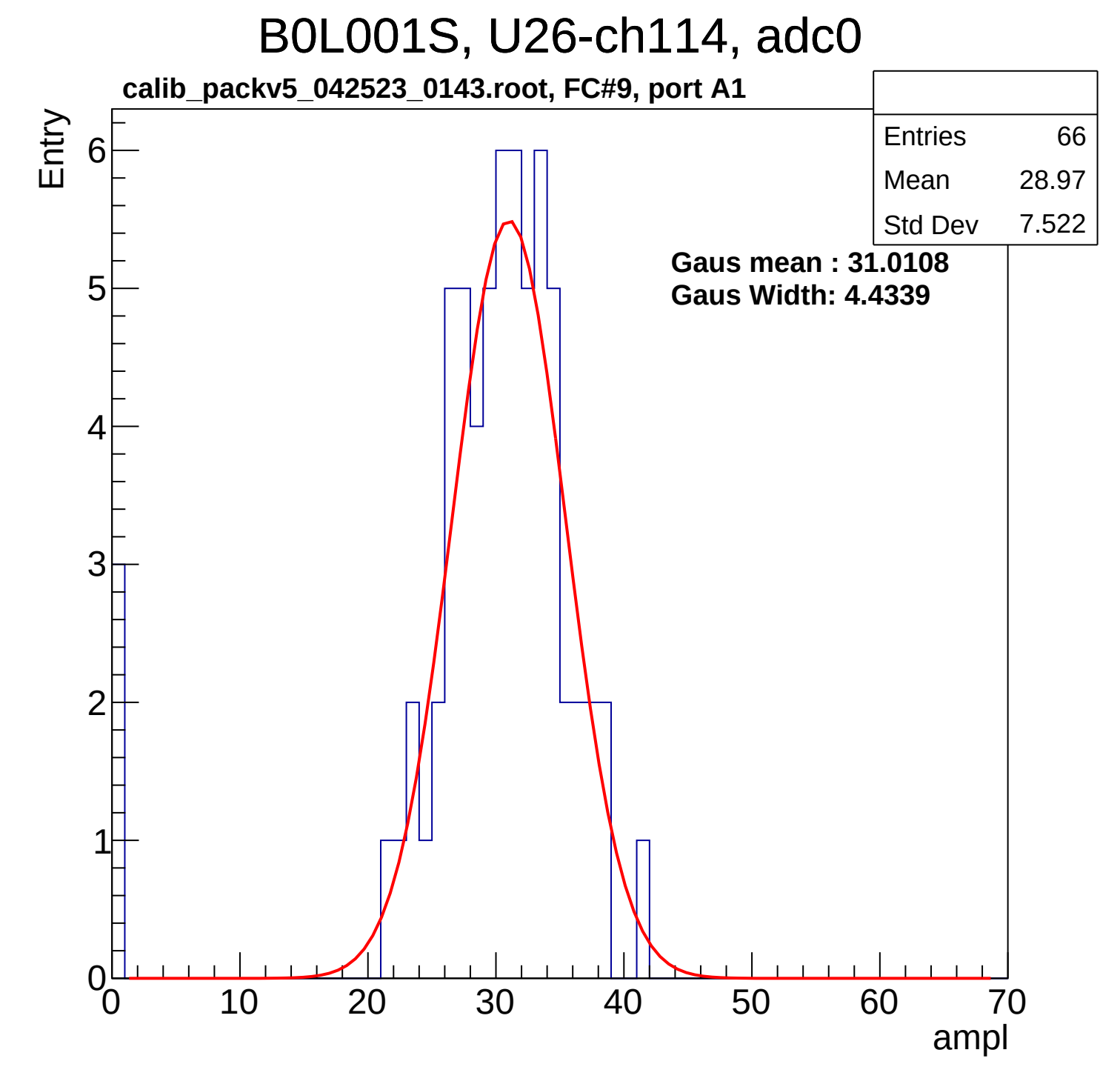
40
50
60
70


B0L001S, U26-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.97
Std Dev 7.522
Gaus mean : 31.0108
Gaus Width: 4.4339
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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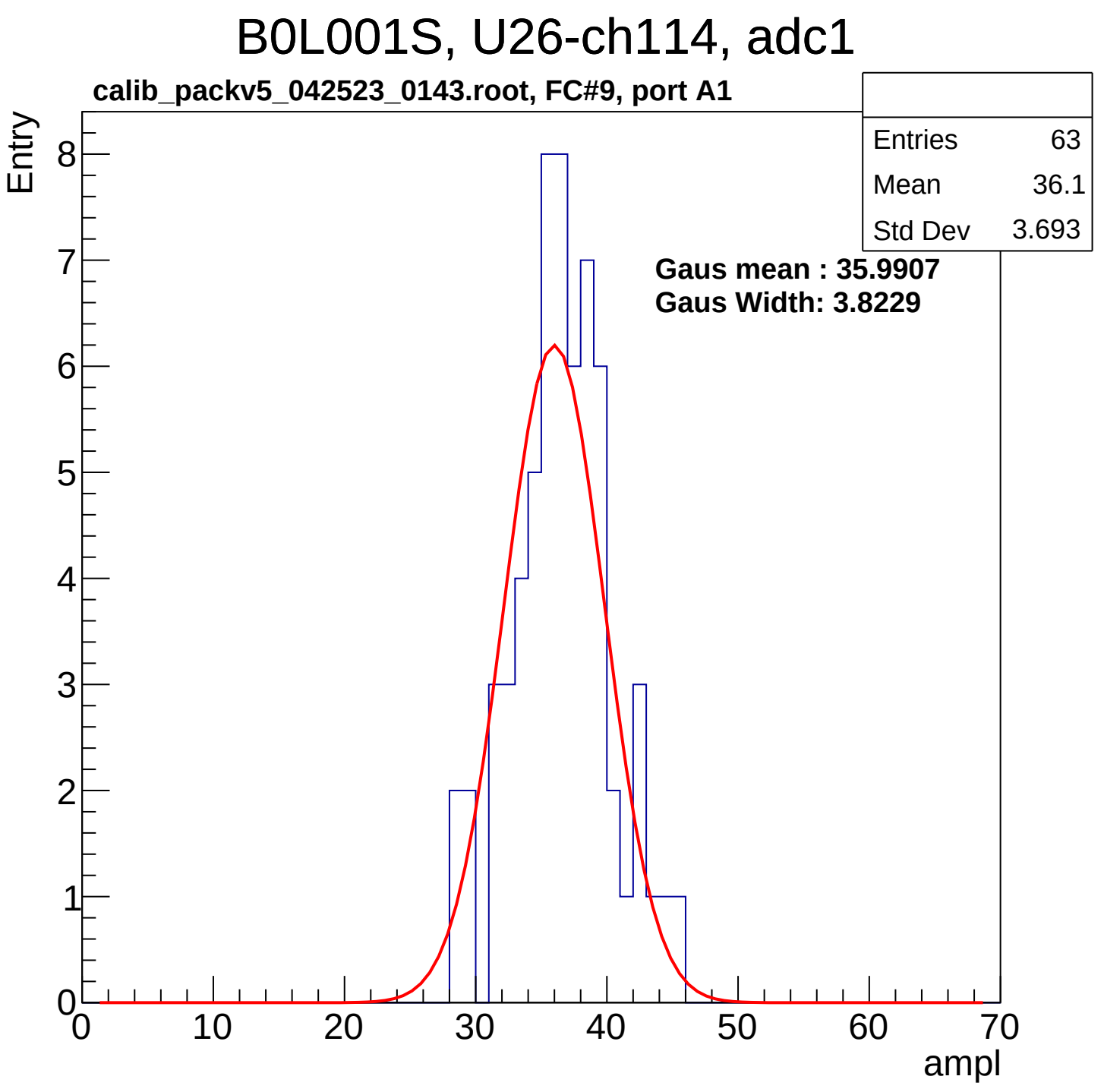
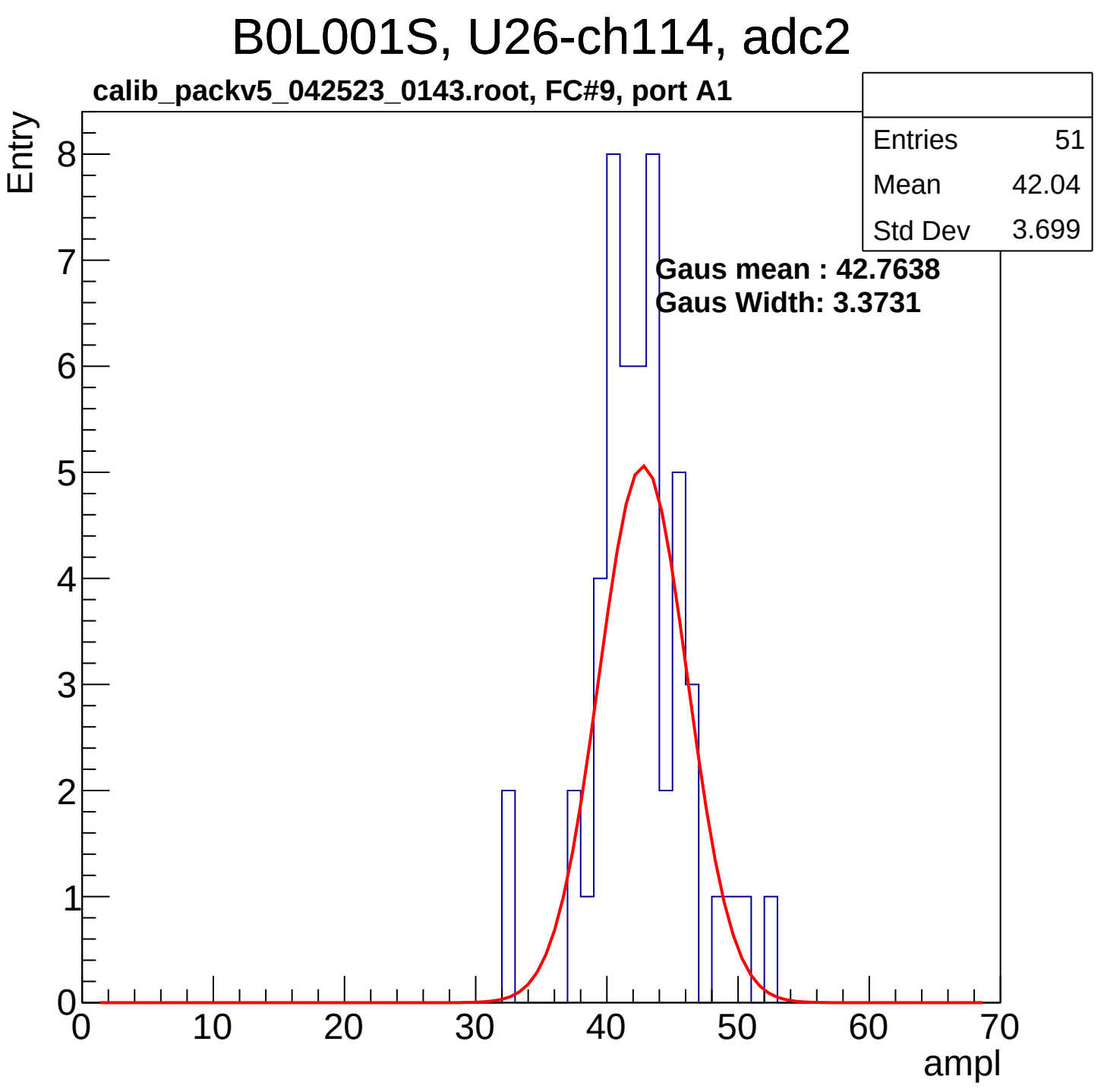

B0L001S, U26-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.1
Std Dev 3.693
Gaus mean : 35.9907
Gaus Width: 3.8229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 42.04
Std Dev 3.699
Gaus mean : 42.7638
Gaus Width: 3.3731
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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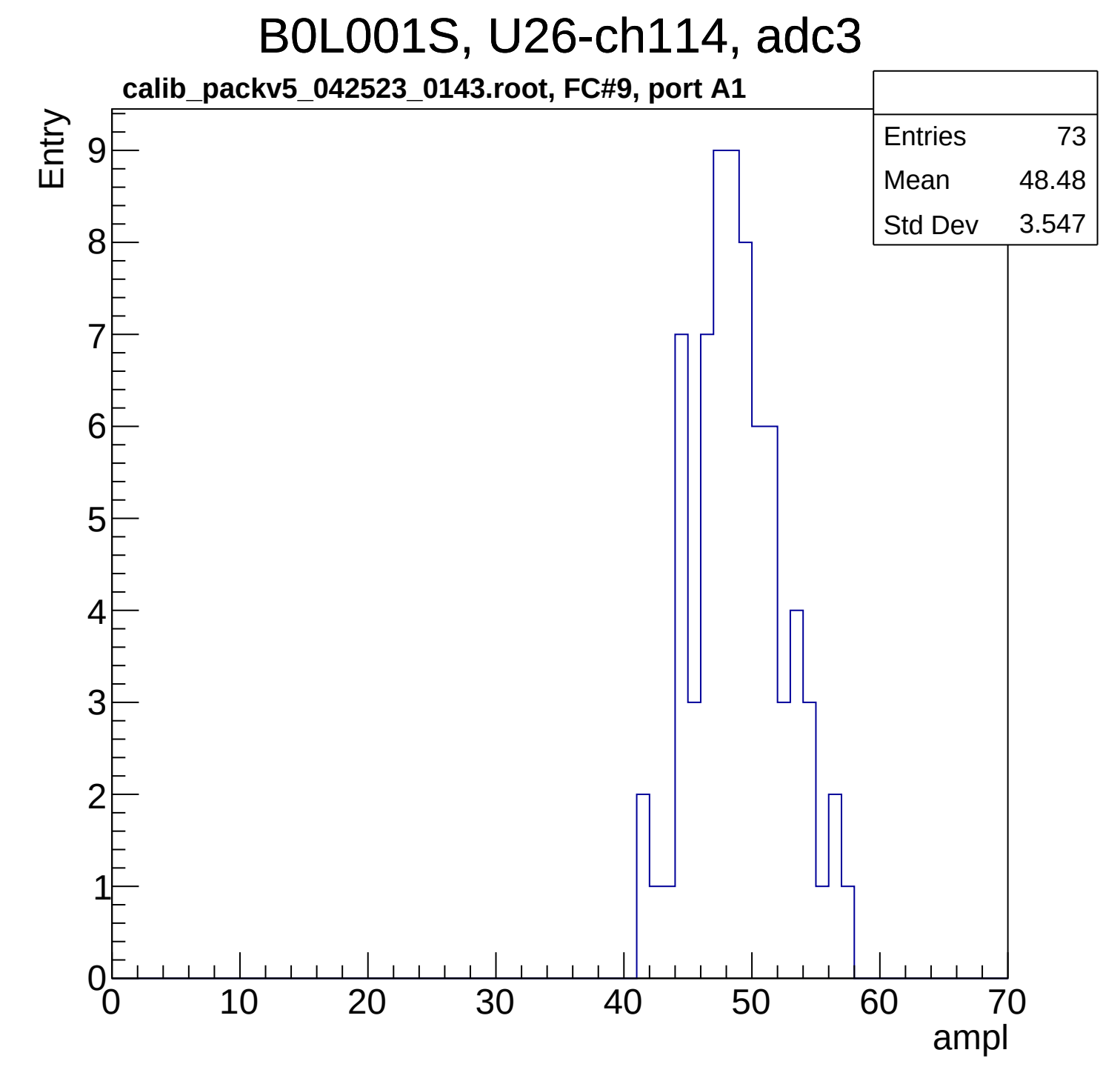
B0L001S, U26-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 48.48
Std Dev 3.547
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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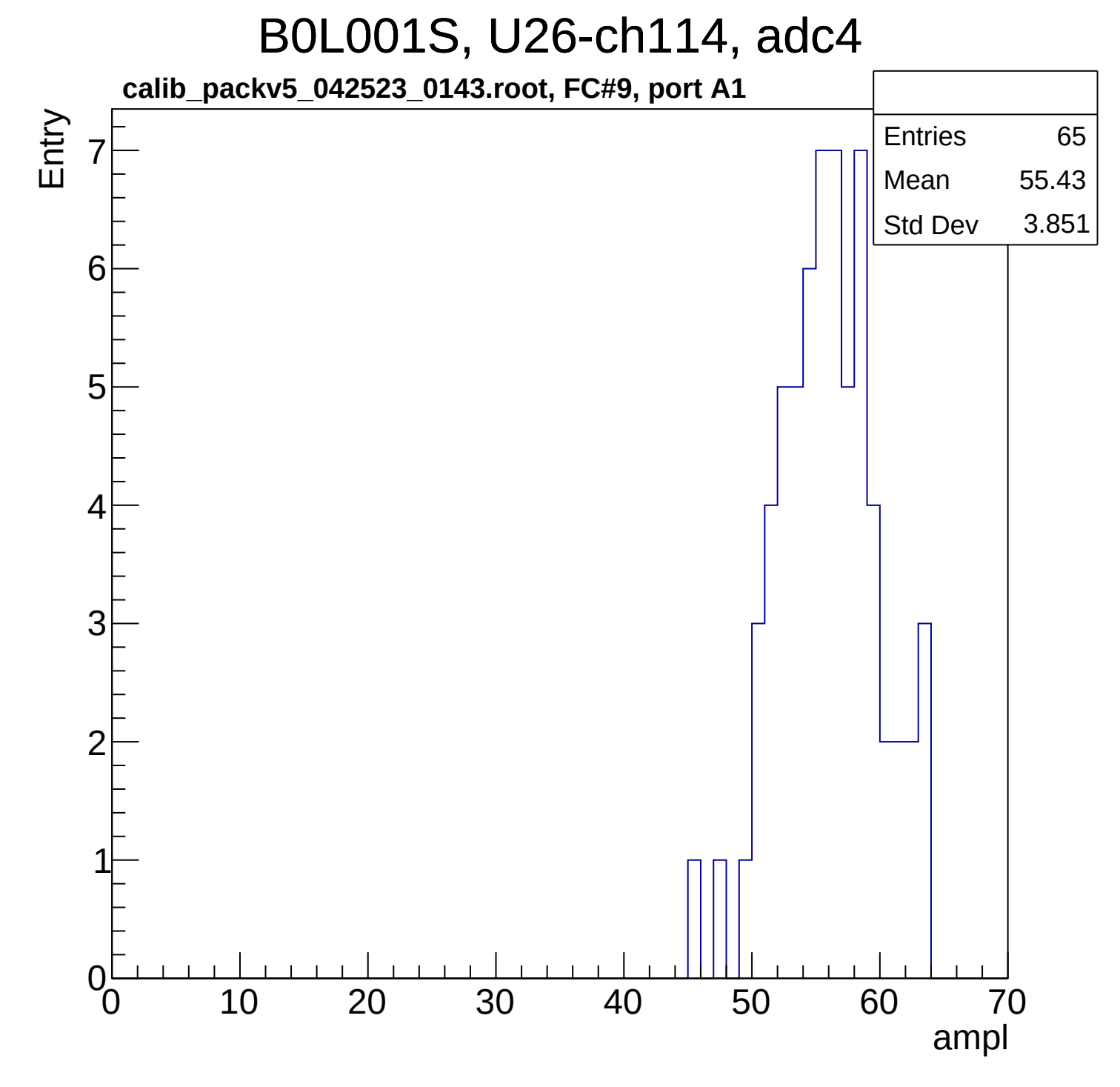
B0L001S, U26-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.43
Std Dev 3.851
Entry
ampl
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1
2
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0
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60
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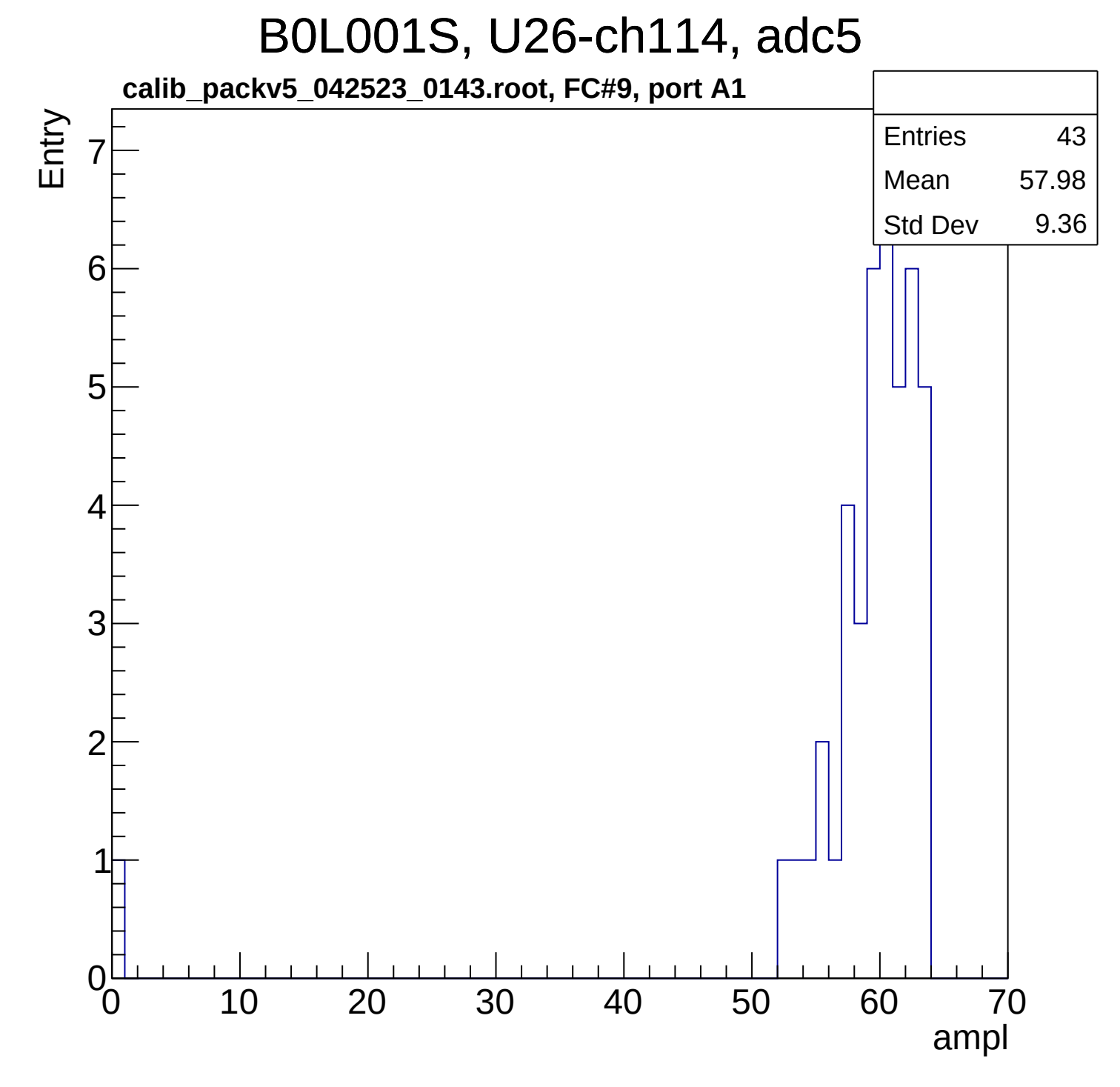
B0L001S, U26-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.98
Std Dev 9.36
Entry
ampl
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1
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3
4
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0
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60
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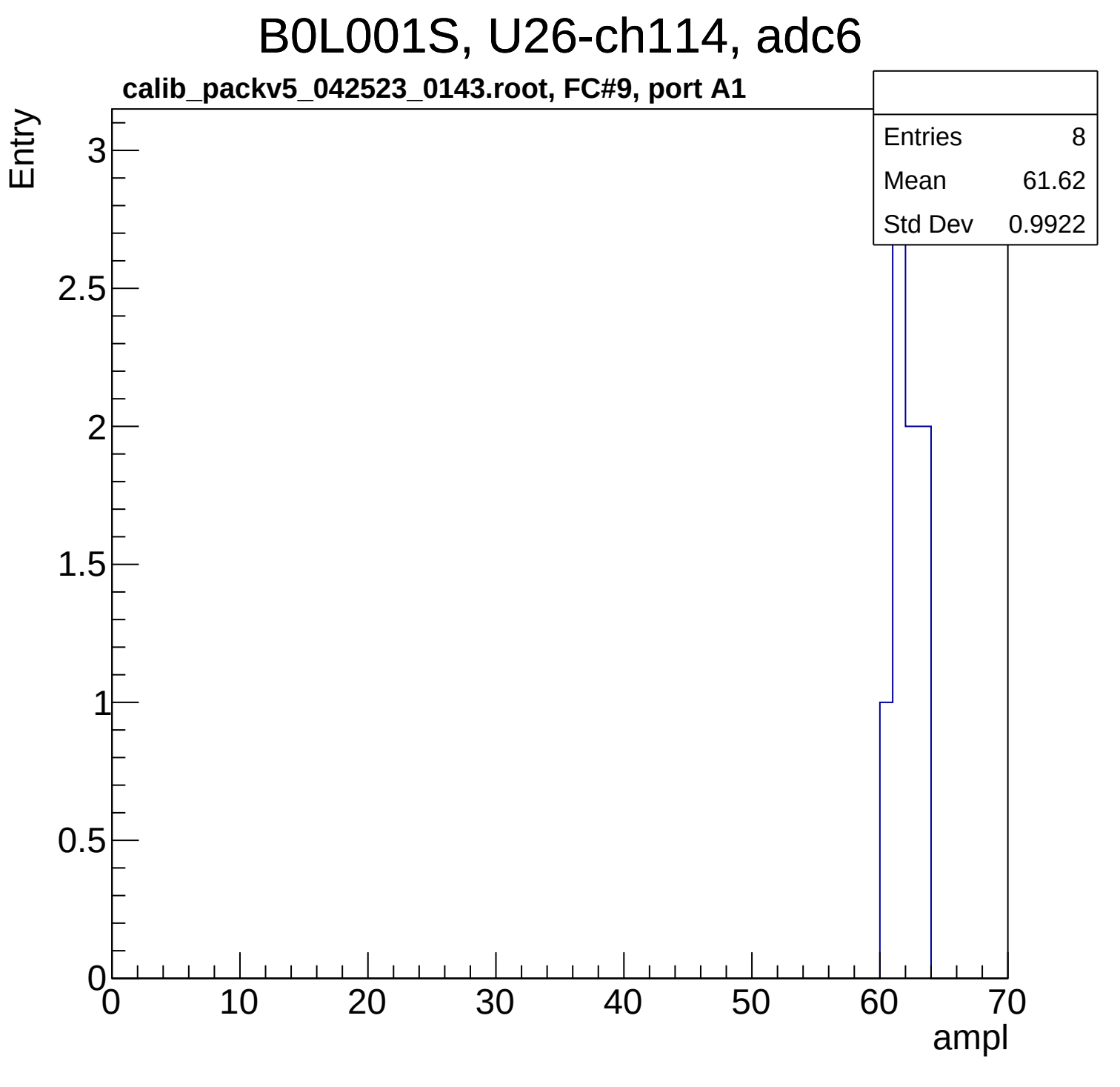
B0L001S, U26-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.62
Std Dev 0.9922
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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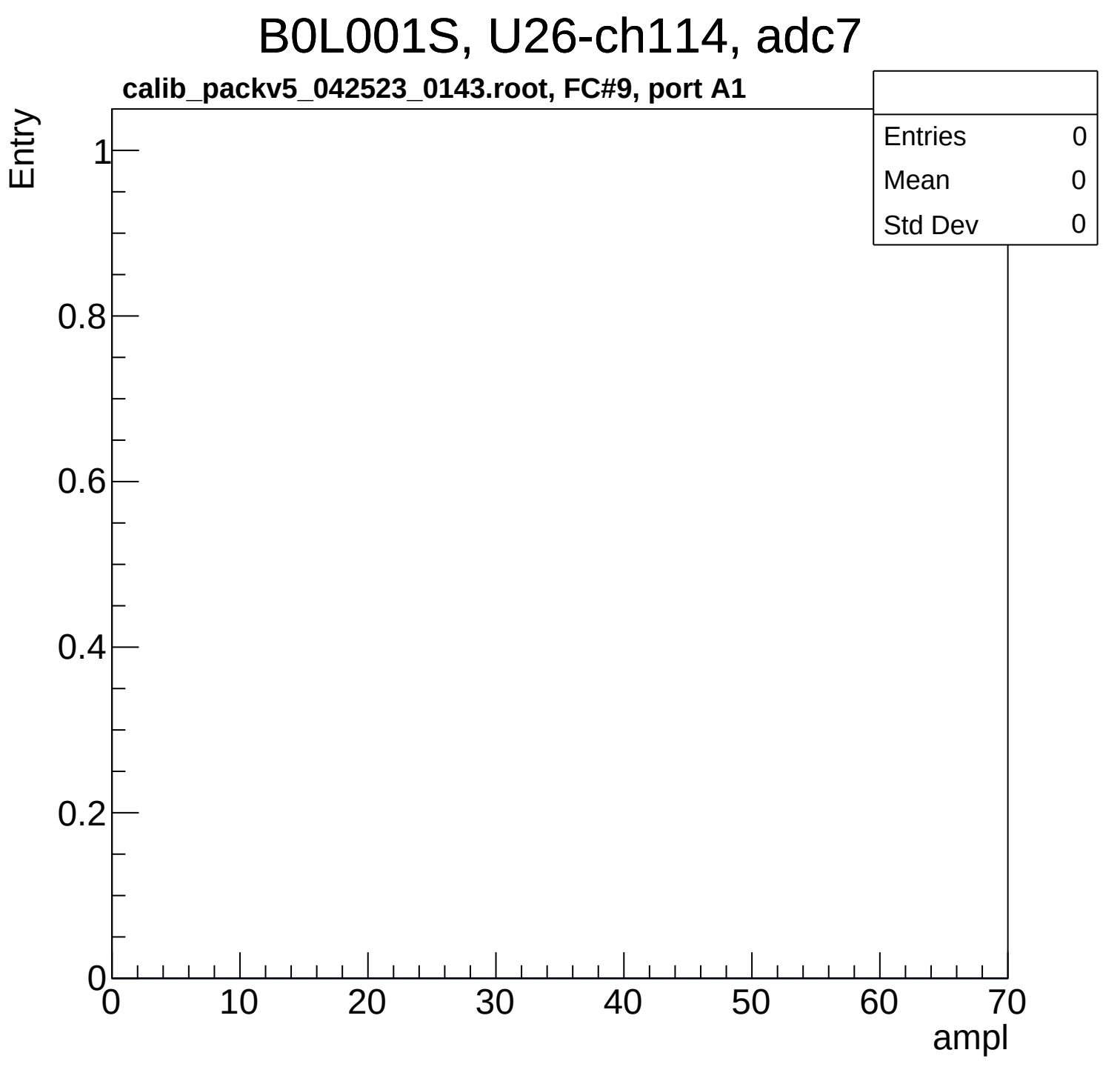
40
50
60
70


B0L001S, U26-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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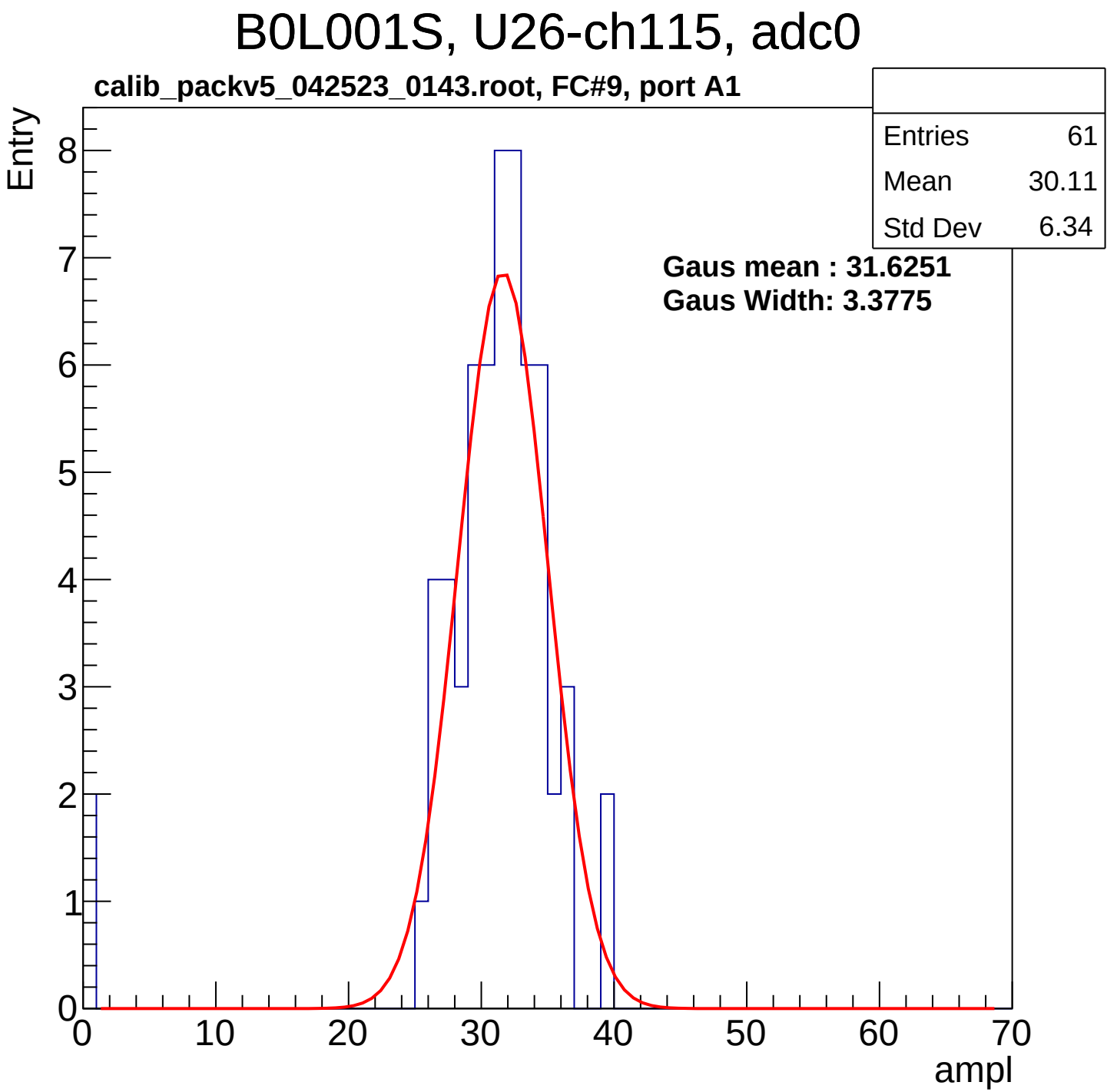
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.11
Std Dev 6.34
Gaus mean : 31.6251
Gaus Width: 3.3775
Entry
ampl
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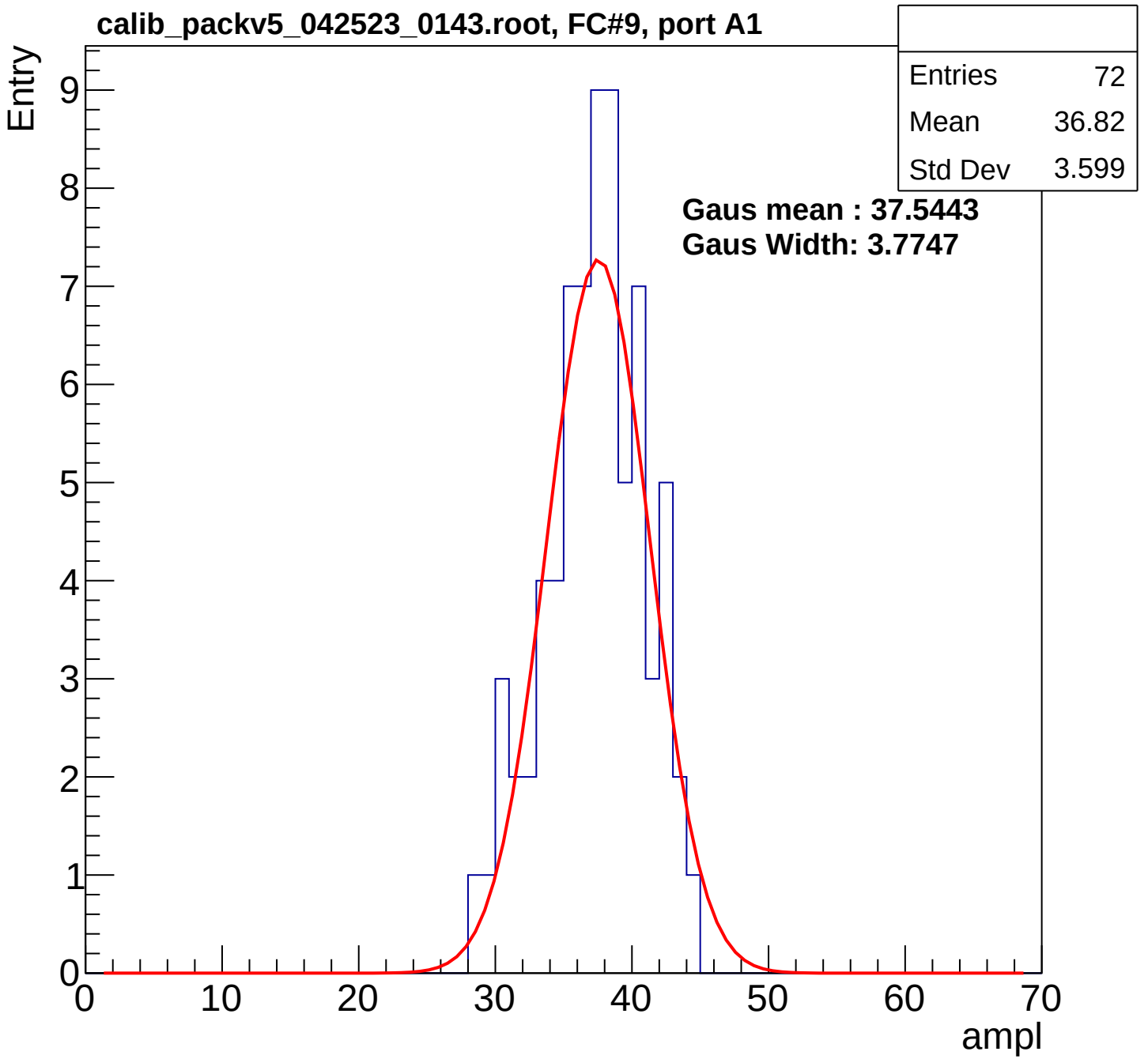
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.82
Std Dev 3.599
Gaus mean : 37.5443
Gaus Width: 3.7747
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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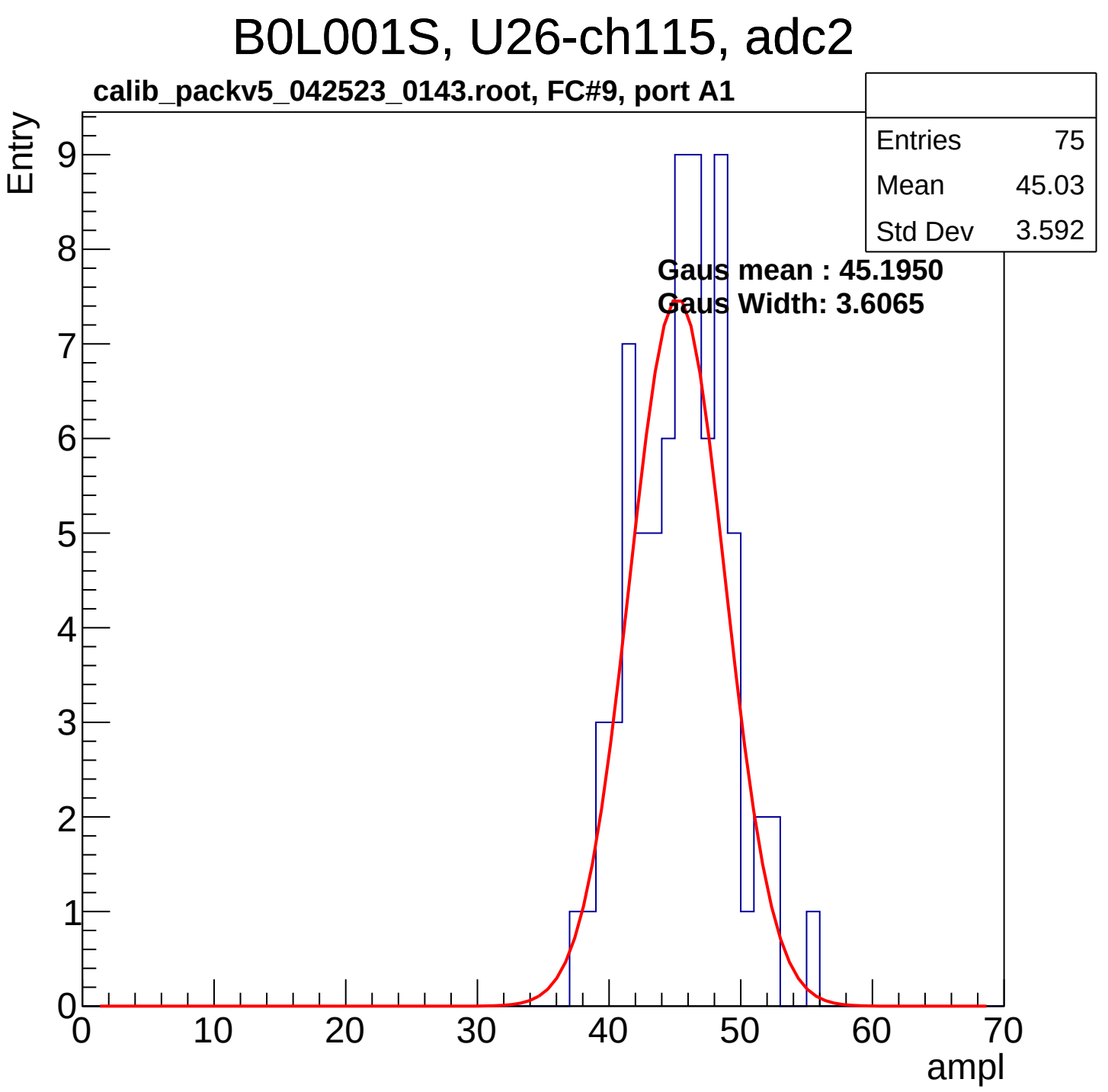
60
70


B0L001S, U26-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 45.03
Std Dev 3.592
Gaus mean : 45.1950
Gaus Width: 3.6065
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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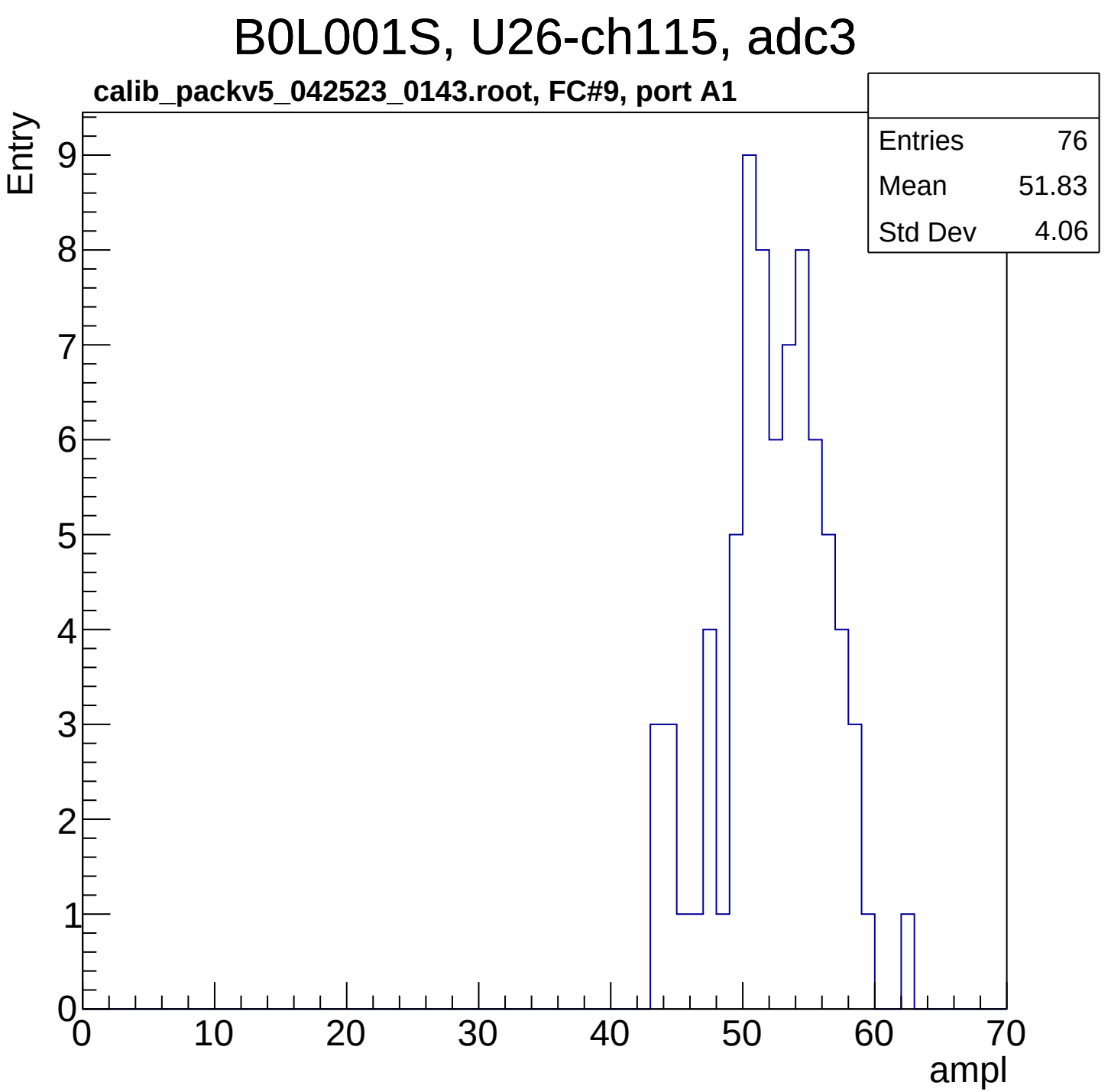
B0L001S, U26-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 51.83
Std Dev 4.06
Entry
ampl
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3
4
5
6
7
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0
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60
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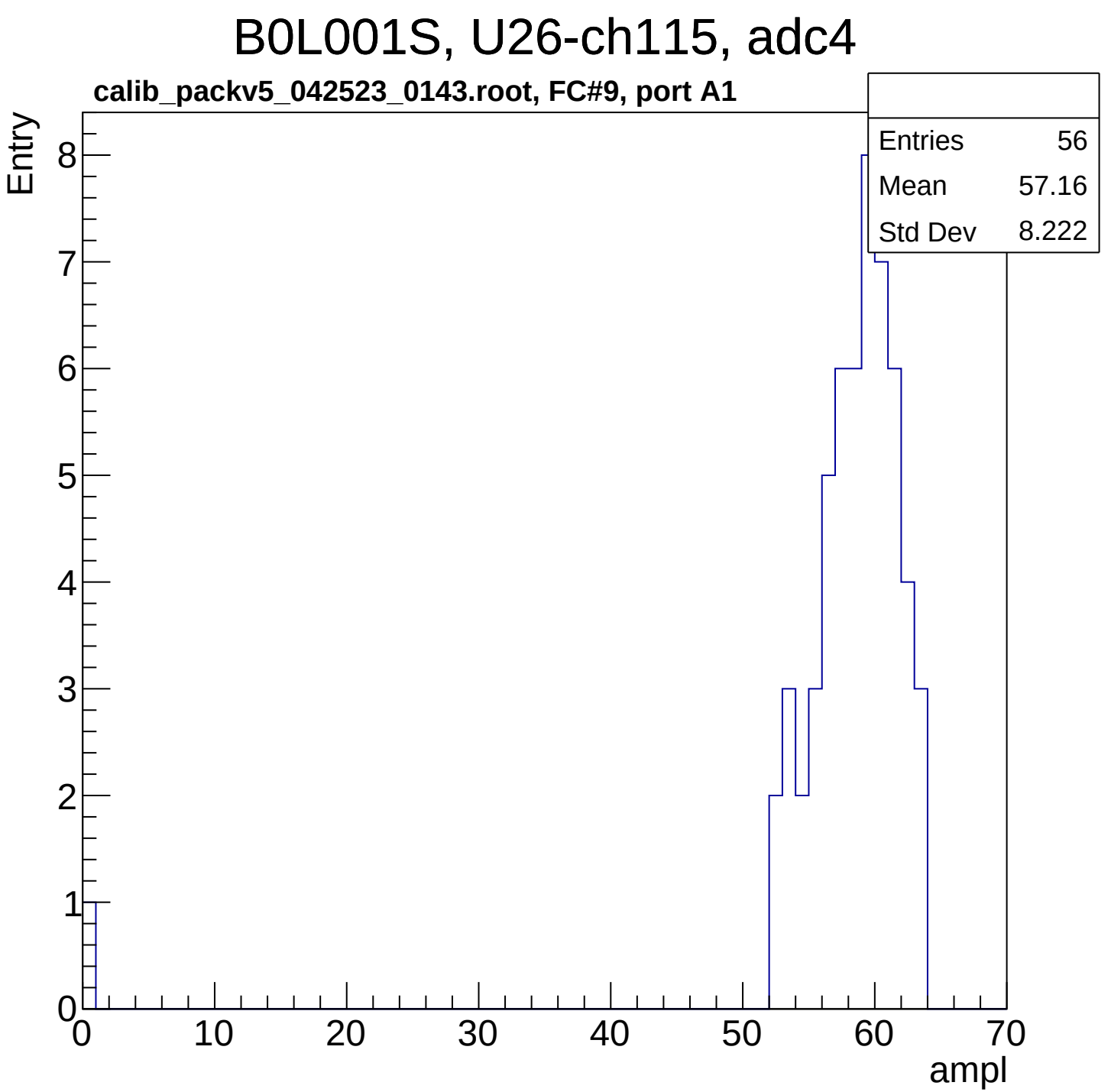
B0L001S, U26-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.16
Std Dev 8.222
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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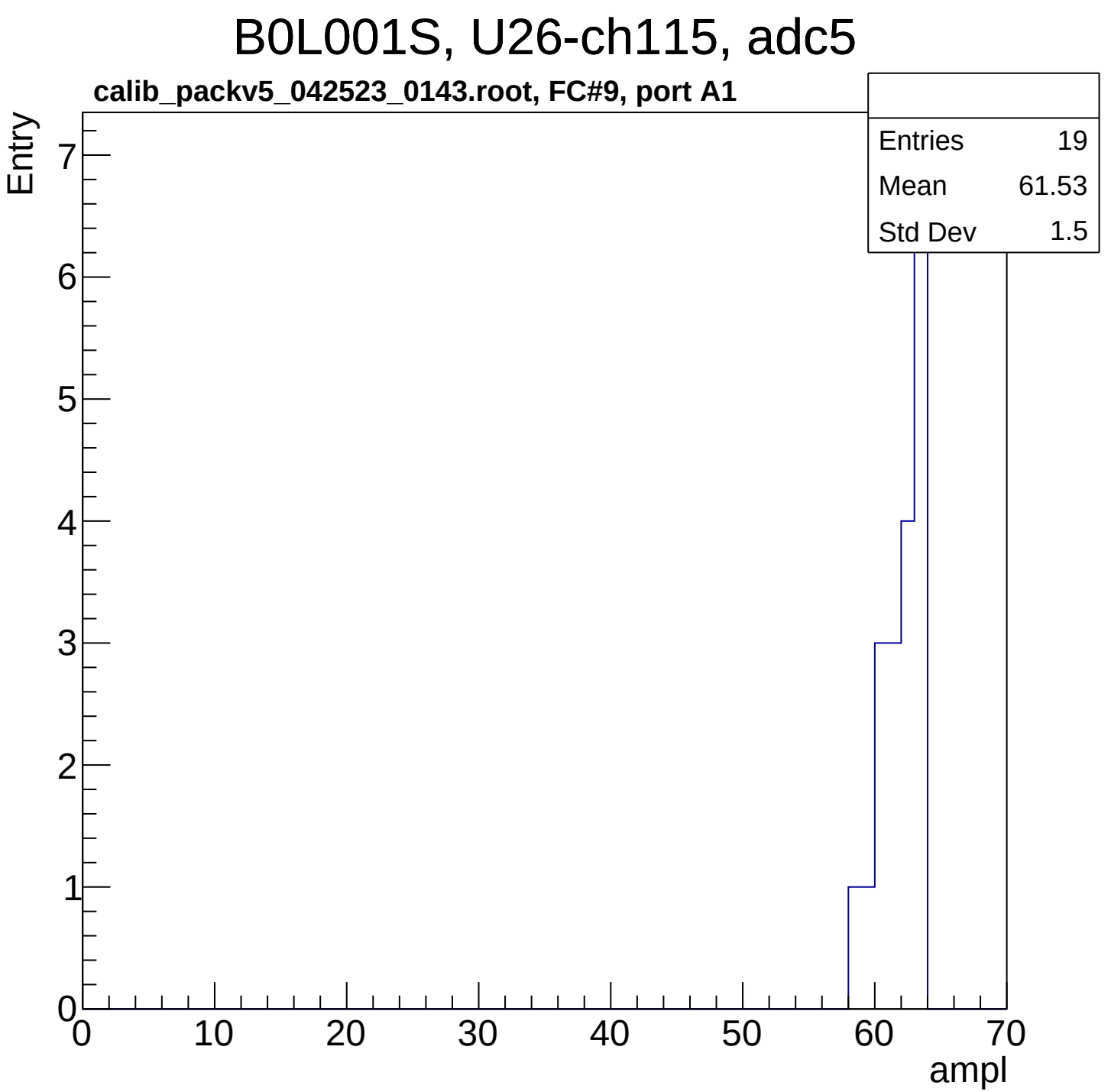
B0L001S, U26-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 61.53
Std Dev 1.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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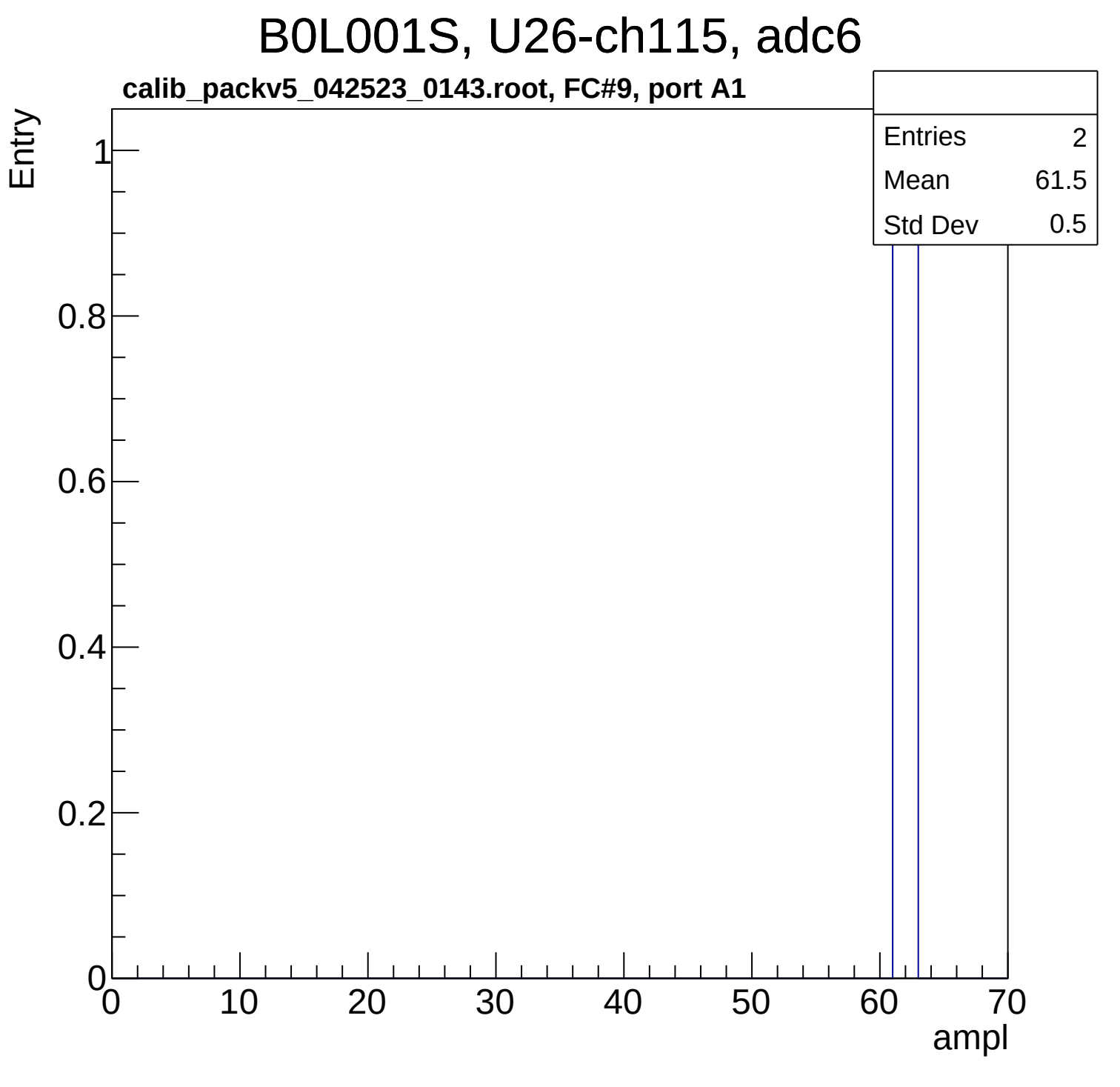
B0L001S, U26-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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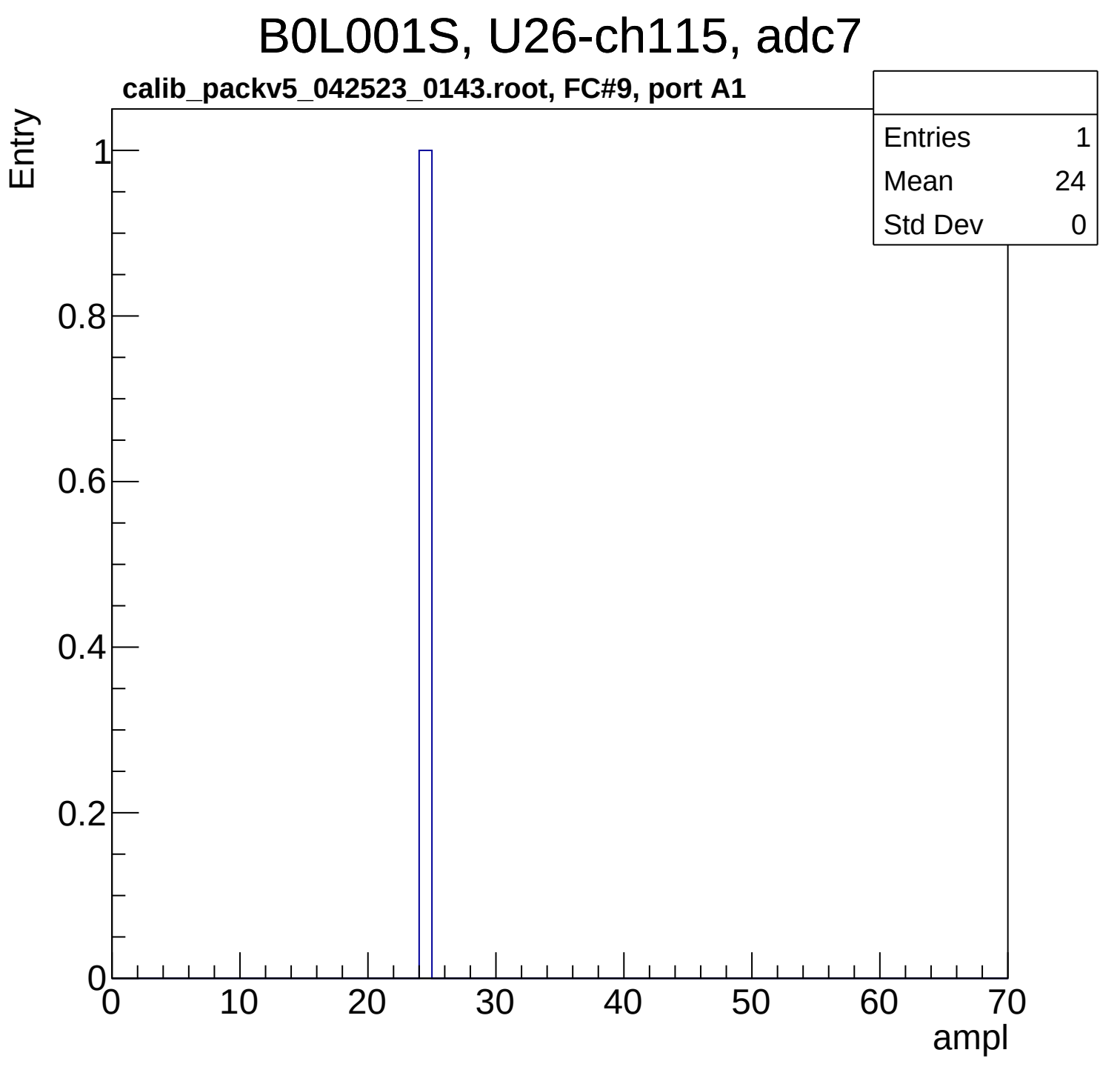
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
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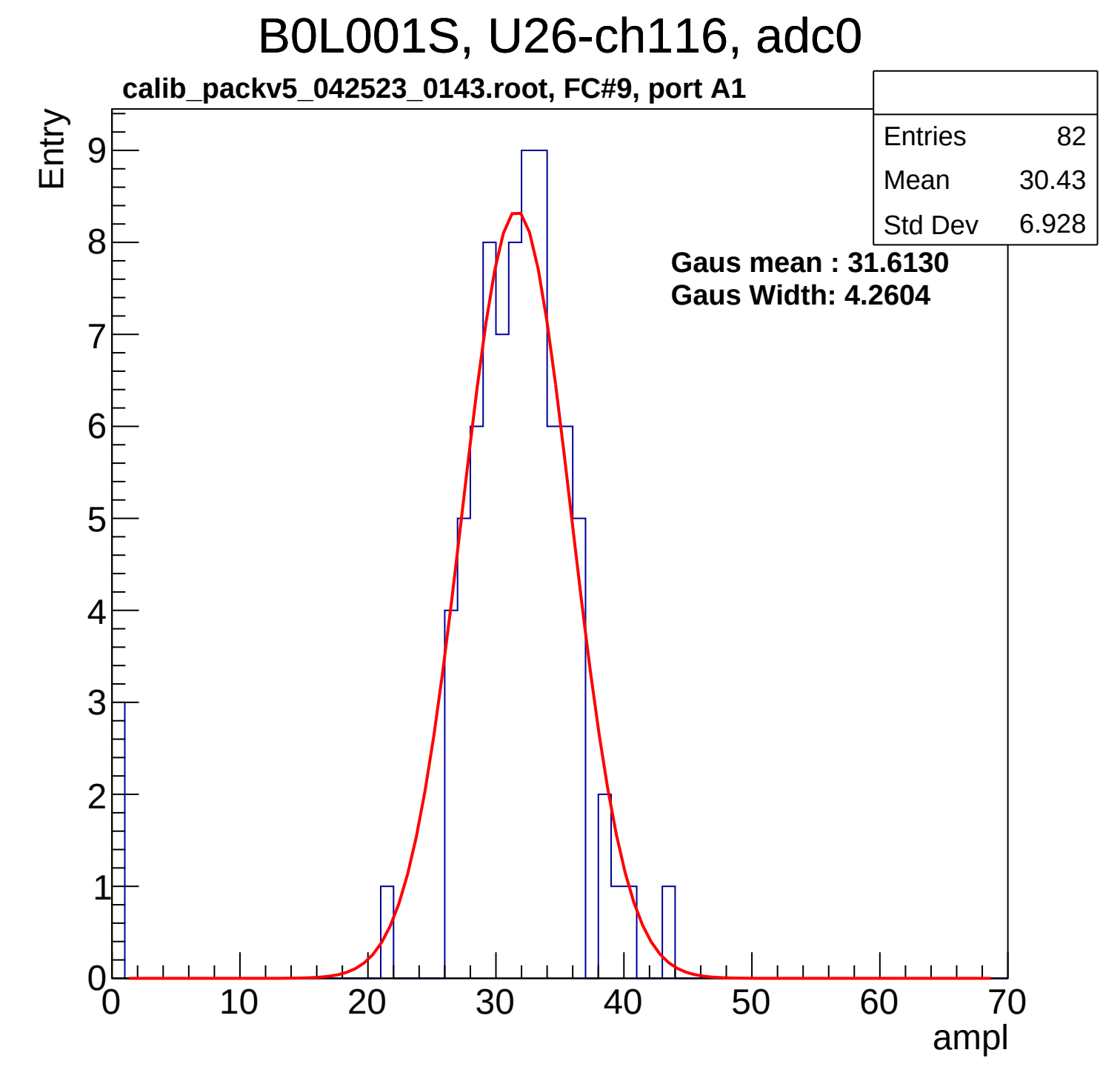
70
ampl


B0L001S, U26-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30.43
Std Dev 6.928
Gaus mean : 31.6130
Gaus Width: 4.2604
Entry
ampl
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2
3
4
5
6
7
8
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0
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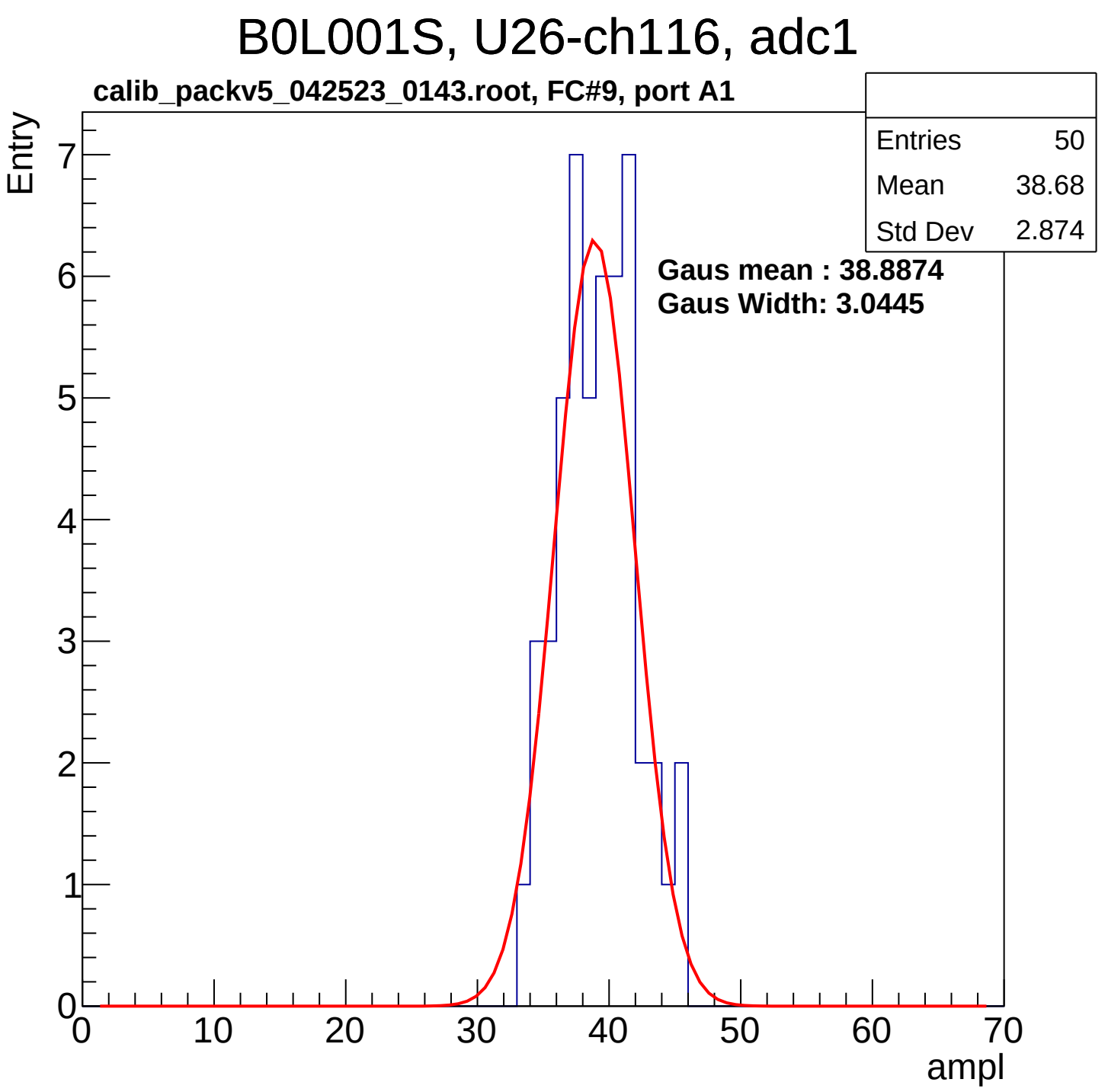
B0L001S, U26-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 38.68
Std Dev 2.874
Gaus mean : 38.8874
Gaus Width: 3.0445
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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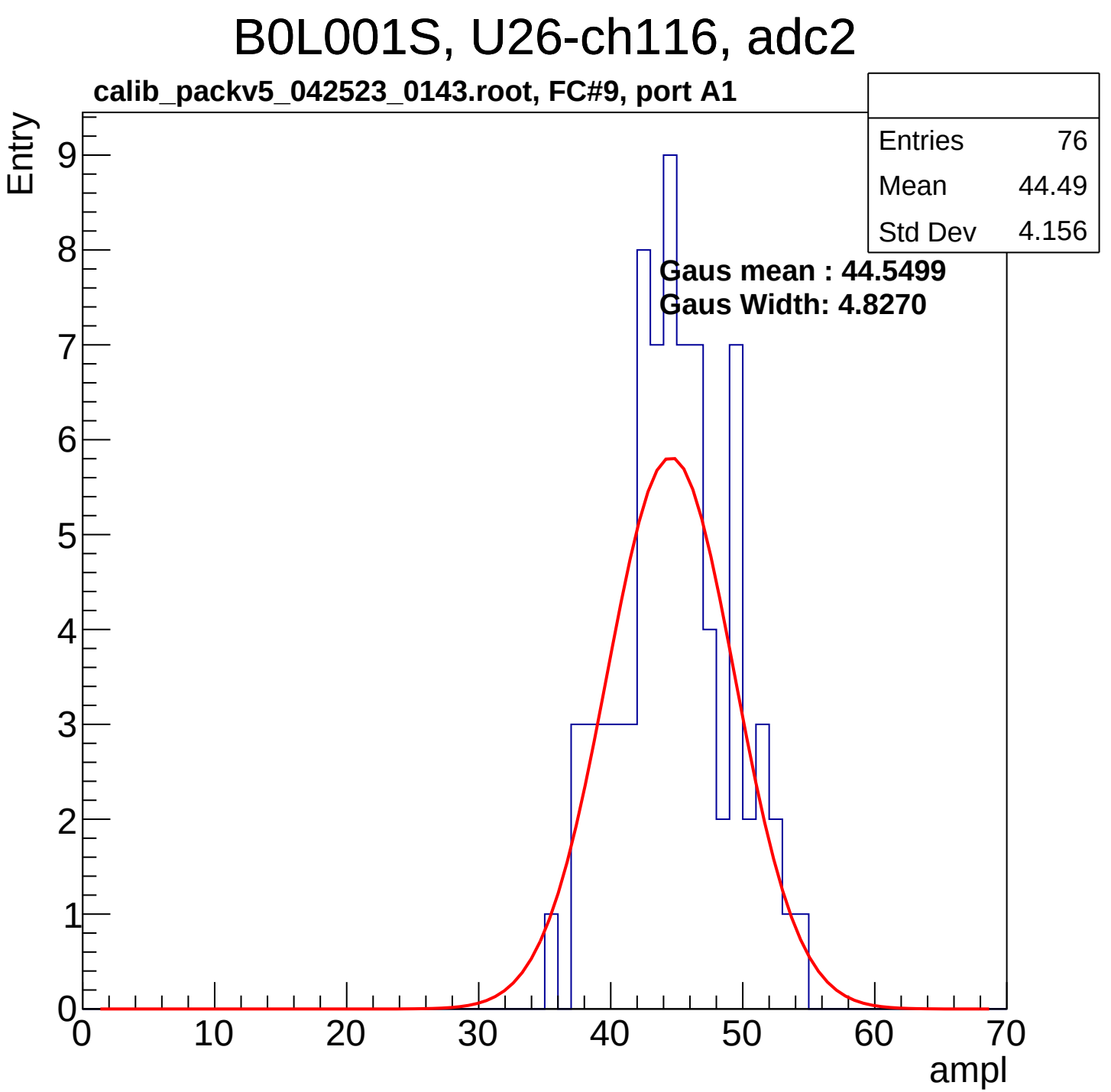
B0L001S, U26-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.49
Std Dev 4.156
Gaus mean : 44.5499
Gaus Width: 4.8270
Entry
ampl
0
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4
5
6
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0
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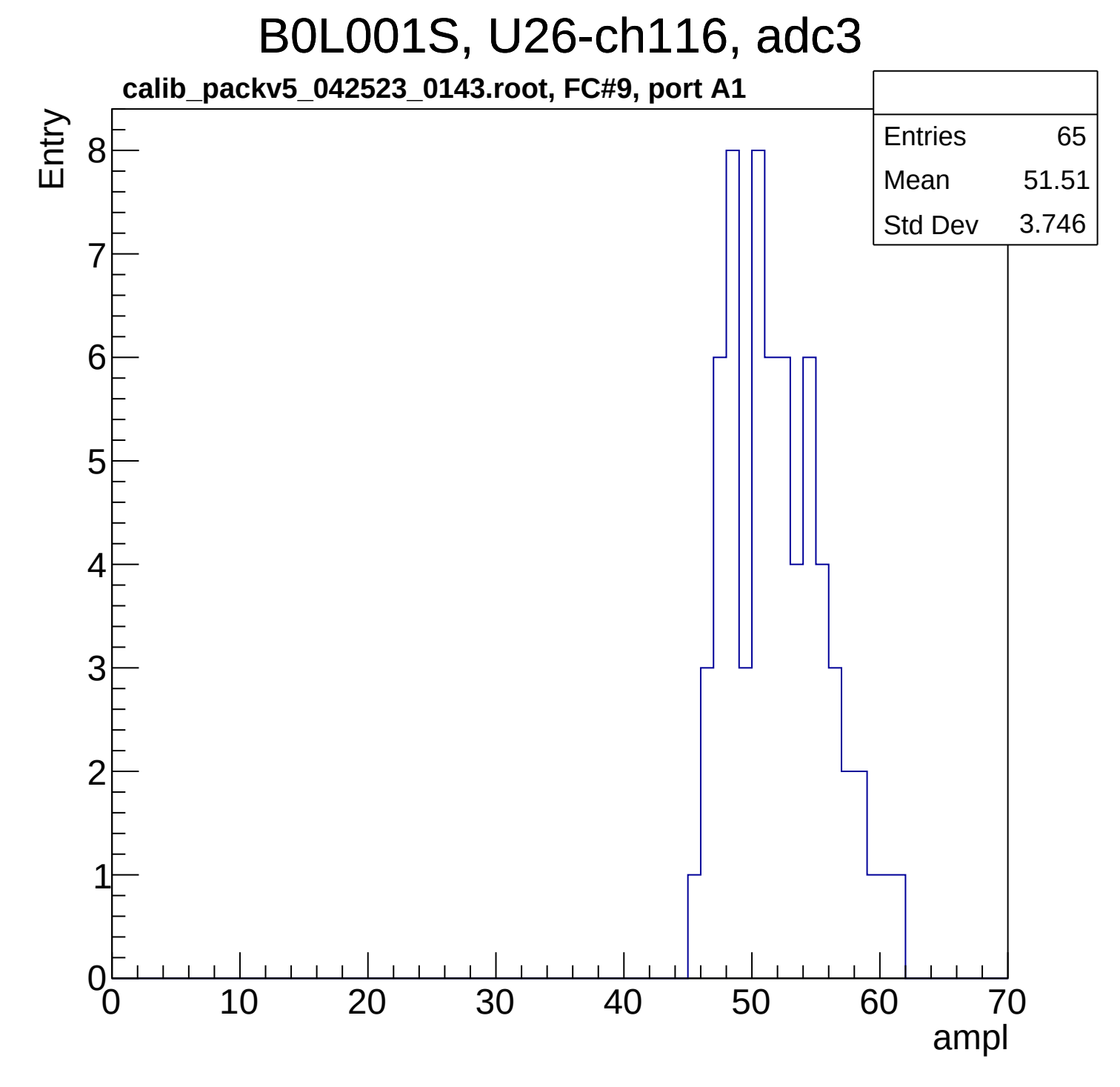
B0L001S, U26-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.51
Std Dev 3.746
Entry
ampl
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3
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5
6
7
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0
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60
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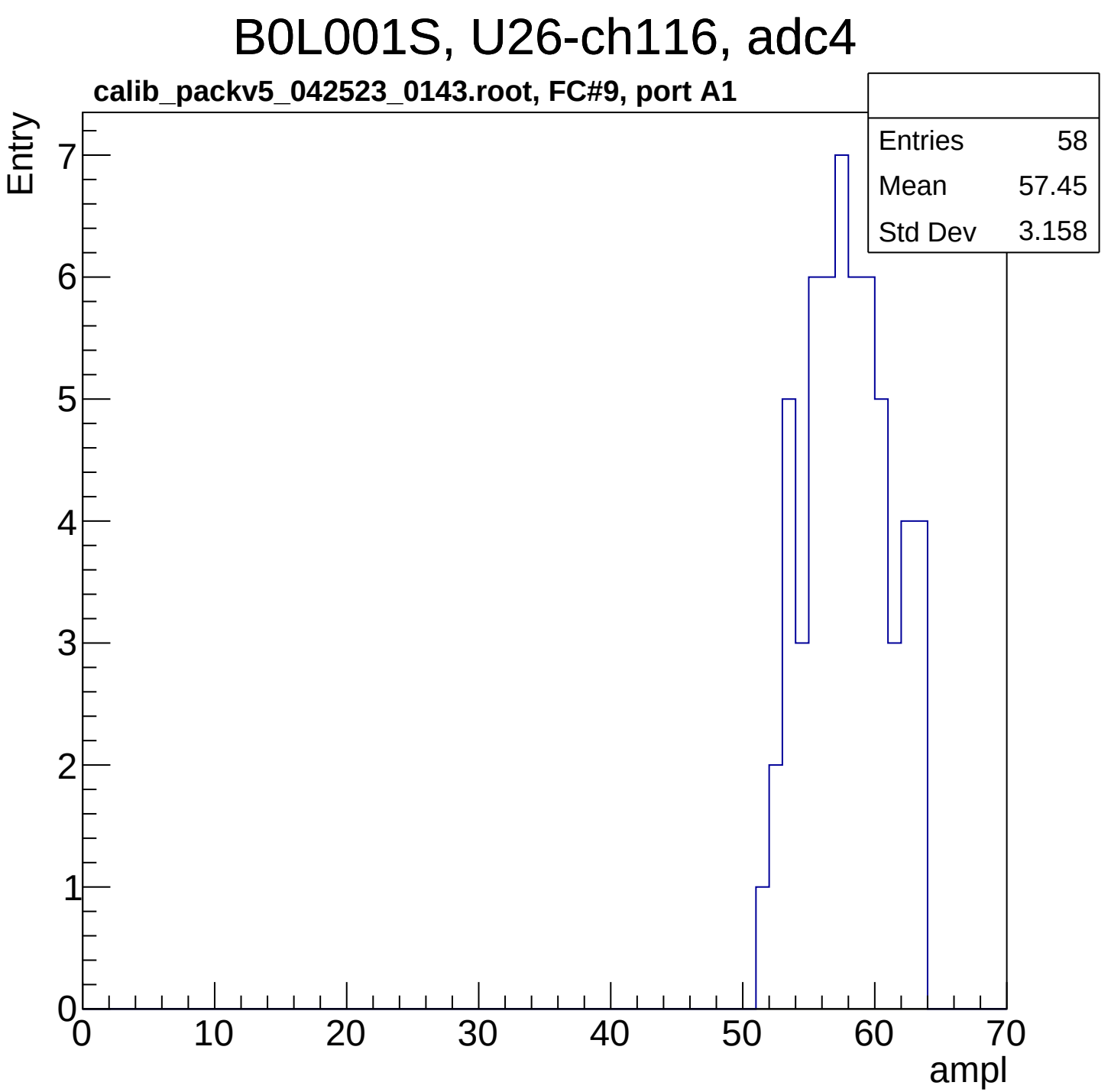
B0L001S, U26-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.45
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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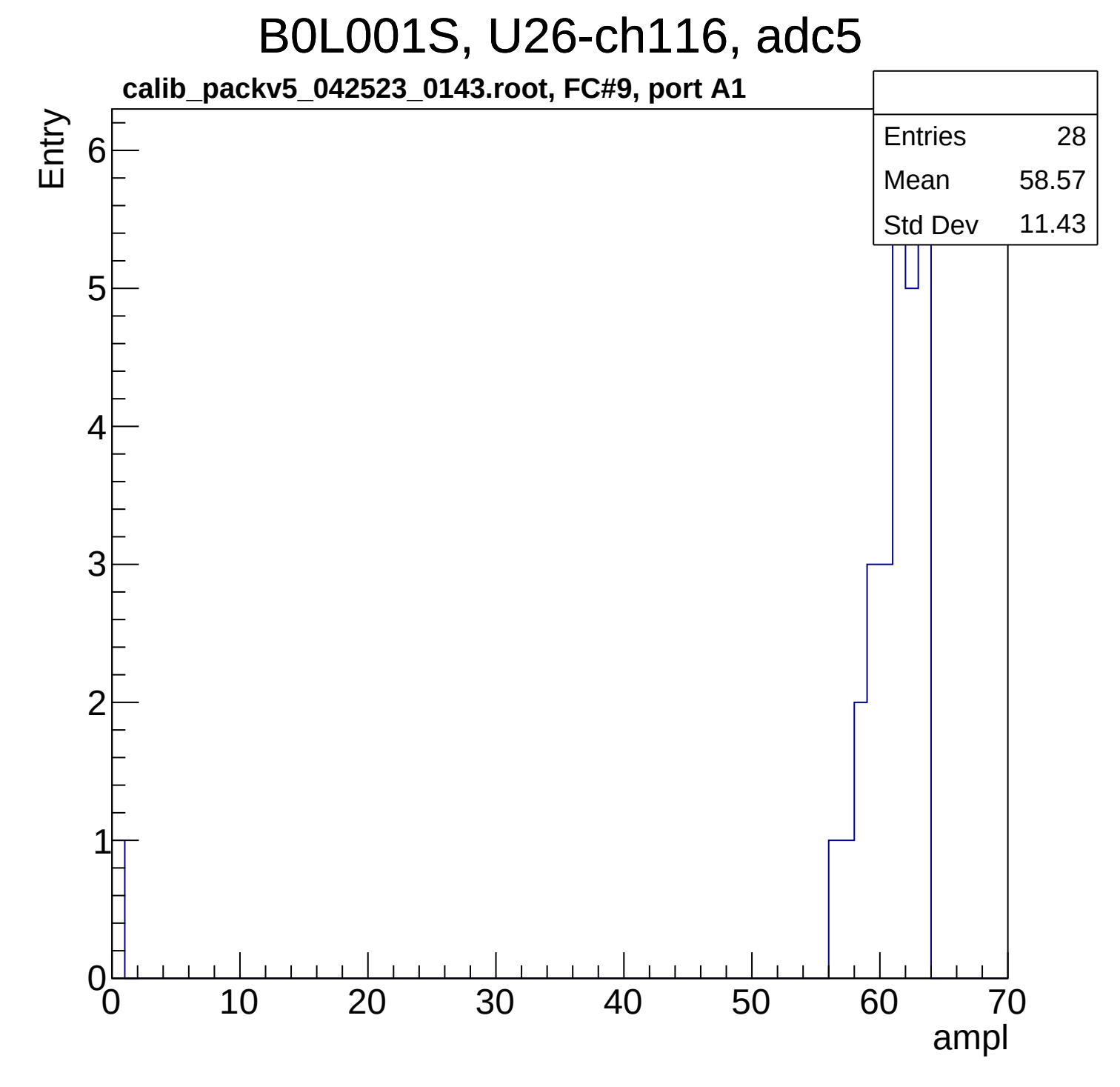
B0L001S, U26-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.57
Std Dev 11.43
Entry
0
1
2
3
4
5
6
0
10
20
30
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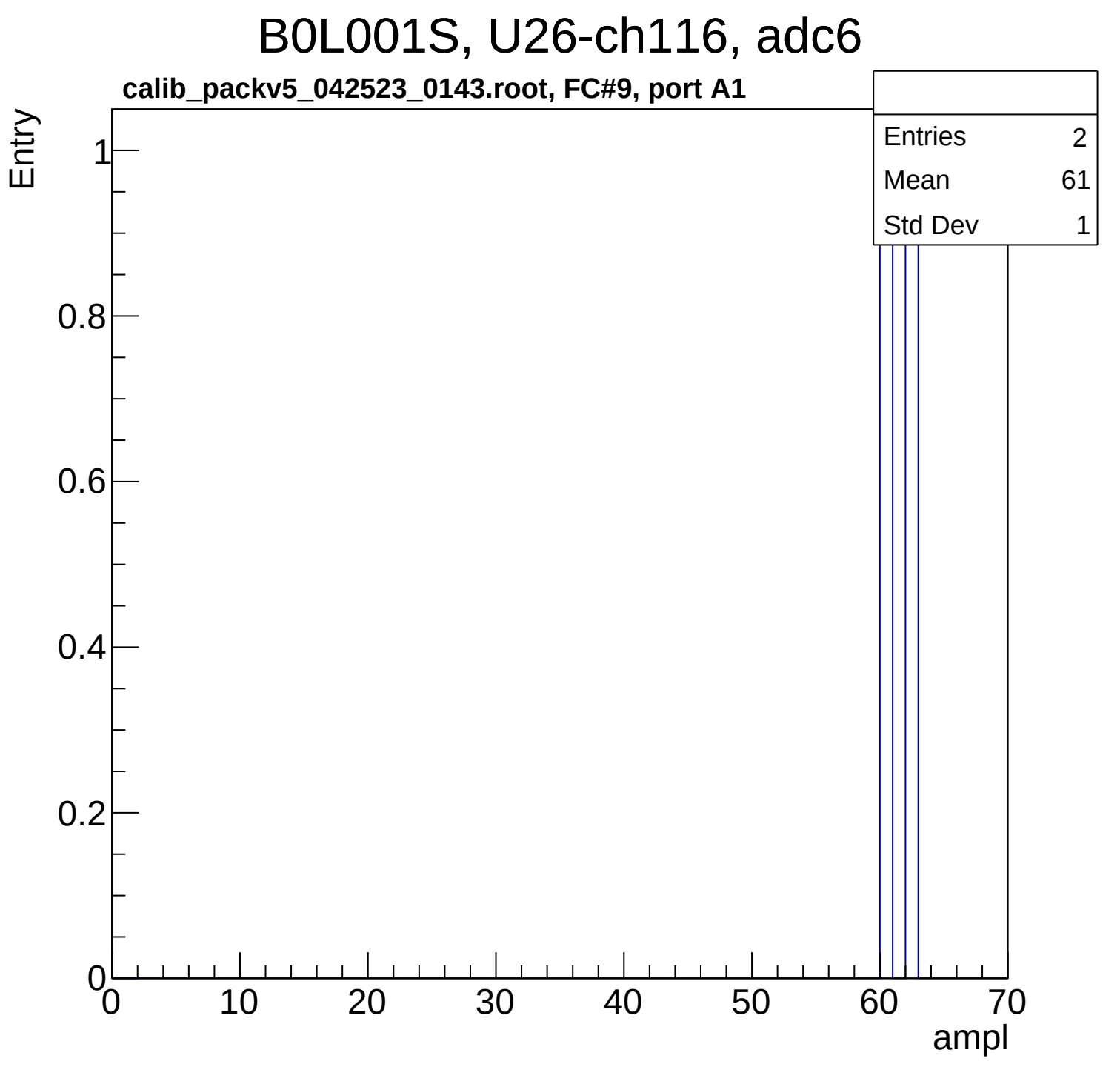
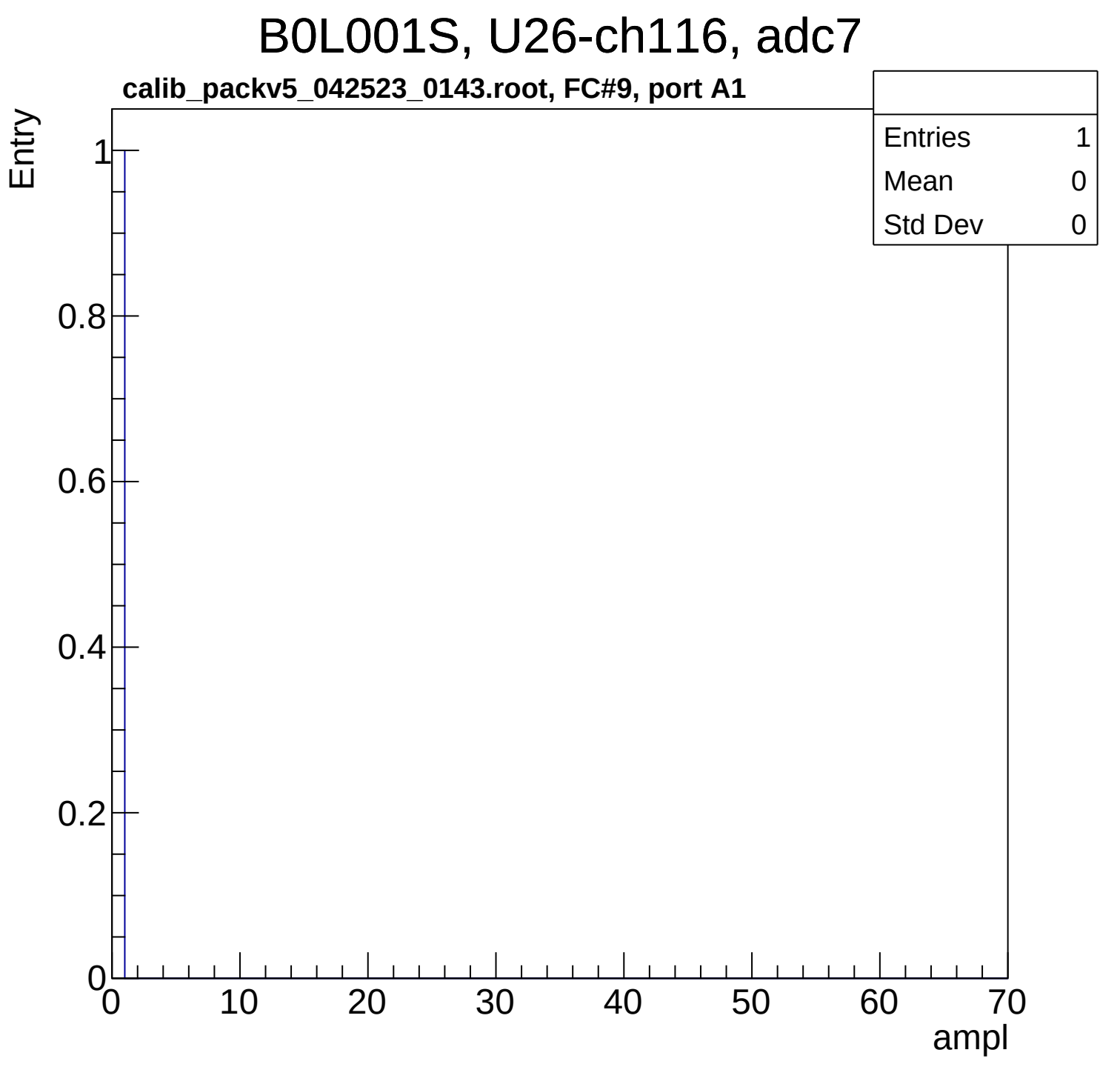
50
60
70
ampl


B0L001S, U26-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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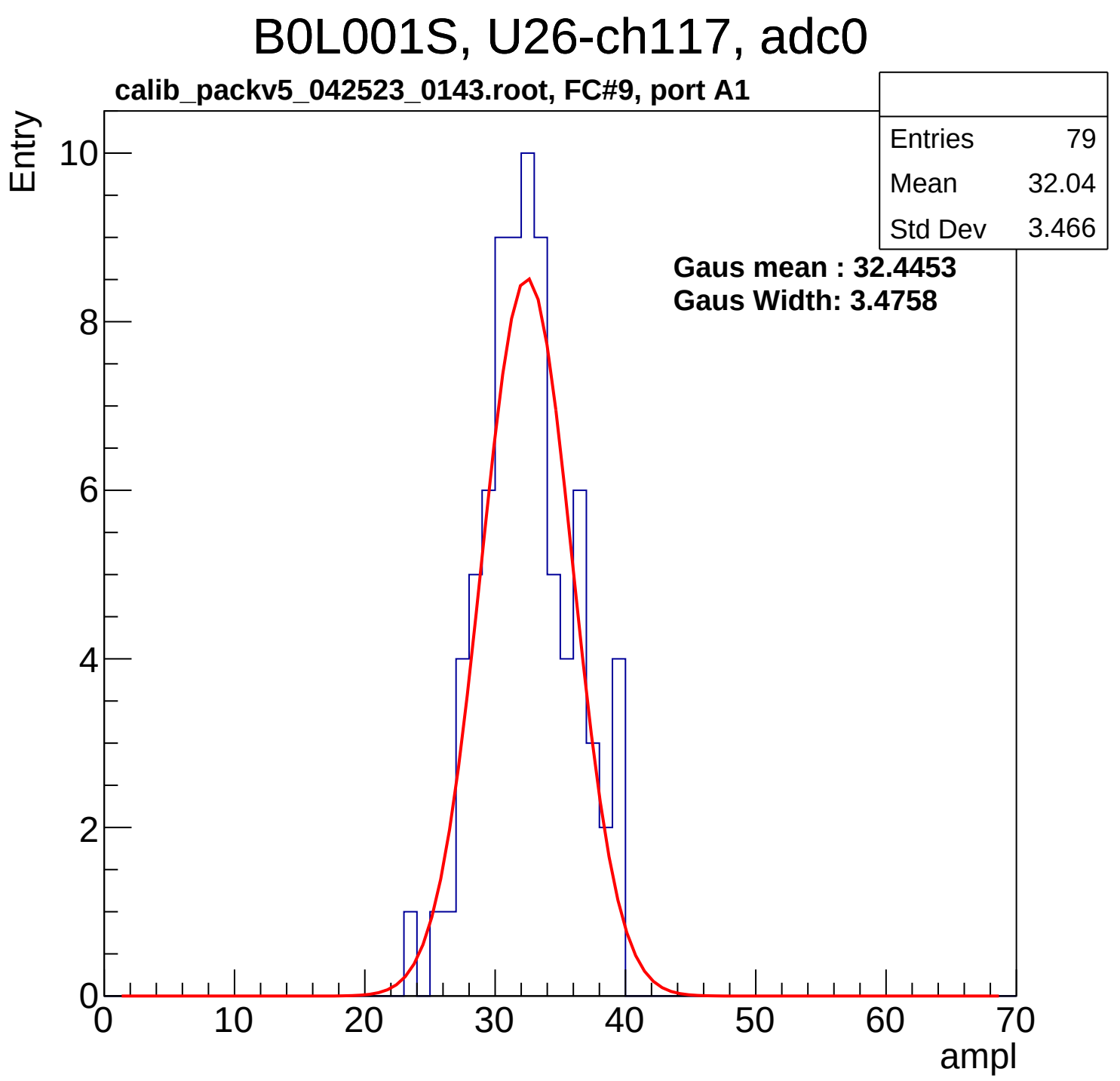
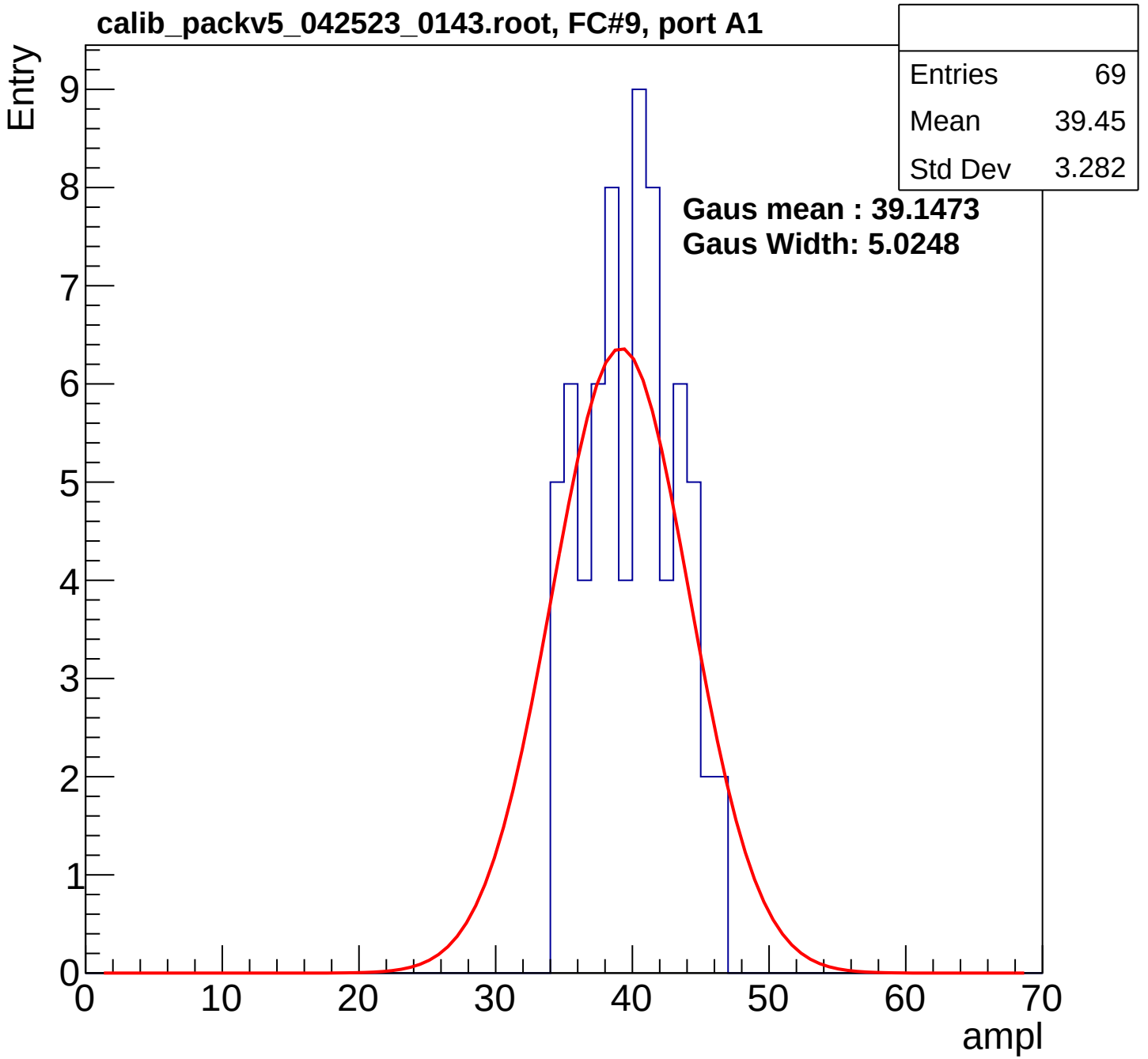
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 32.04
Std Dev 3.466
Gaus mean : 32.4453
Gaus Width: 3.4758
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 39.45
Std Dev 3.282
Gaus mean : 39.1473
Gaus Width: 5.0248
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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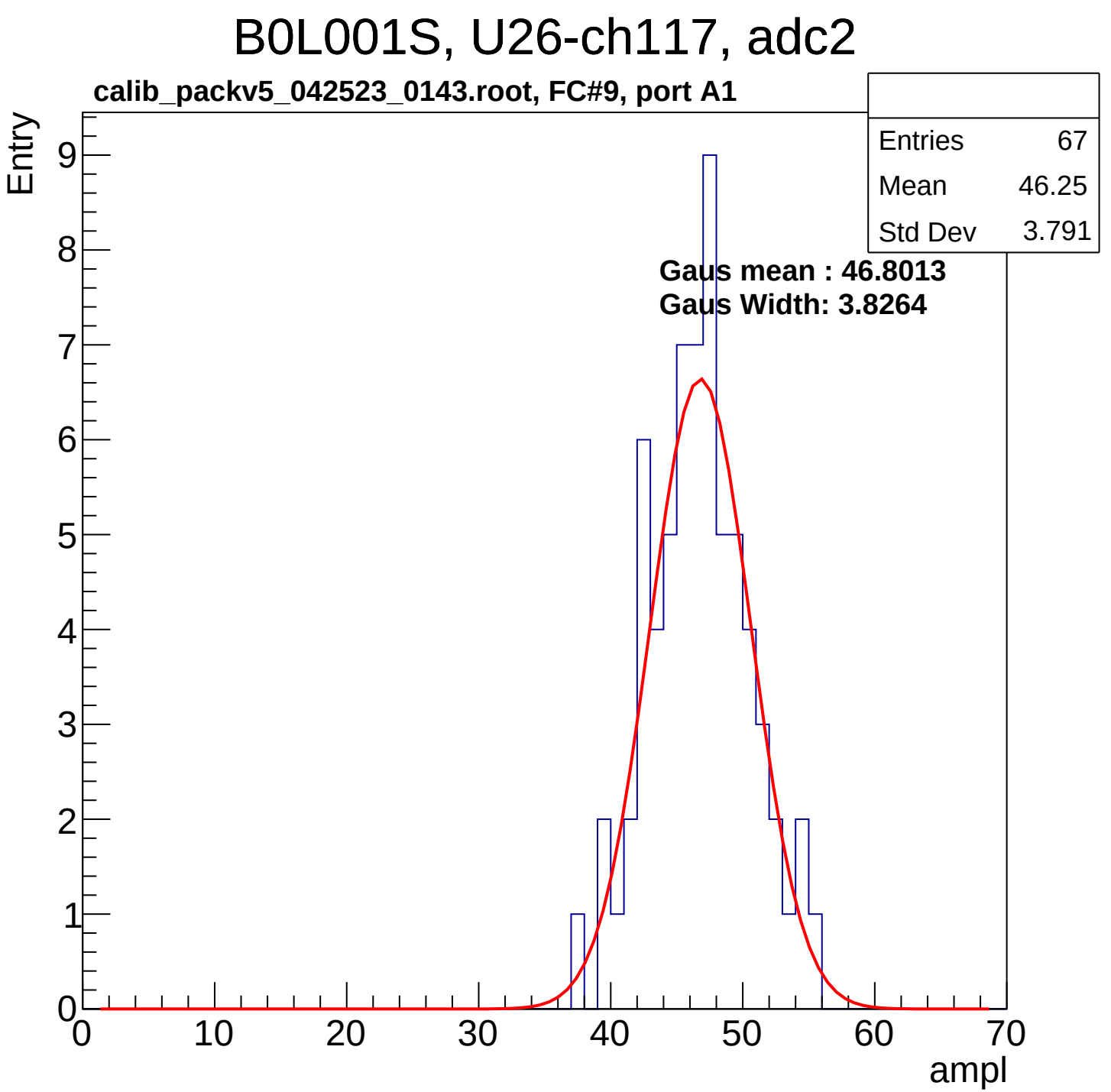
B0L001S, U26-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 46.25
Std Dev 3.791
Gaus mean : 46.8013
Gaus Width: 3.8264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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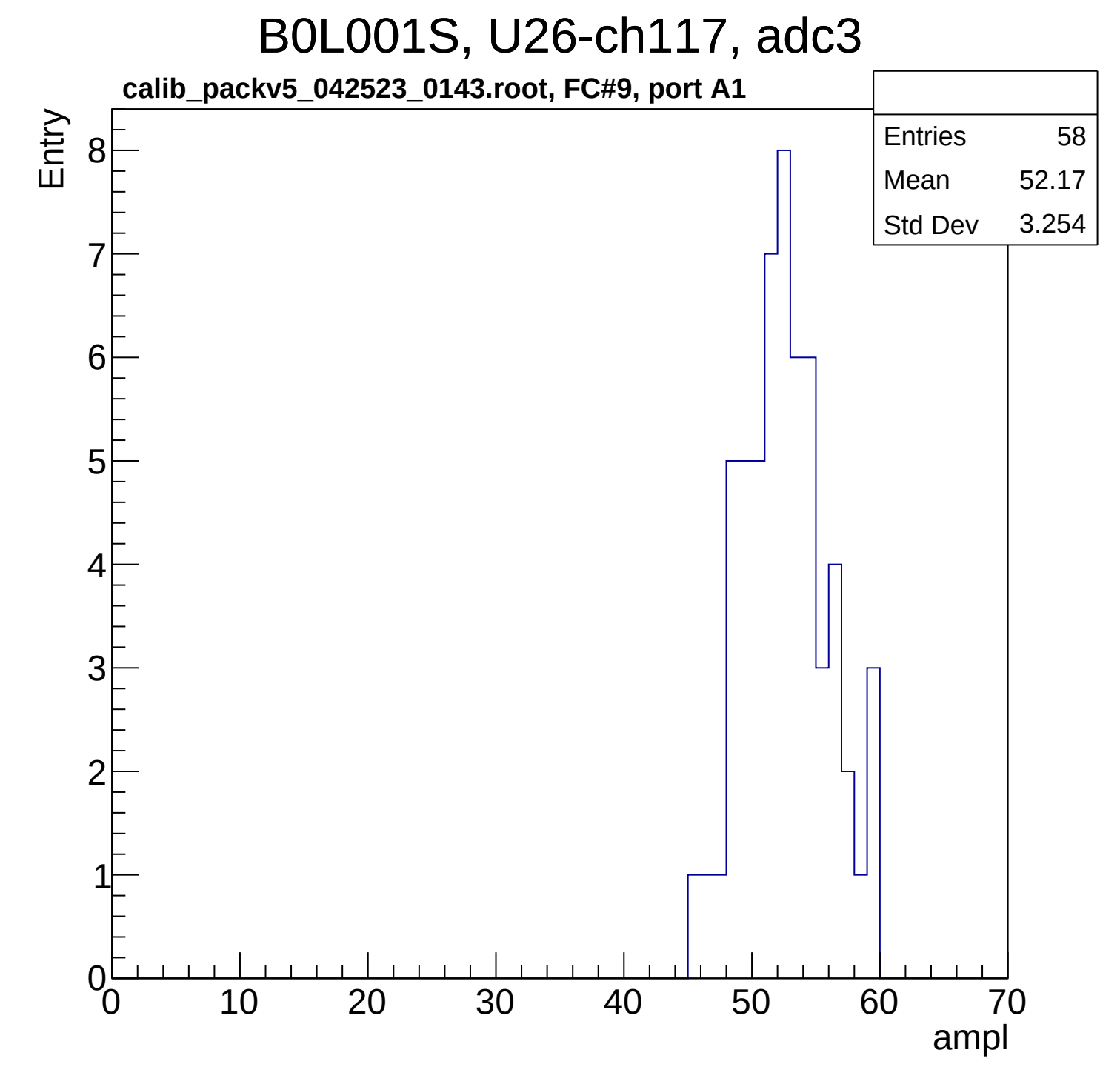
B0L001S, U26-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 52.17
Std Dev 3.254
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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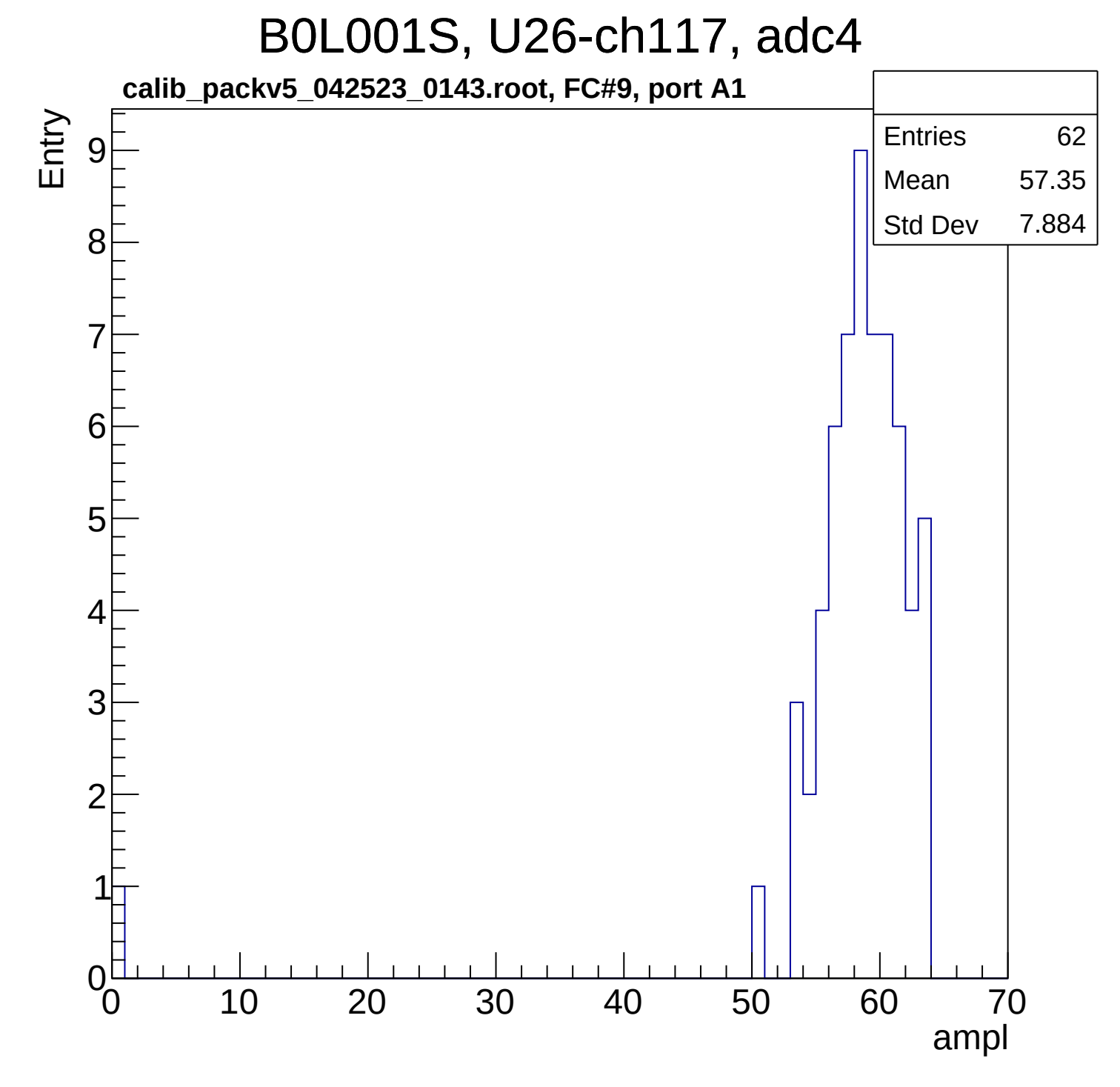
B0L001S, U26-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 57.35
Std Dev 7.884
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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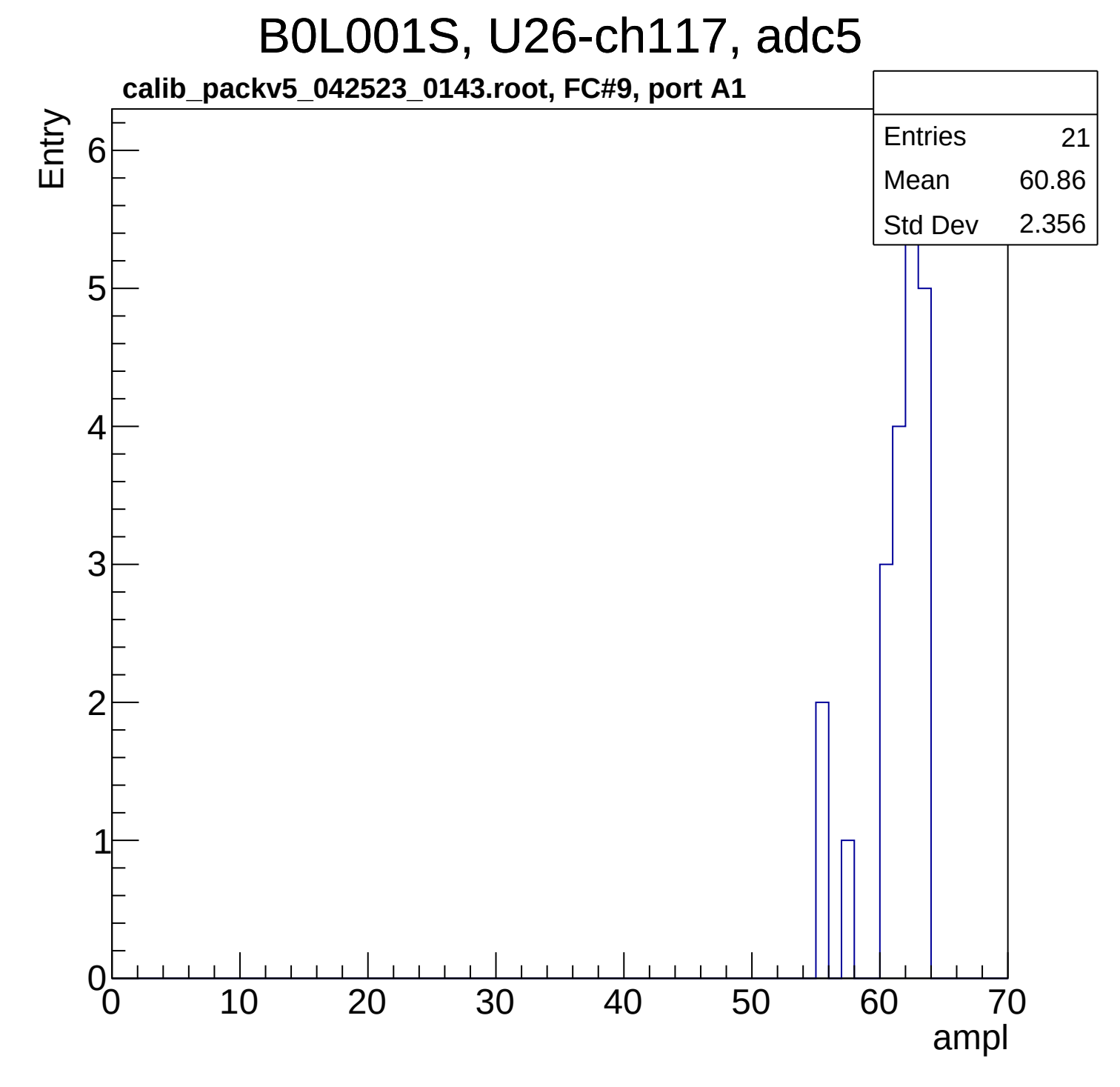
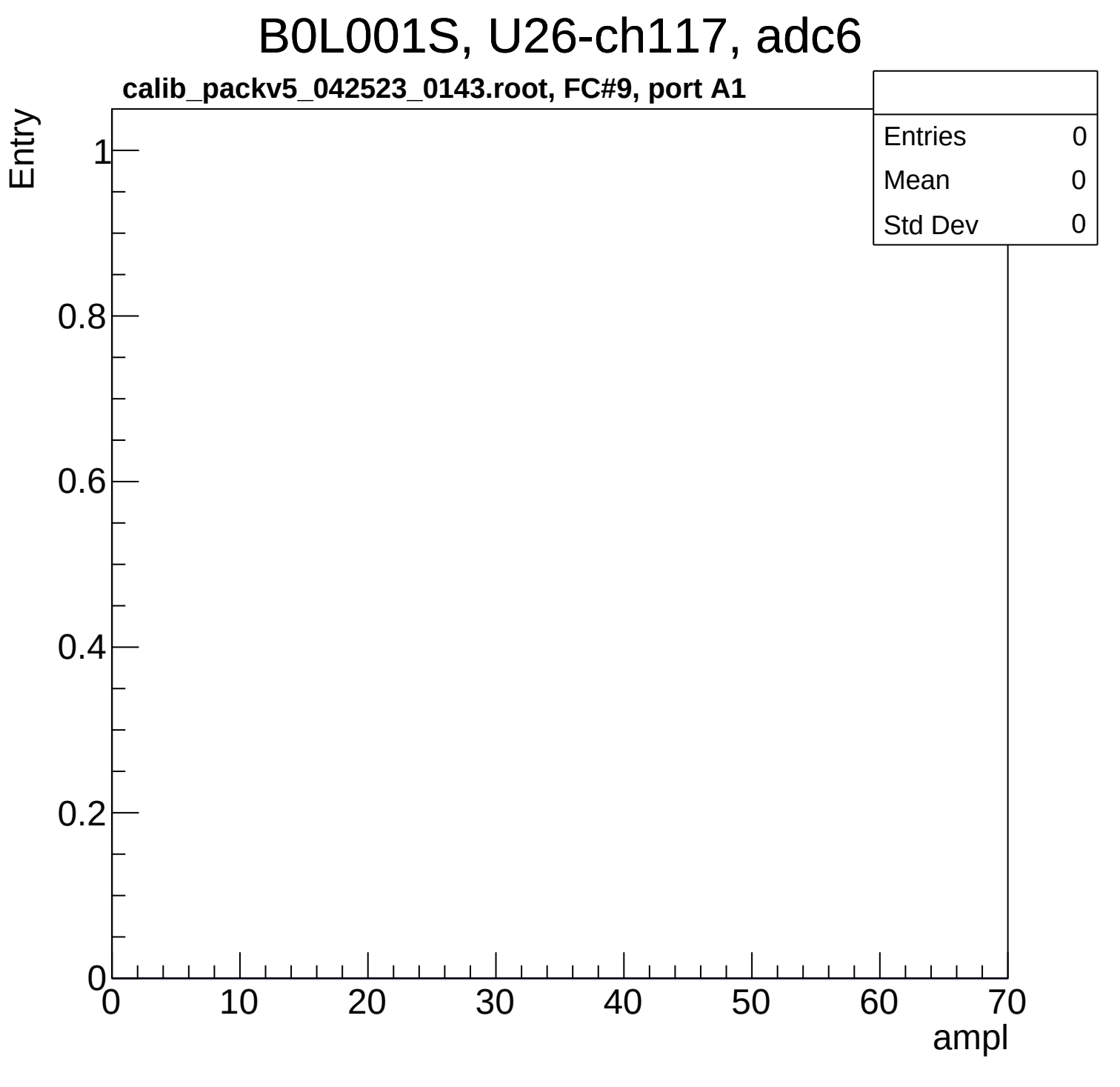
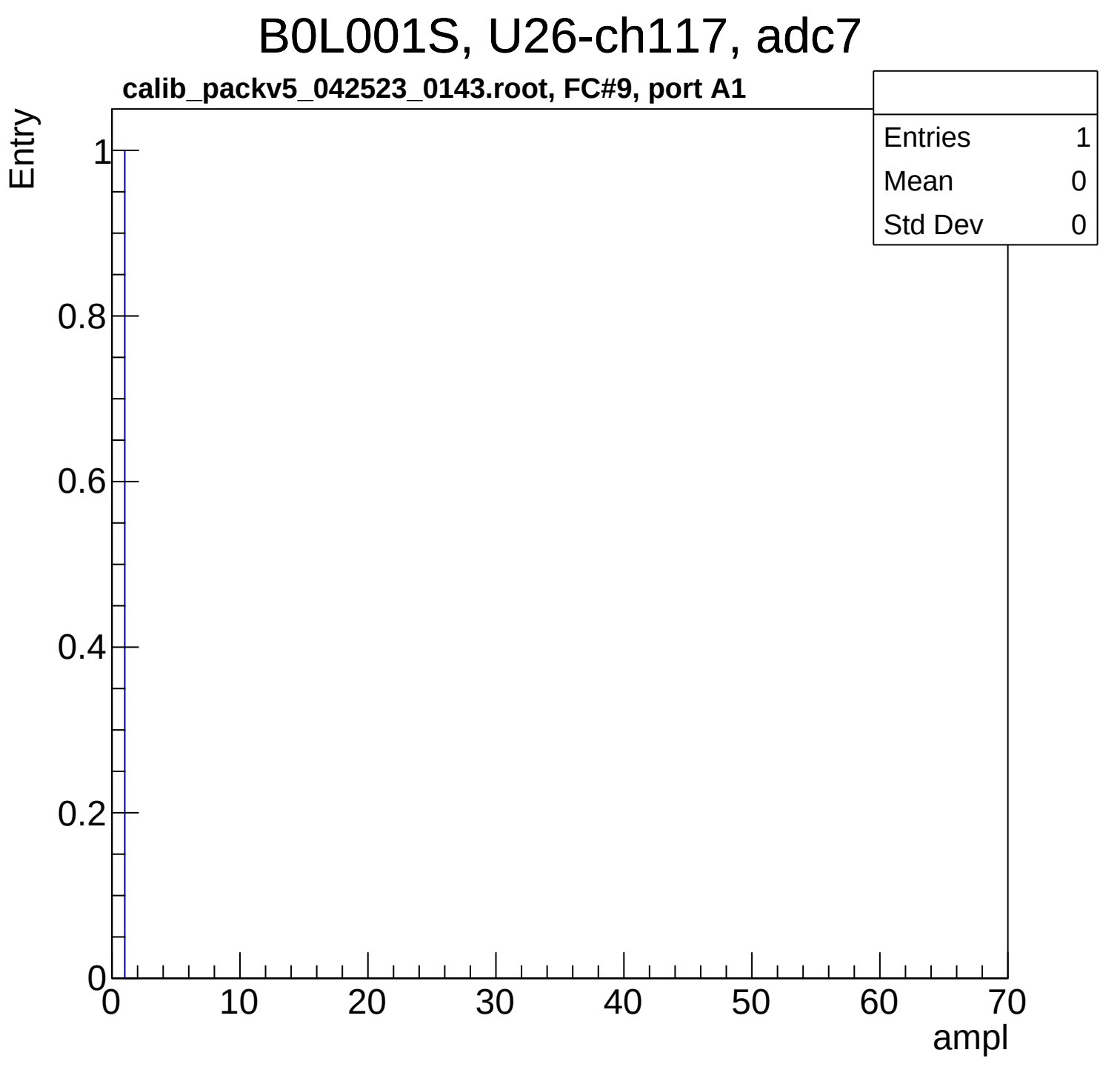
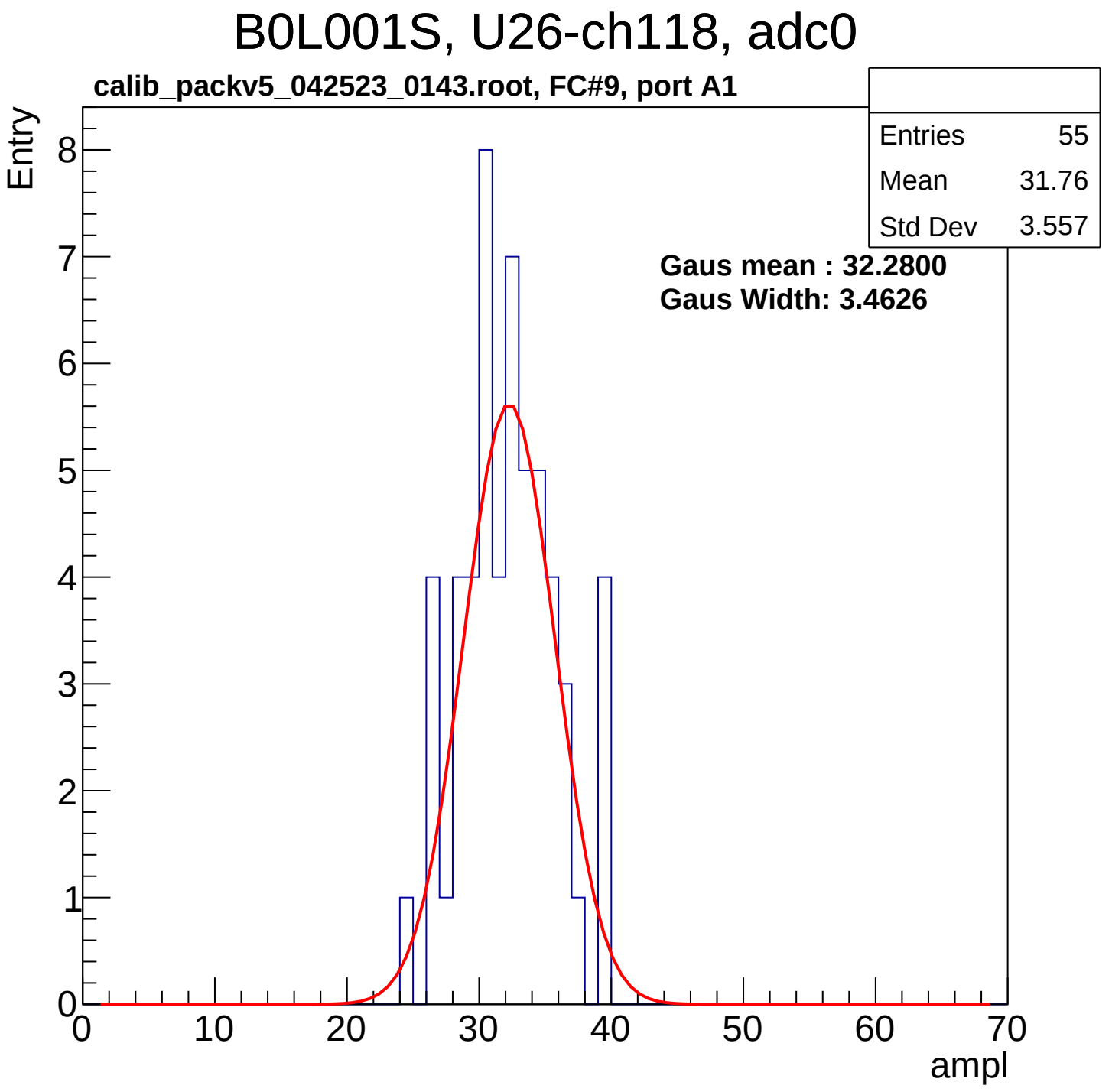

B0L001S, U26-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 60.86
Std Dev 2.356
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 31.76
Std Dev 3.557
Gaus mean : 32.2800
Gaus Width: 3.4626
Entry
ampl
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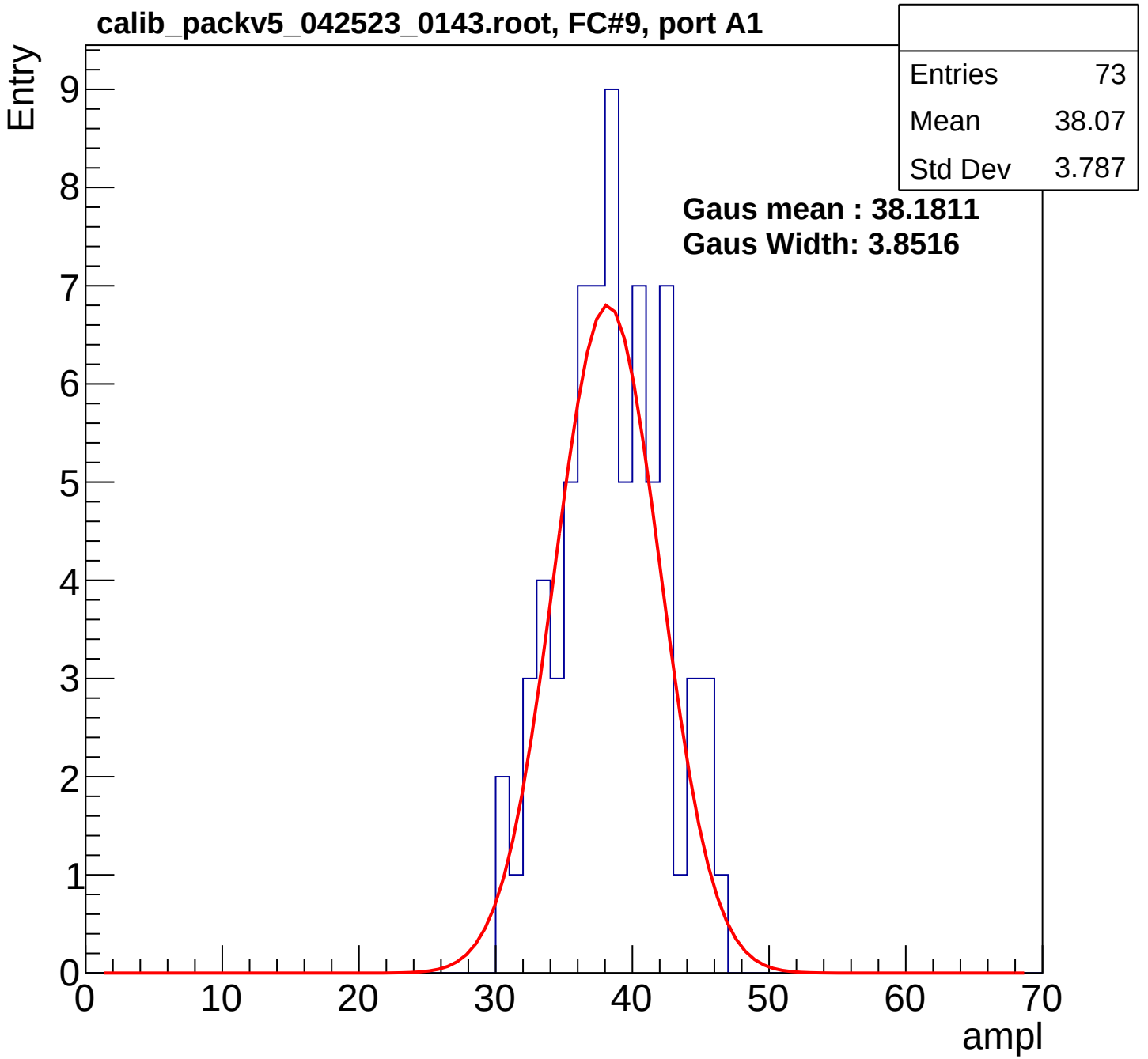
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.07
Std Dev 3.787
Gaus mean : 38.1811
Gaus Width: 3.8516
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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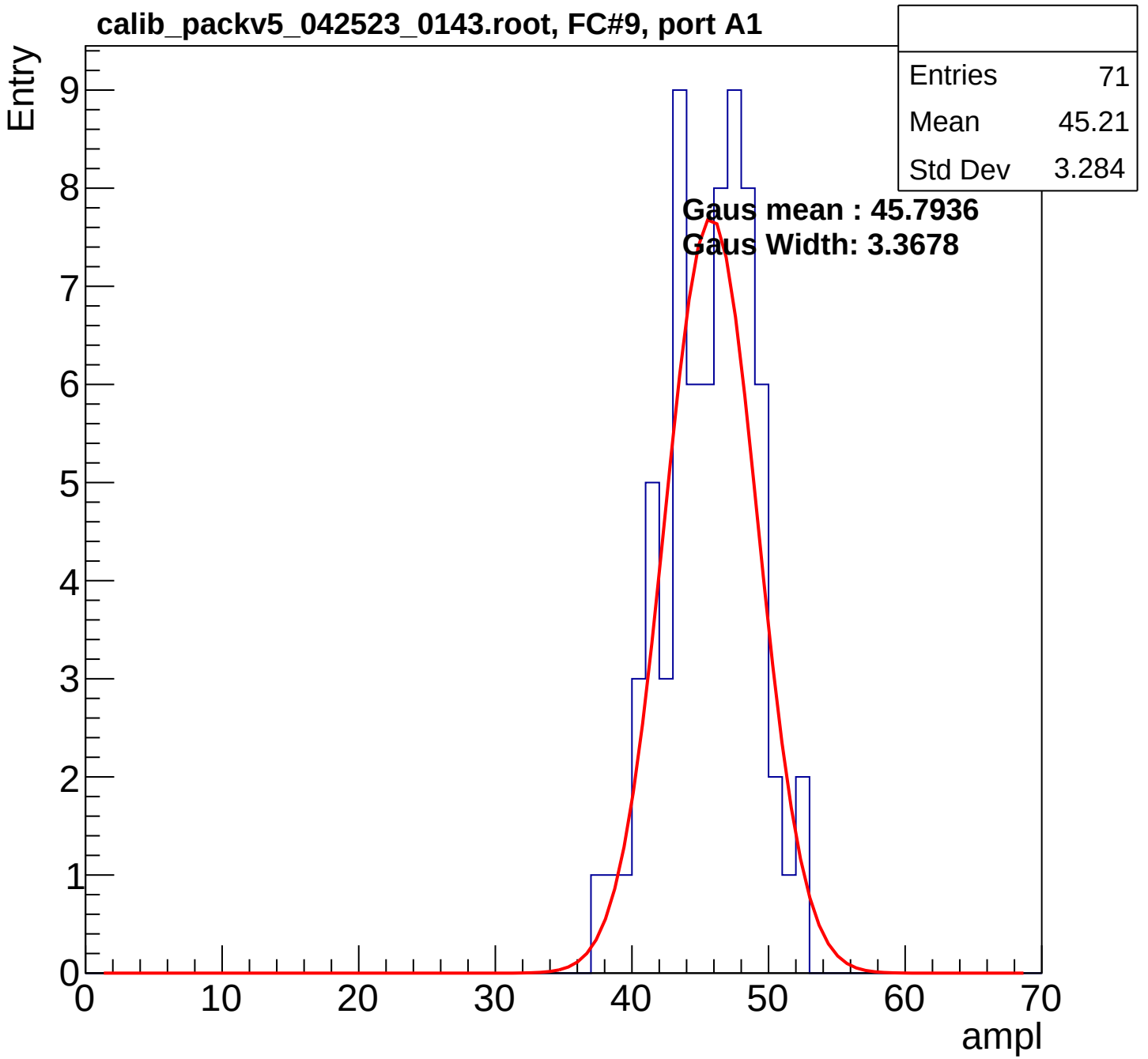
B0L001S, U26-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.21
Std Dev 3.284
Gaus mean : 45.7936
Gaus Width: 3.3678
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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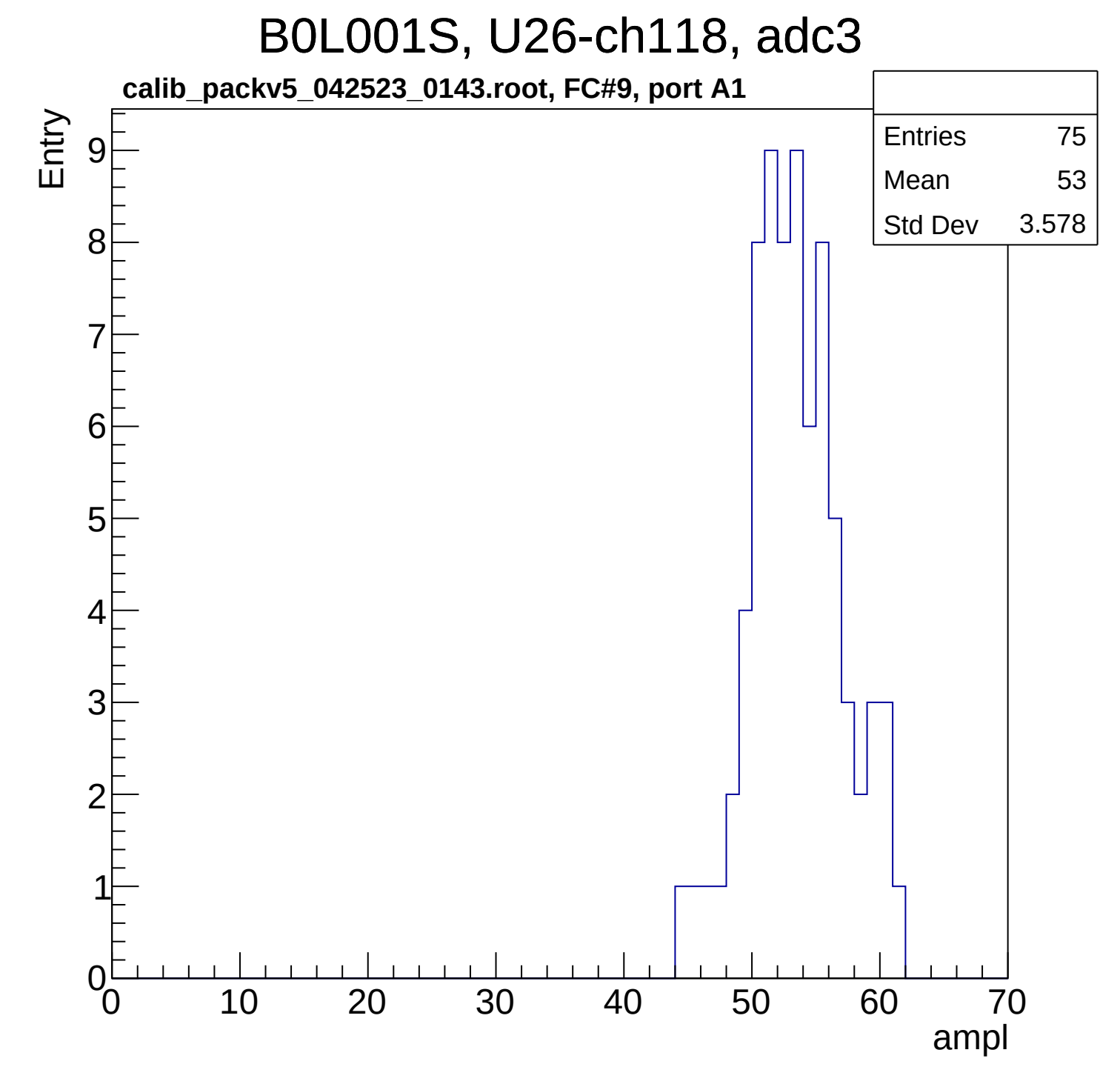
B0L001S, U26-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 53
Std Dev 3.578
Entry
ampl
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6
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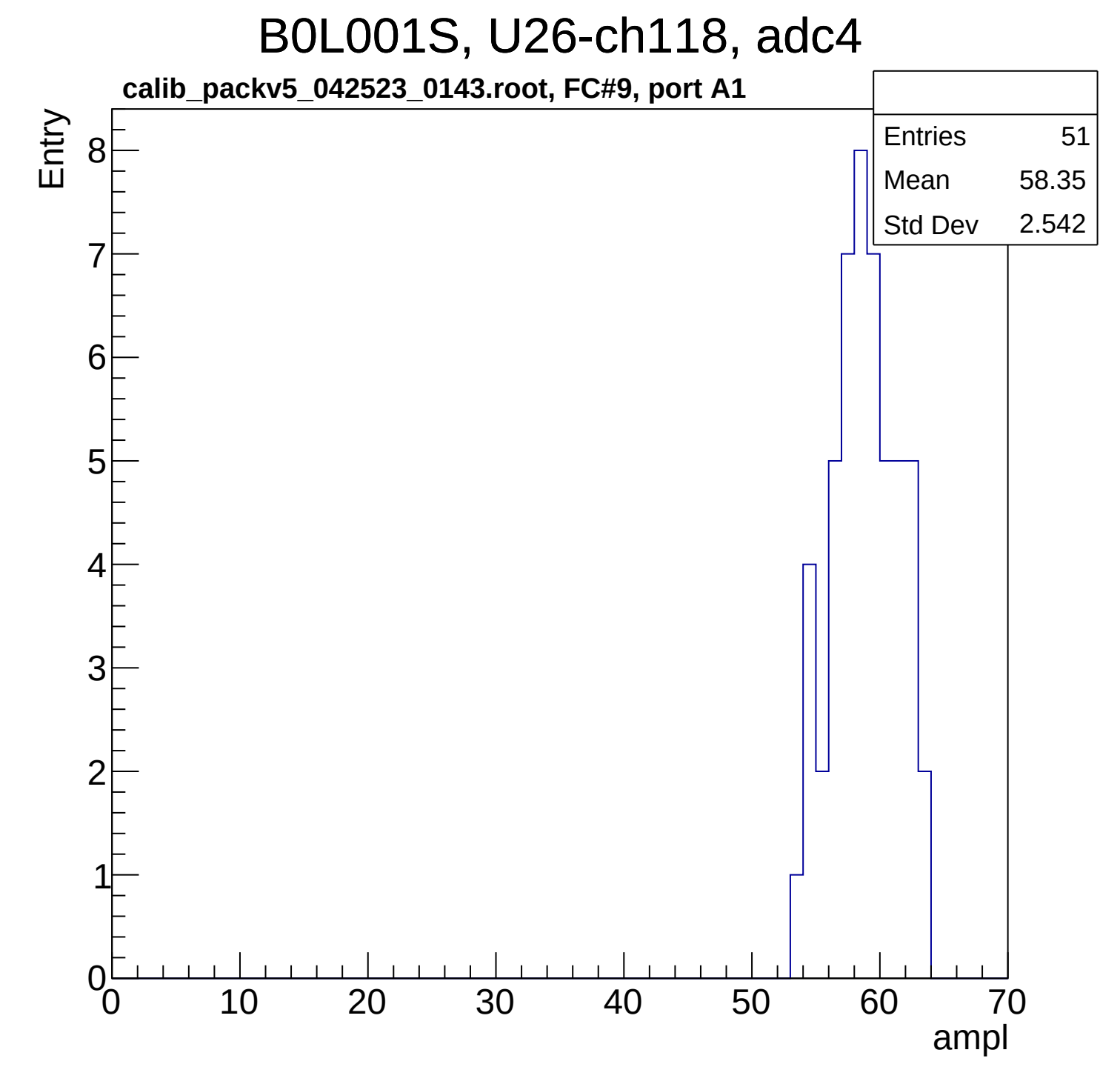
B0L001S, U26-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.35
Std Dev 2.542
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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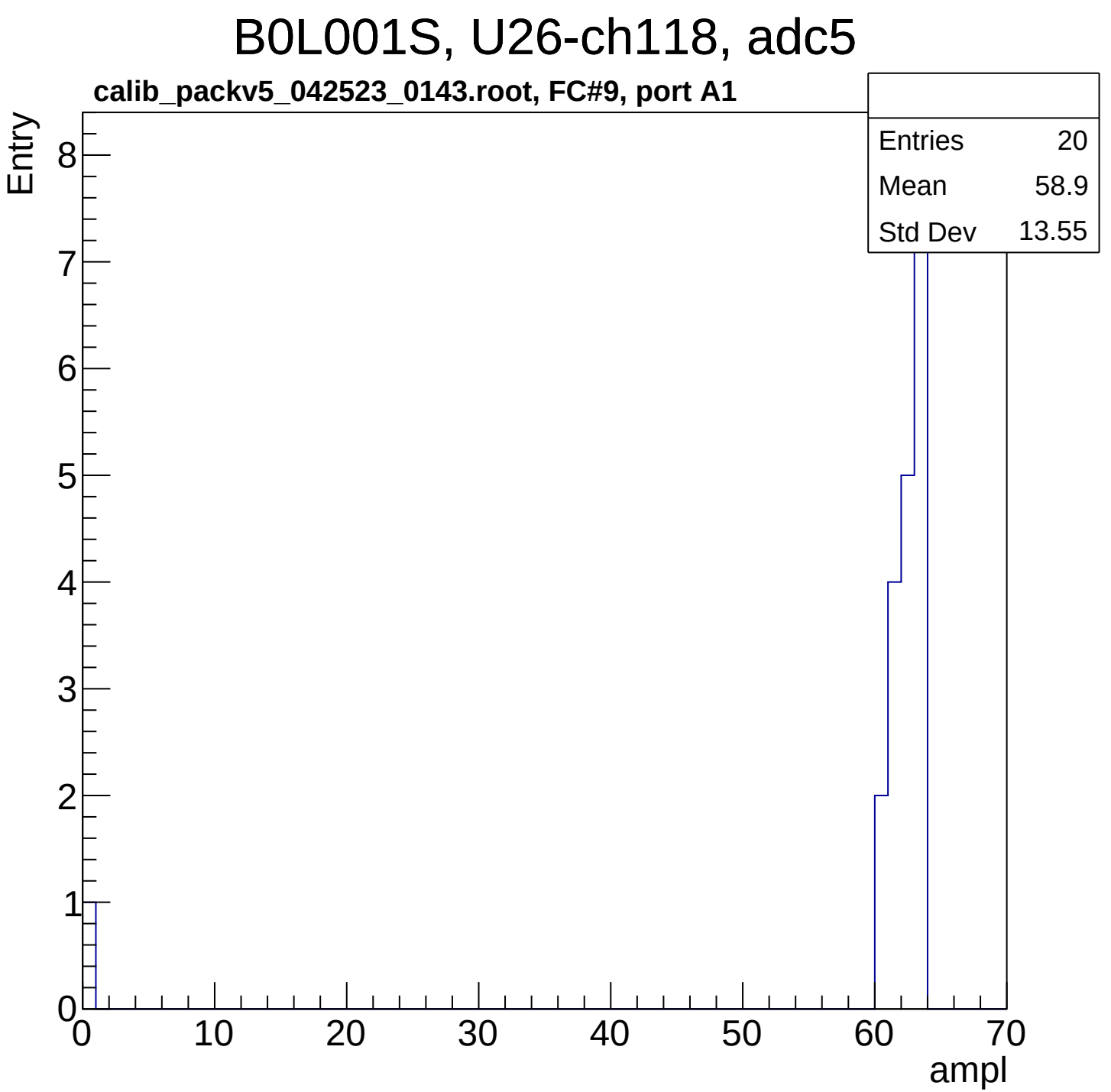
B0L001S, U26-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 58.9
Std Dev 13.55
Entry
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2
3
4
5
6
7
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0
10
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30
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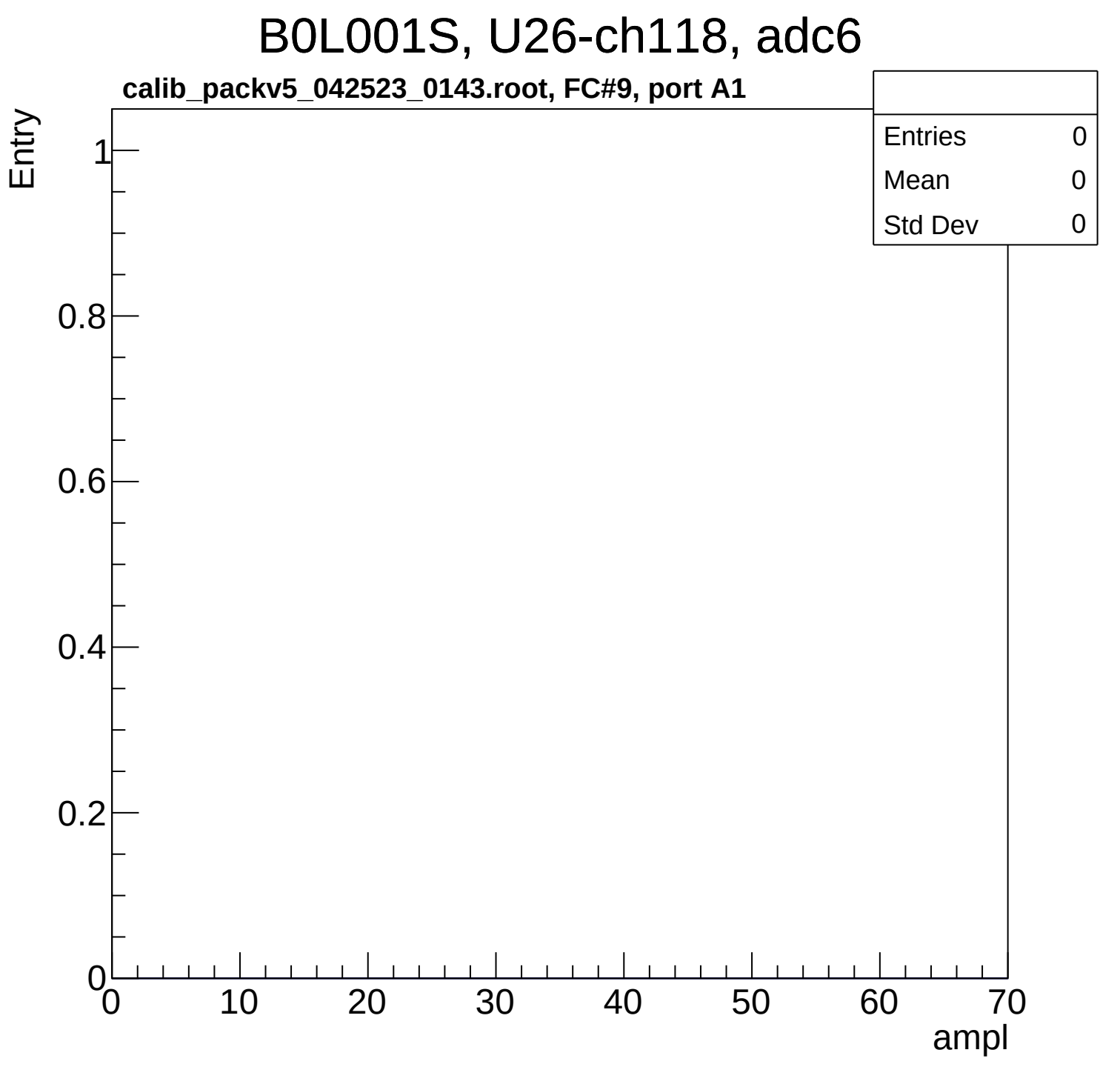
50
60
70
ampl


B0L001S, U26-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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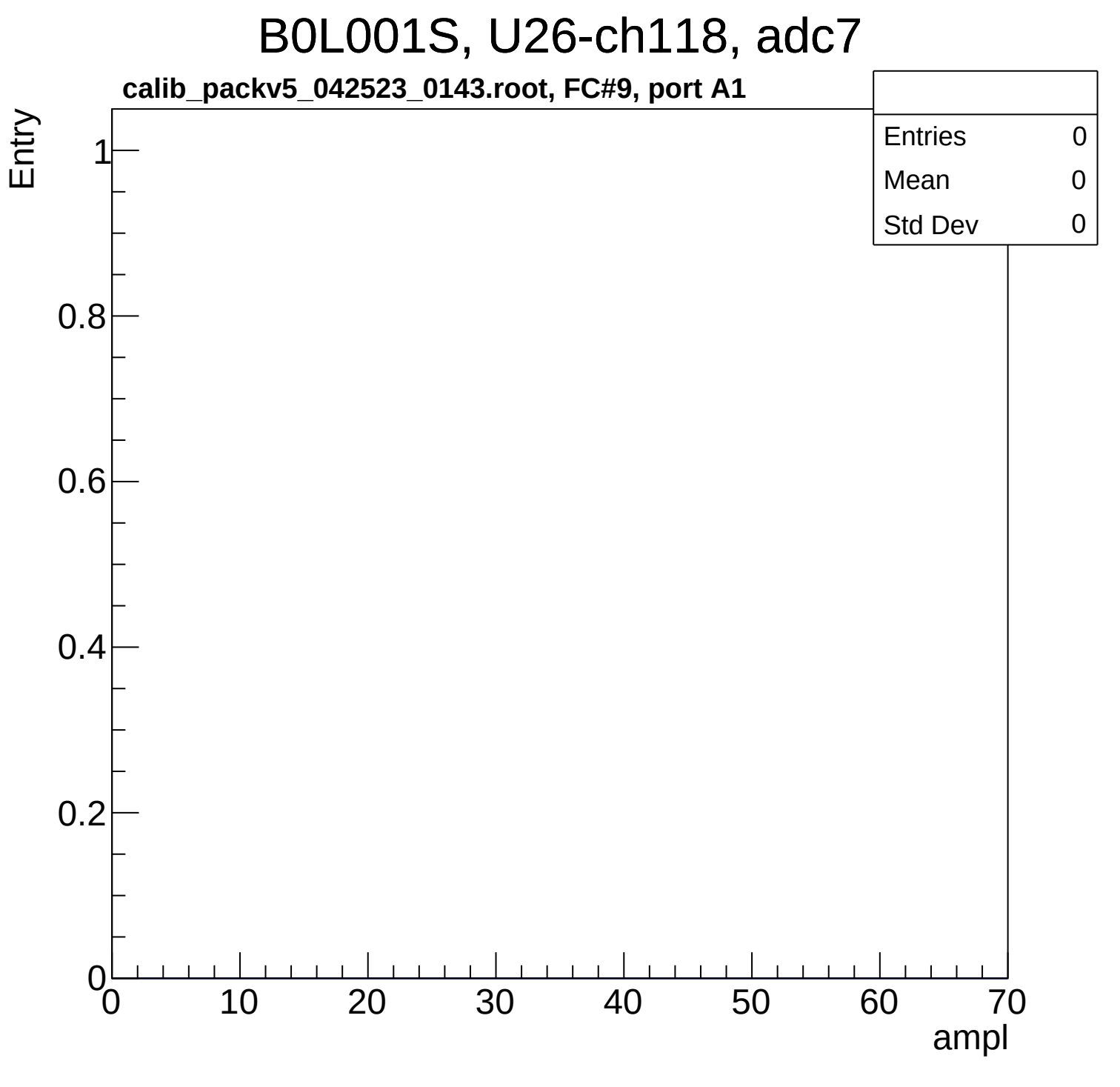
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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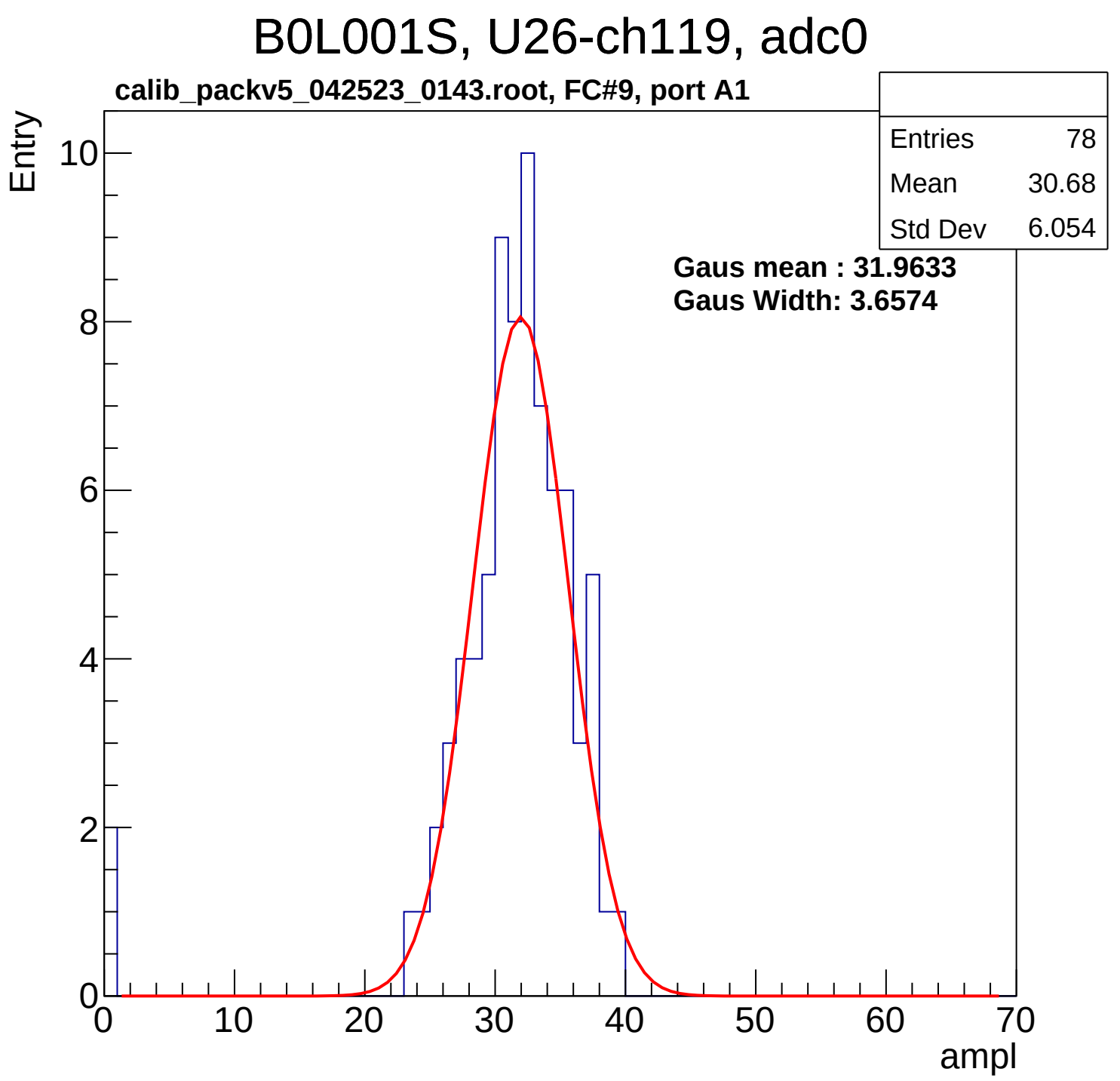
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.68
Std Dev 6.054
Gaus mean : 31.9633
Gaus Width: 3.6574
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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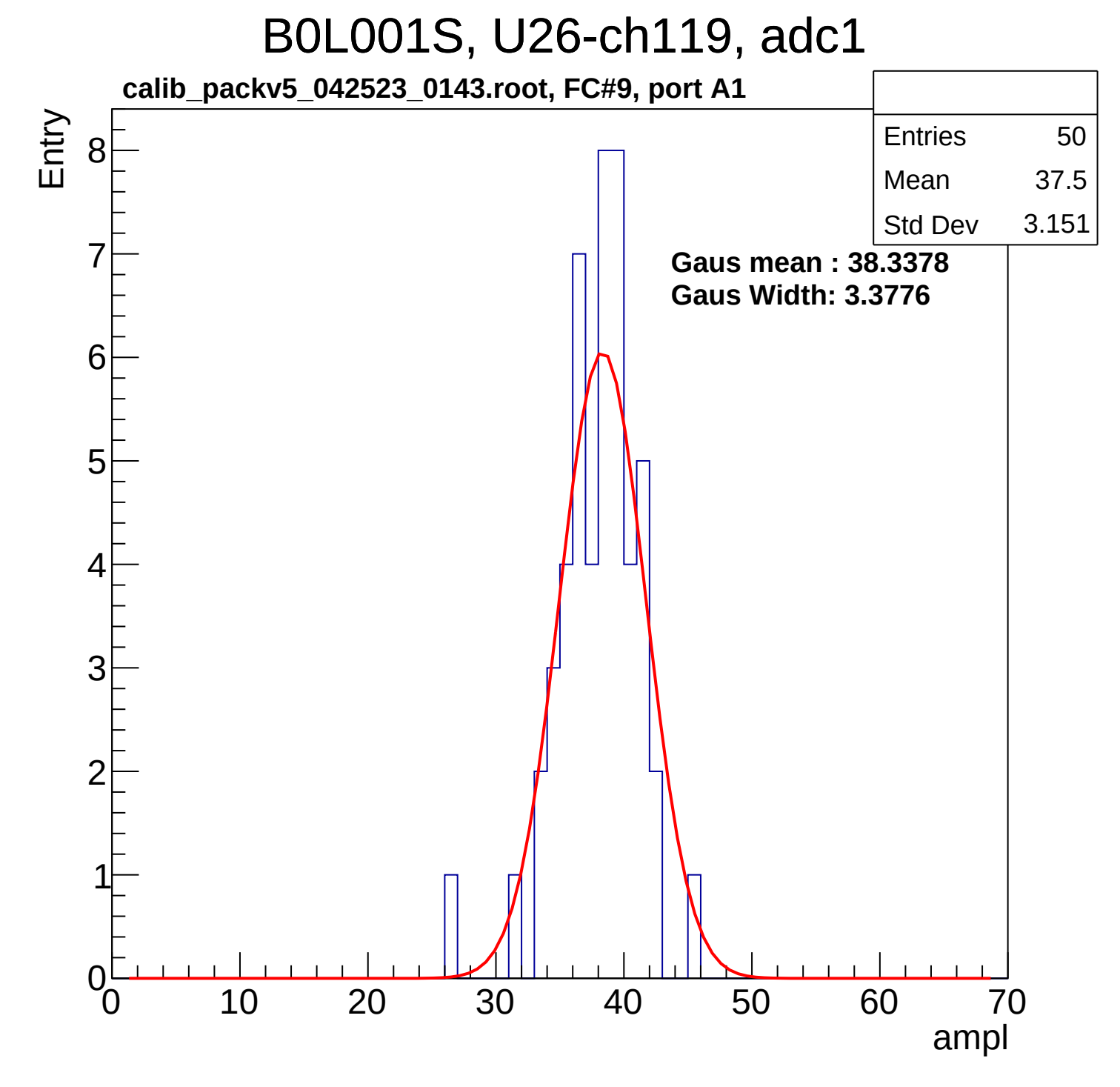
B0L001S, U26-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 37.5
Std Dev 3.151
Gaus mean : 38.3378
Gaus Width: 3.3776
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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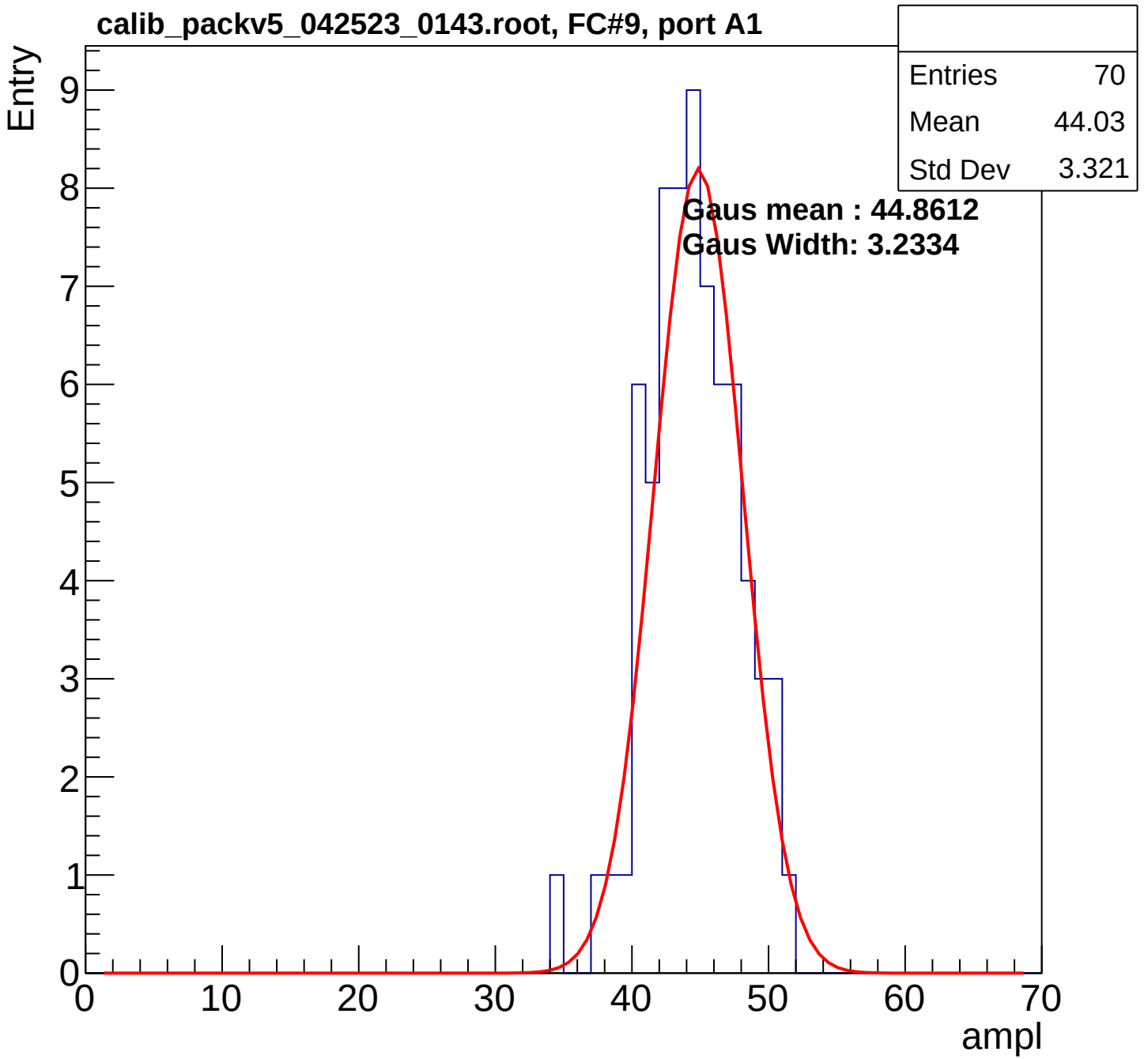
B0L001S, U26-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.03
Std Dev 3.321
Gaus mean : 44.8612
Gaus Width: 3.2334
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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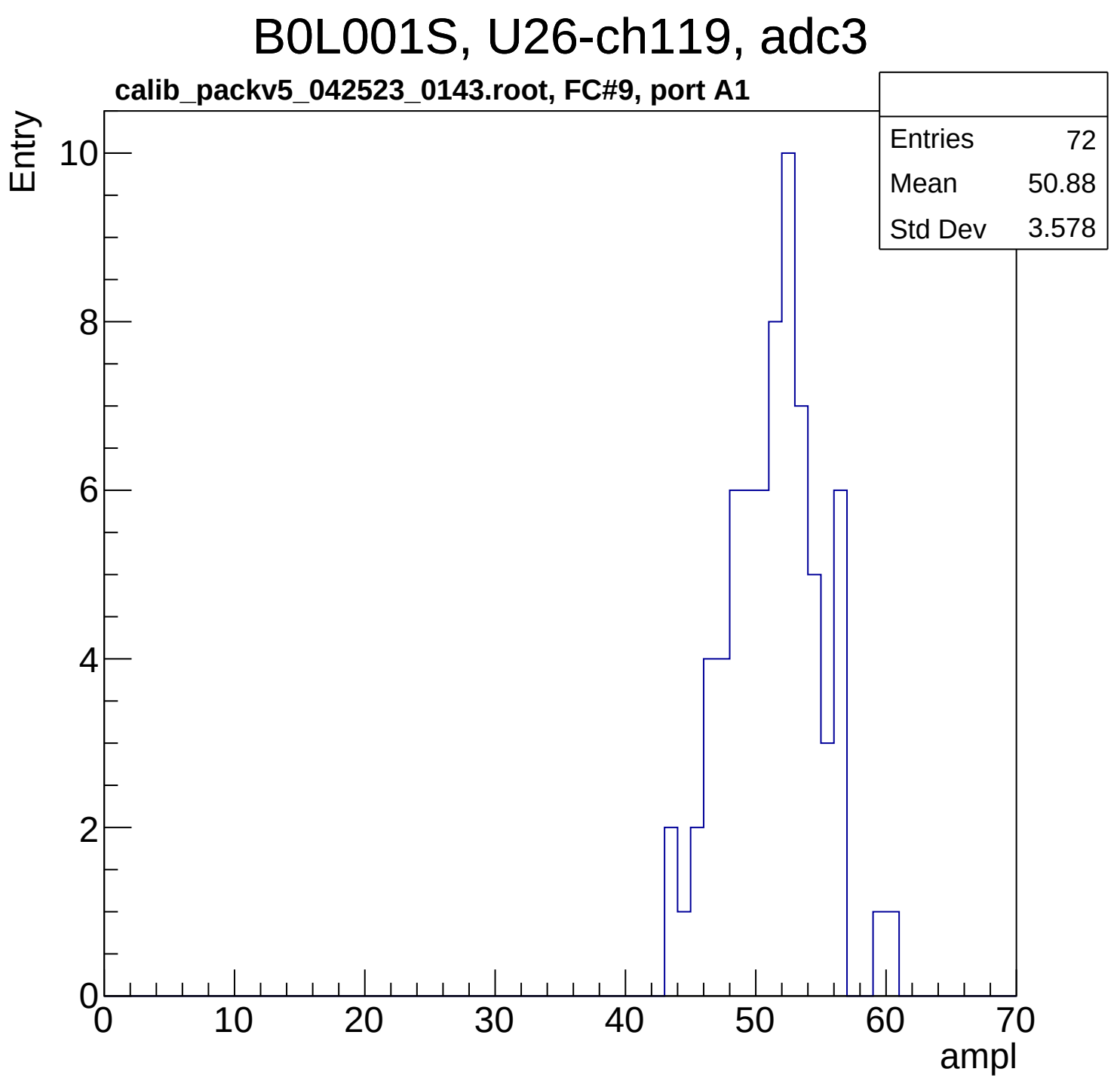
B0L001S, U26-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.88
Std Dev 3.578
Entry
ampl
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0
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40
50
60
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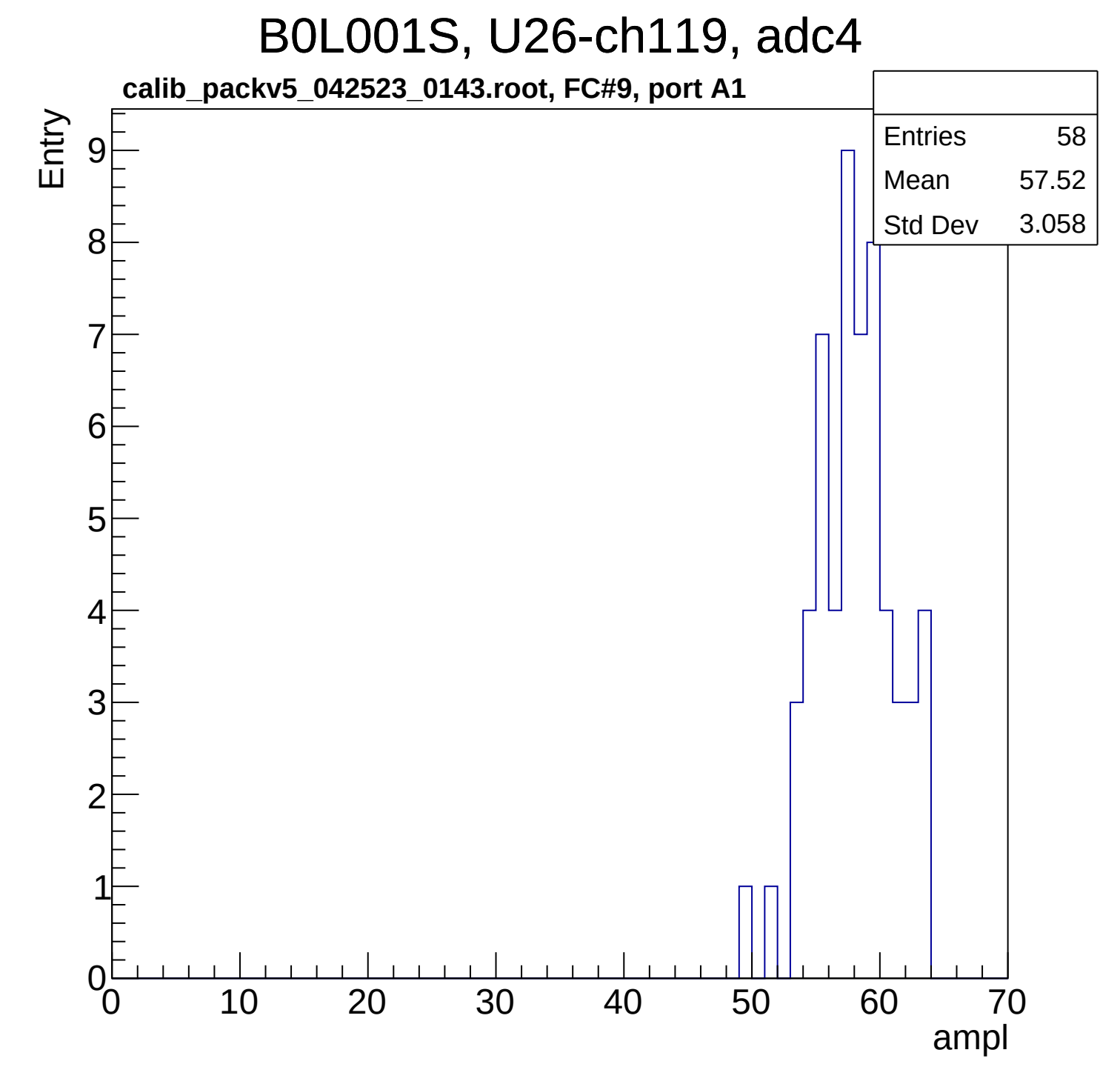
B0L001S, U26-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.52
Std Dev 3.058
Entry
ampl
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3
4
5
6
7
8
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0
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40
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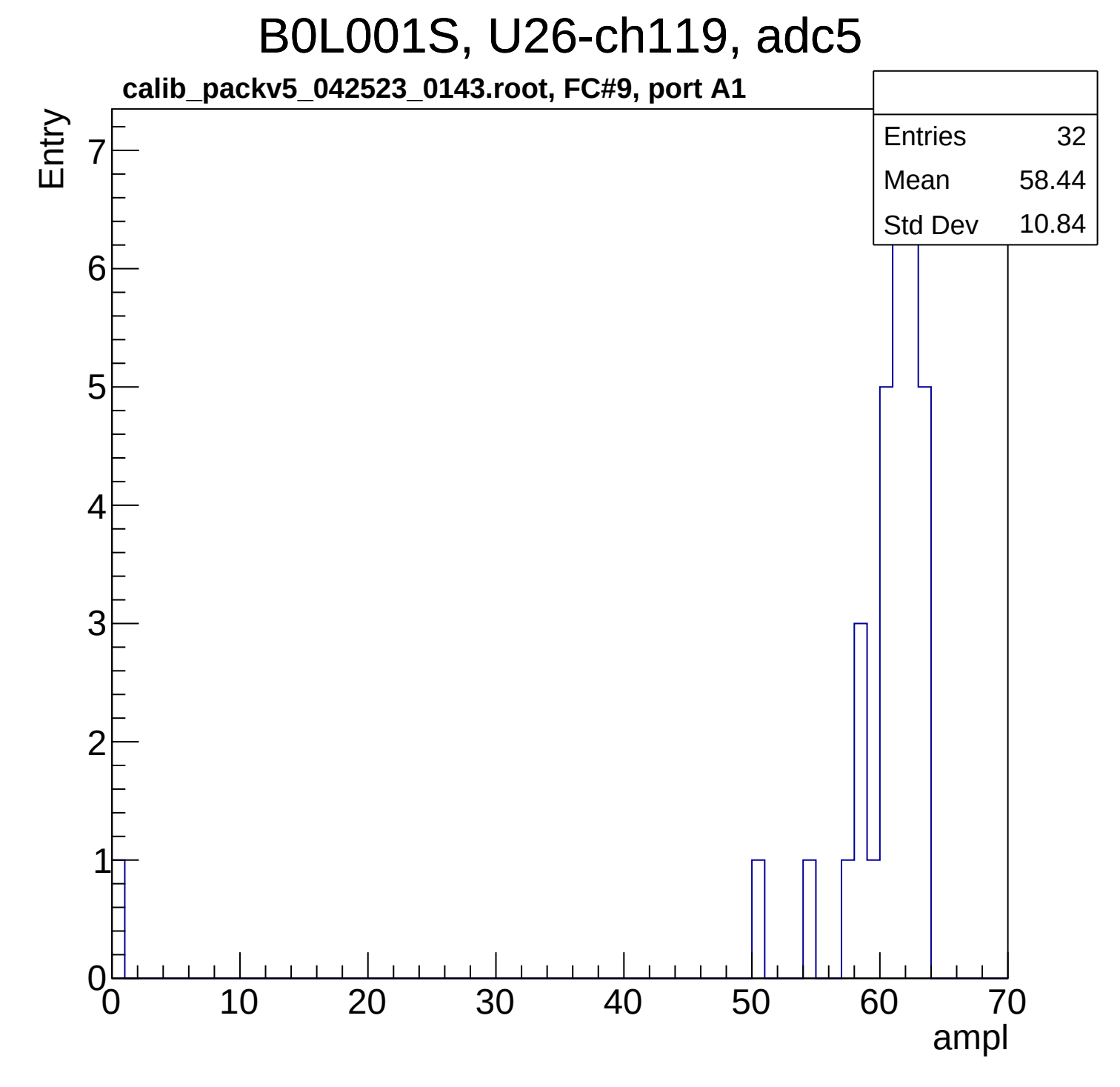
B0L001S, U26-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.44
Std Dev 10.84
Entry
ampl
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4
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0
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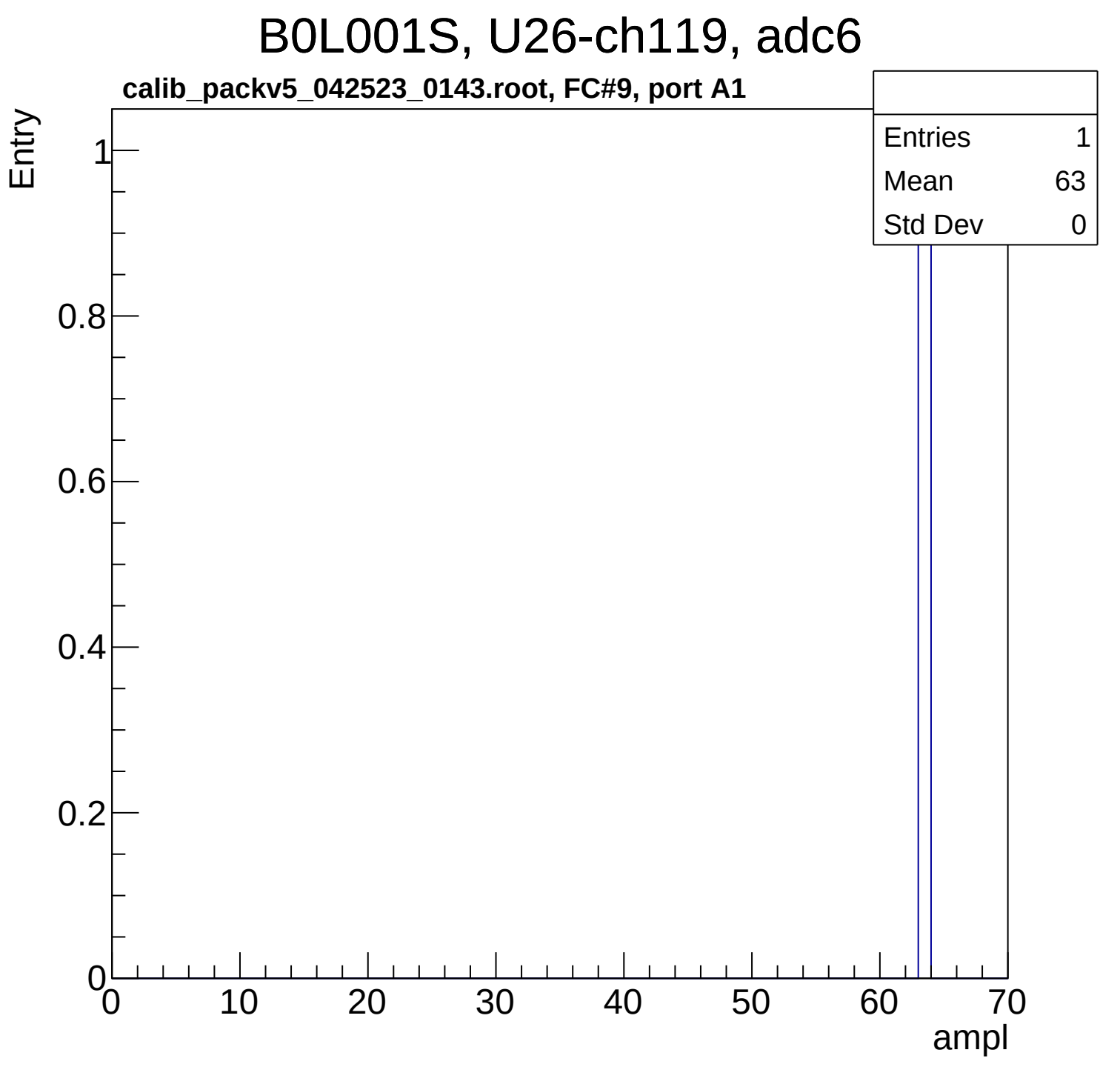
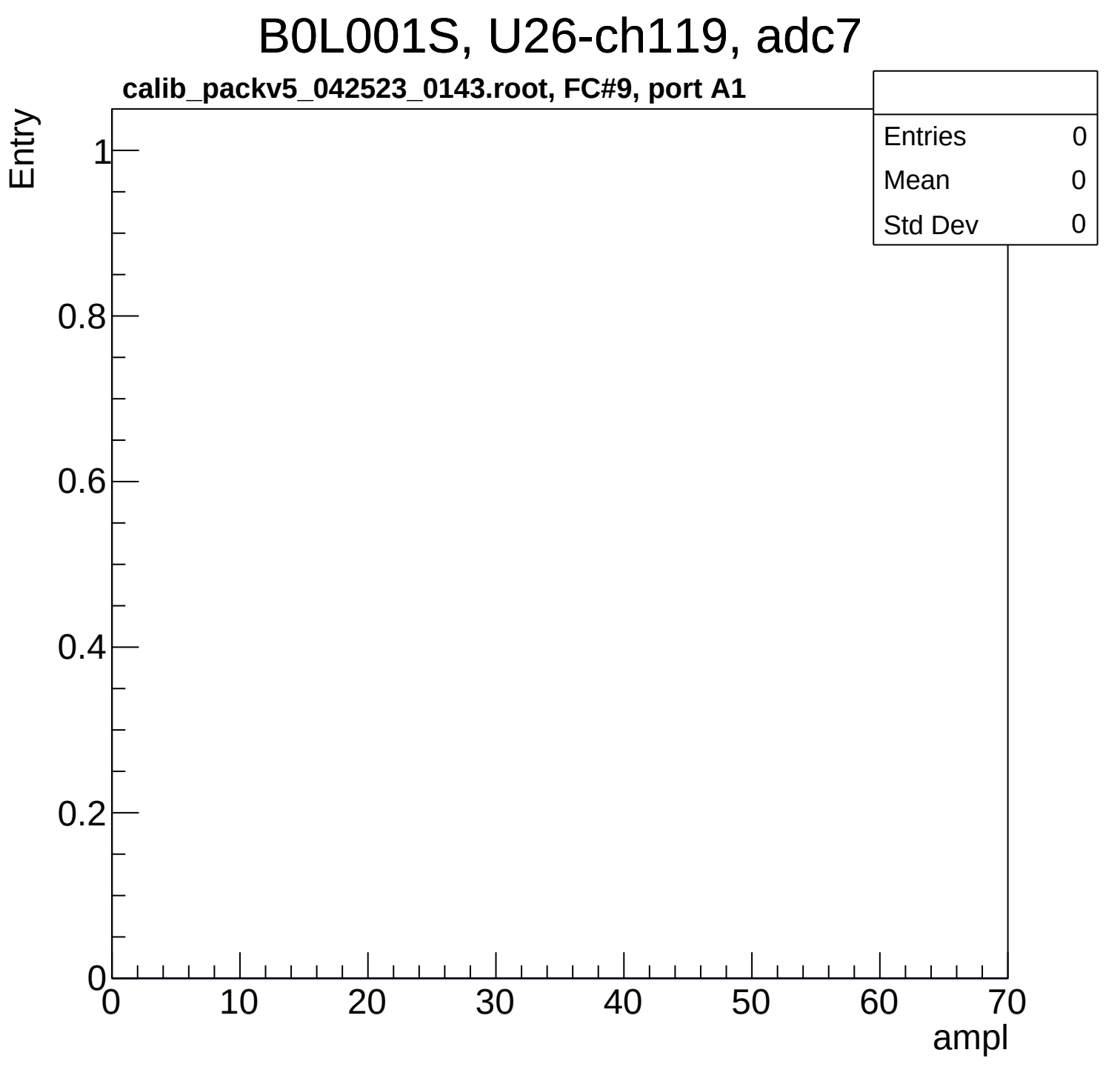
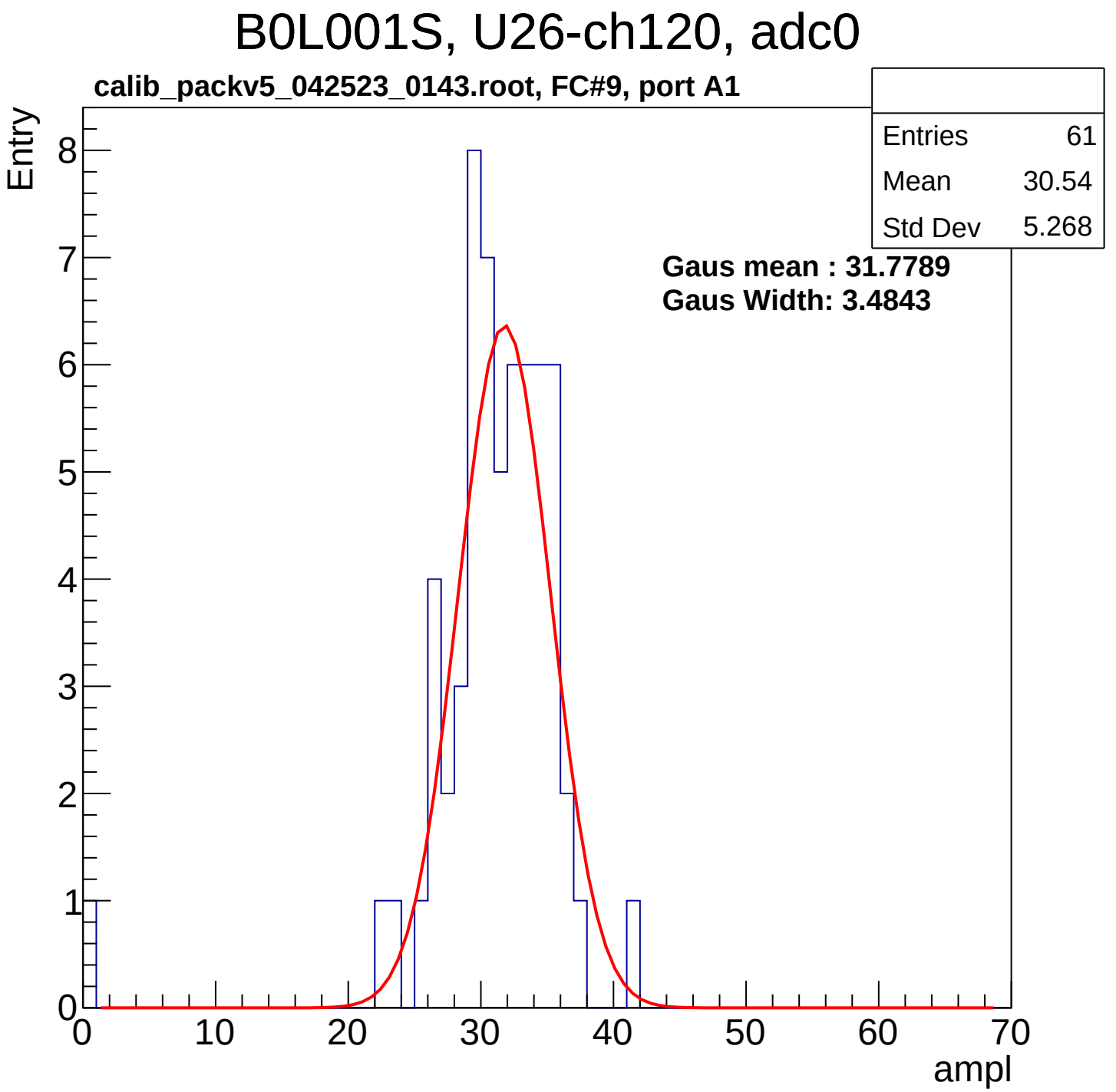

B0L001S, U26-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.54
Std Dev 5.268
Gaus mean : 31.7789
Gaus Width: 3.4843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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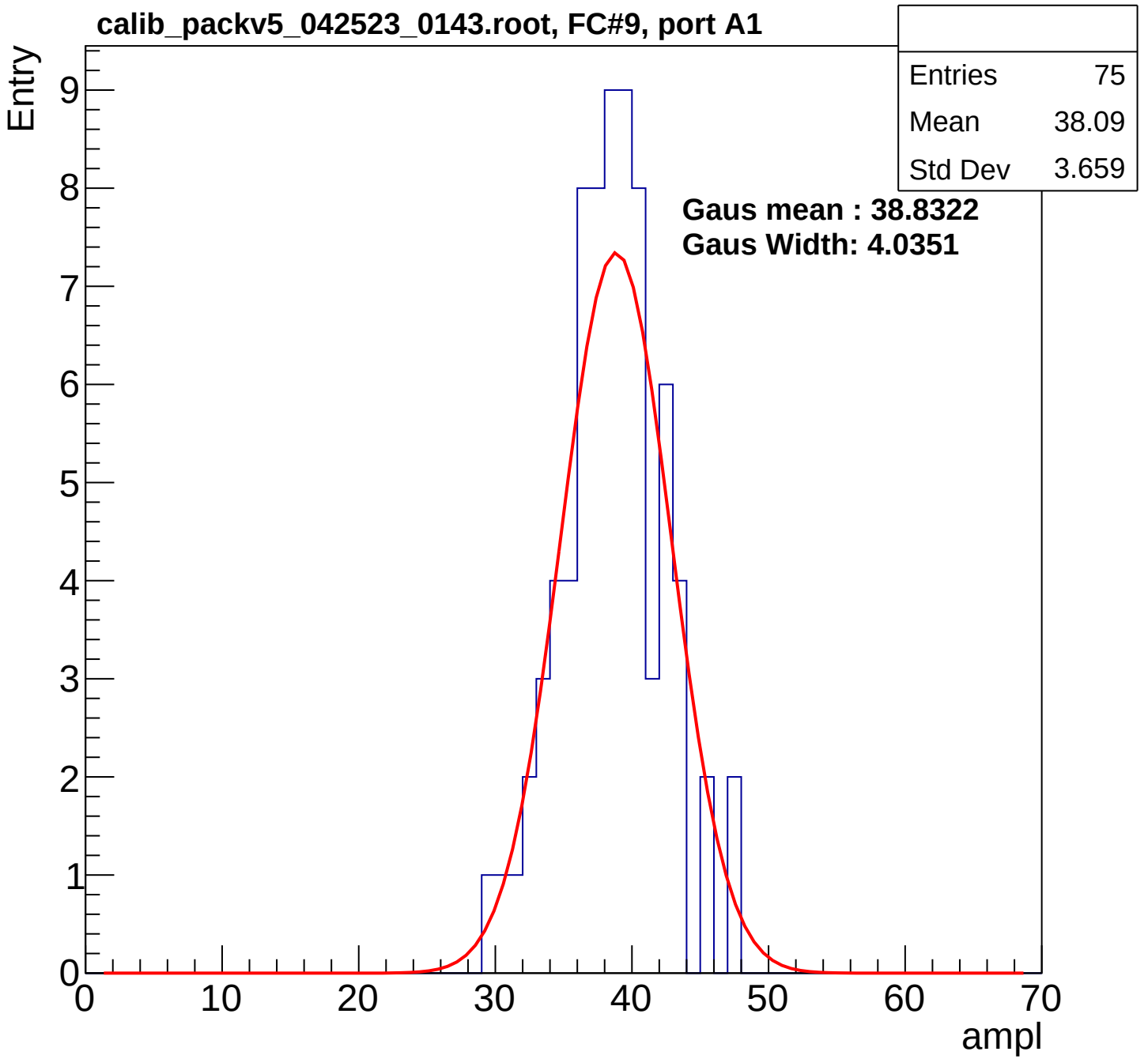
B0L001S, U26-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.09
Std Dev 3.659
Gaus mean : 38.8322
Gaus Width: 4.0351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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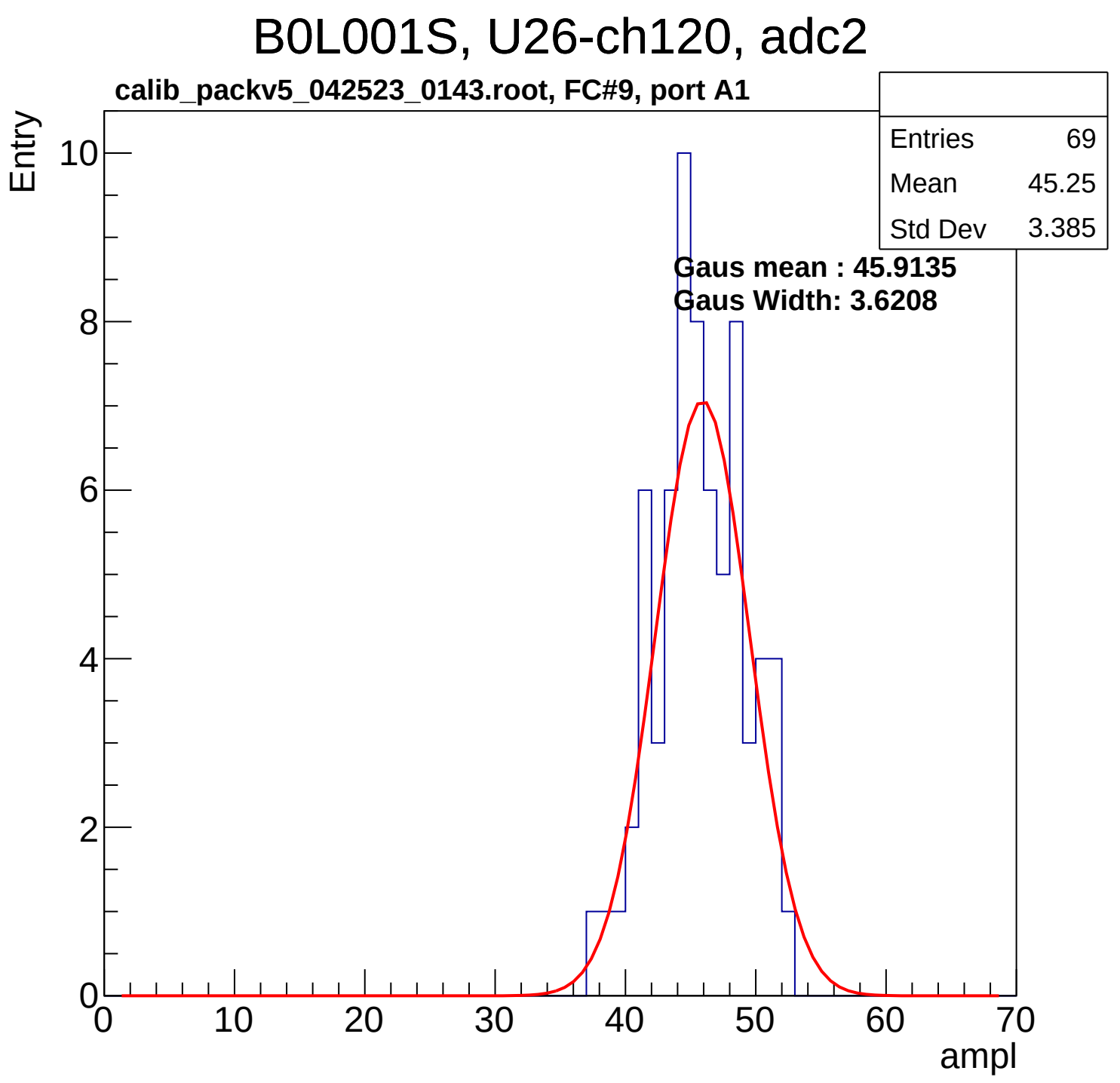
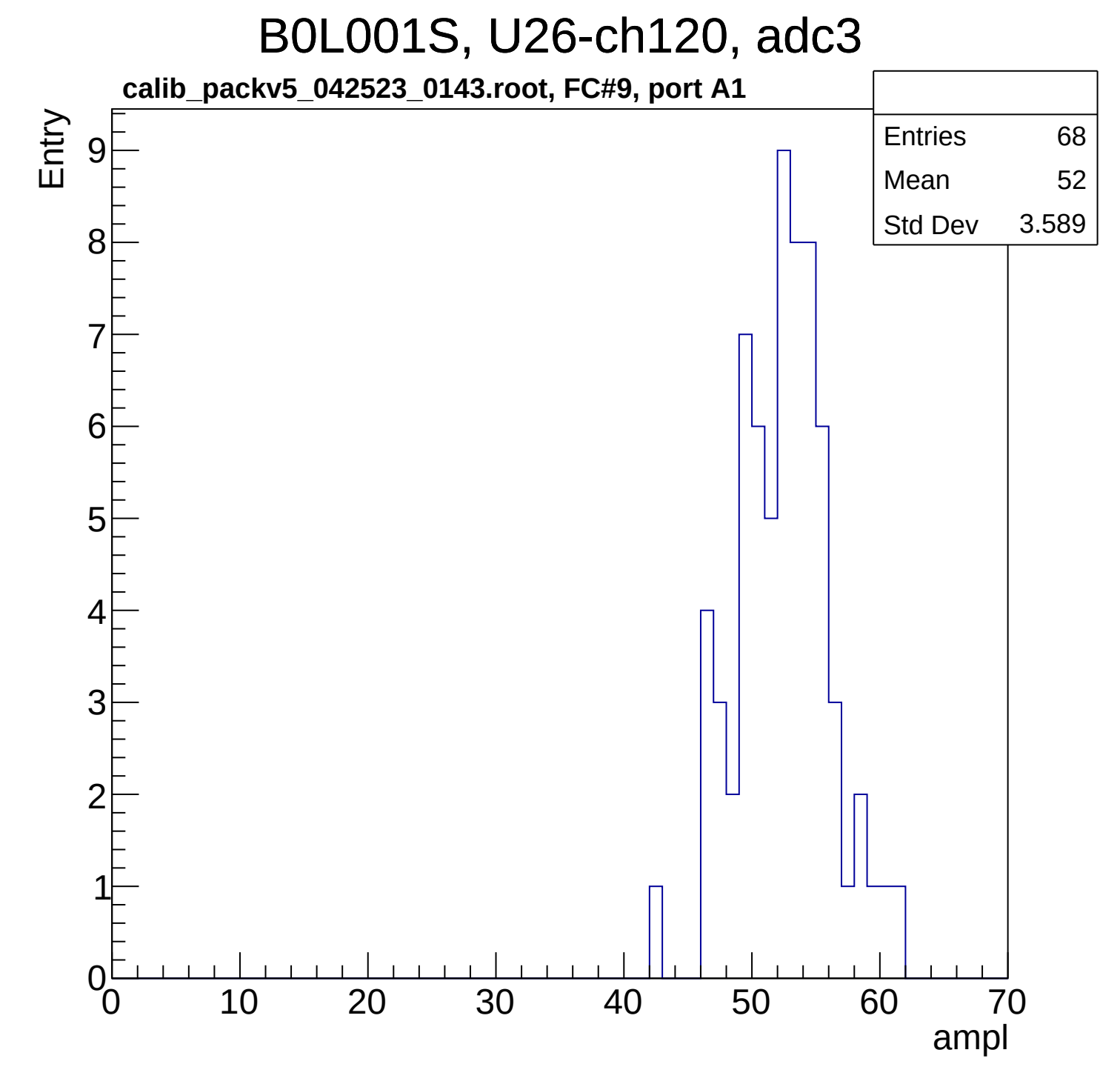

B0L001S, U26-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.25
Std Dev 3.385
Gaus mean : 45.9135
Gaus Width: 3.6208
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52
Std Dev 3.589
Entry
ampl
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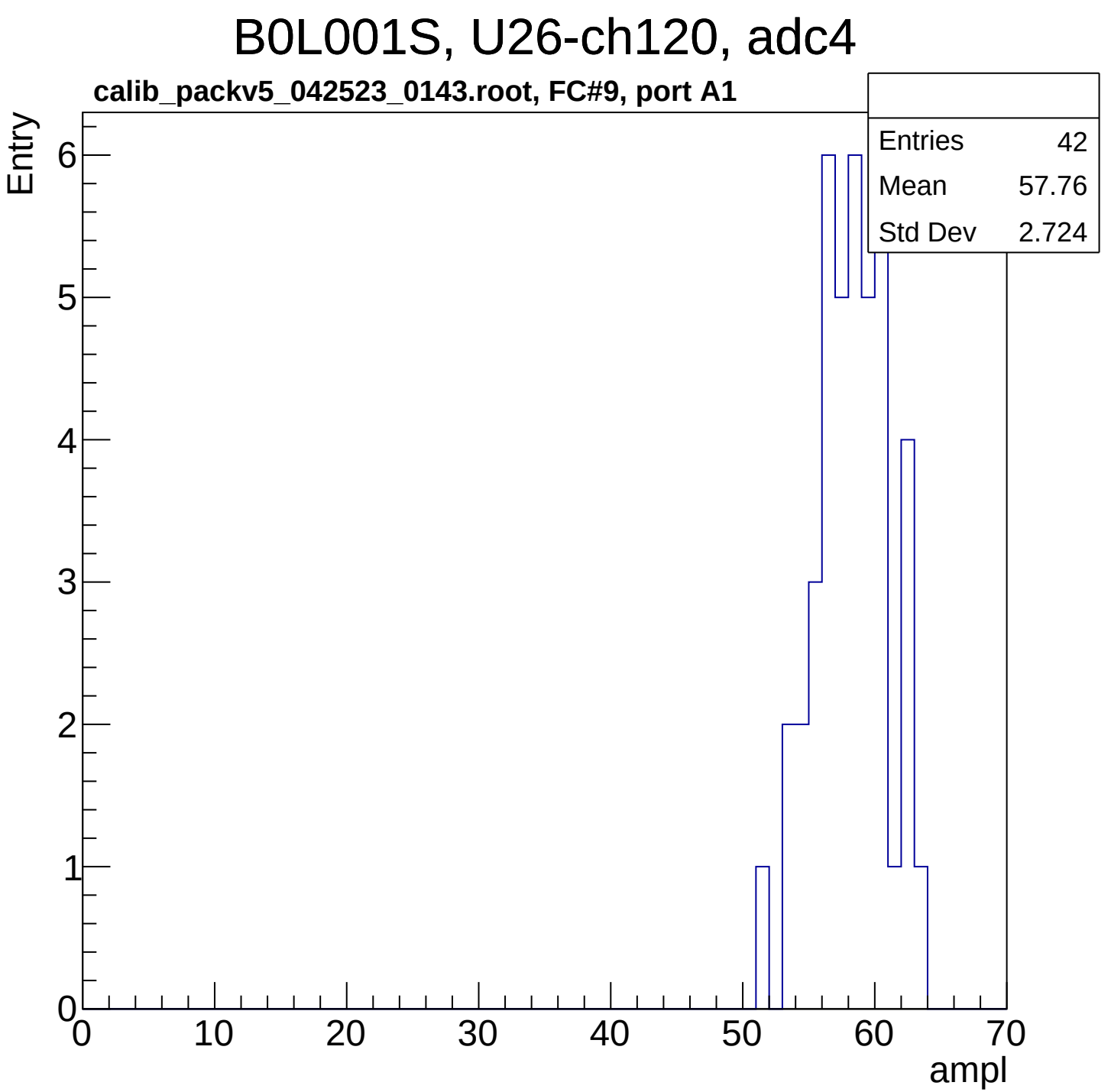
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 57.76
Std Dev 2.724
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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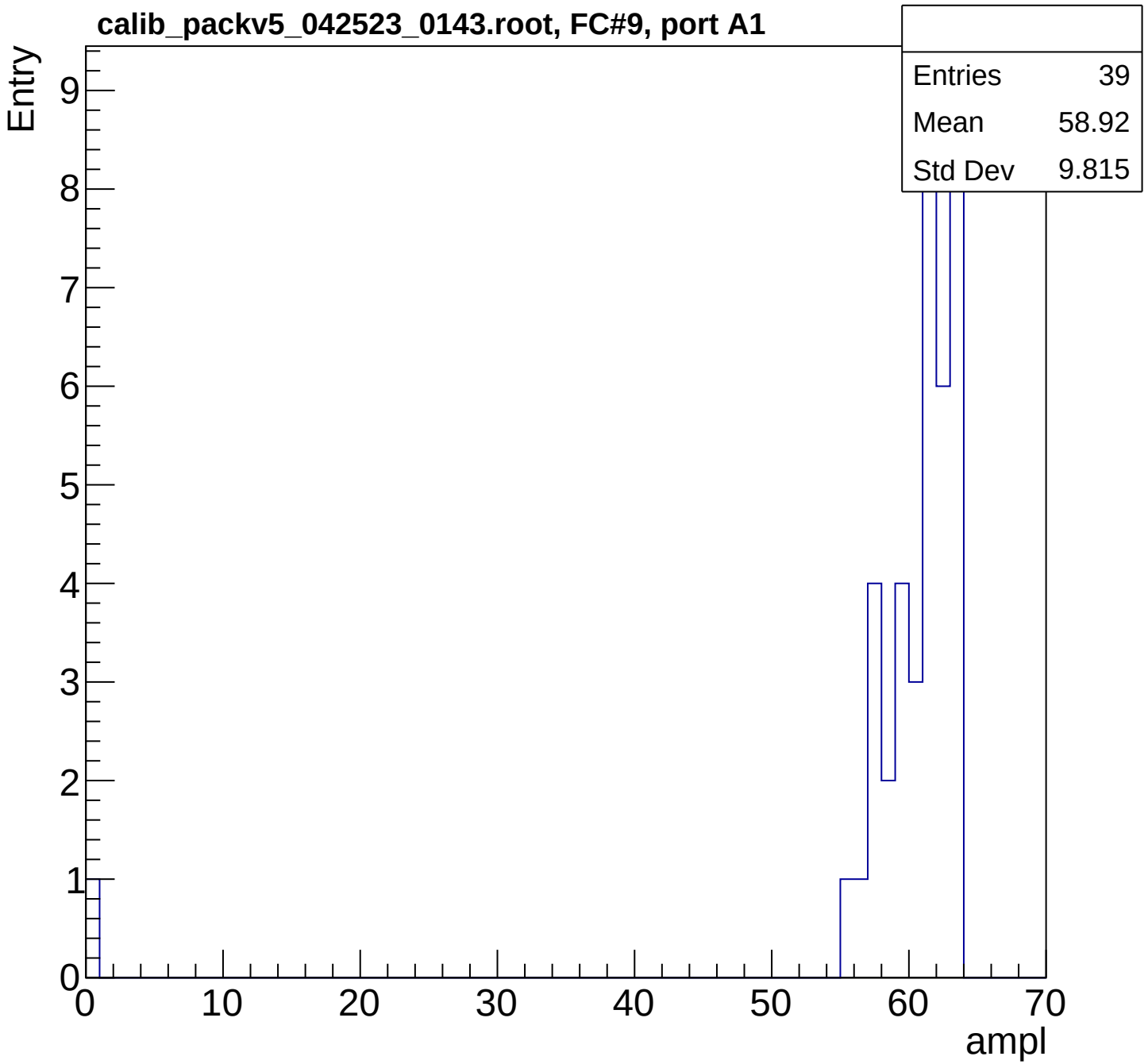
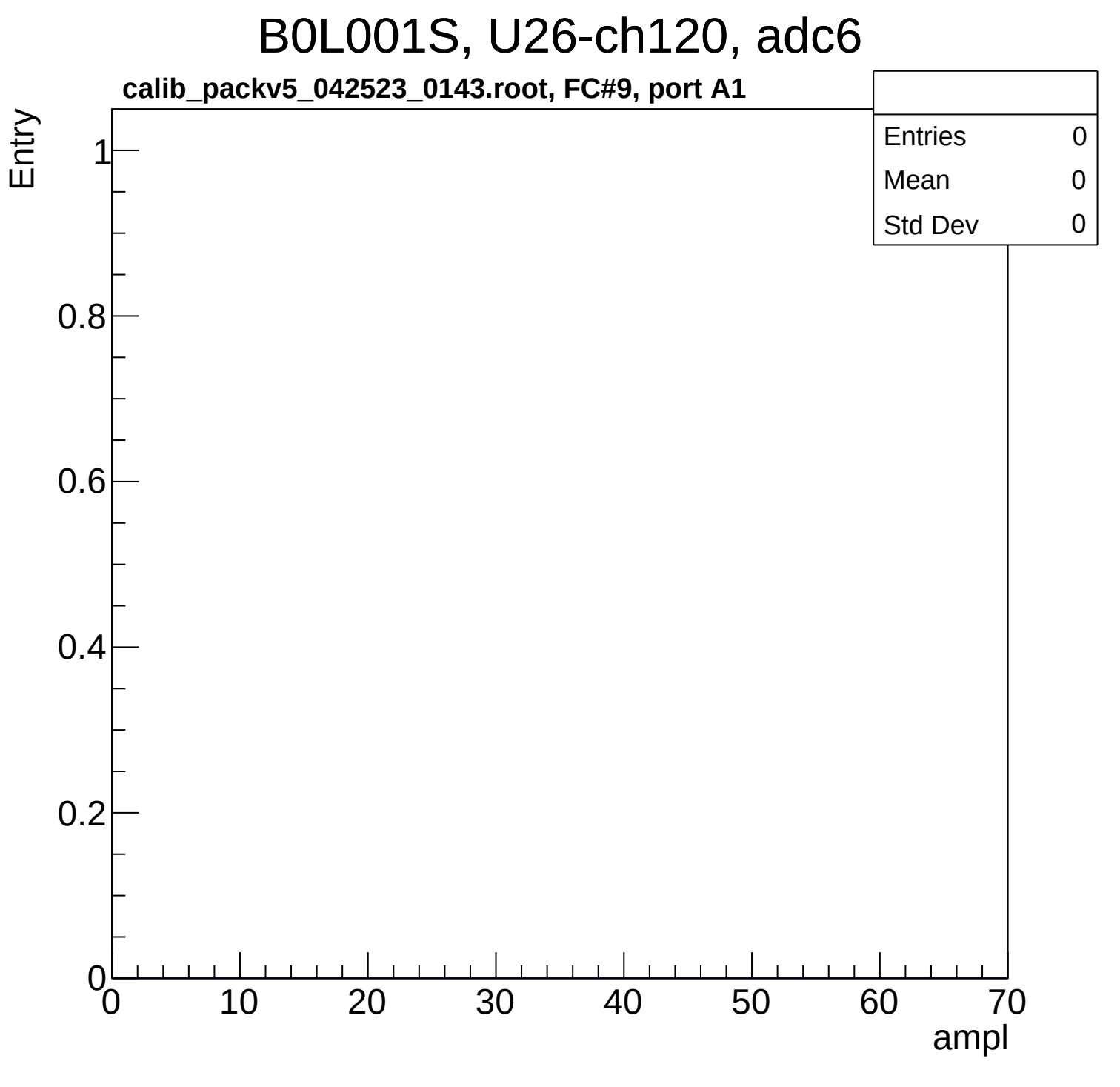

B0L001S, U26-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.92
Std Dev 9.815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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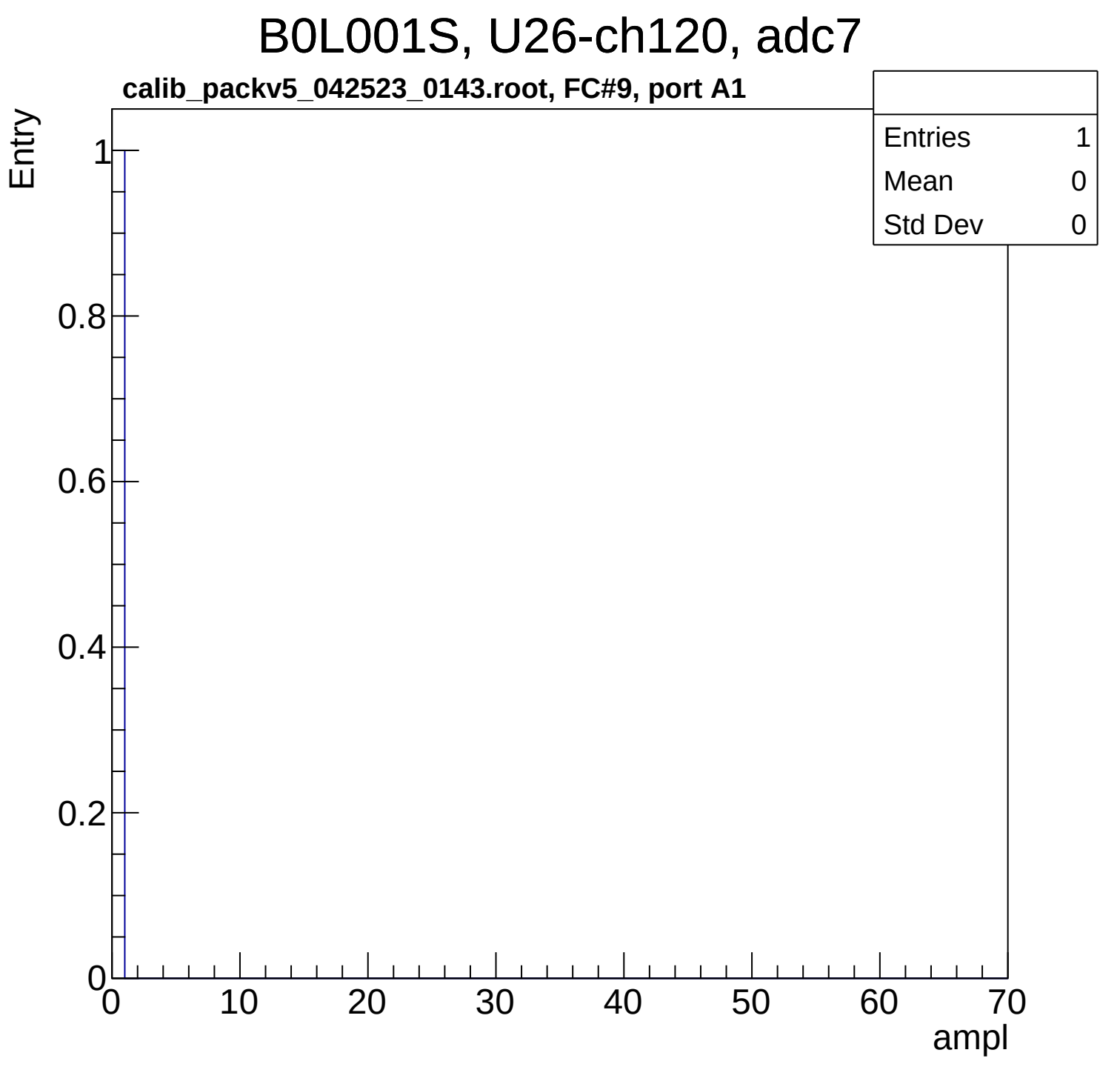
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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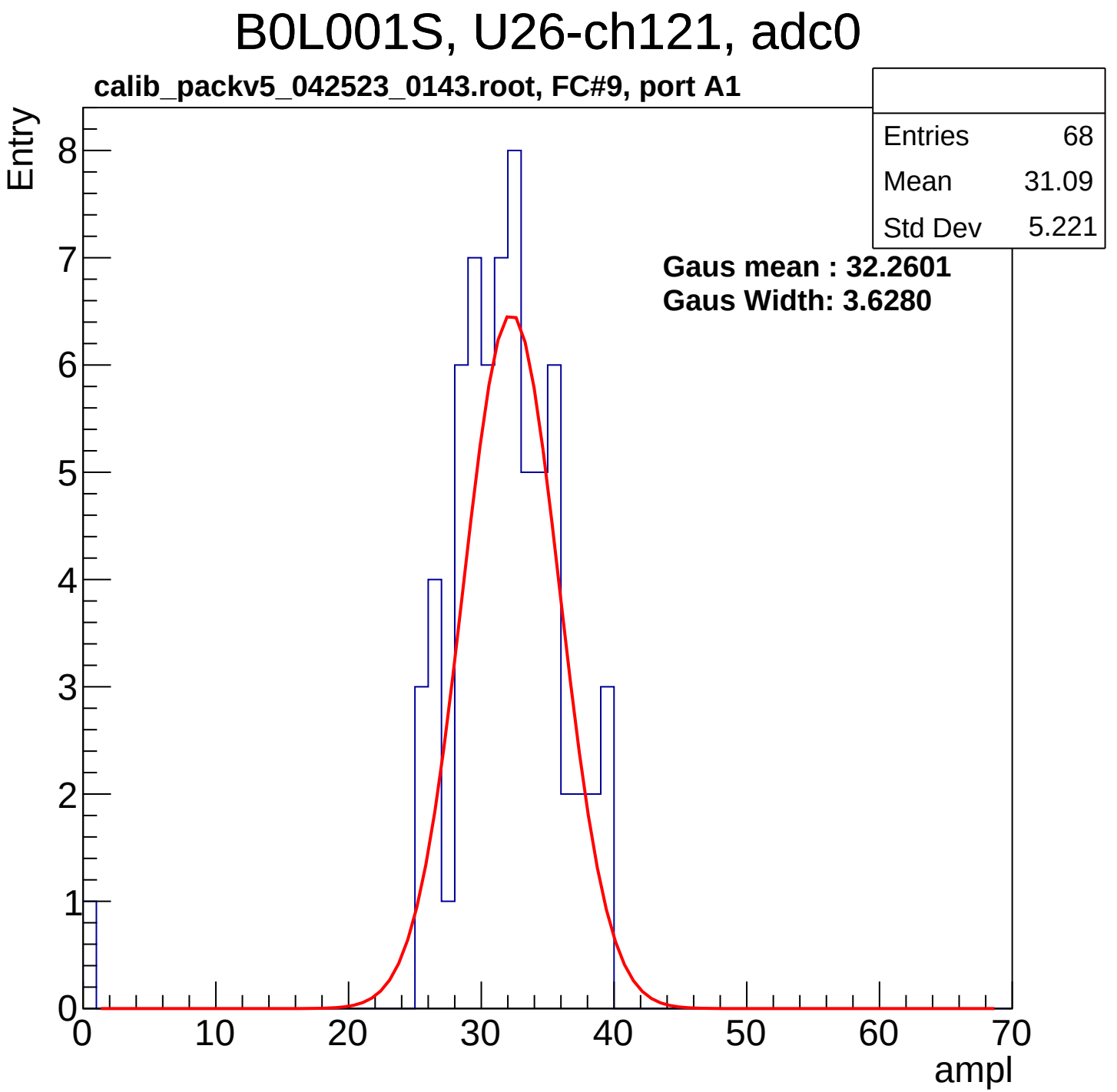
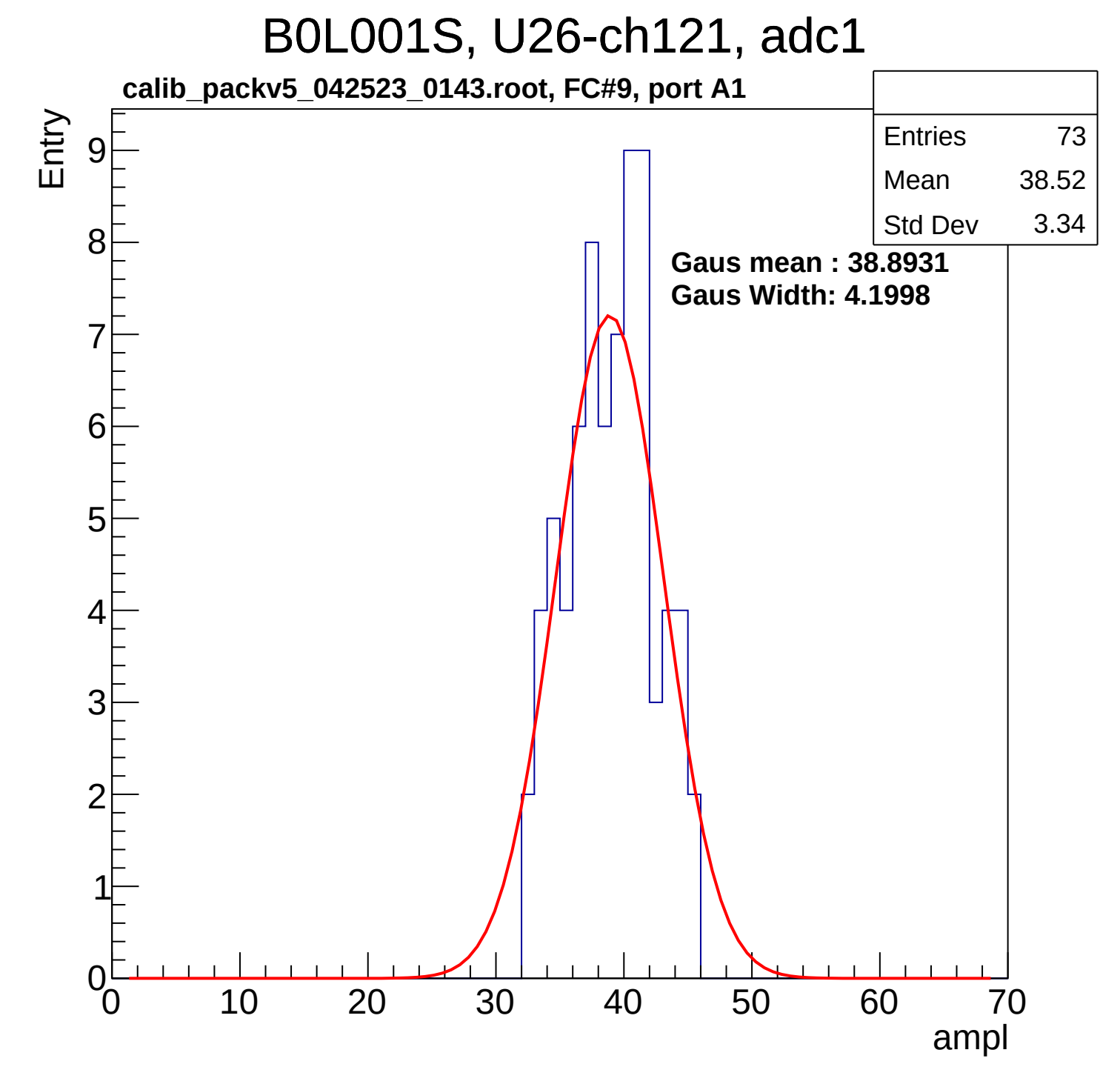
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.09
Std Dev 5.221
Gaus mean : 32.2601
Gaus Width: 3.6280
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.52
Std Dev 3.34
Gaus mean : 38.8931
Gaus Width: 4.1998
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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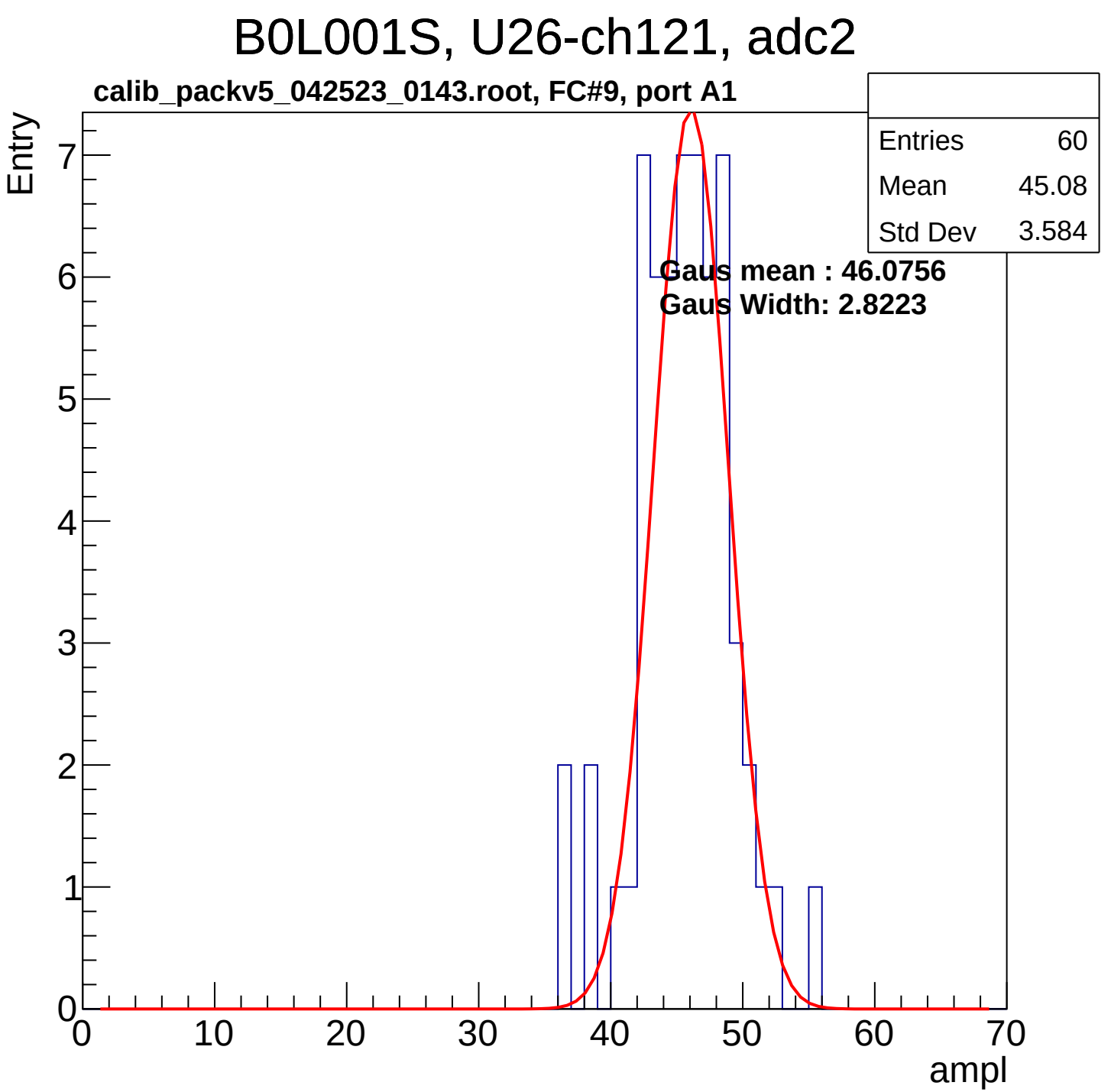
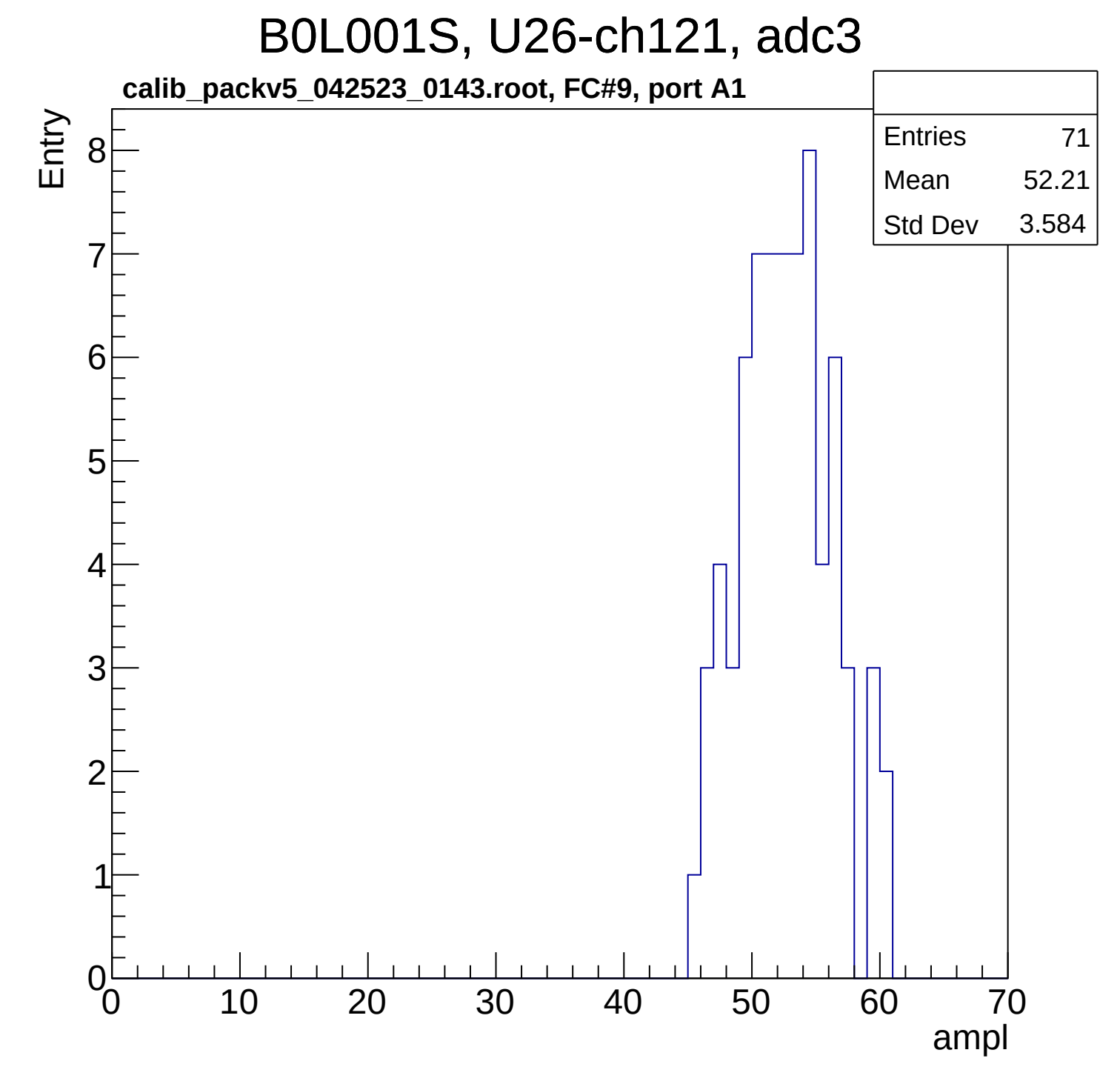

B0L001S, U26-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.08
Std Dev 3.584
Gaus mean : 46.0756
Gaus Width: 2.8223
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U26-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 52.21
Std Dev 3.584
Entry
ampl
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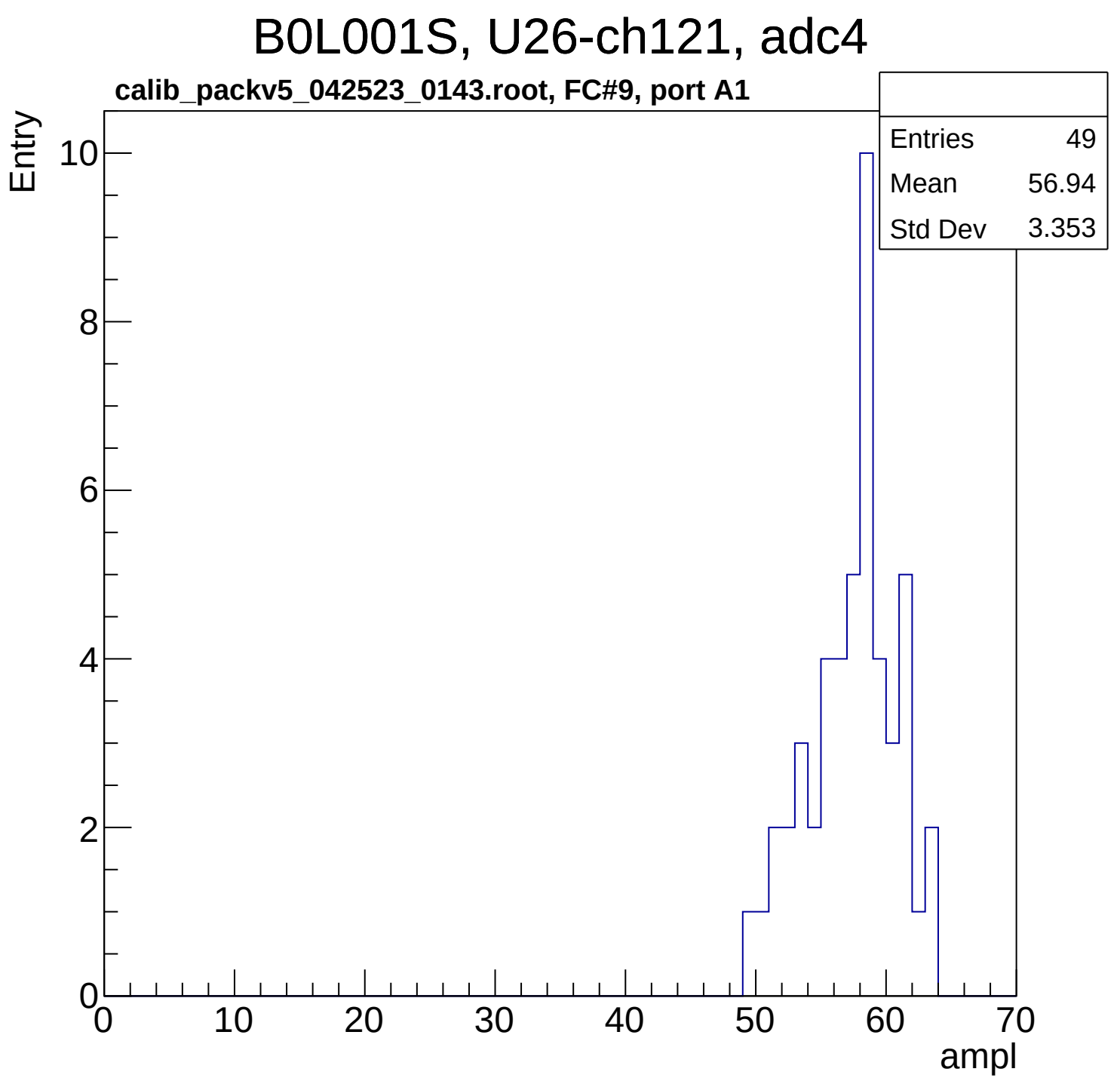
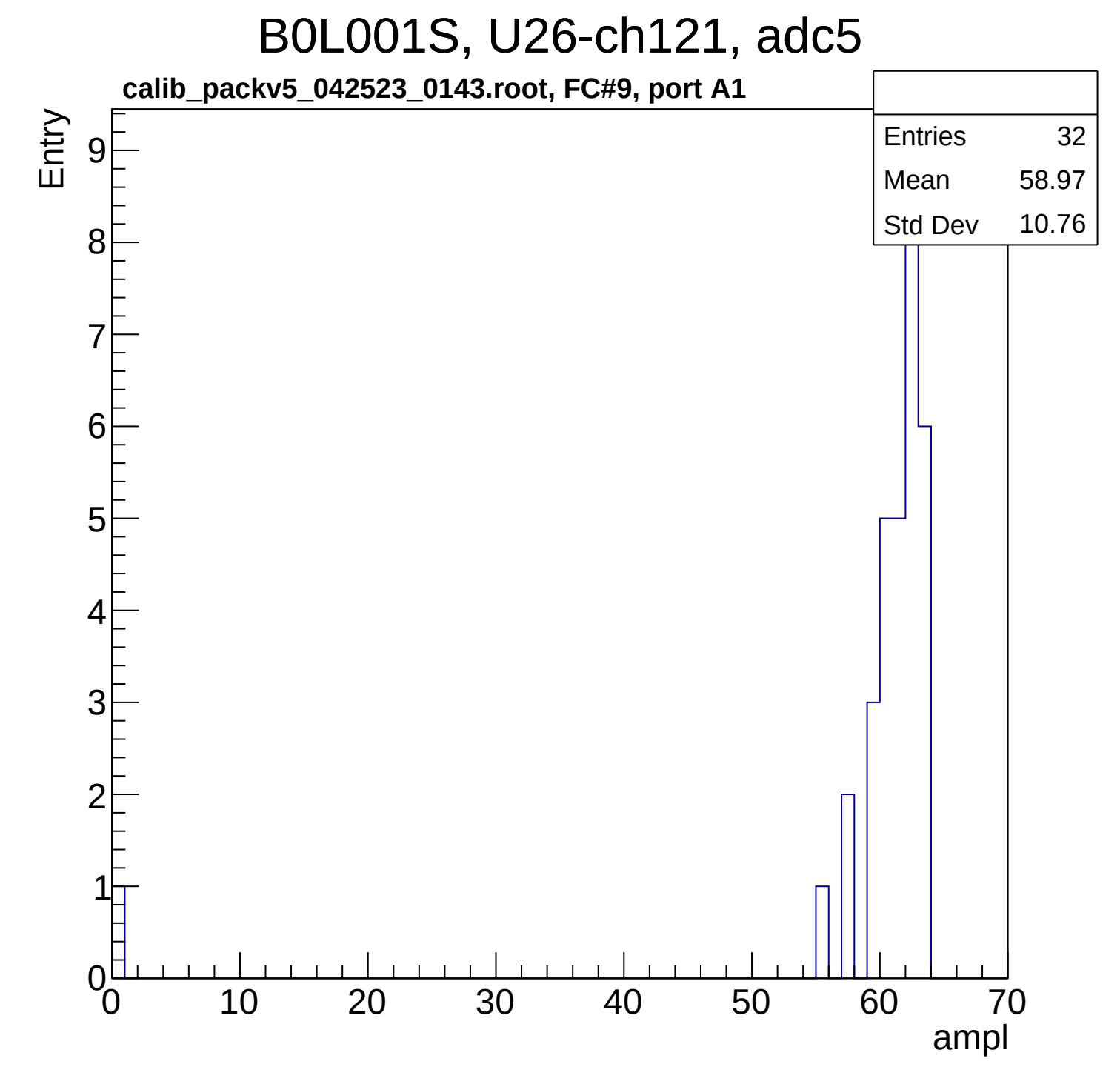
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 56.94
Std Dev 3.353
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.97
Std Dev 10.76
Entry
ampl
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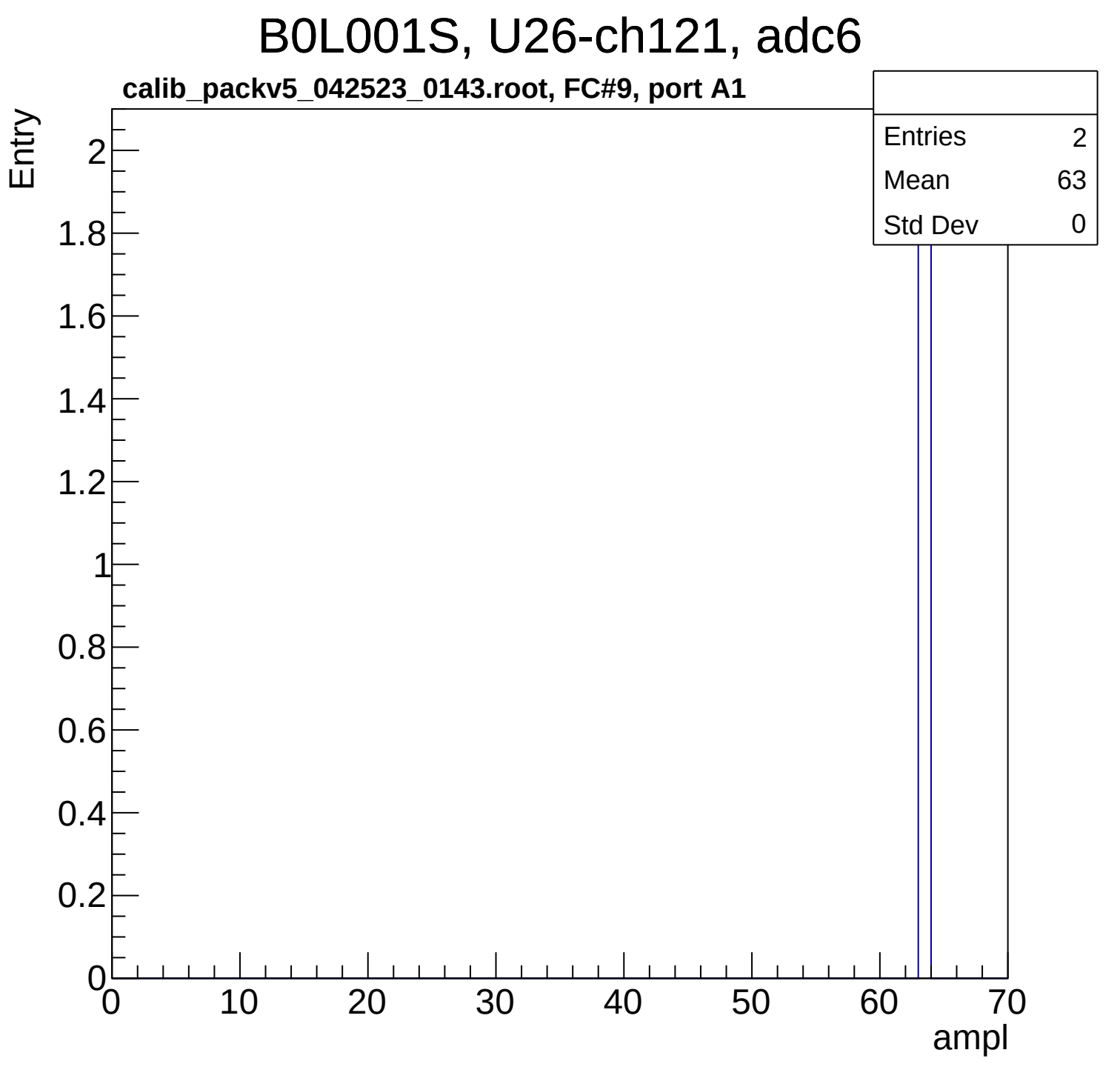
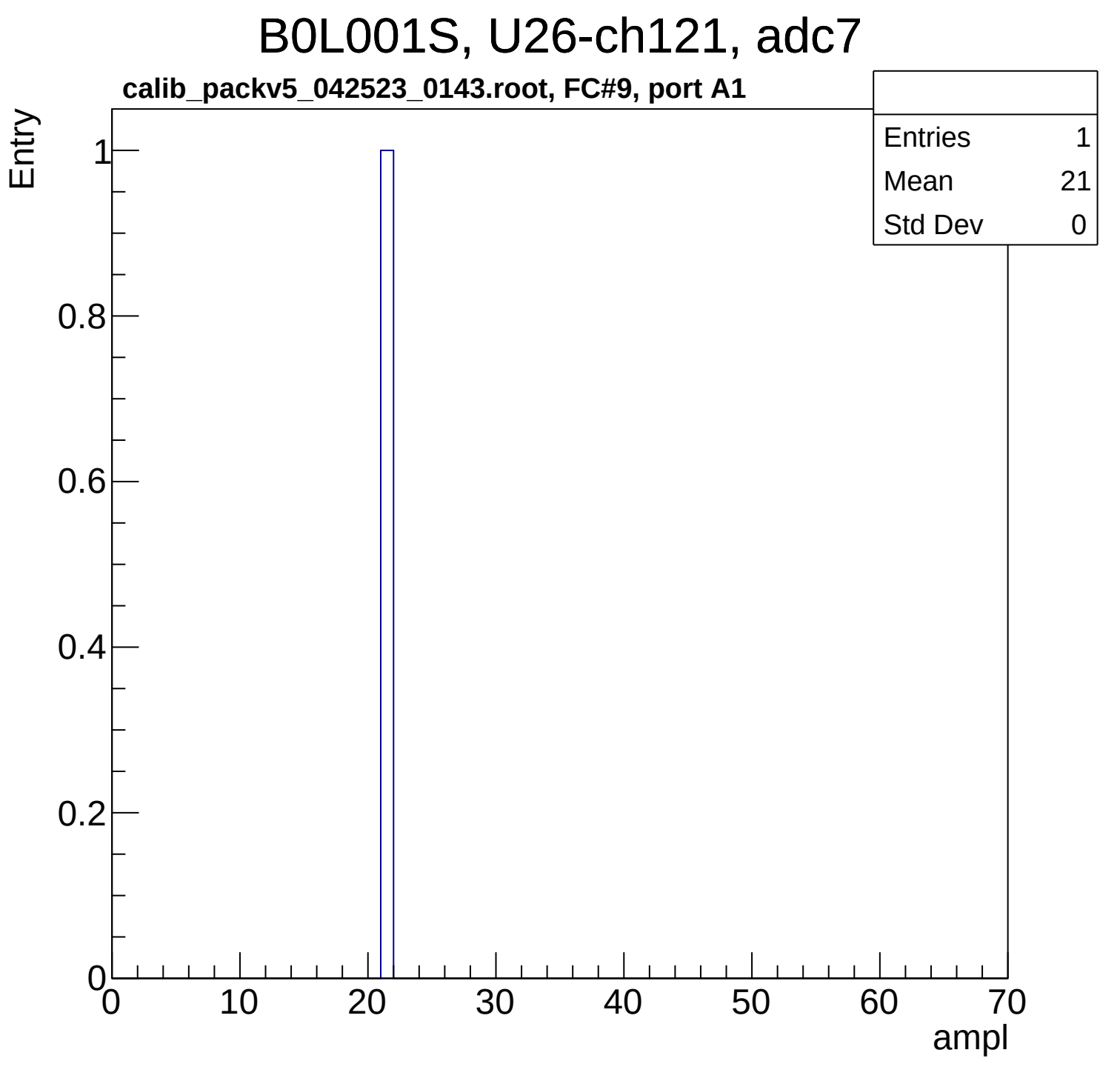
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U26-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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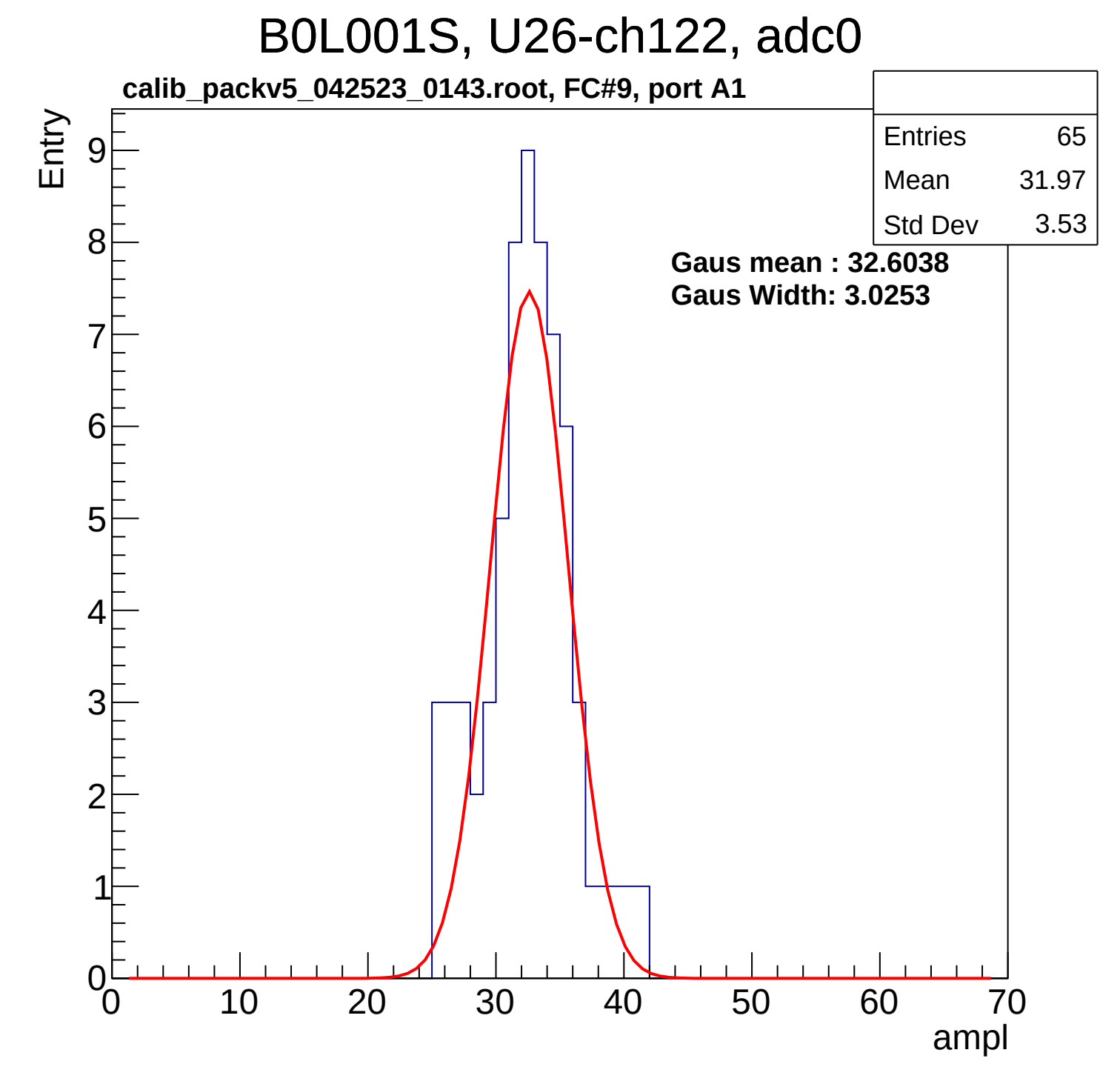
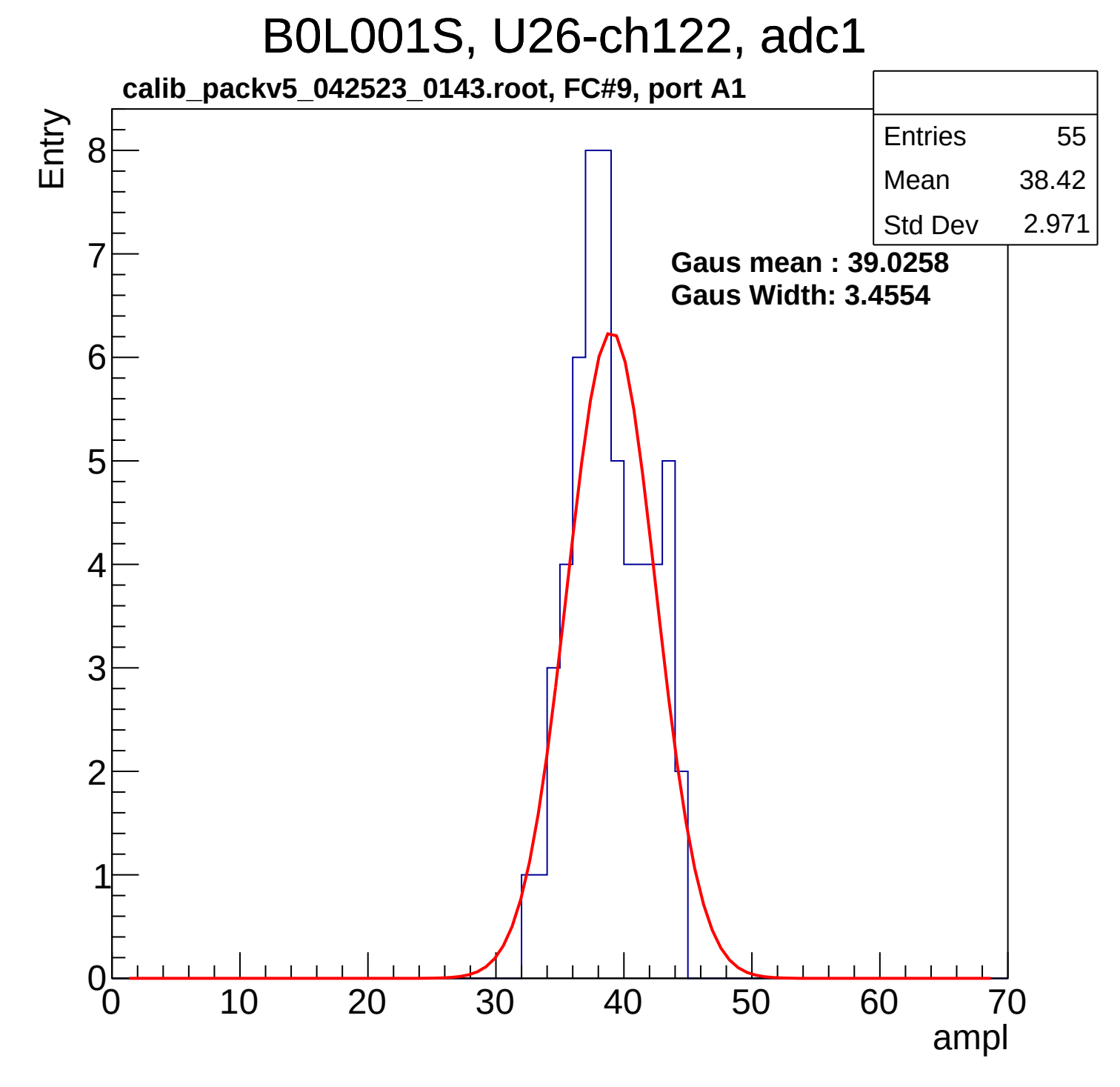
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 31.97
Std Dev 3.53
Gaus mean : 32.6038
Gaus Width: 3.0253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 38.42
Std Dev 2.971
Gaus mean : 39.0258
Gaus Width: 3.4554
Entry
ampl
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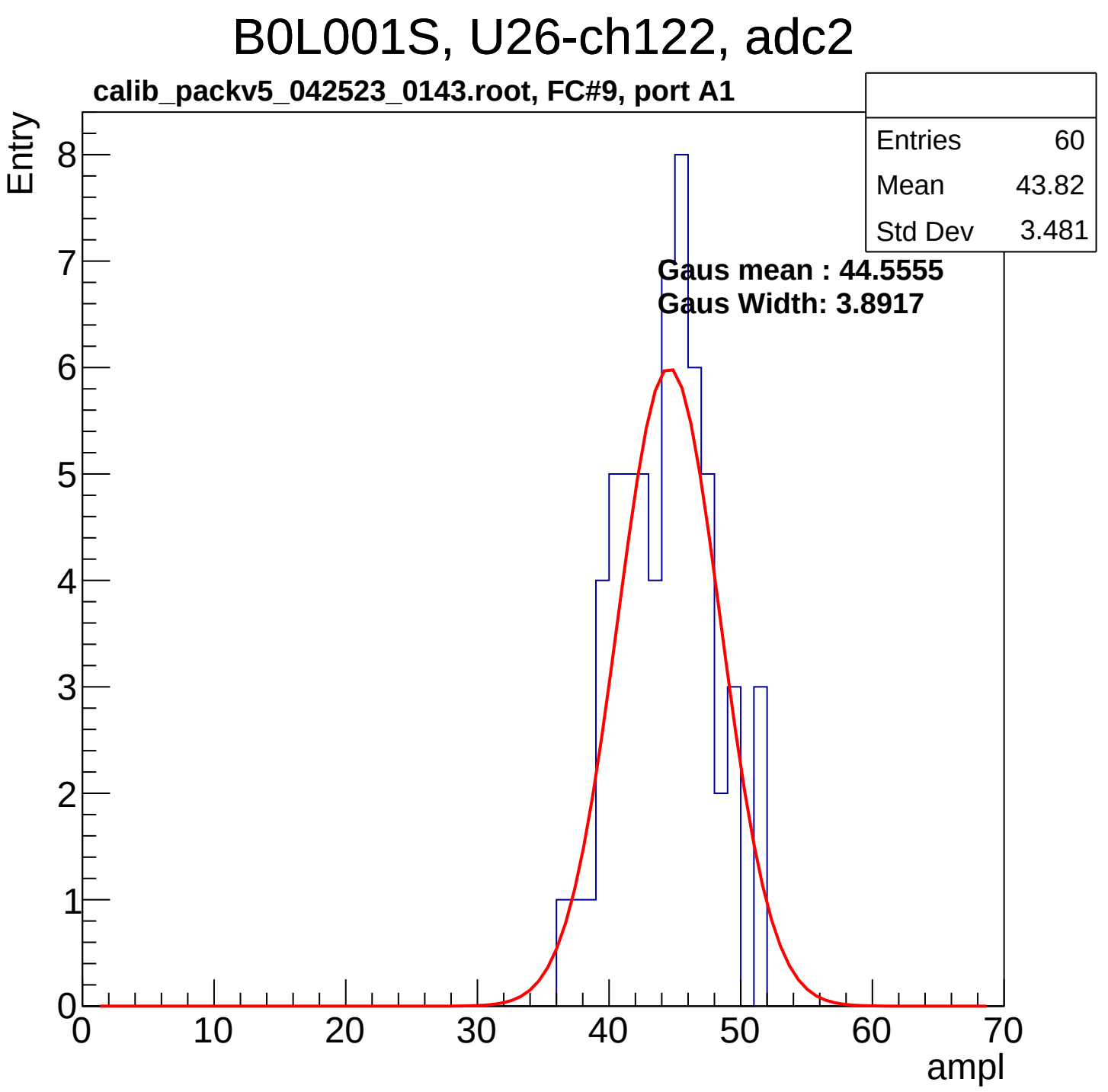
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.82
Std Dev 3.481
Gaus mean : 44.5555
Gaus Width: 3.8917
Entry
ampl
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2
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5
6
7
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0
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20
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60
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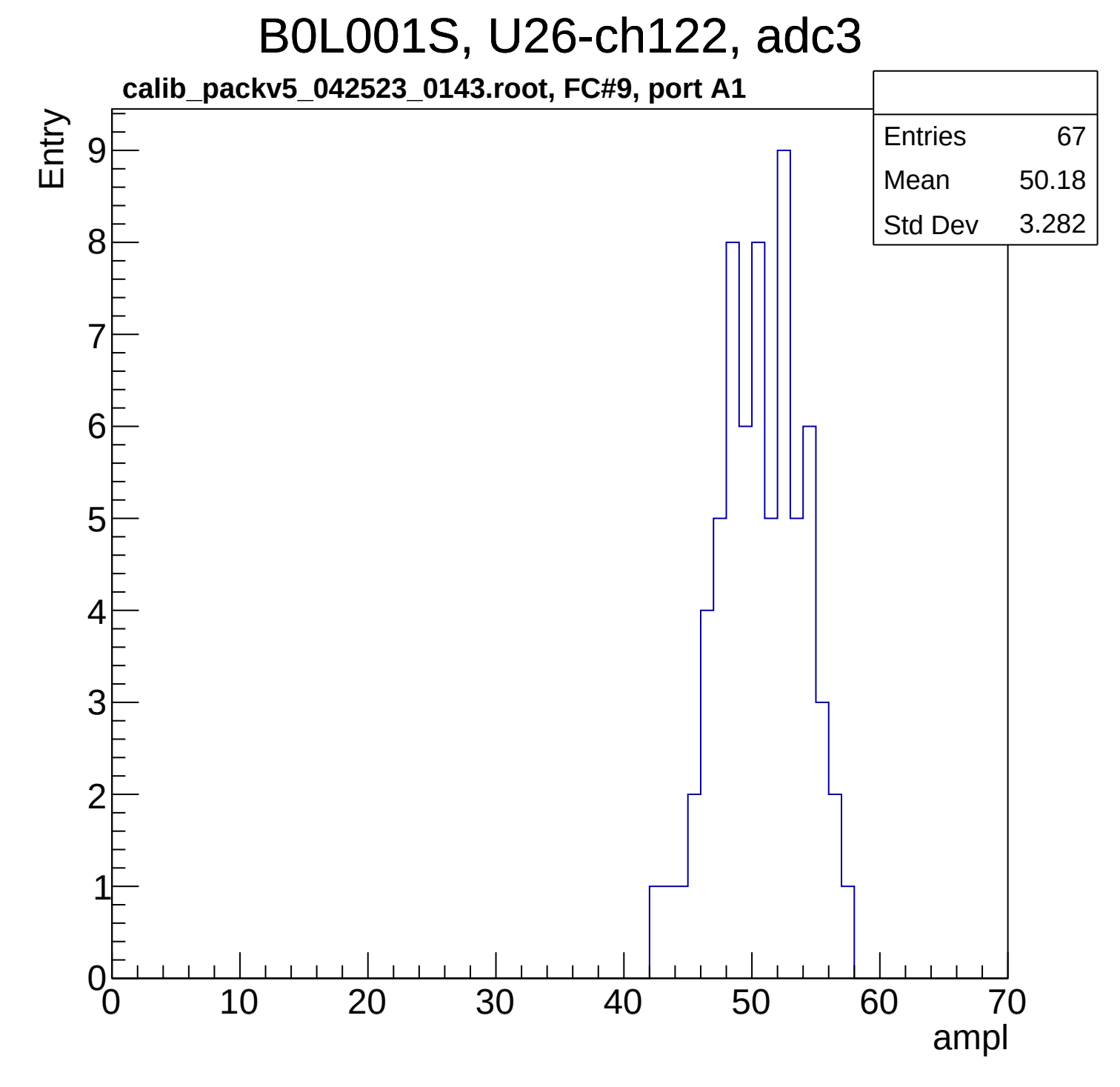
B0L001S, U26-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.18
Std Dev 3.282
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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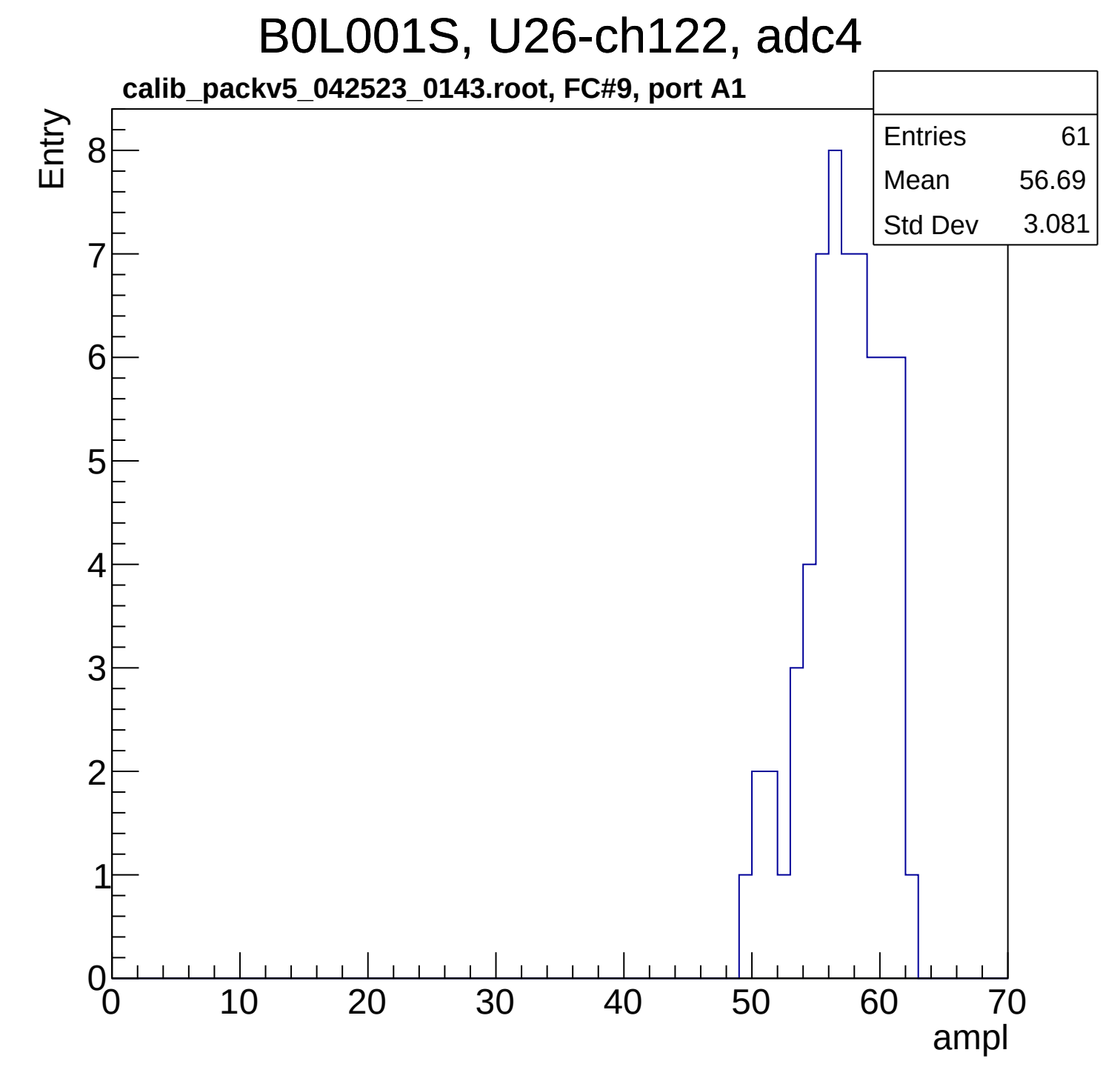
B0L001S, U26-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.69
Std Dev 3.081
Entry
ampl
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3
4
5
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7
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0
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20
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40
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60
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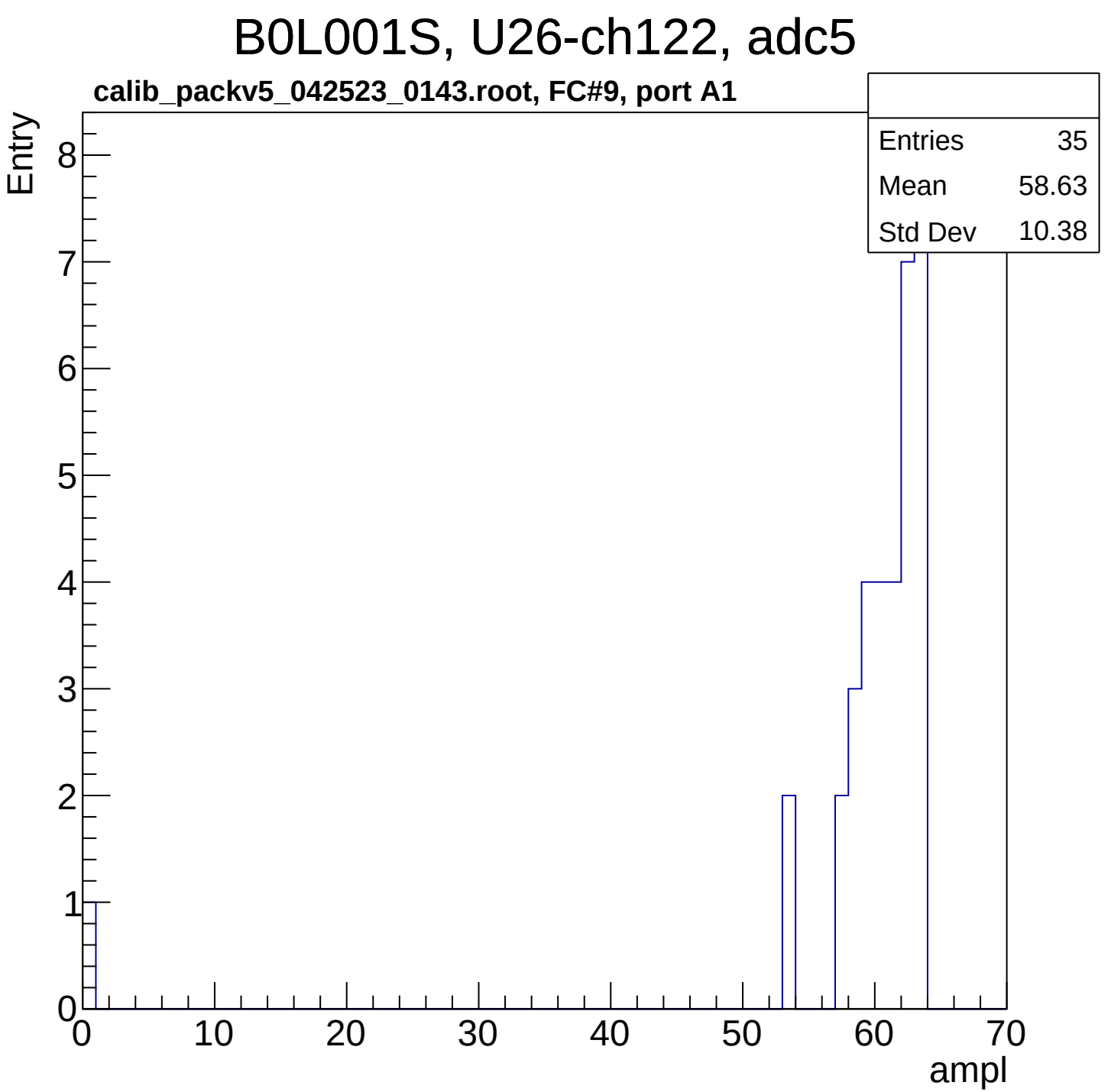
B0L001S, U26-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.63
Std Dev 10.38
Entry
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2
3
4
5
6
7
8
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10
20
30
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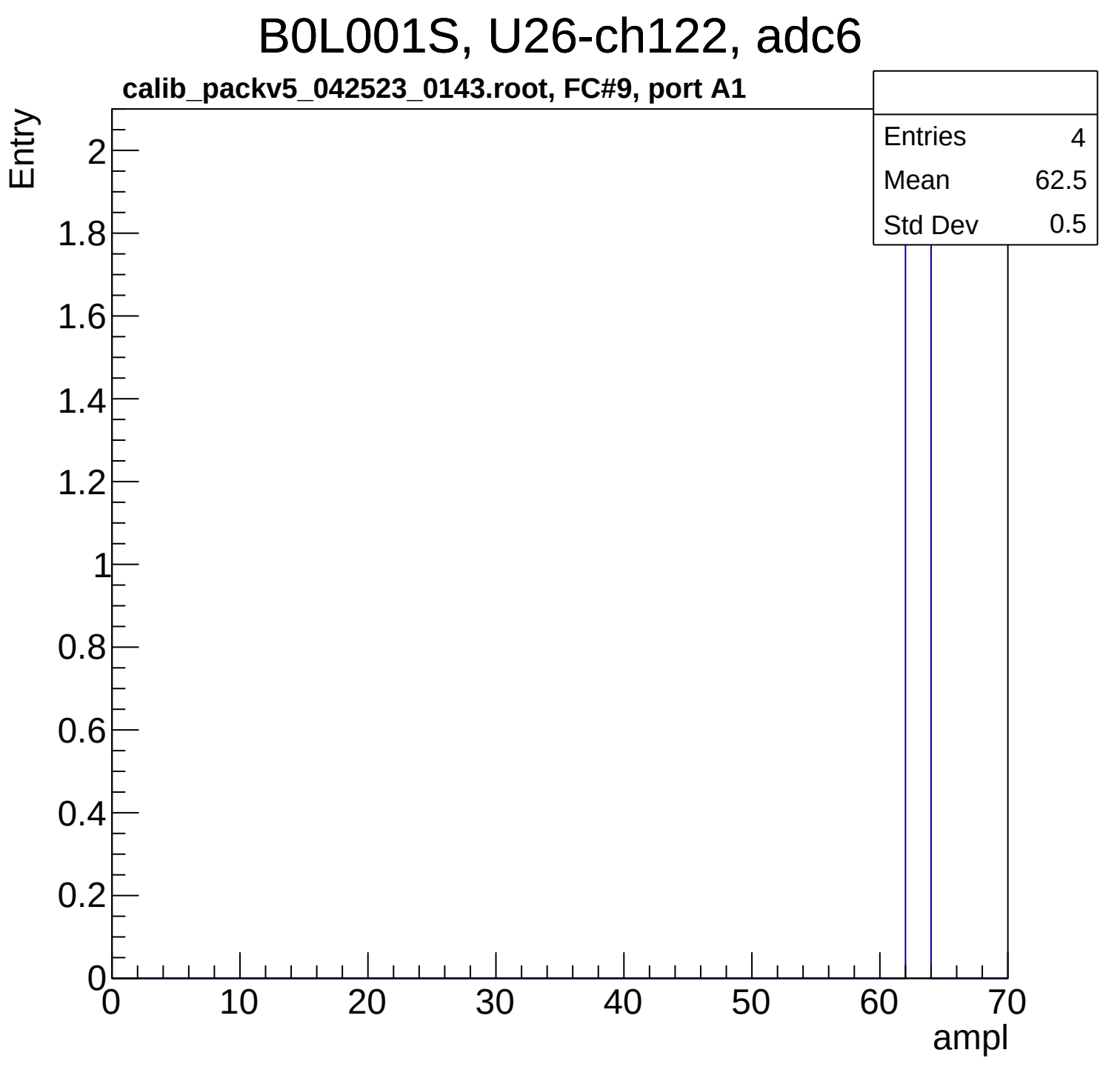
50
60
70
ampl


B0L001S, U26-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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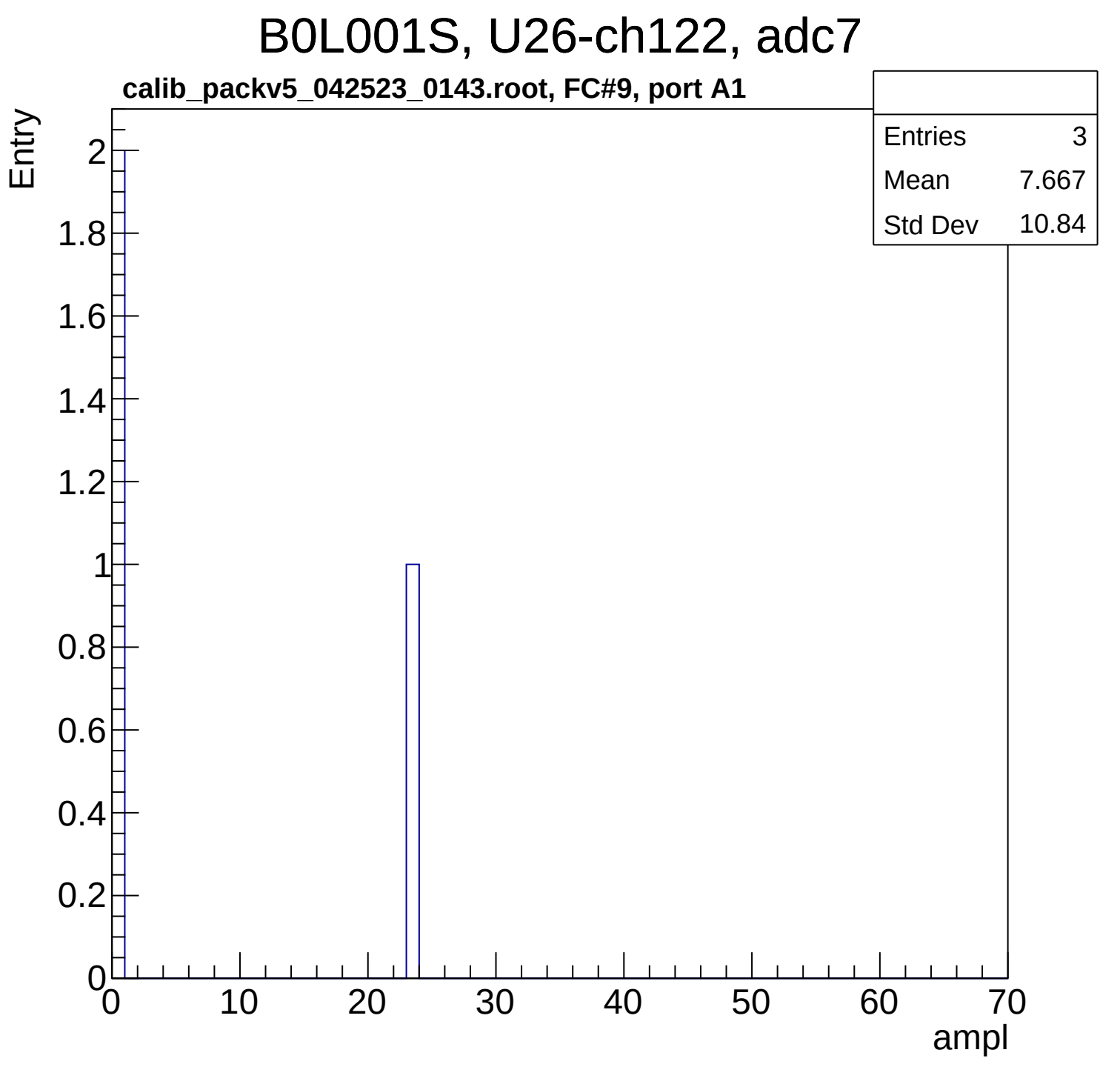
40
50
60
70


B0L001S, U26-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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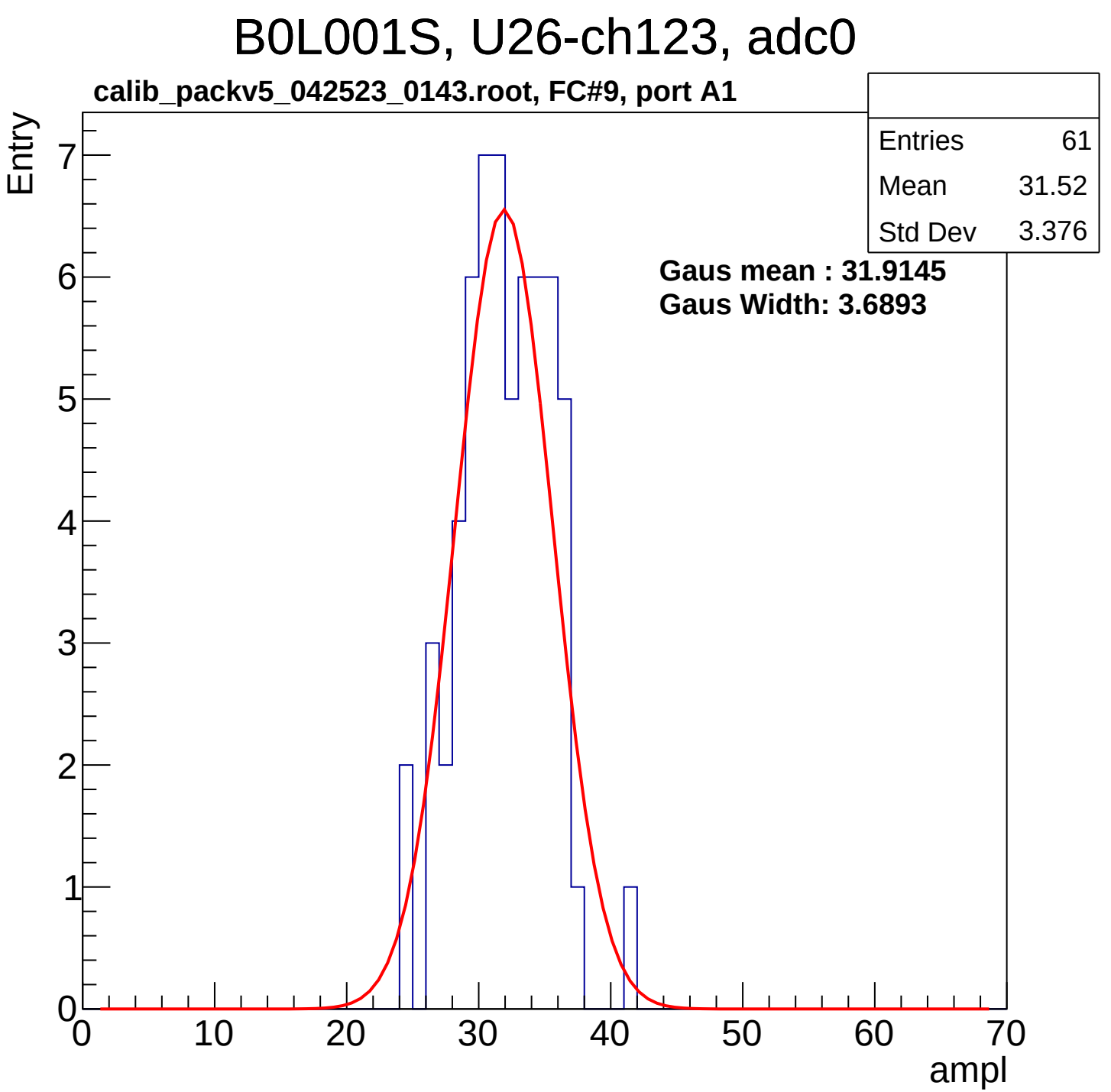
B0L001S, U26-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.52
Std Dev 3.376
Gaus mean : 31.9145
Gaus Width: 3.6893
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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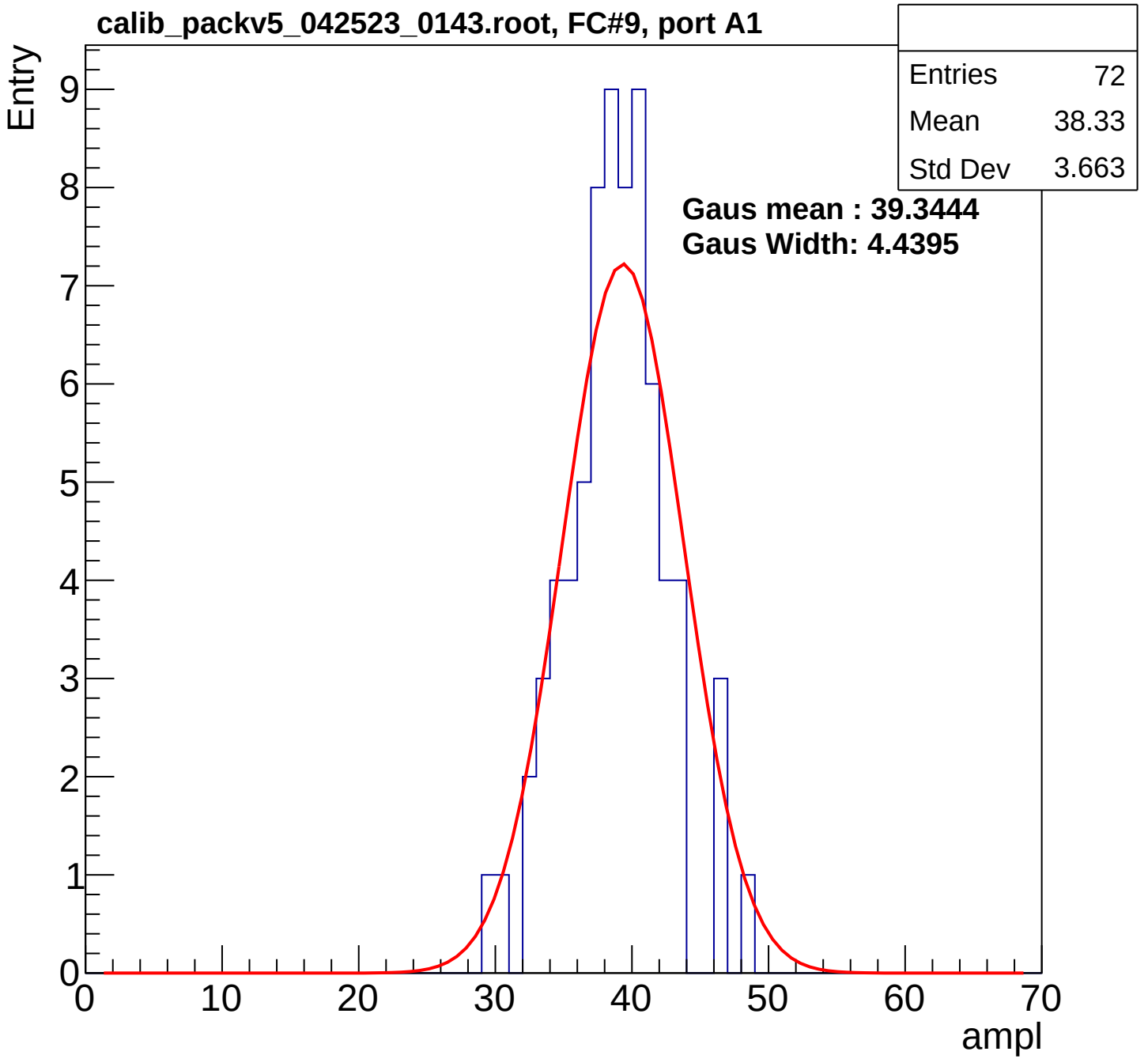
60
70


B0L001S, U26-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.33
Std Dev 3.663
Gaus mean : 39.3444
Gaus Width: 4.4395
Entry
ampl
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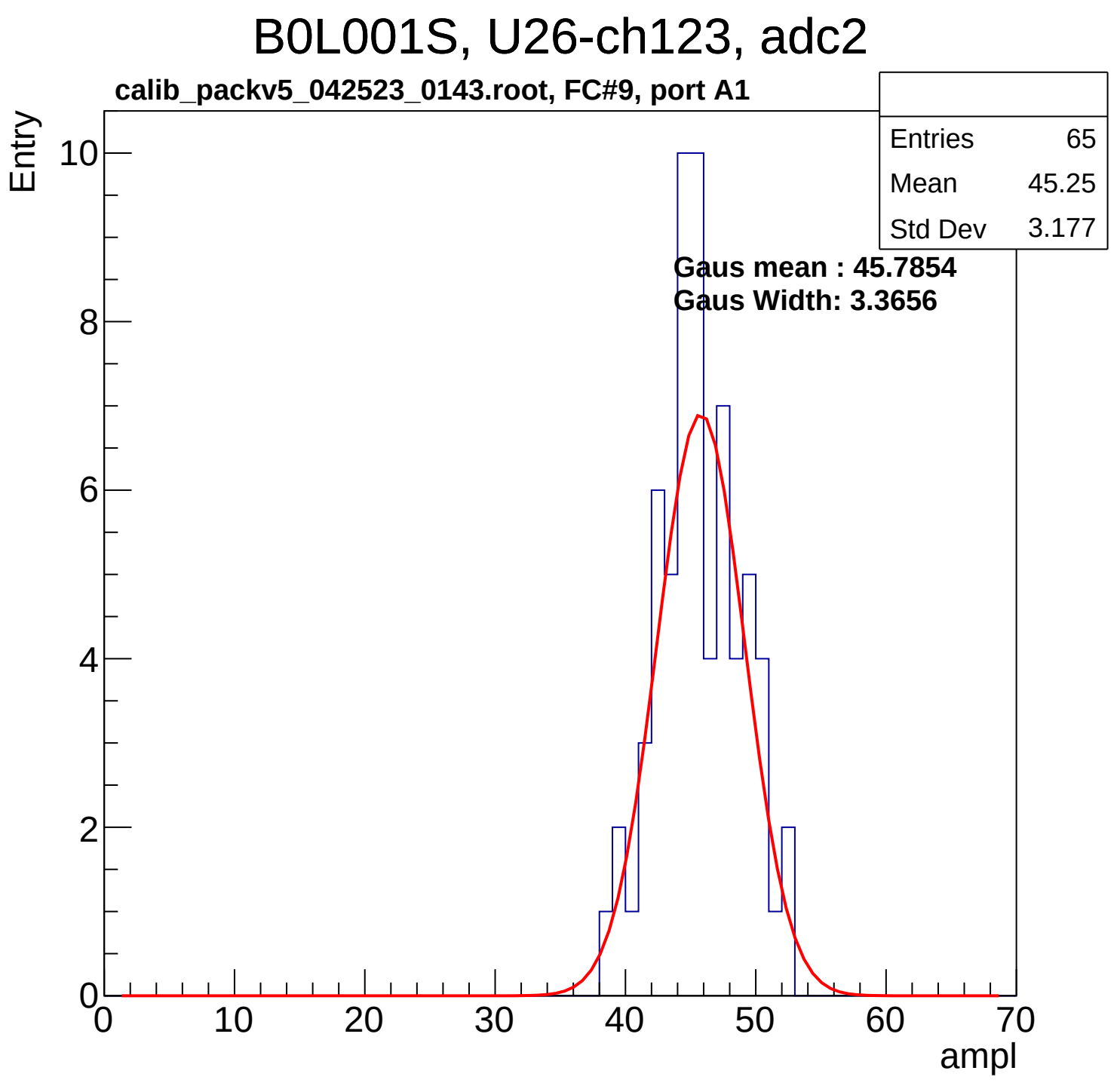
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.25
Std Dev 3.177
Gaus mean : 45.7854
Gaus Width: 3.3656
Entry
ampl
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6
8
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0
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20
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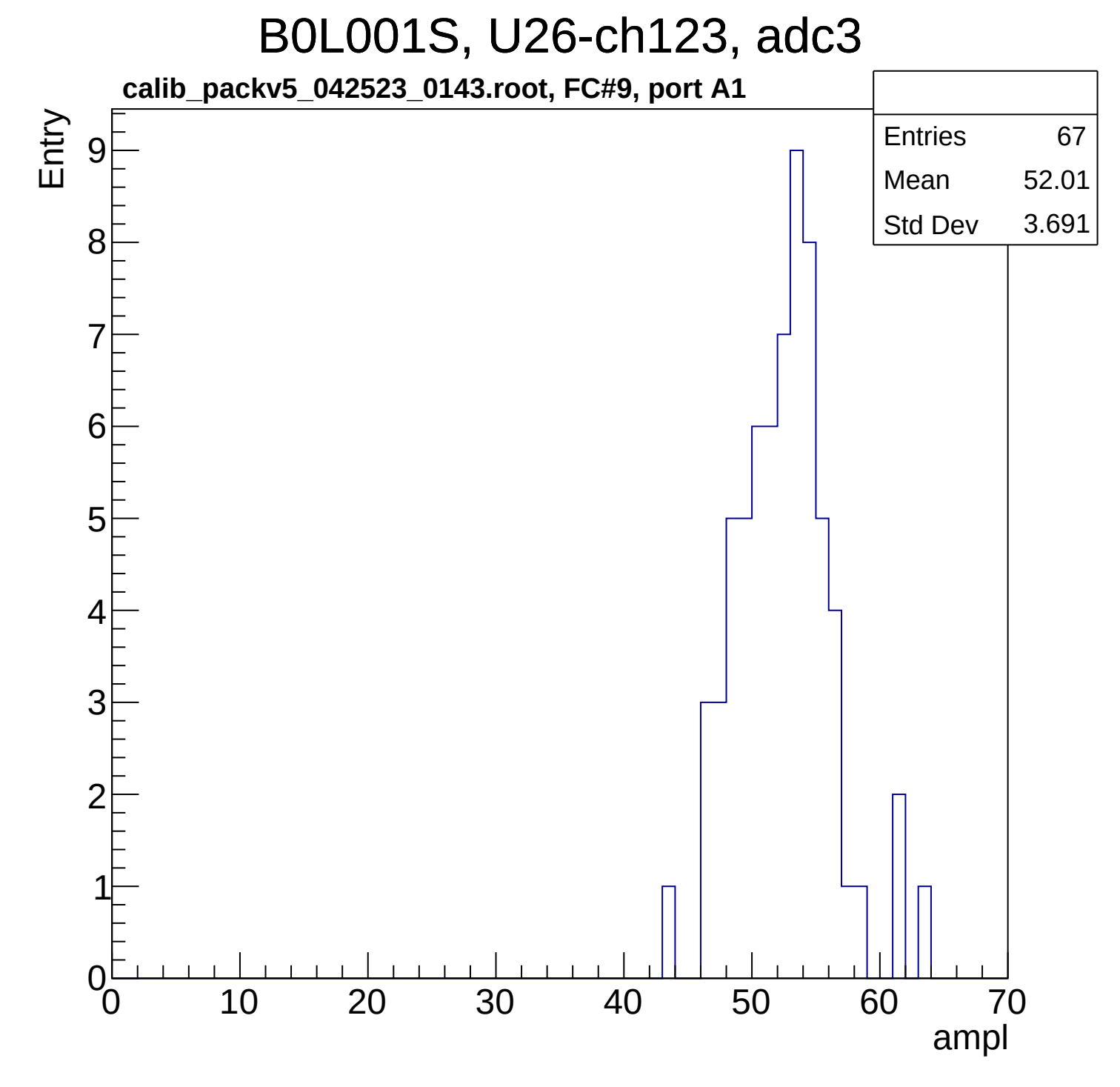
50
60
70


B0L001S, U26-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 52.01
Std Dev 3.691
Entry
ampl
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4
5
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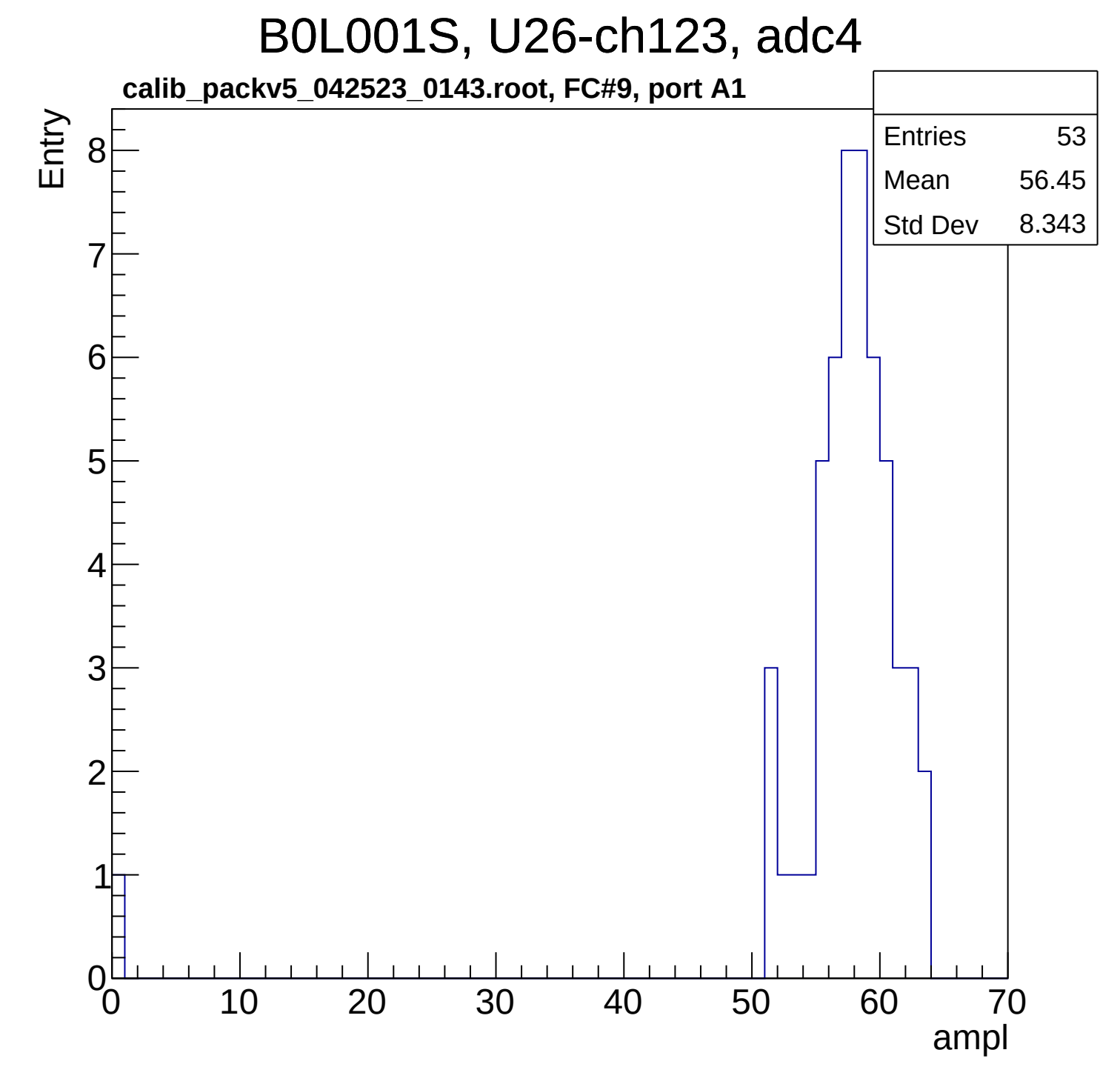
B0L001S, U26-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.45
Std Dev 8.343
Entry
ampl
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10
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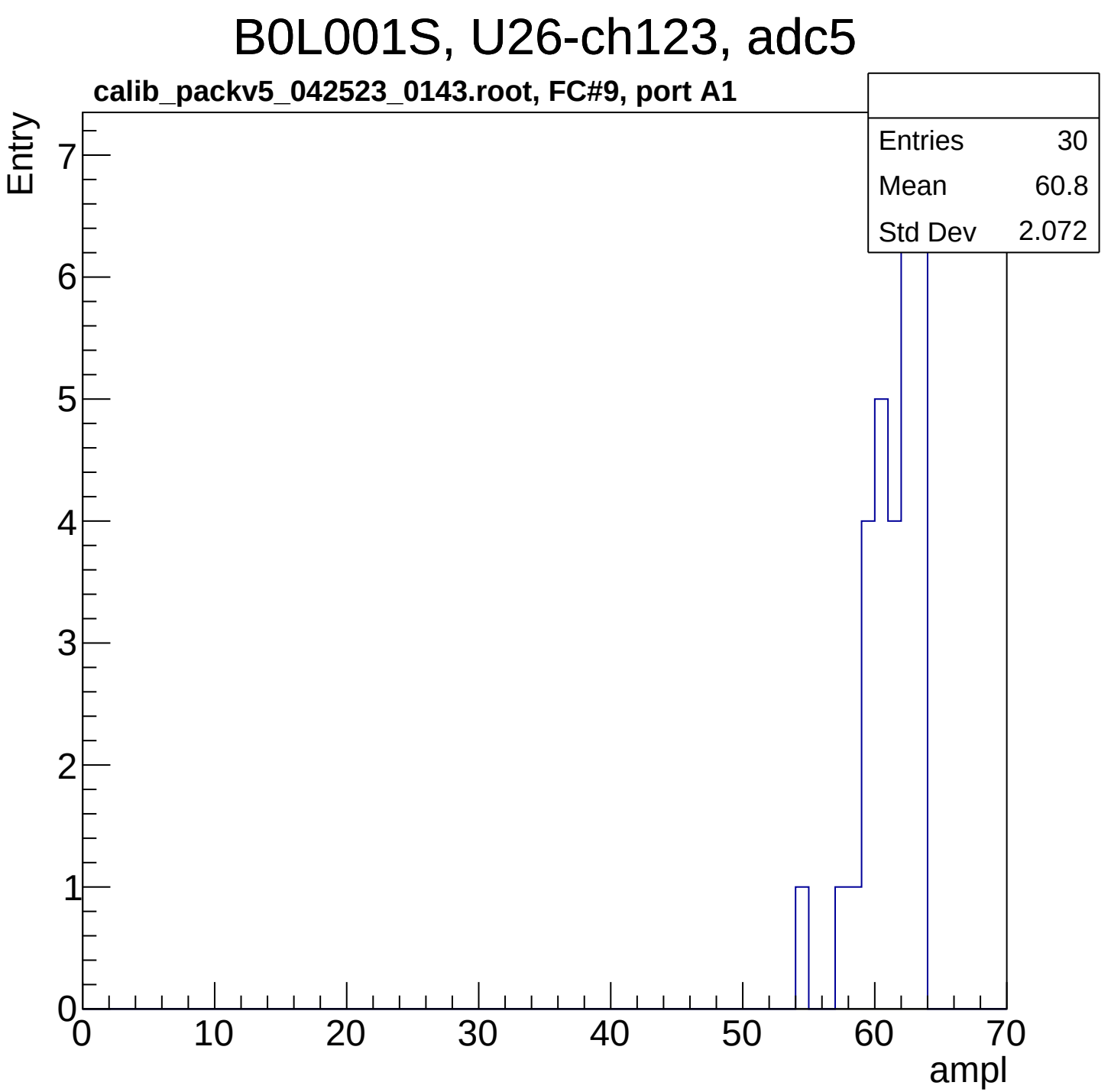
B0L001S, U26-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.8
Std Dev 2.072
Entry
ampl
0
1
2
3
4
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6
7
0
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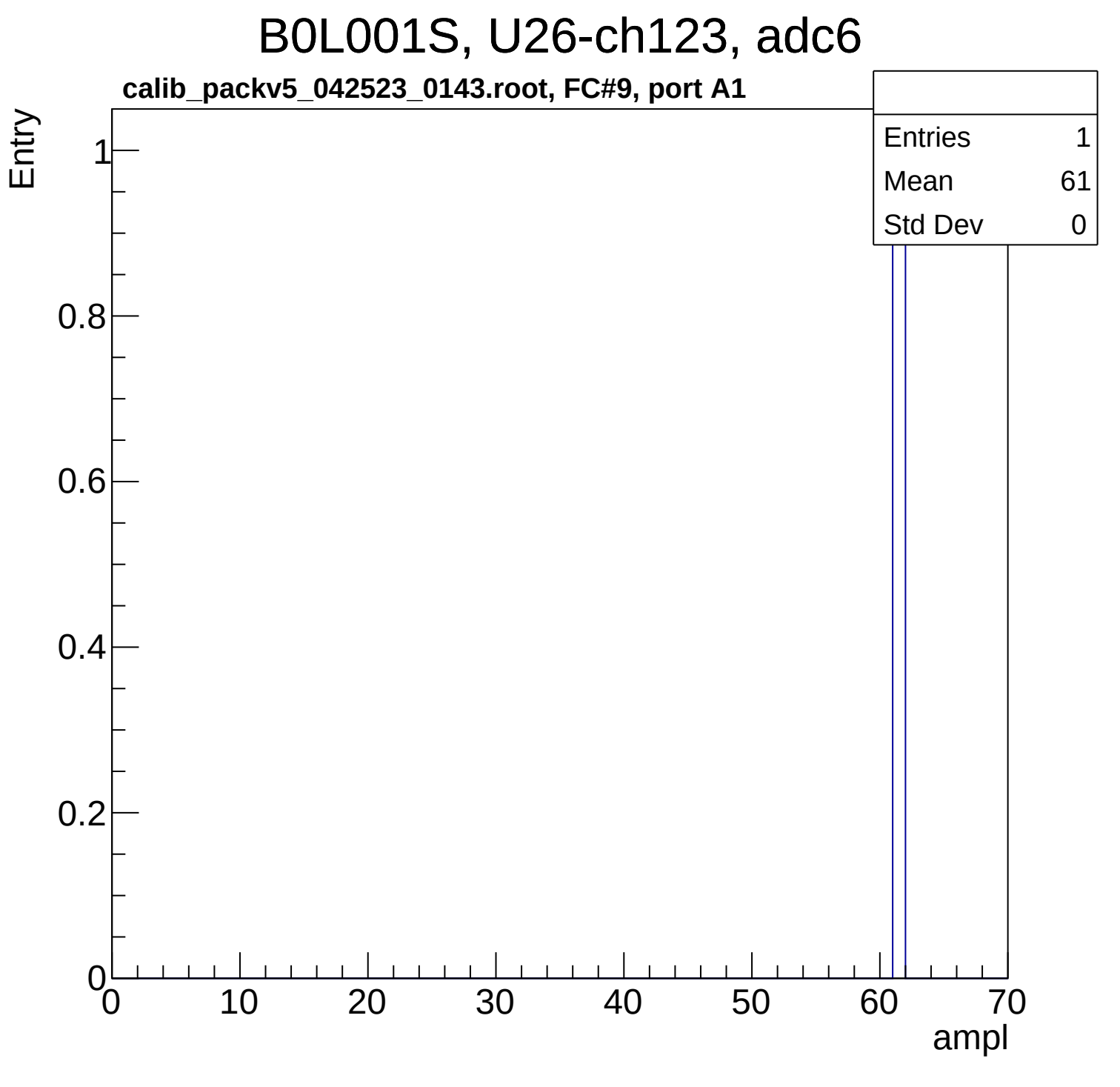
40
50
60
70


B0L001S, U26-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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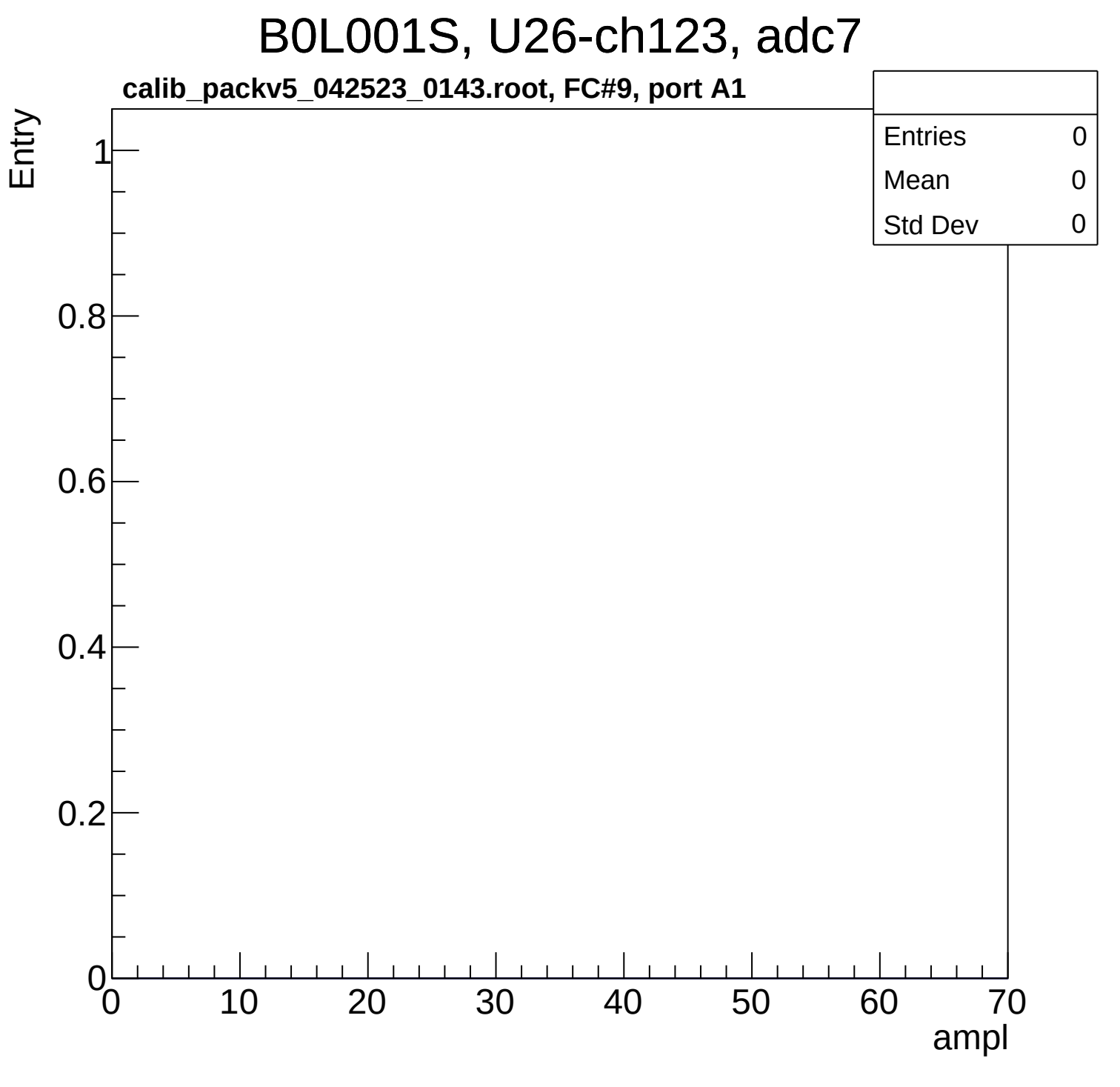
70
ampl


B0L001S, U26-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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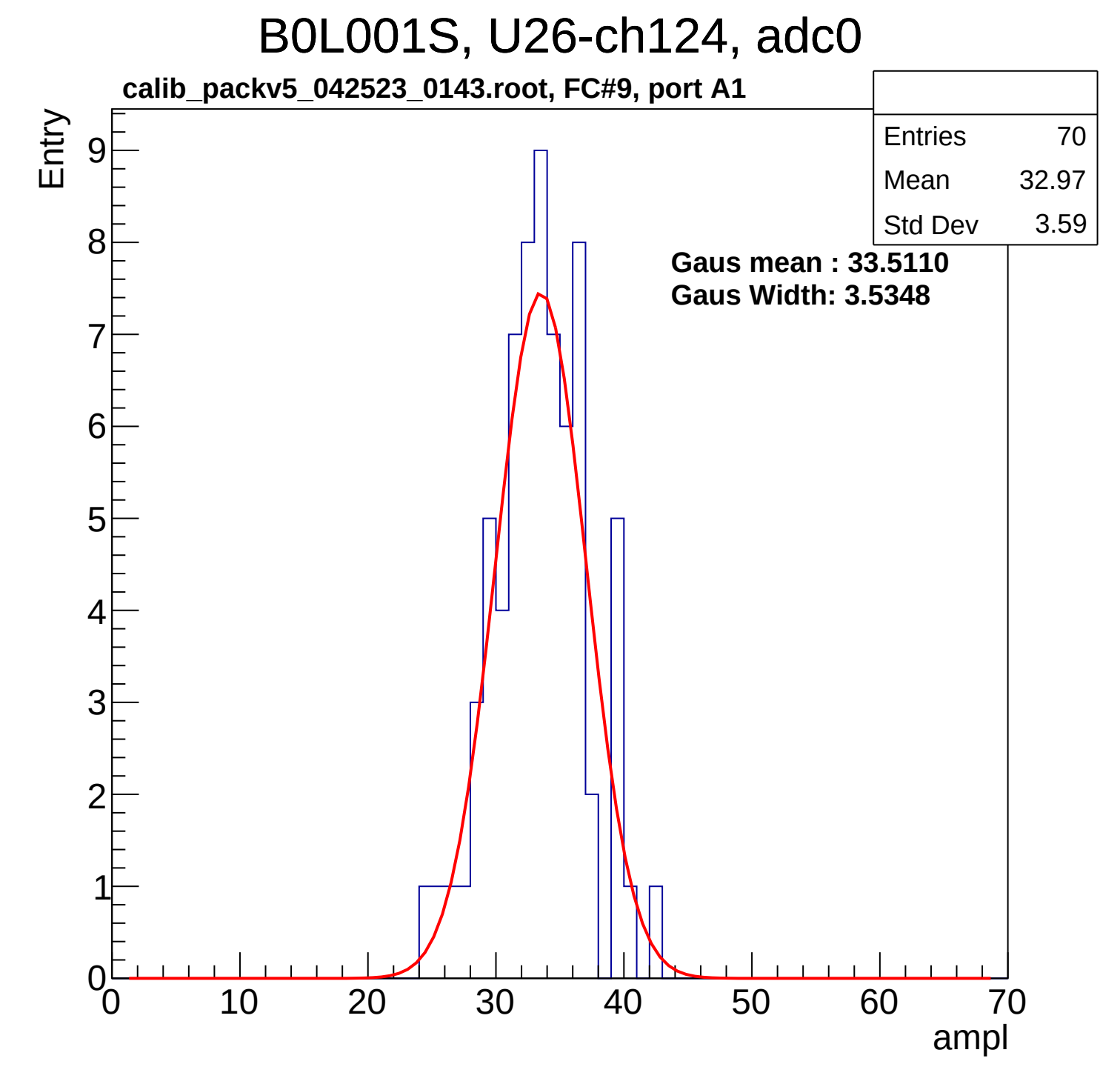
50
60
70
ampl


B0L001S, U26-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 32.97
Std Dev 3.59
Gaus mean : 33.5110
Gaus Width: 3.5348
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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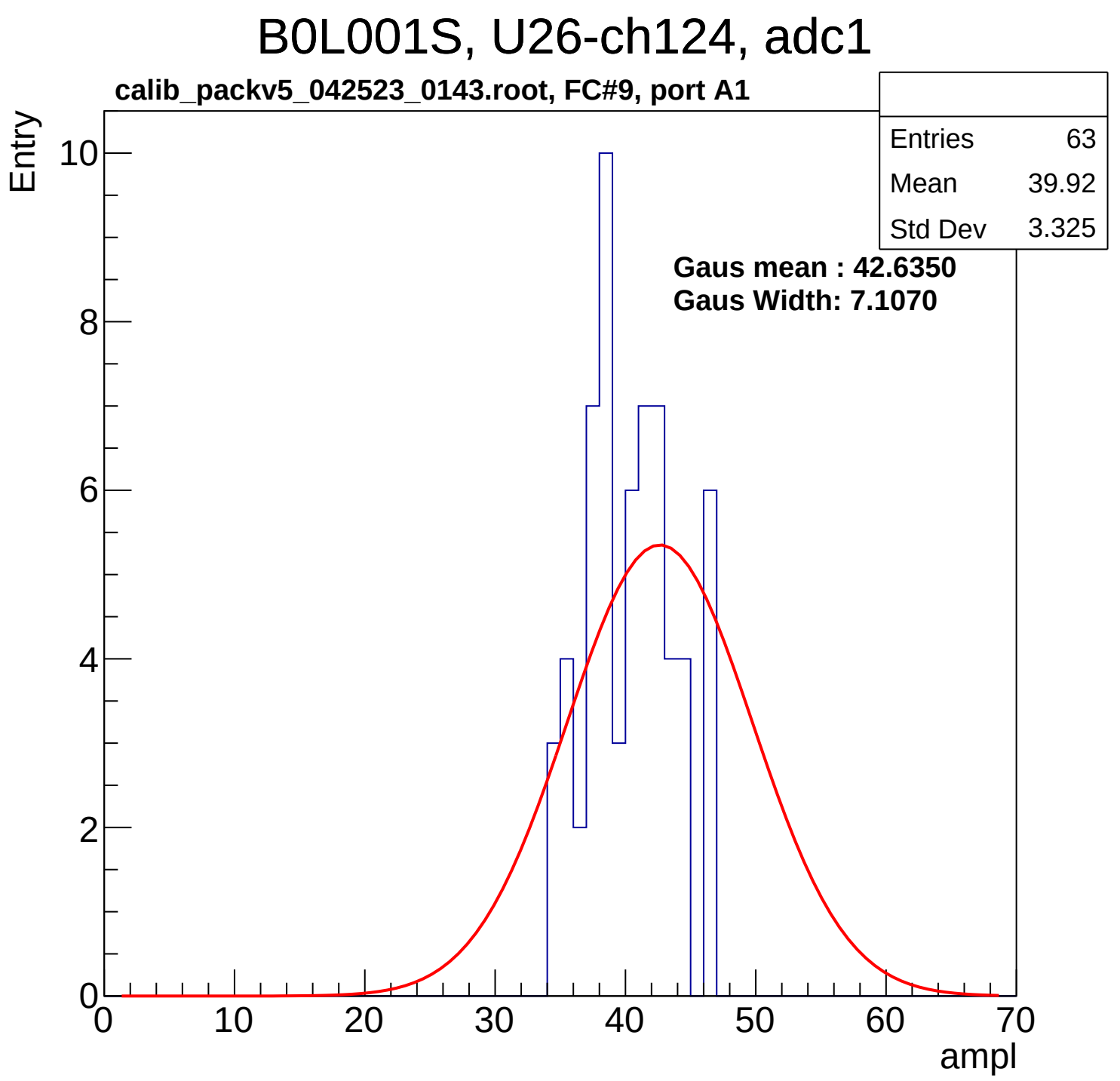
B0L001S, U26-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 39.92
Std Dev 3.325
Gaus mean : 42.6350
Gaus Width: 7.1070
Entry
ampl
0
2
4
6
8
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0
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40
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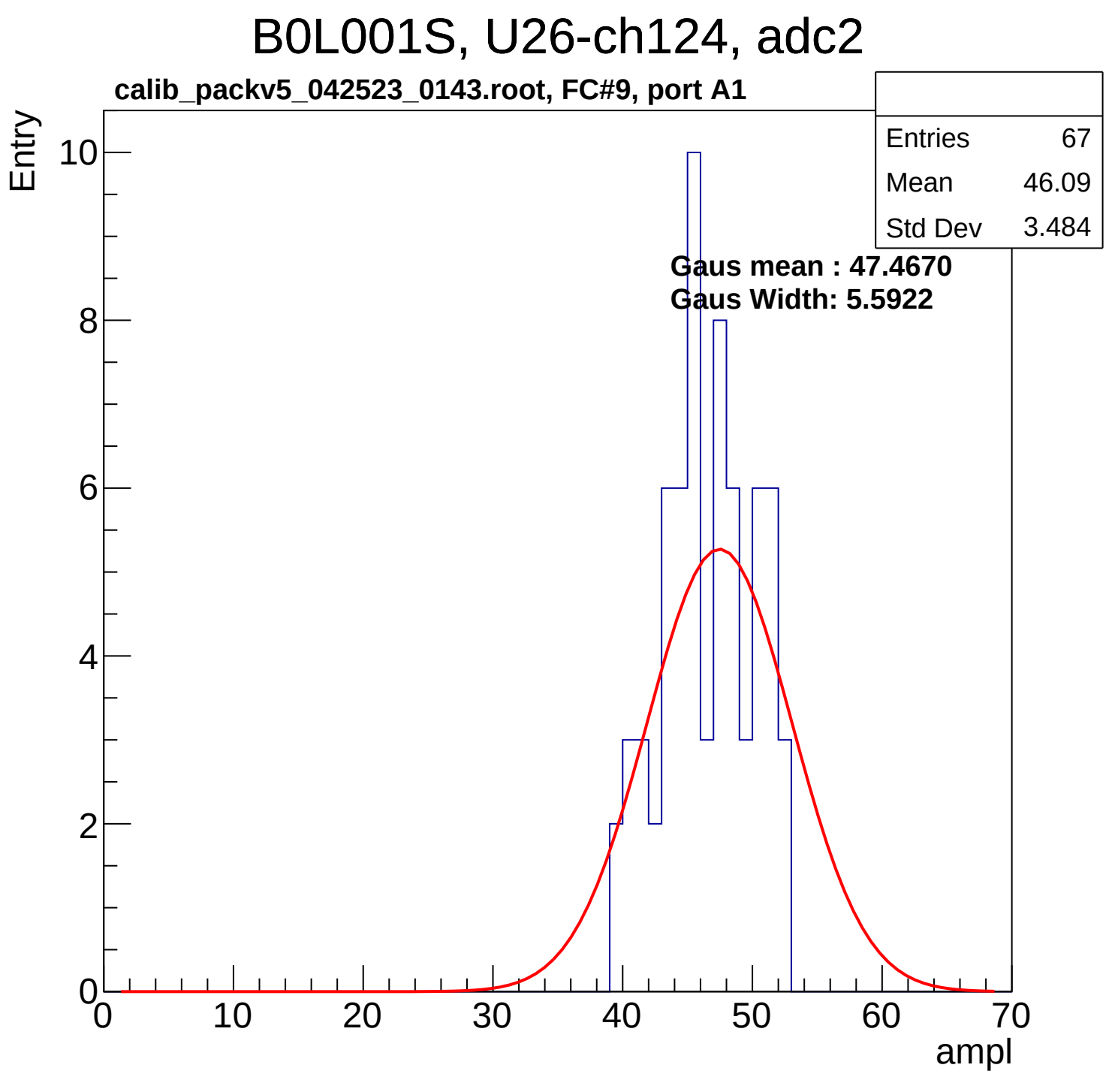
B0L001S, U26-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 46.09
Std Dev 3.484
Gaus mean : 47.4670
Gaus Width: 5.5922
Entry
ampl
0
2
4
6
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0
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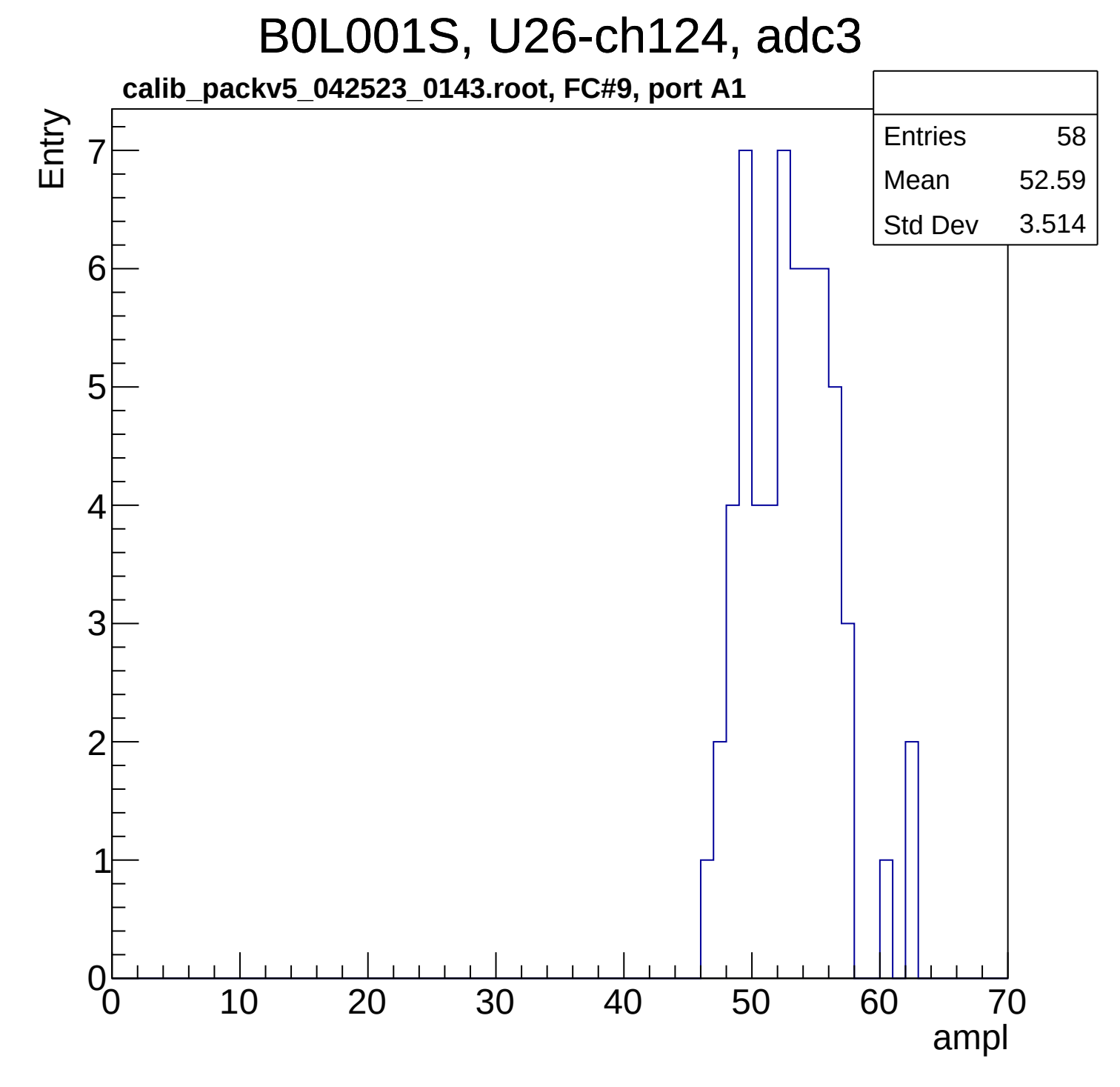
B0L001S, U26-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 52.59
Std Dev 3.514
Entry
ampl
0
1
2
3
4
5
6
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0
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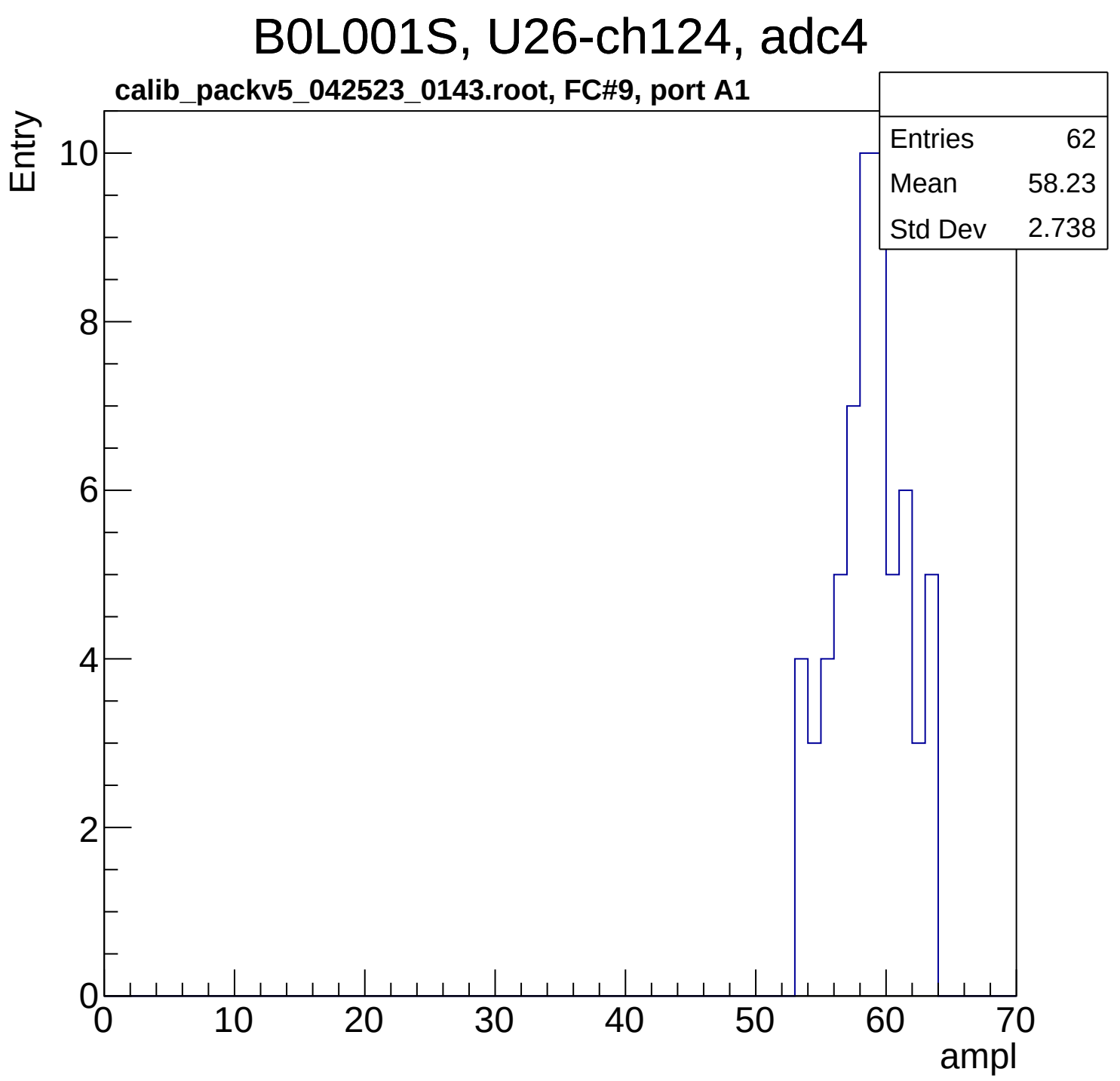
B0L001S, U26-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 58.23
Std Dev 2.738
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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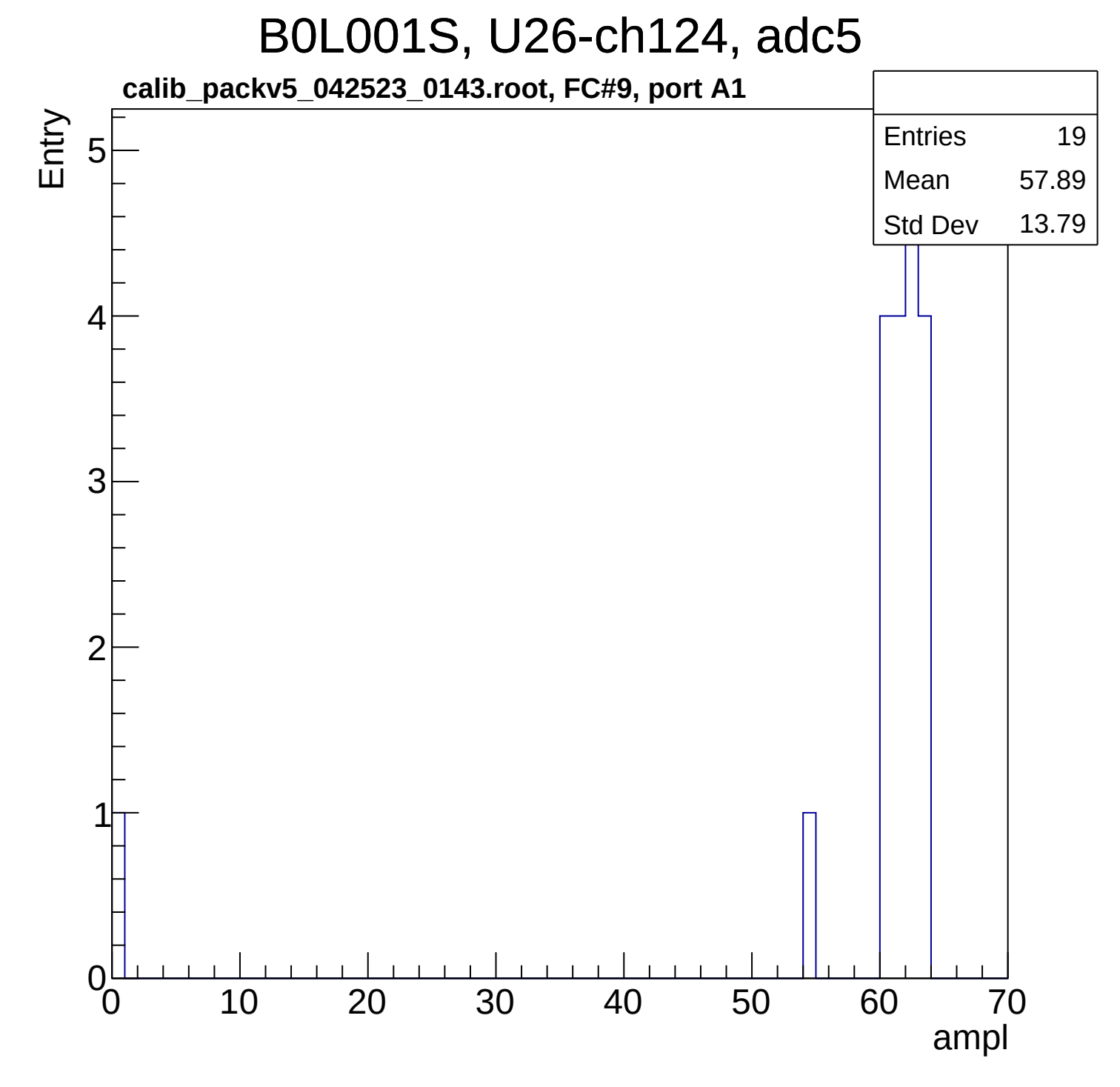
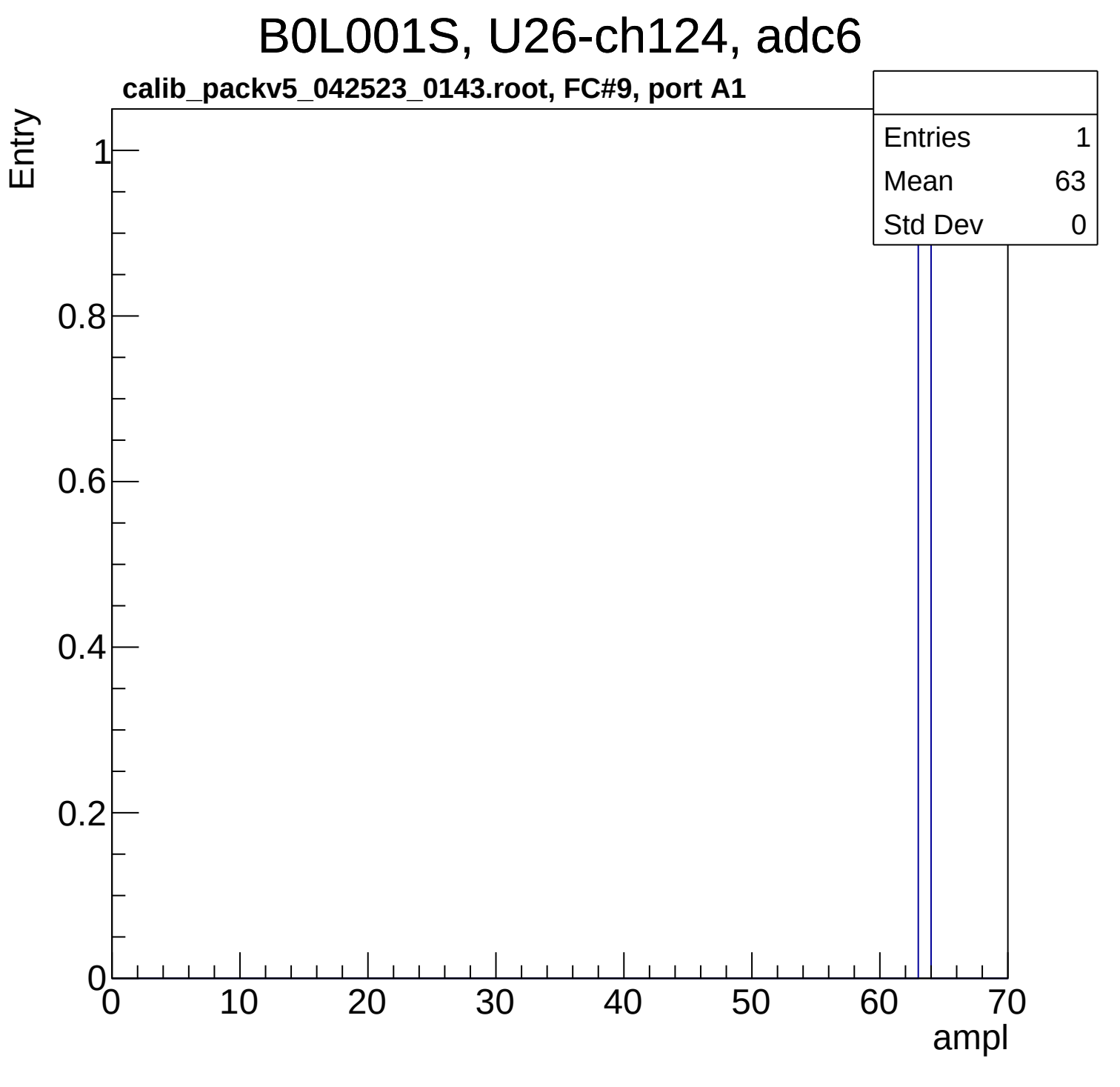
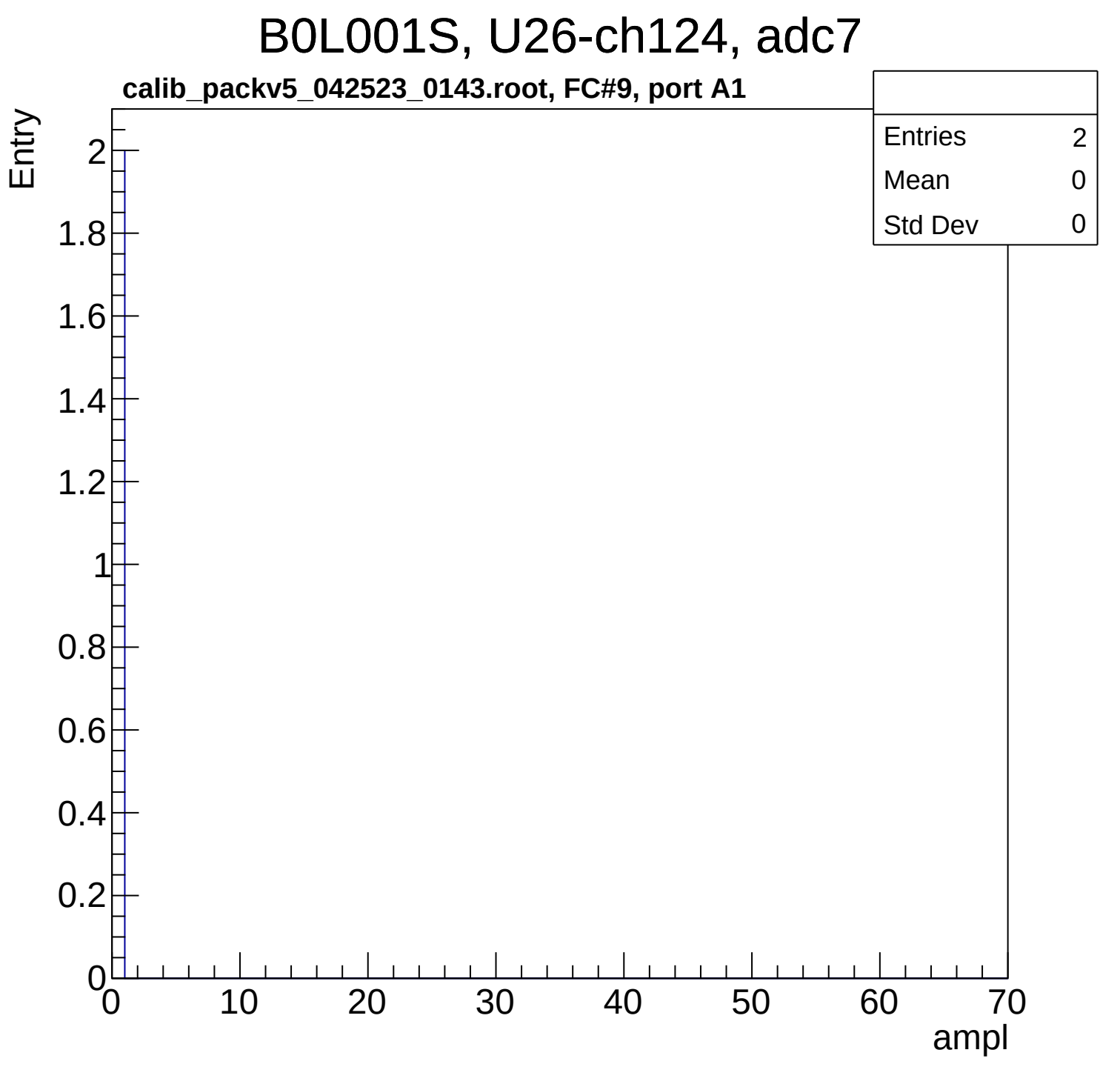
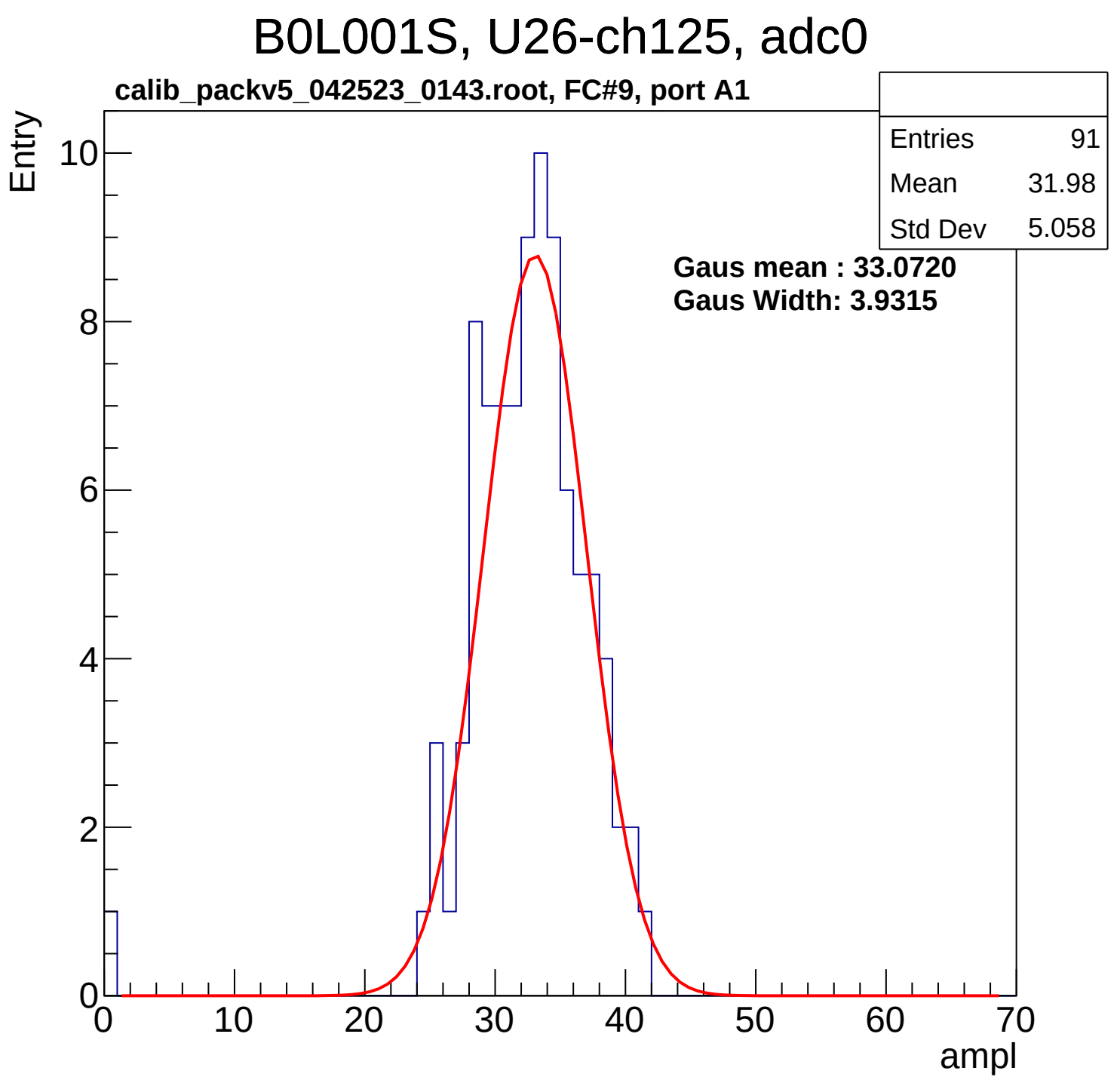
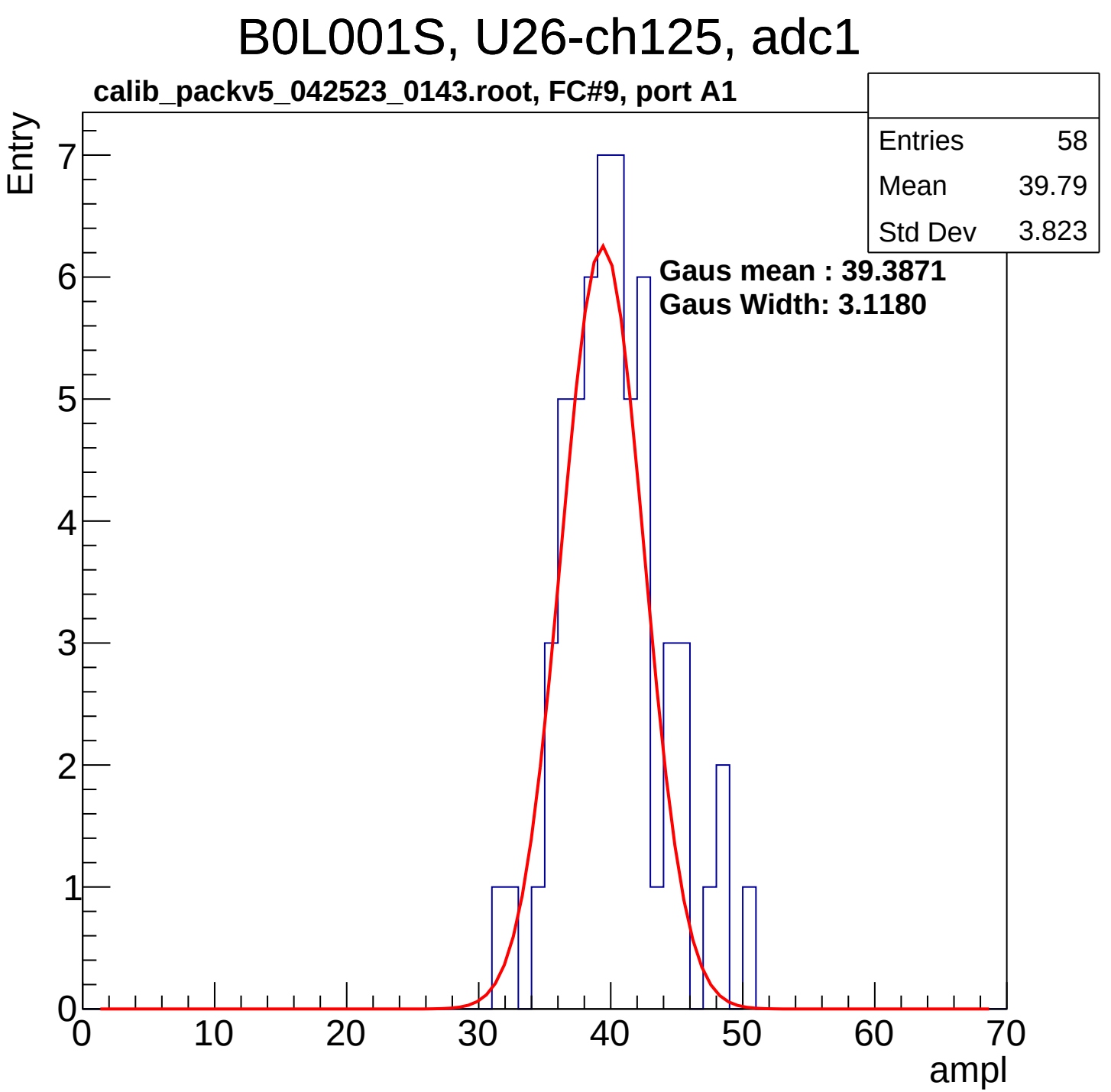

B0L001S, U26-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 57.89
Std Dev 13.79
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 31.98
Std Dev 5.058
Gaus mean : 33.0720
Gaus Width: 3.9315
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 39.79
Std Dev 3.823
Gaus mean : 39.3871
Gaus Width: 3.1180
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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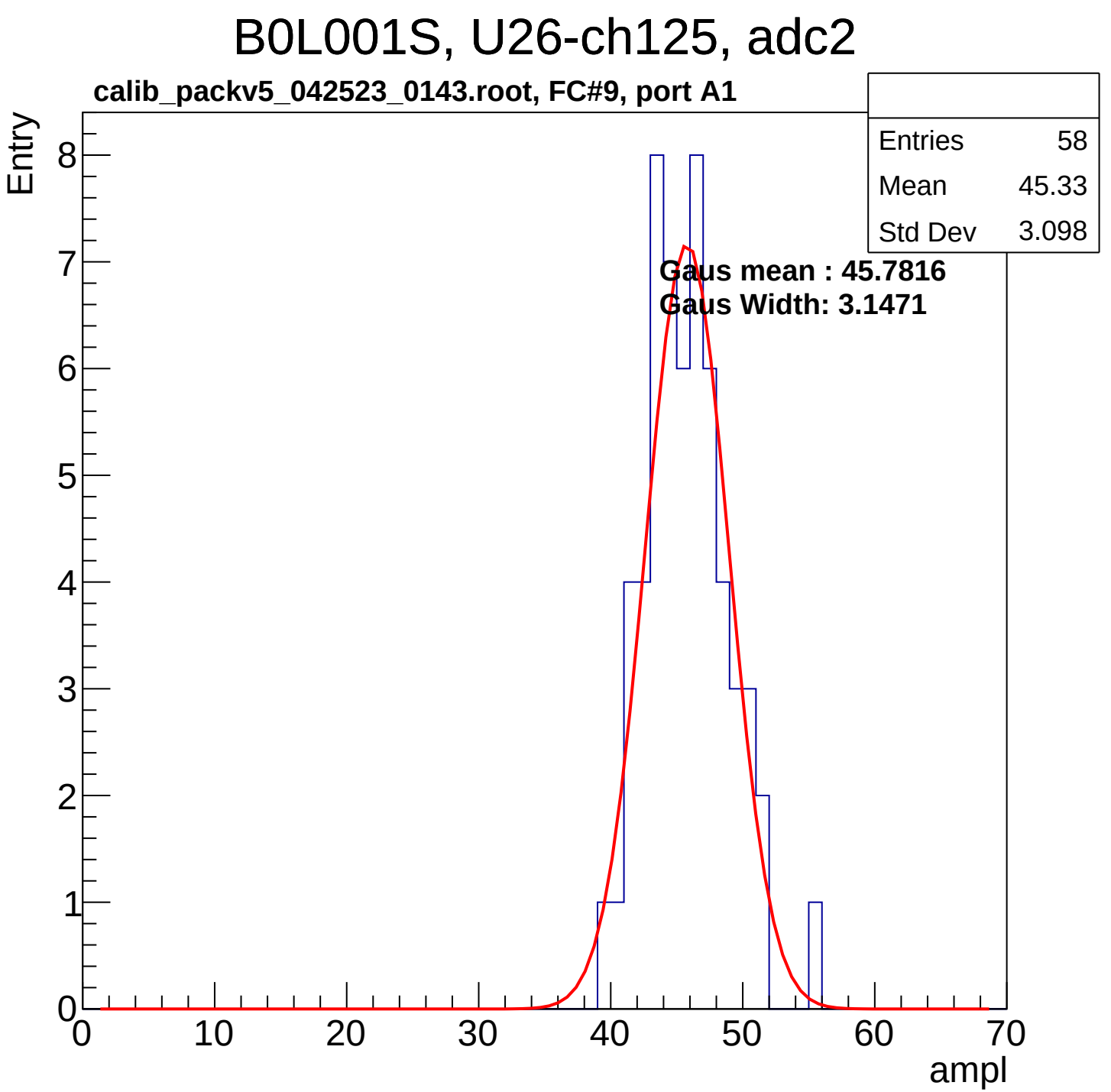
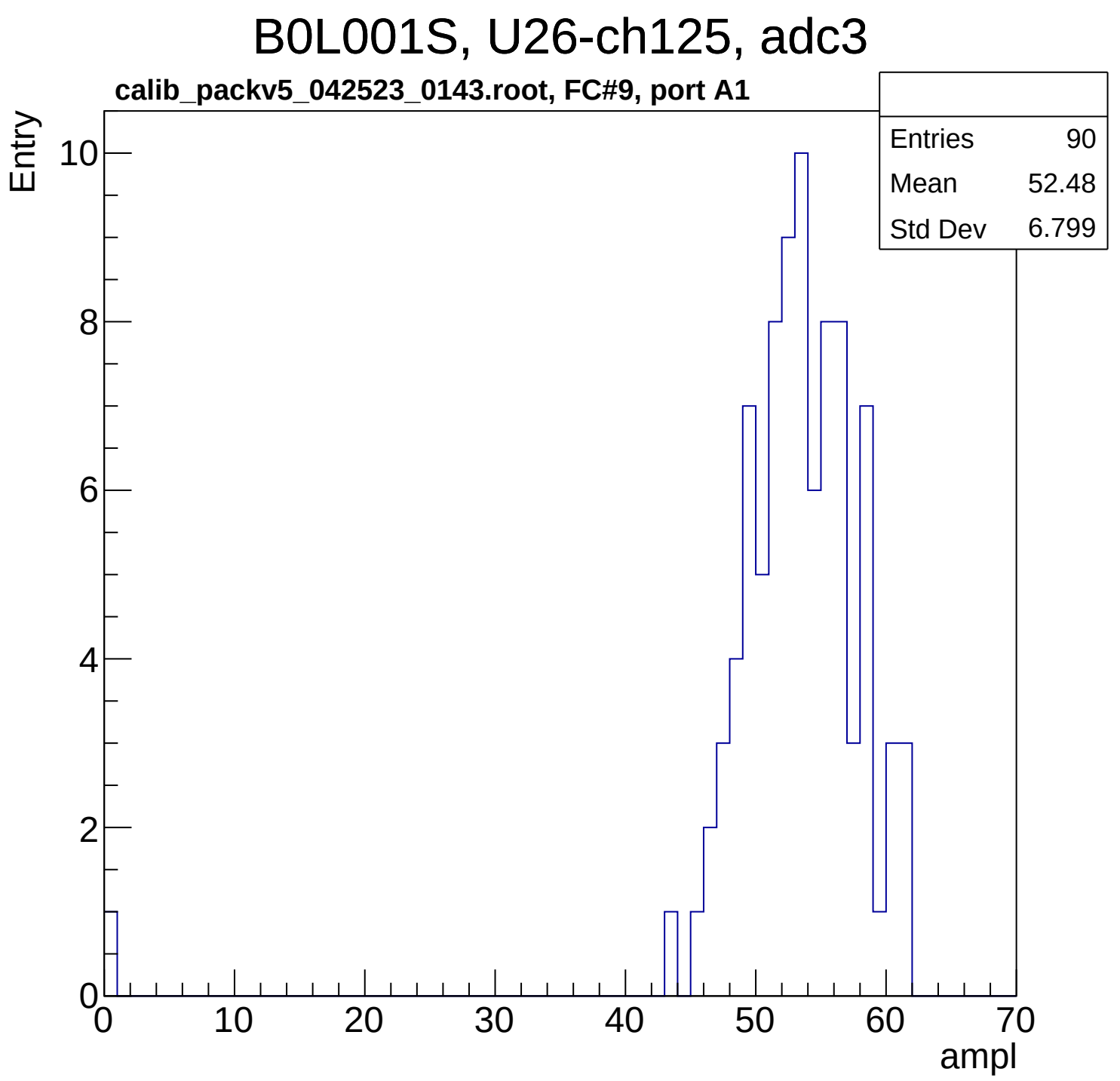

B0L001S, U26-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 45.33
Std Dev 3.098
Gaus mean : 45.7816
Gaus Width: 3.1471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 52.48
Std Dev 6.799
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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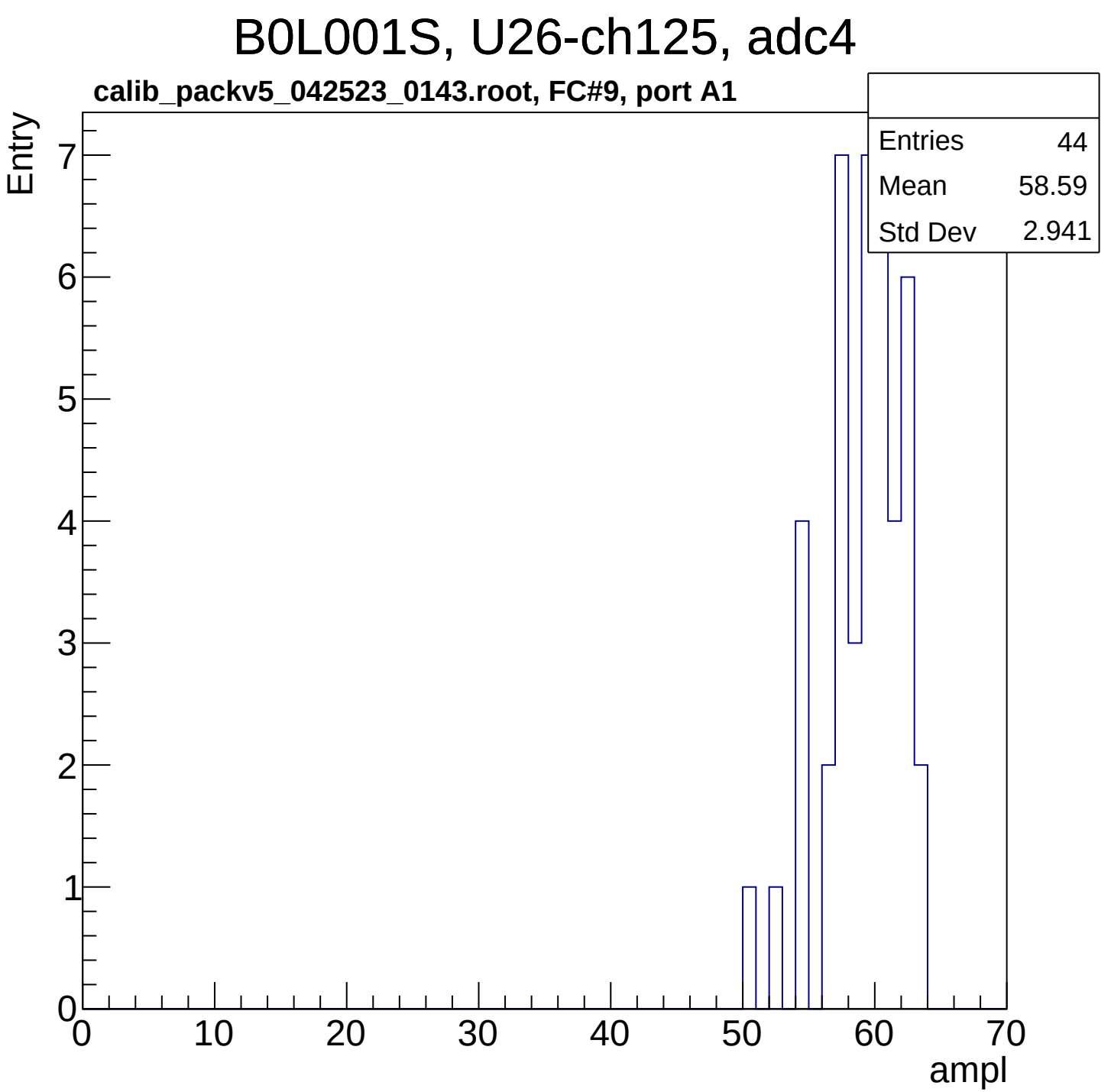
B0L001S, U26-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.59
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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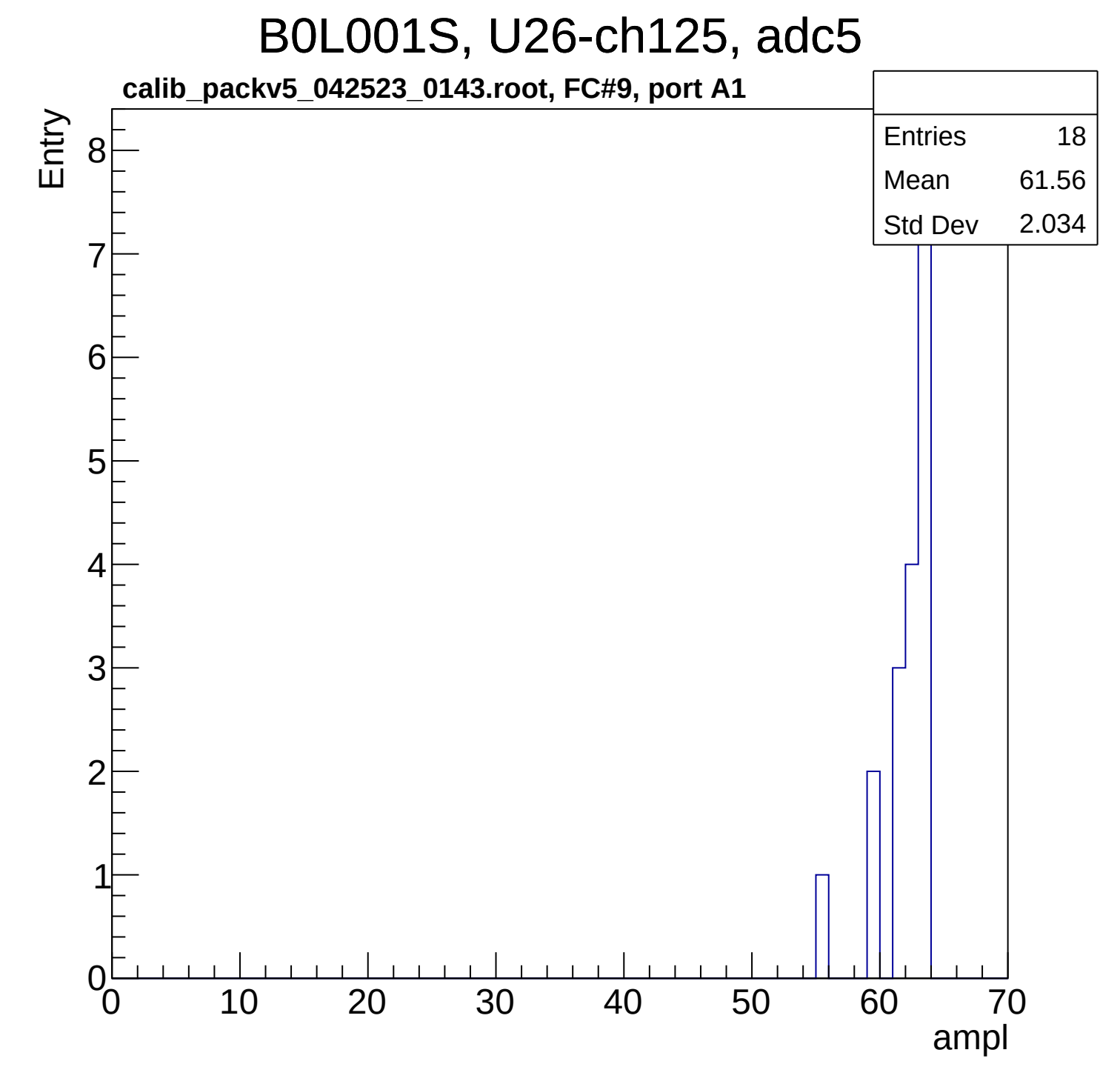
B0L001S, U26-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 61.56
Std Dev 2.034
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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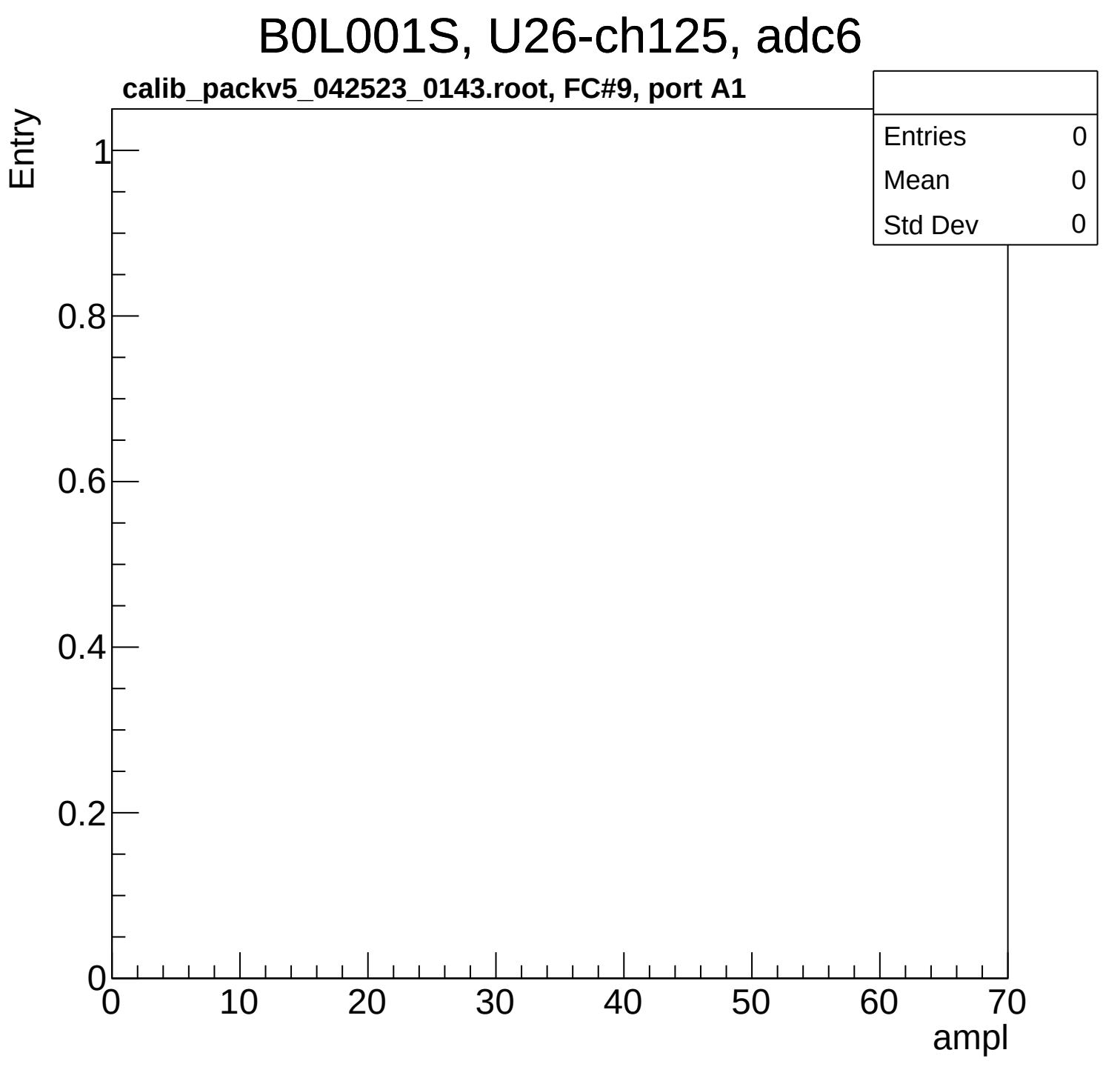
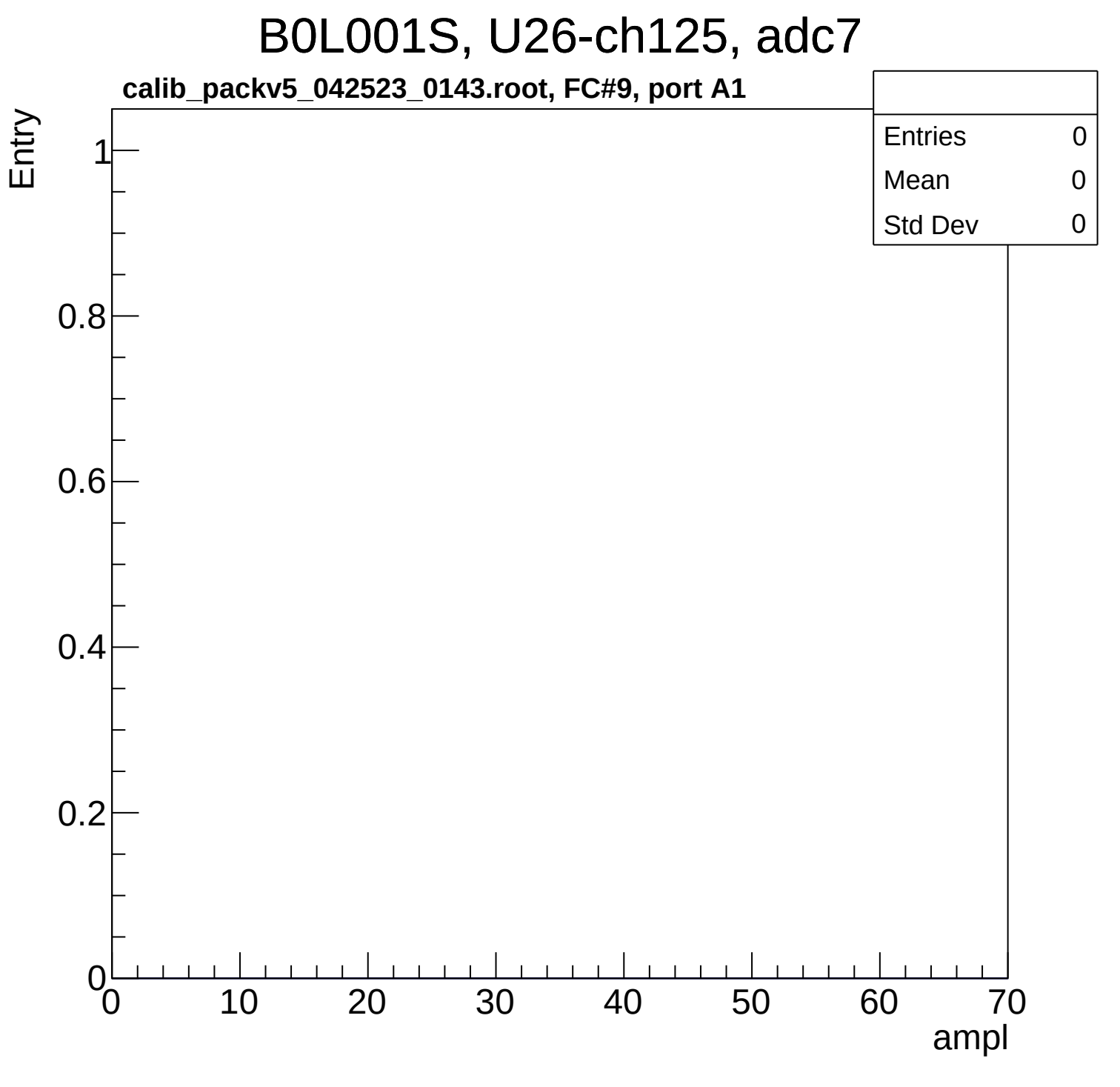

B0L001S, U26-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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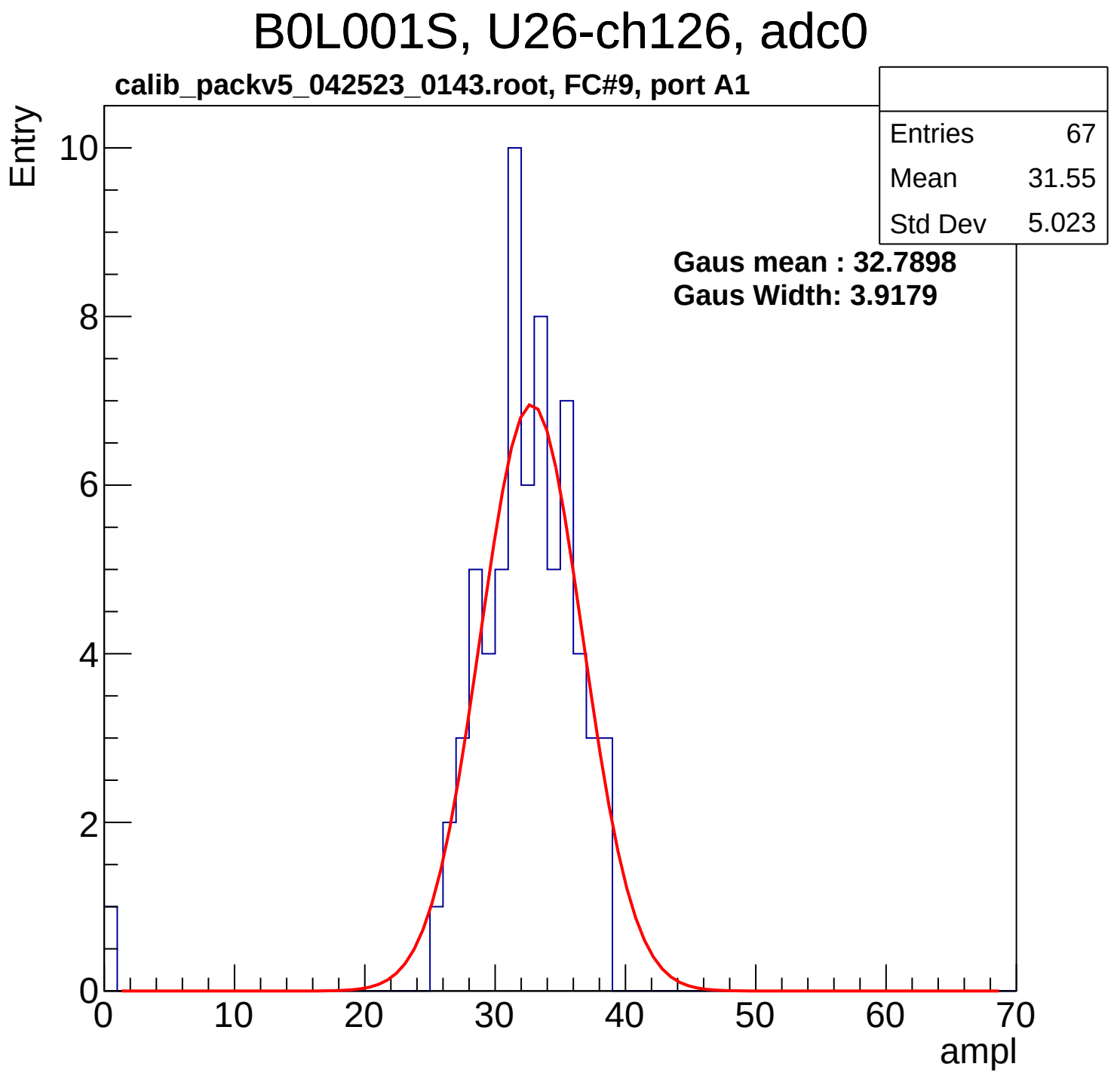
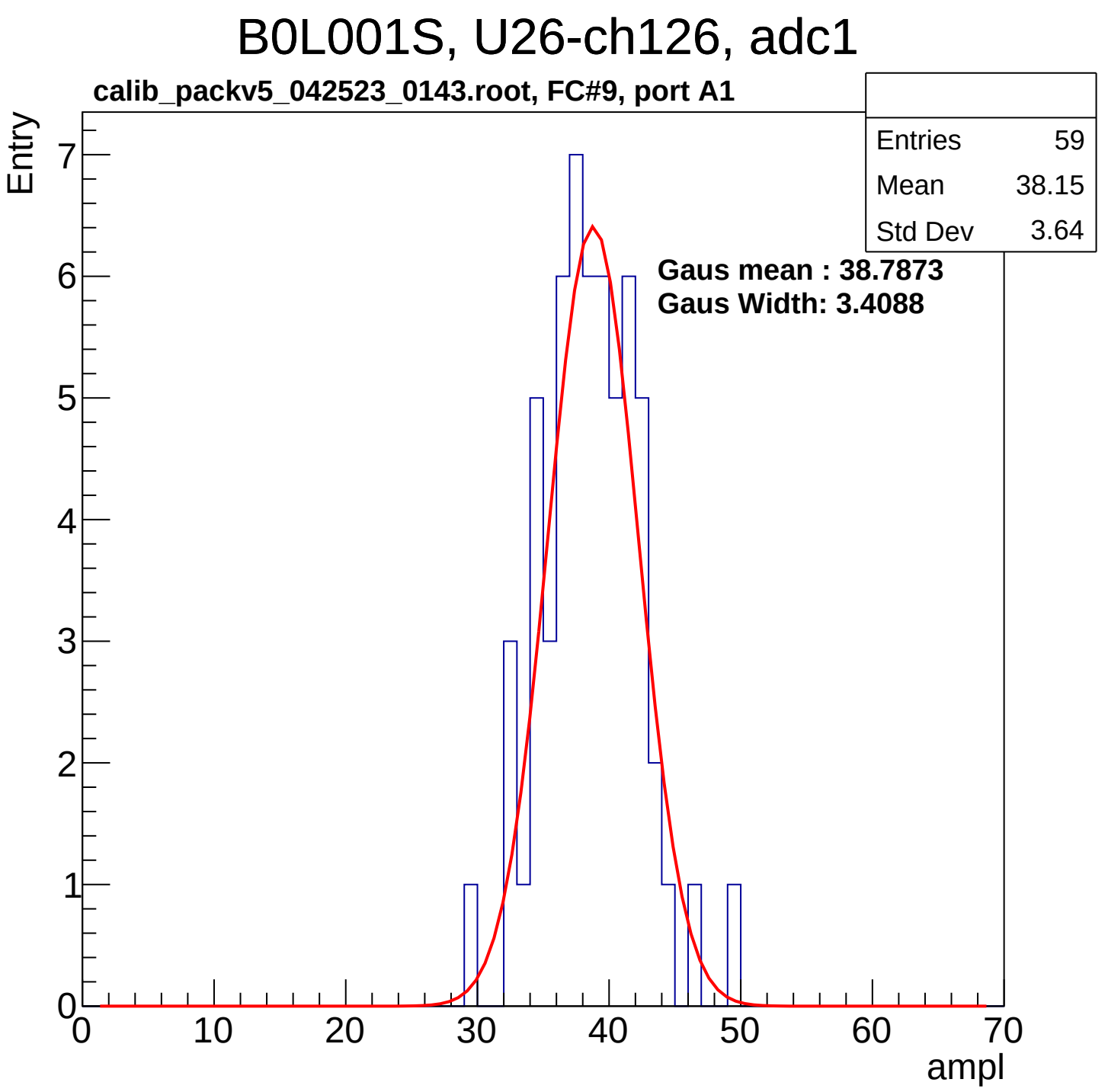
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.55
Std Dev 5.023
Gaus mean : 32.7898
Gaus Width: 3.9179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U26-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.15
Std Dev 3.64
Gaus mean : 38.7873
Gaus Width: 3.4088
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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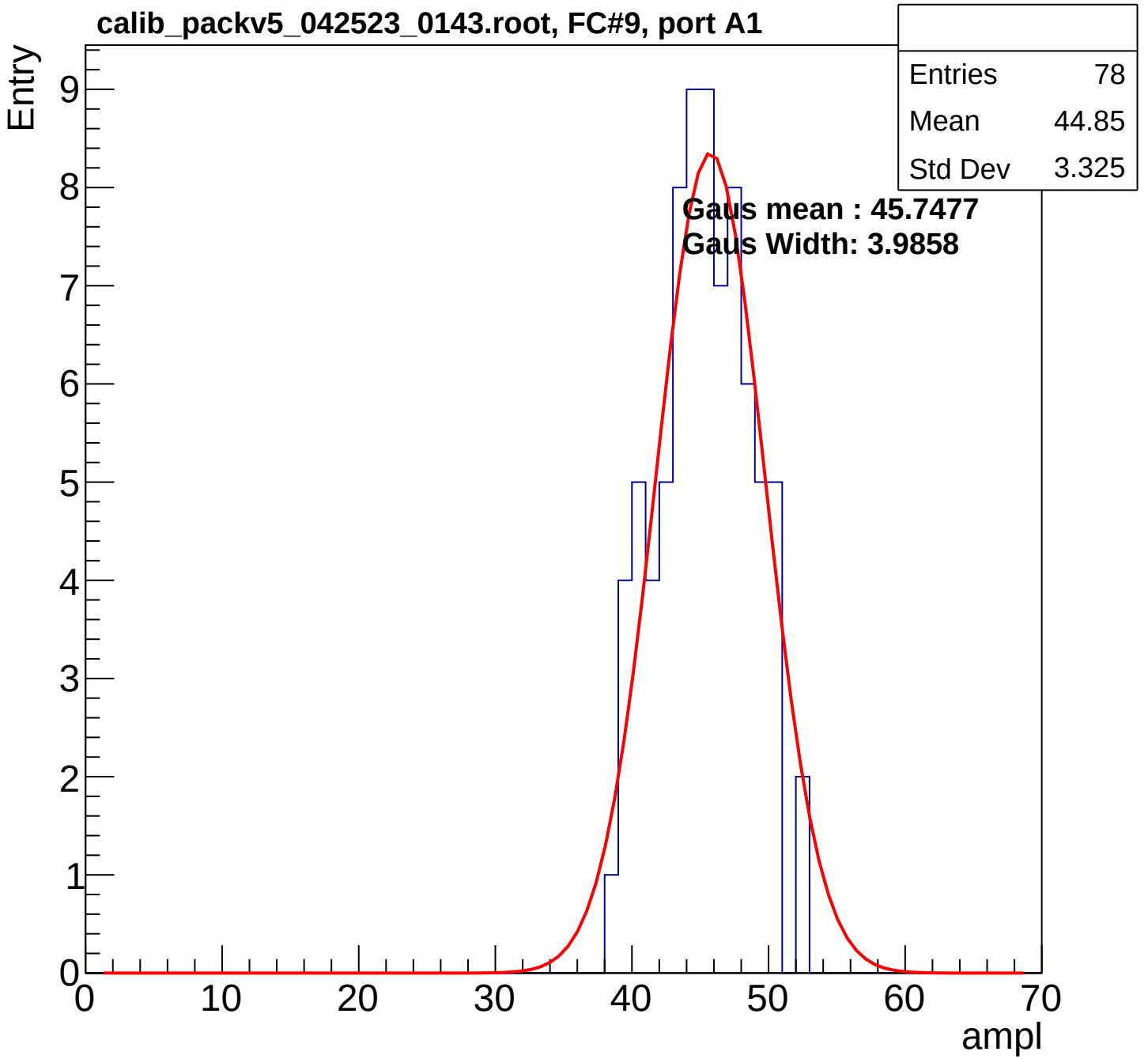
B0L001S, U26-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.85
Std Dev 3.325
Gaus mean : 45.7477
Gaus Width: 3.9858
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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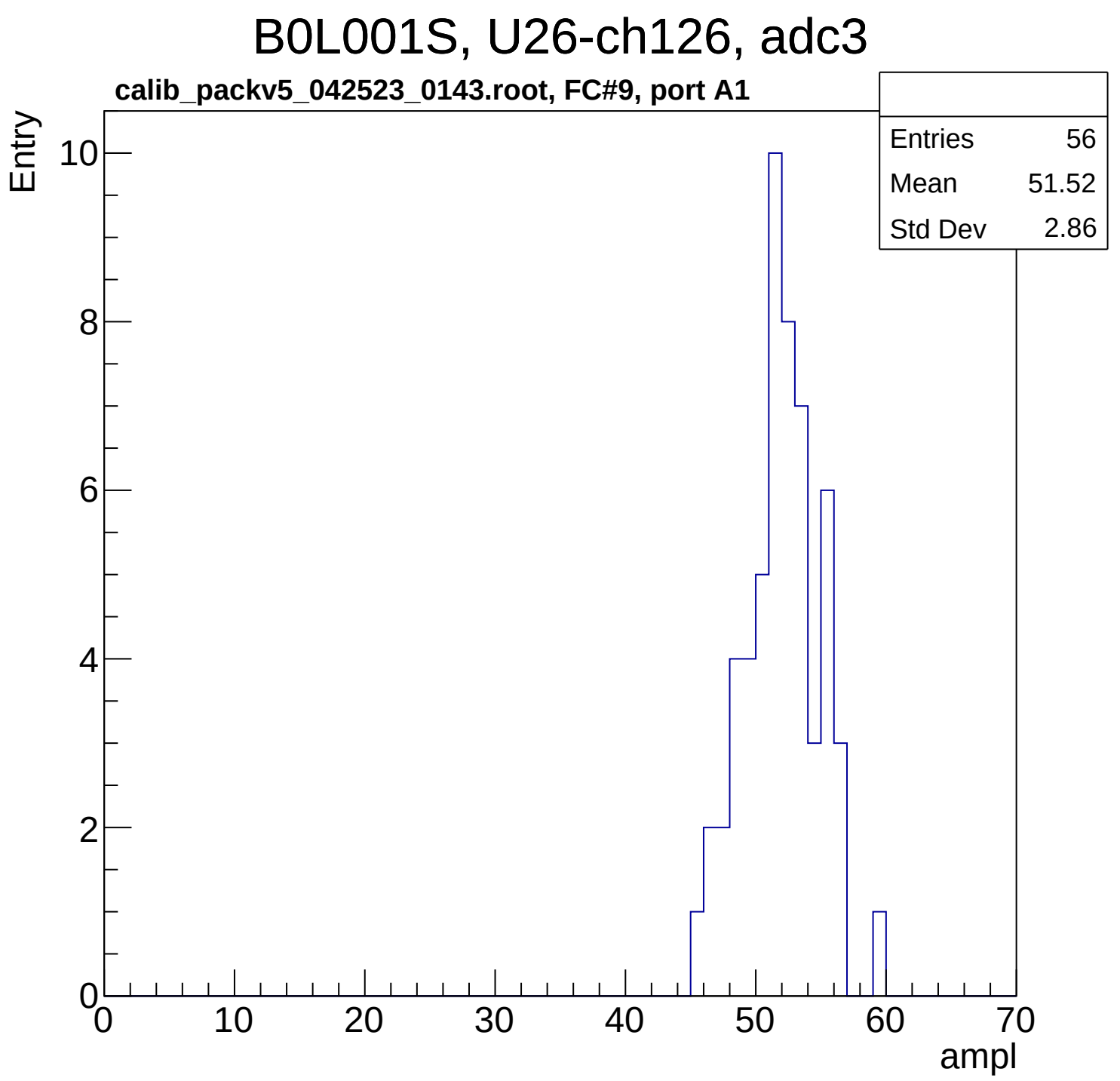
B0L001S, U26-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 51.52
Std Dev 2.86
Entry
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2
4
6
8
10
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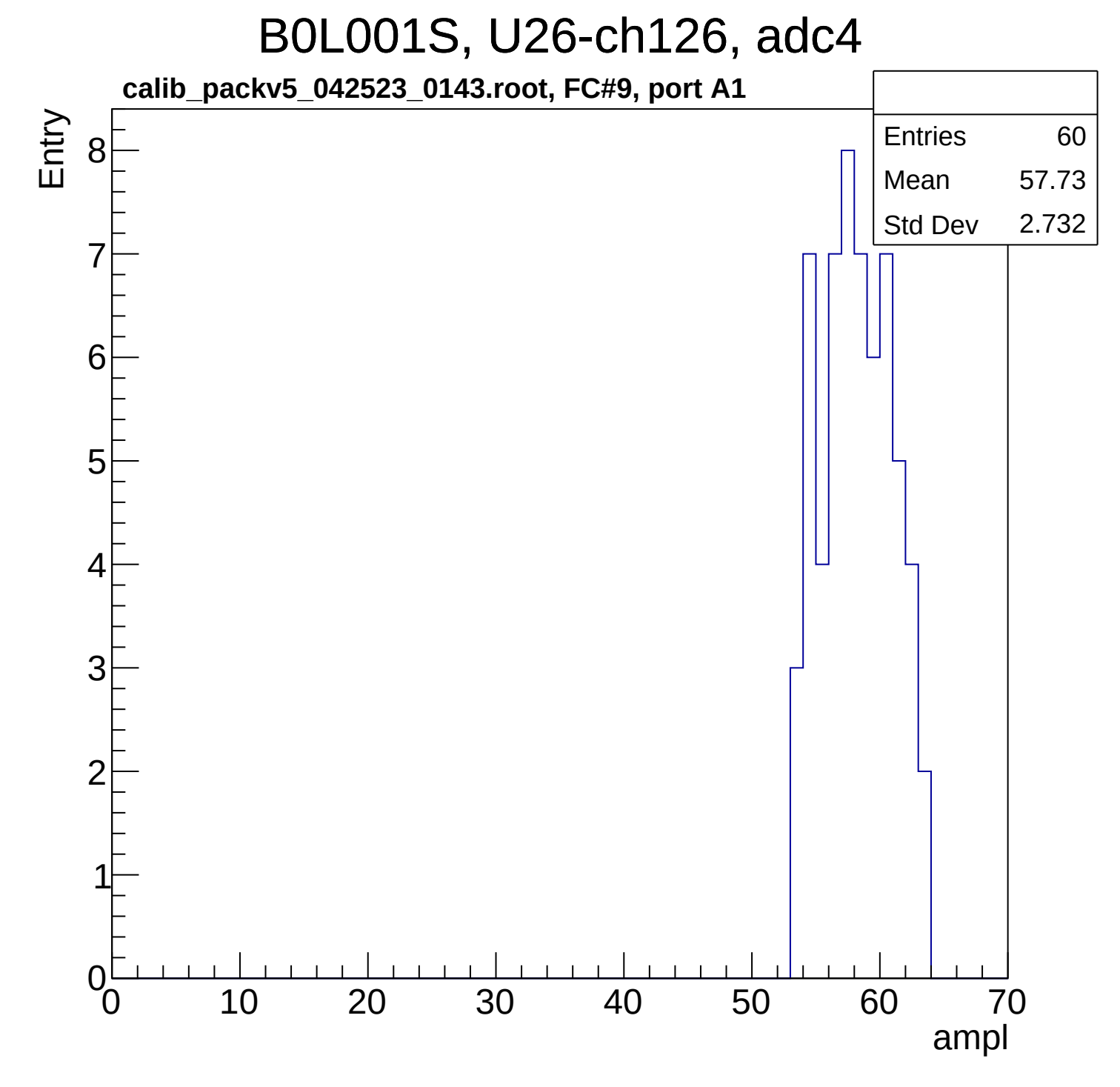
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.73
Std Dev 2.732
Entry
ampl
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1
2
3
4
5
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7
8
0
10
20
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40
50
60
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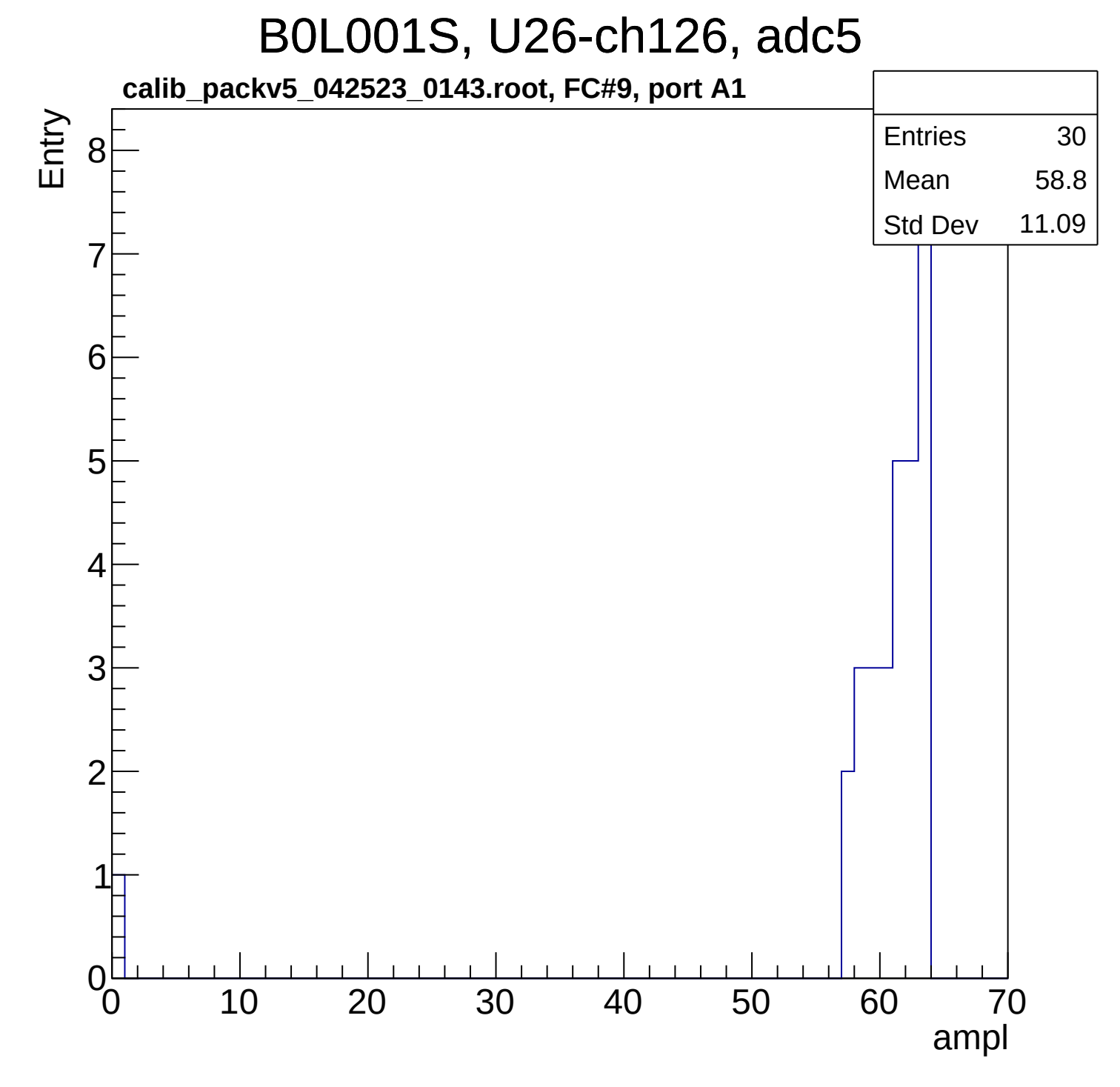
B0L001S, U26-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.8
Std Dev 11.09
Entry
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2
3
4
5
6
7
8
0
10
20
30
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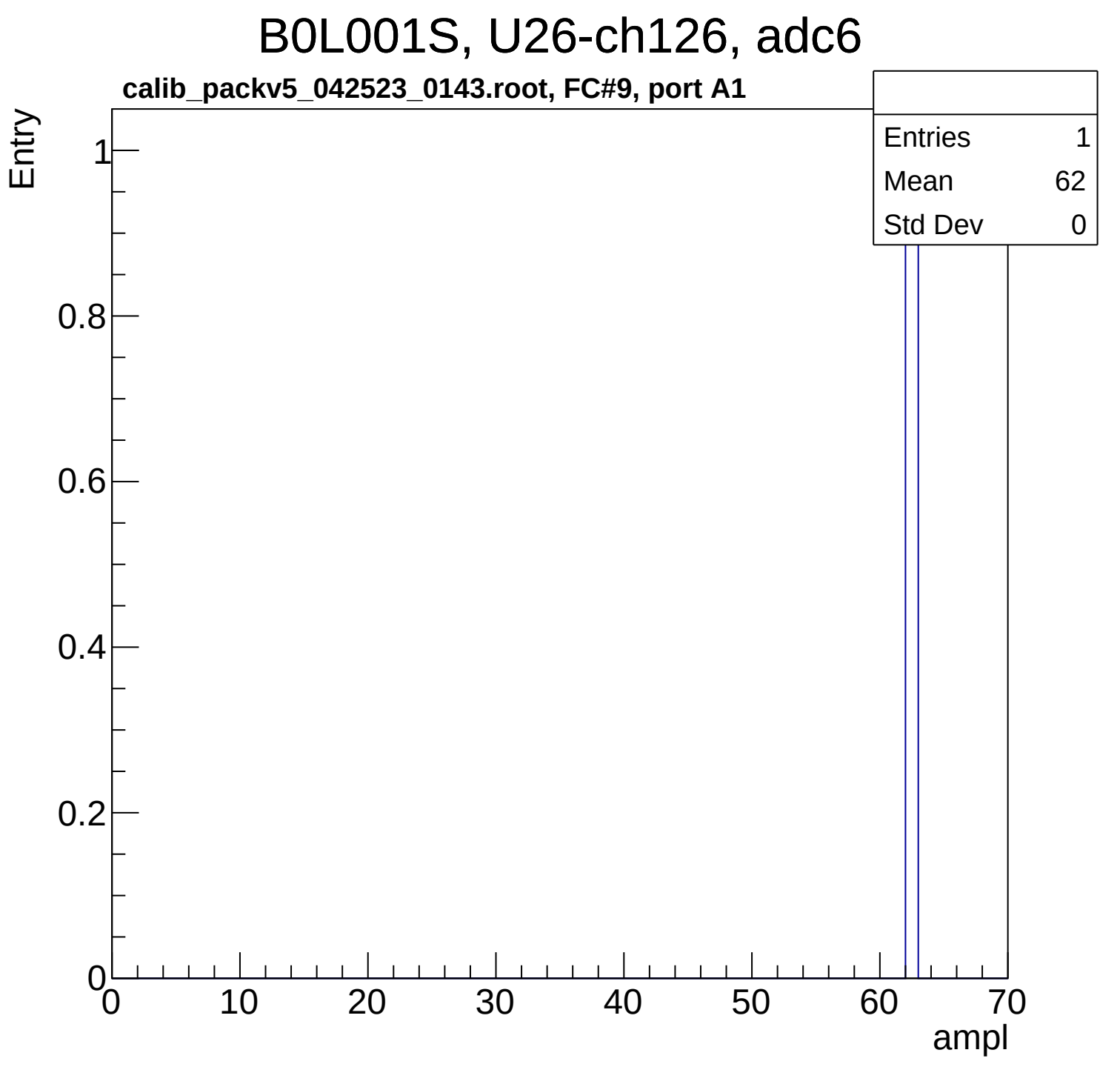
50
60
70
ampl


B0L001S, U26-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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0.4
0.2
0
0
10
20
30
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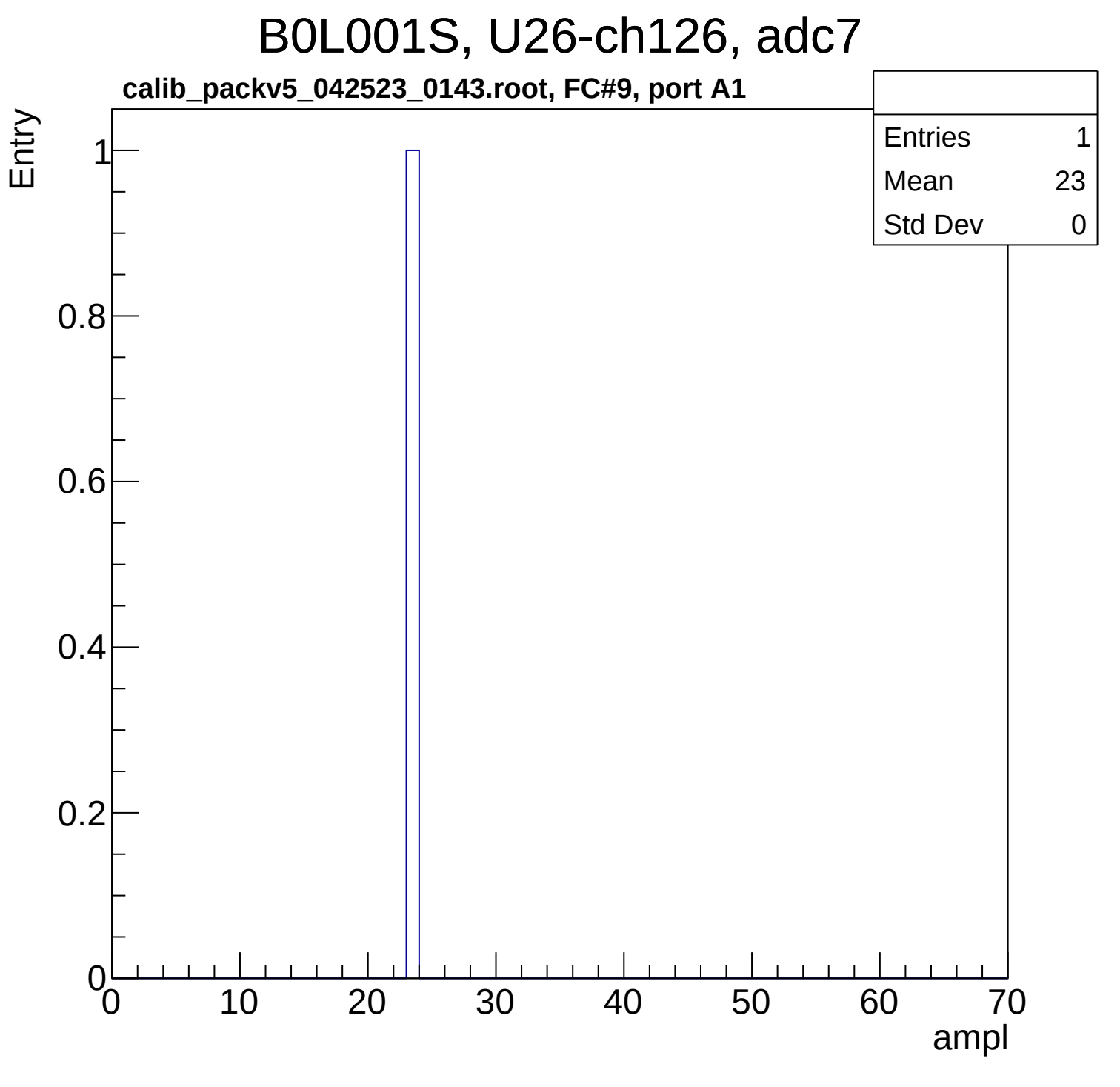
50
60
70
ampl


B0L001S, U26-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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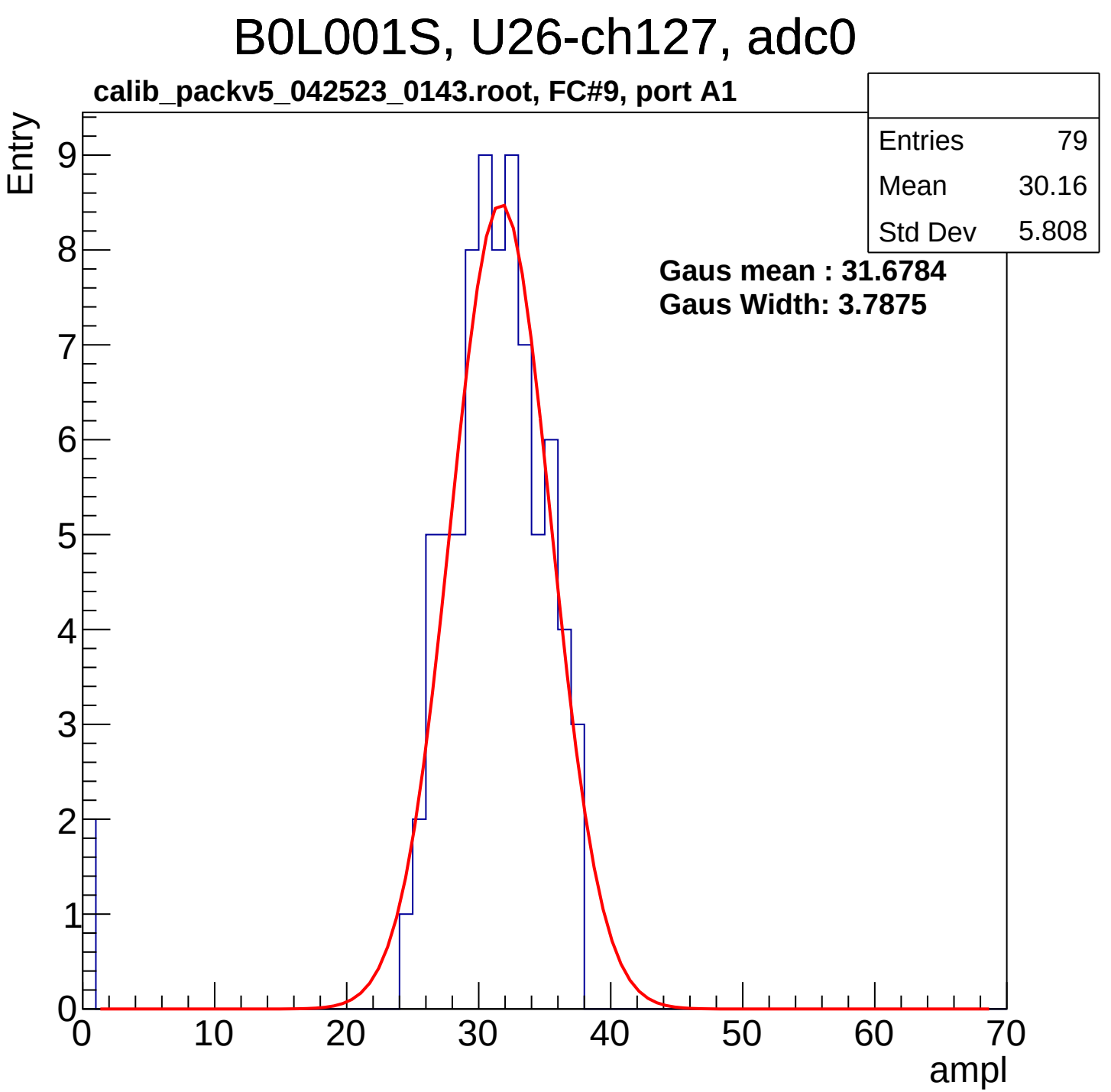
50
60
70
ampl


B0L001S, U26-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.16
Std Dev 5.808
Gaus mean : 31.6784
Gaus Width: 3.7875
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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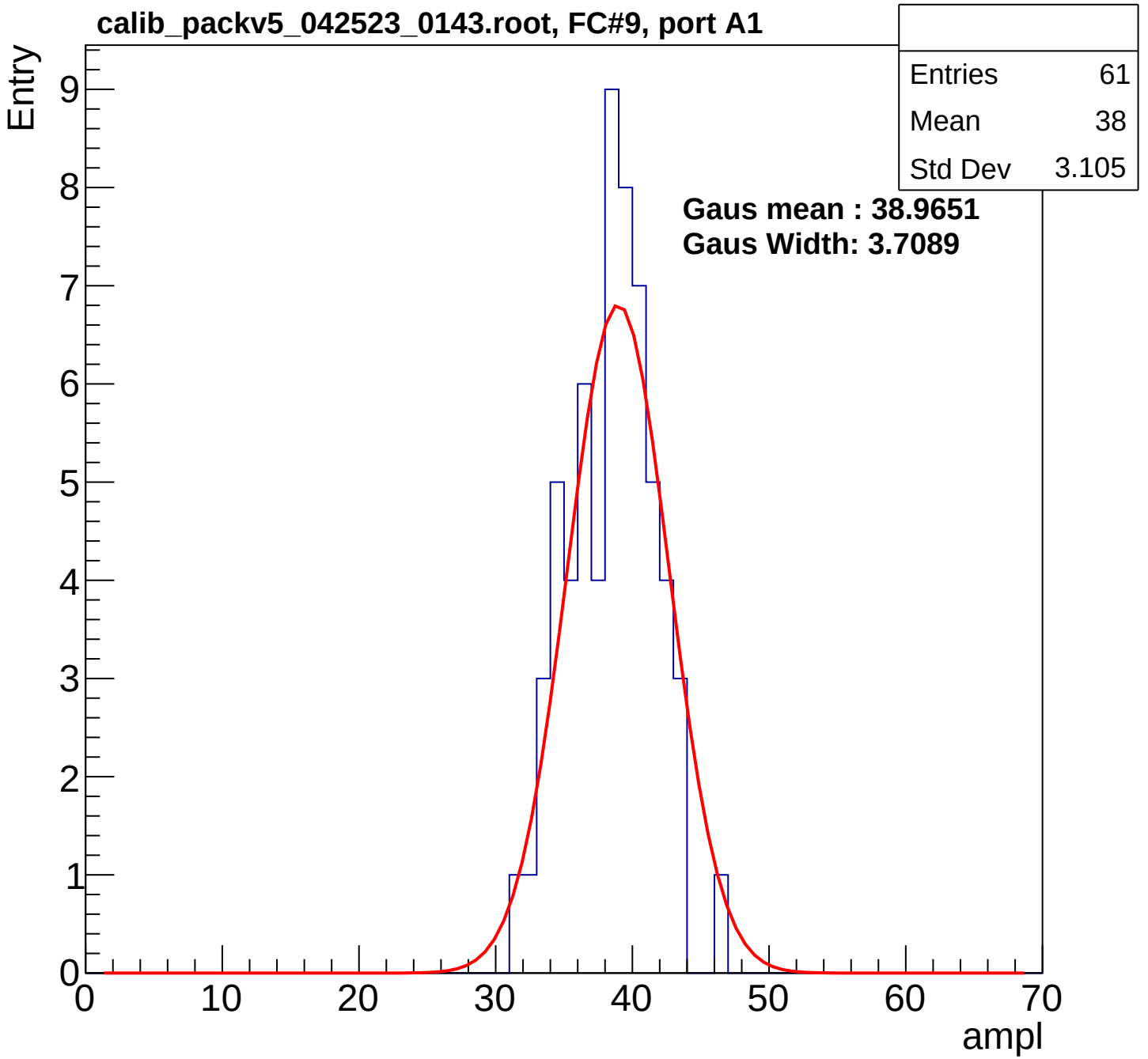
B0L001S, U26-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 38
Std Dev 3.105
Gaus mean : 38.9651
Gaus Width: 3.7089
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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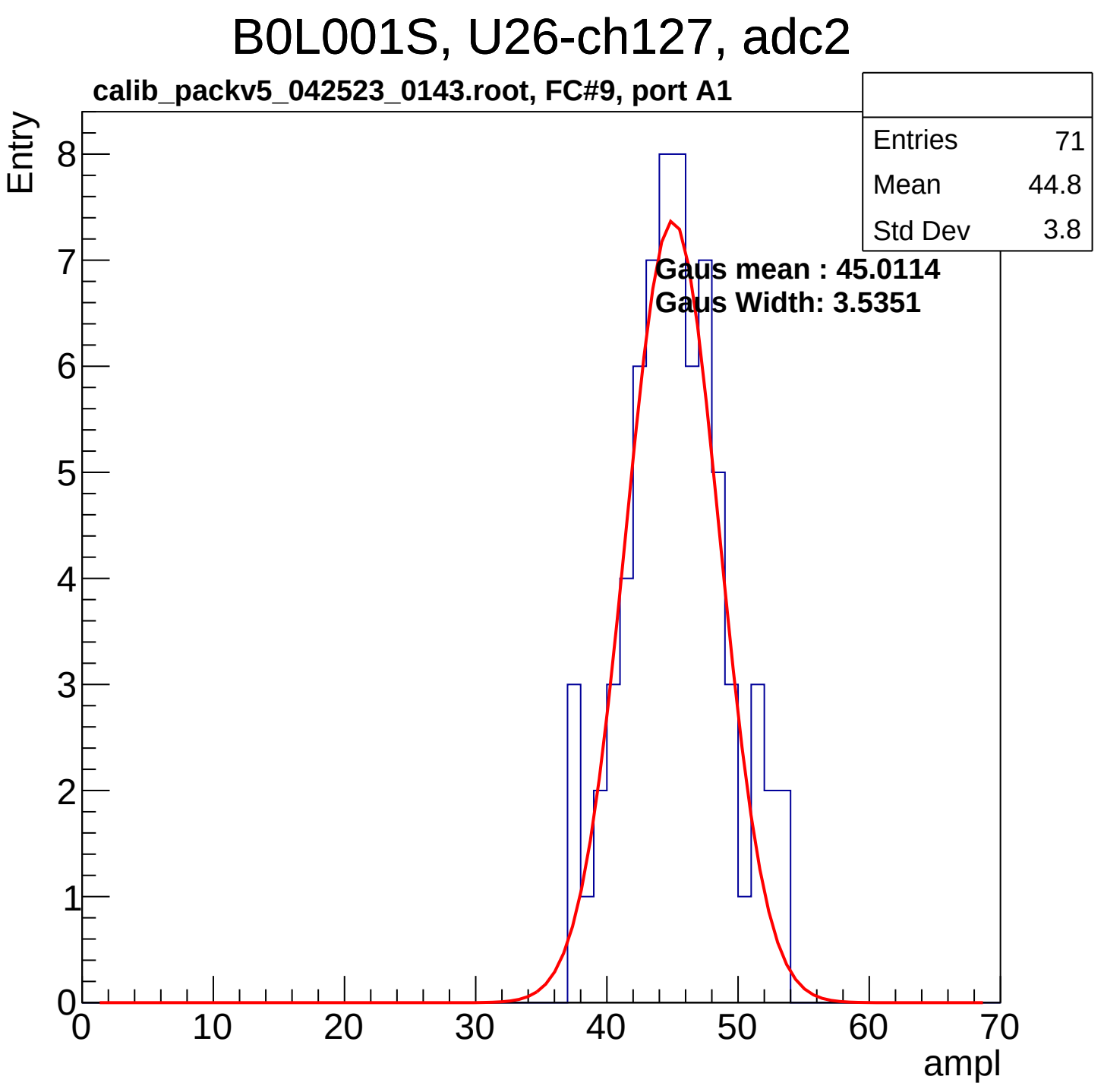
B0L001S, U26-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.8
Std Dev 3.8
Gaus mean : 45.0114
Gaus Width: 3.5351
Entry
ampl
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4
5
6
7
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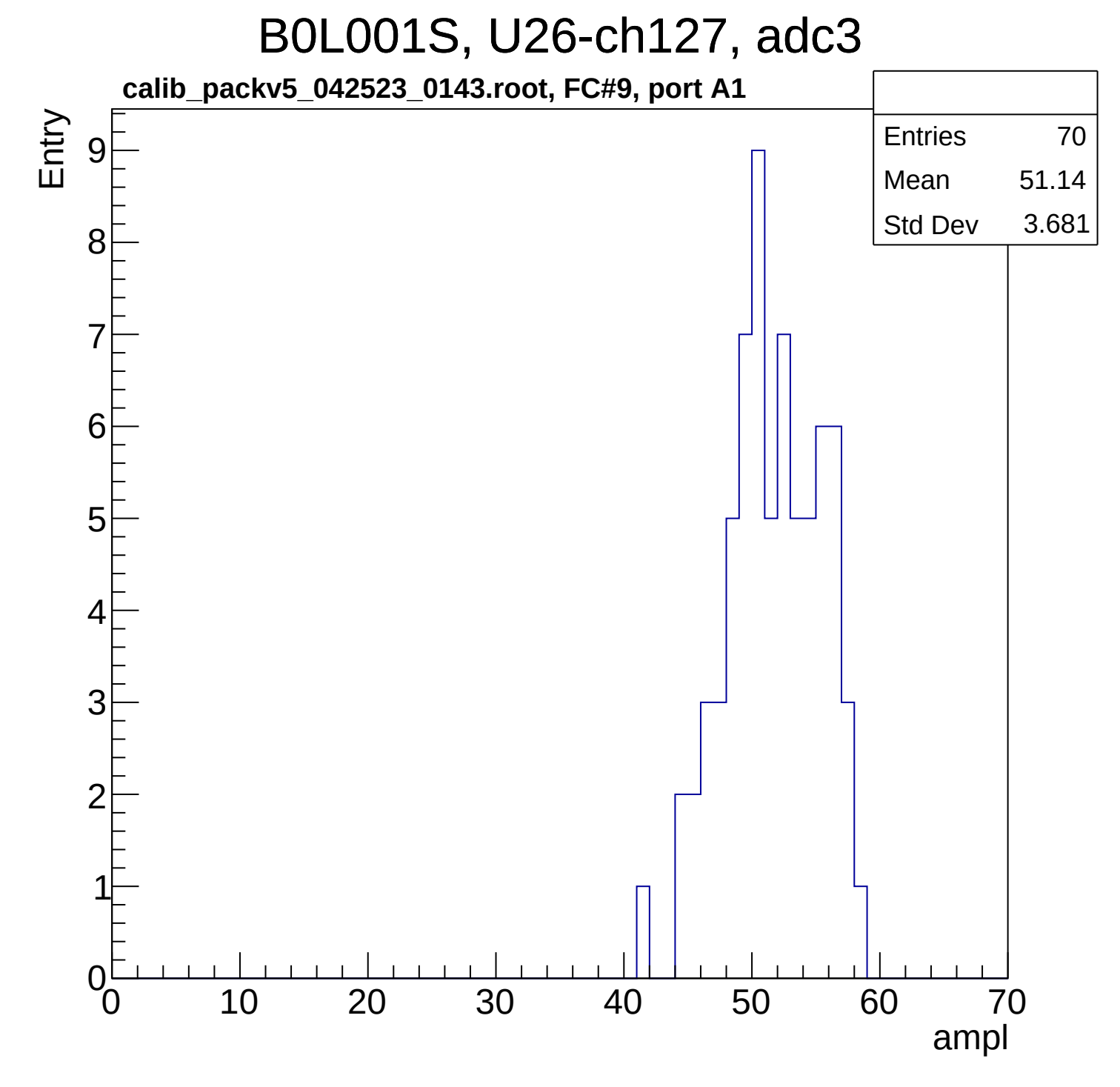
B0L001S, U26-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.14
Std Dev 3.681
Entry
ampl
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3
4
5
6
7
8
9
0
10
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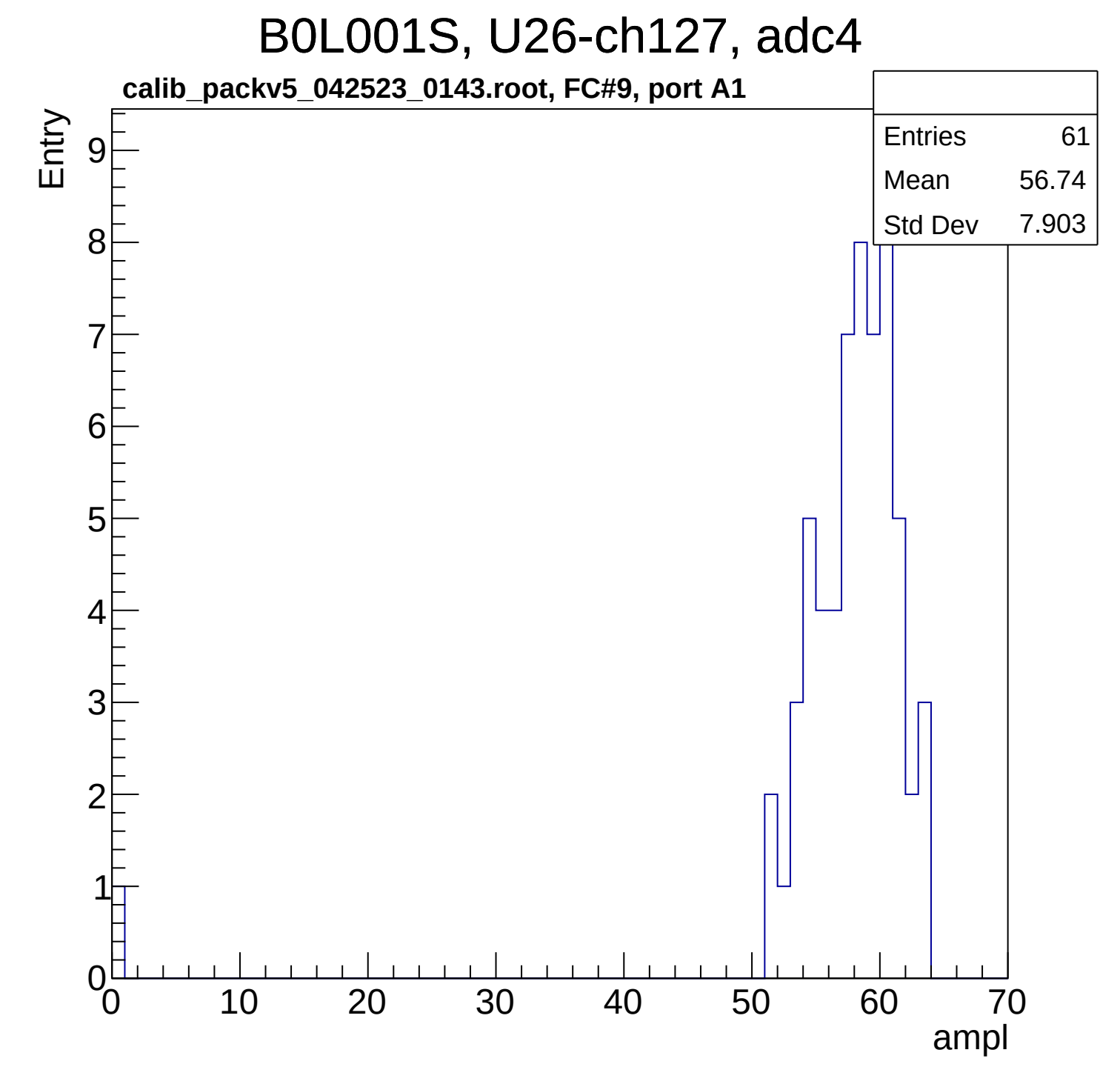
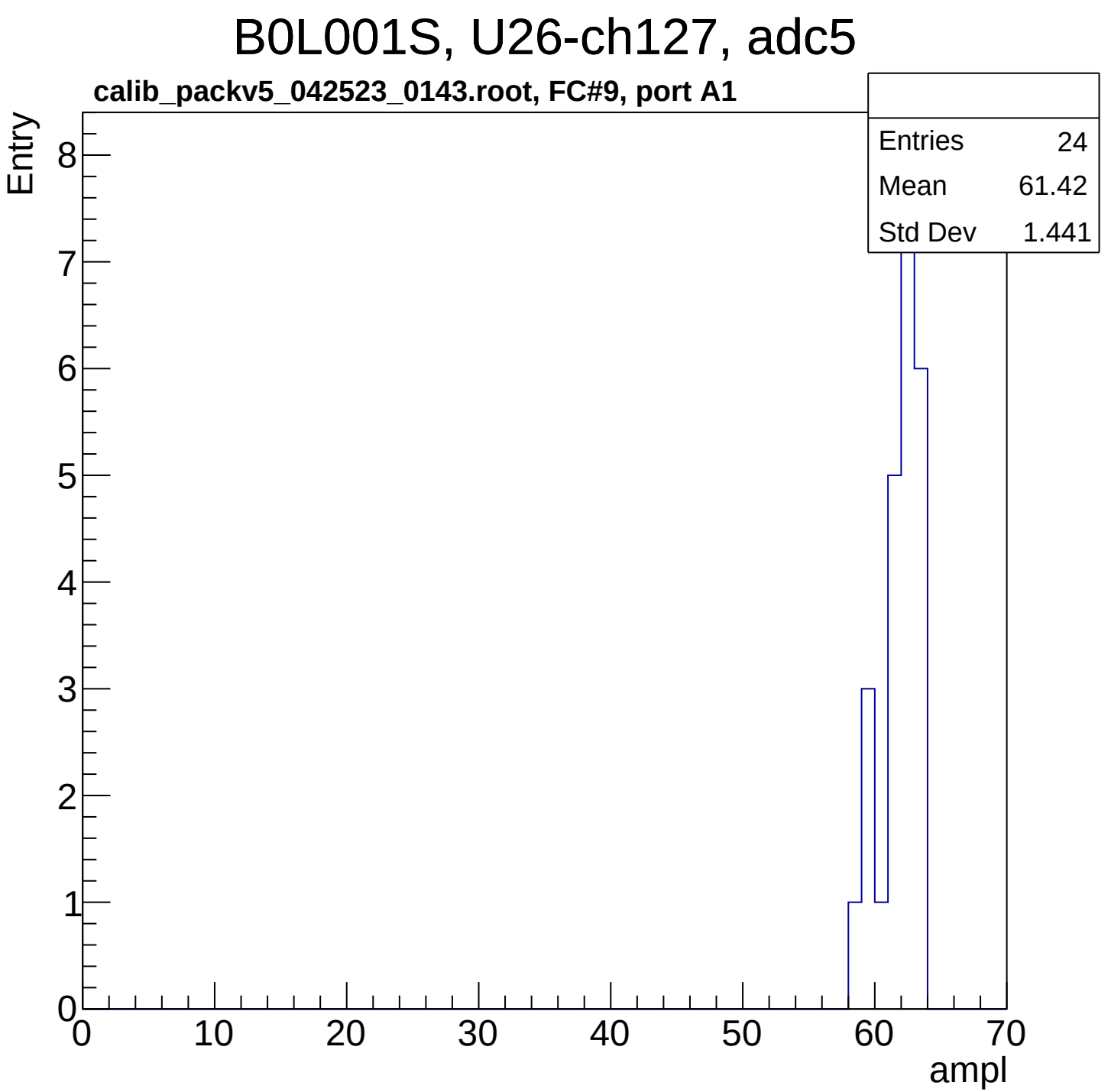
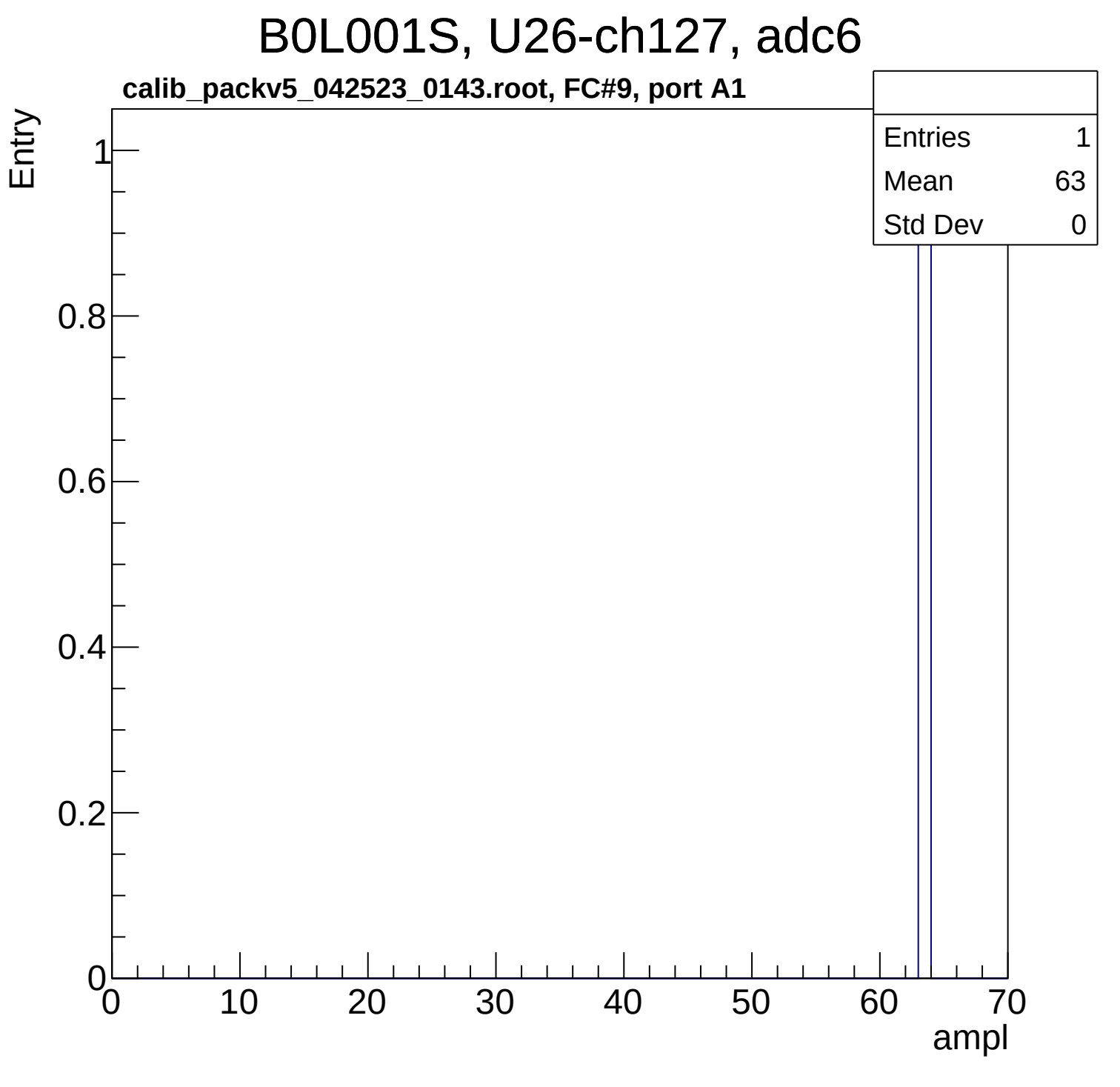
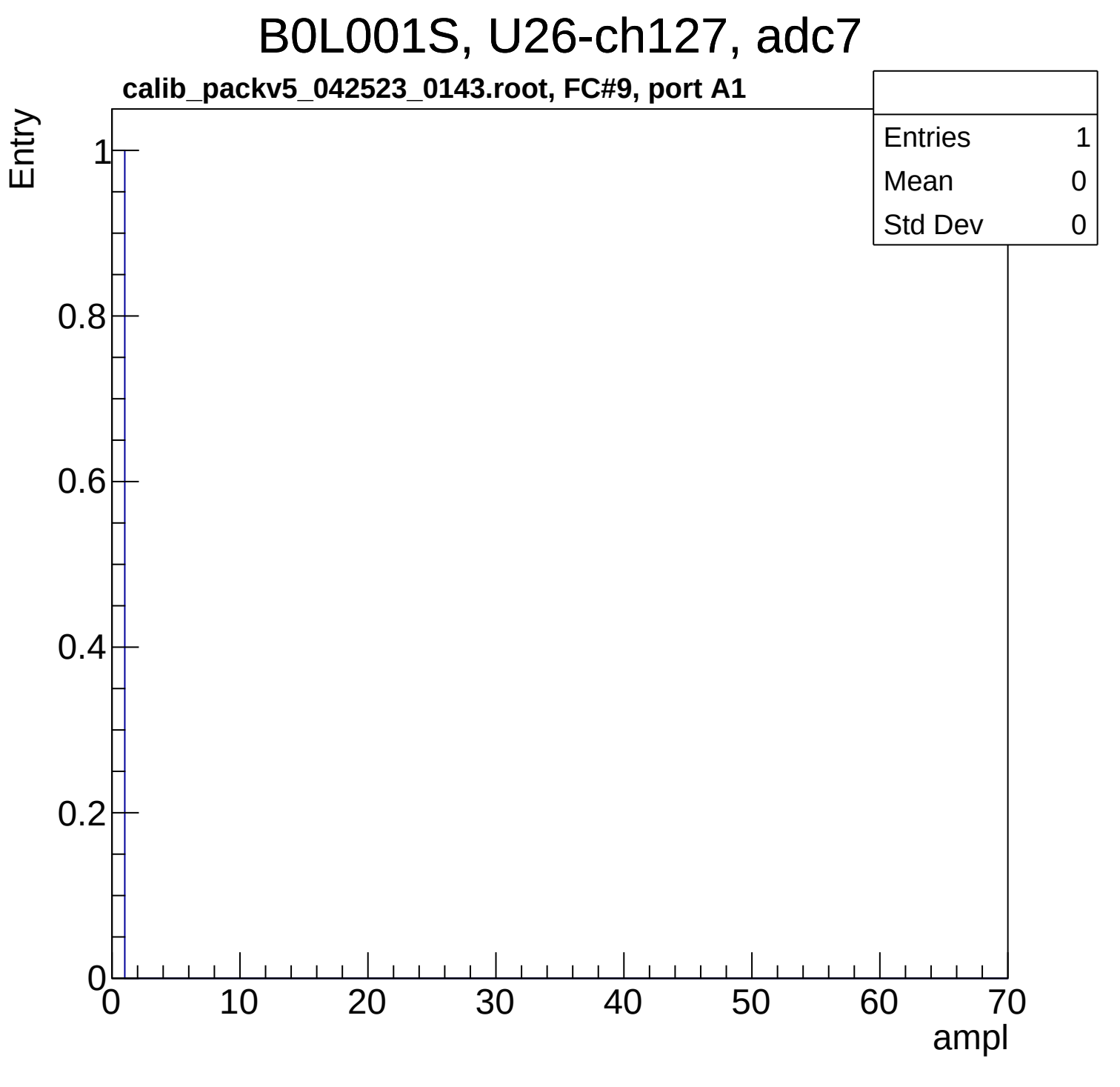
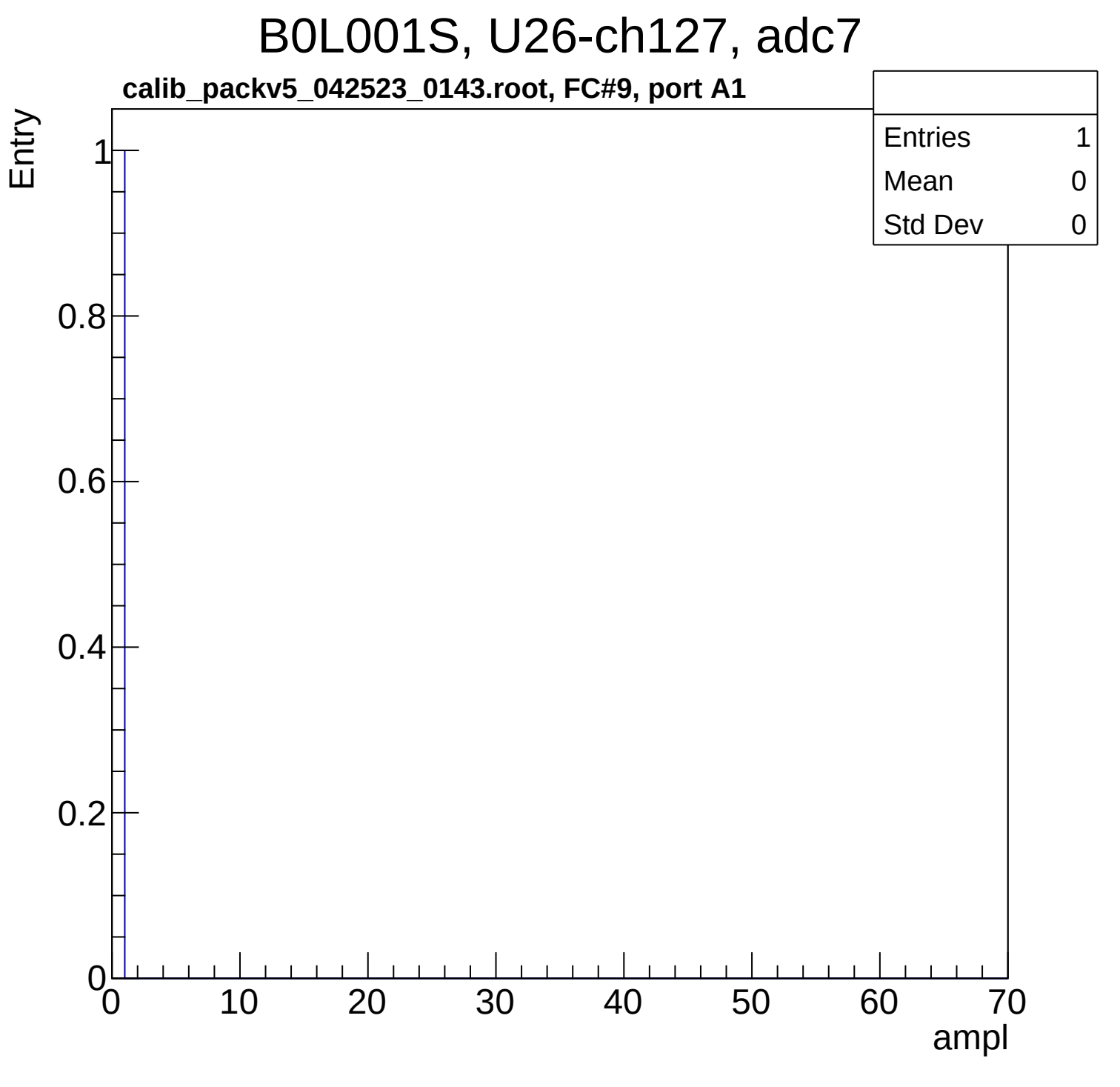

B0L001S, U26-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.74
Std Dev 7.903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U26-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 61.42
Std Dev 1.441
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U26-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U26-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl