



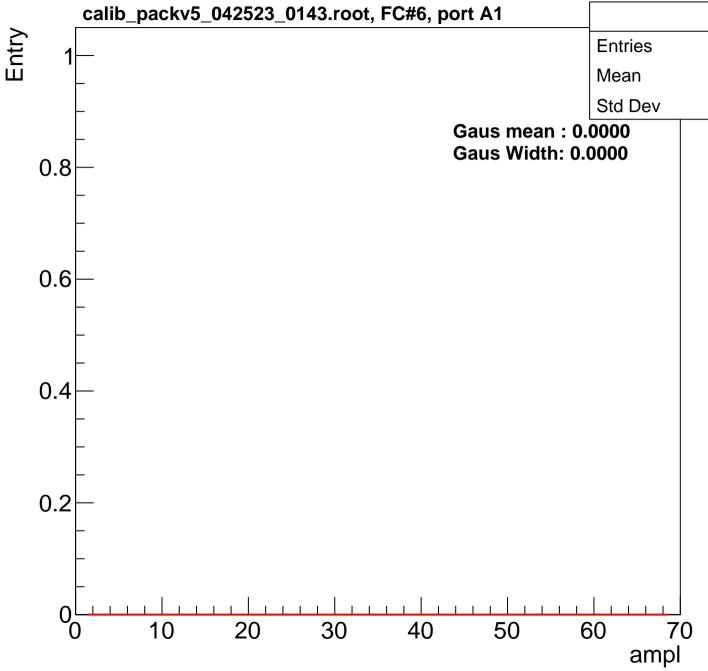


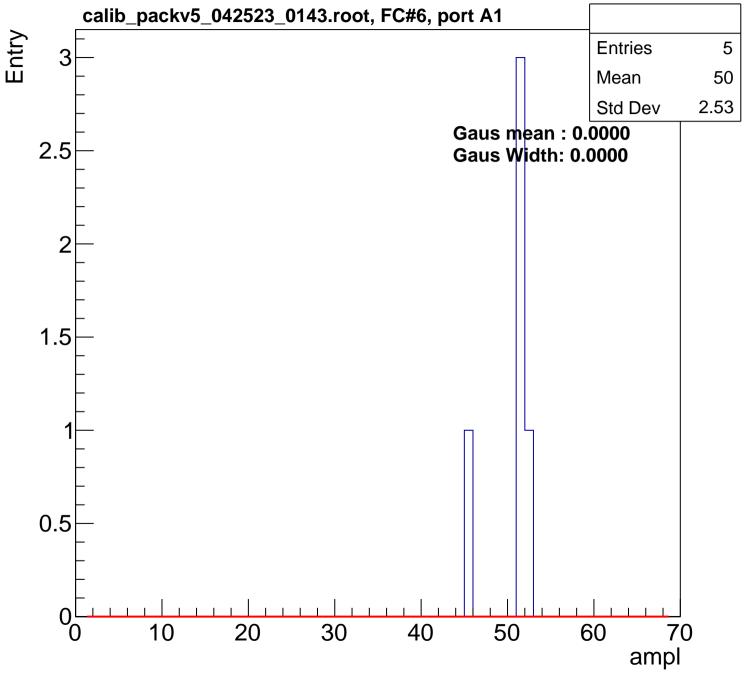


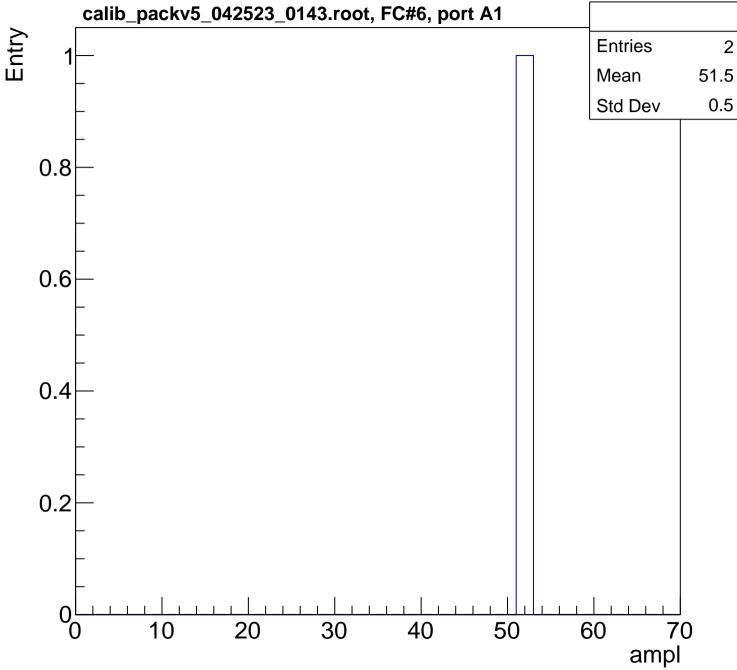


B0L100S, U3-ch0, adc7 calib_packv5_042523_0143.root, FC#6, port A1 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl





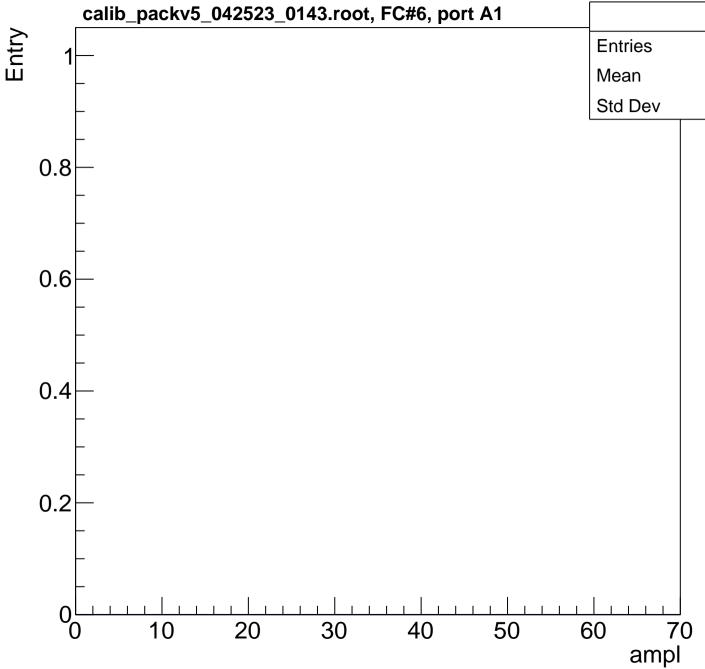


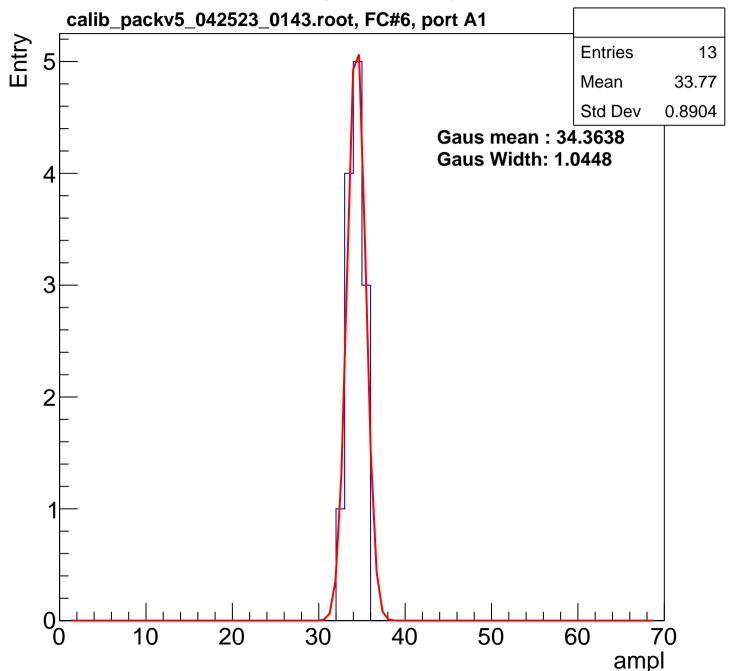


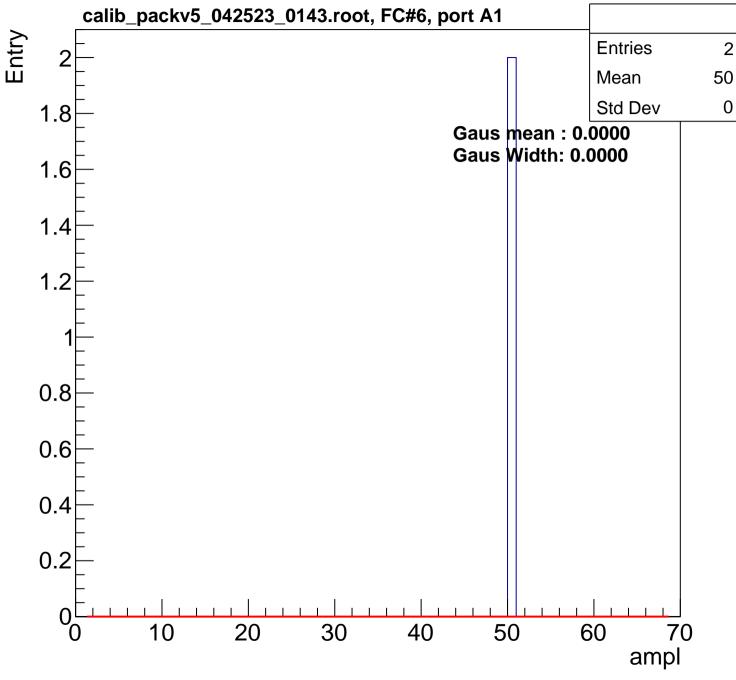


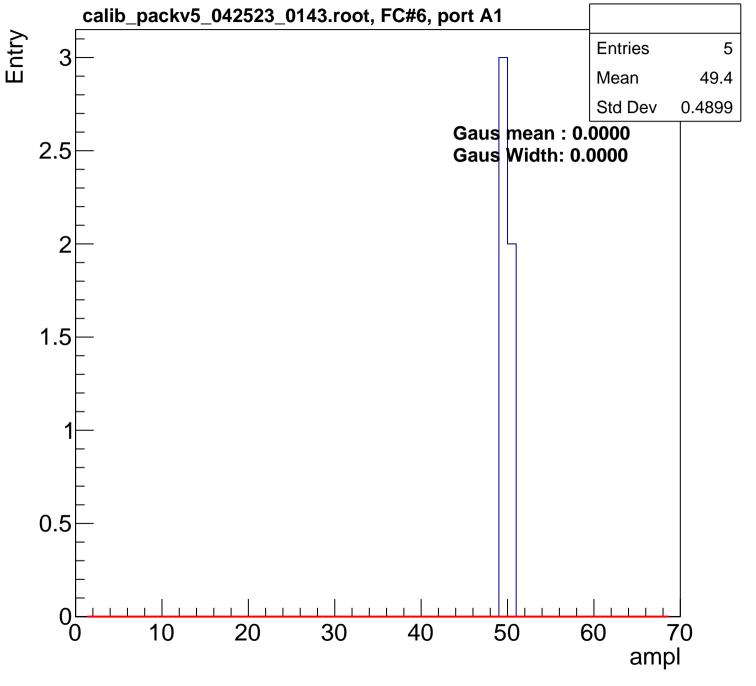


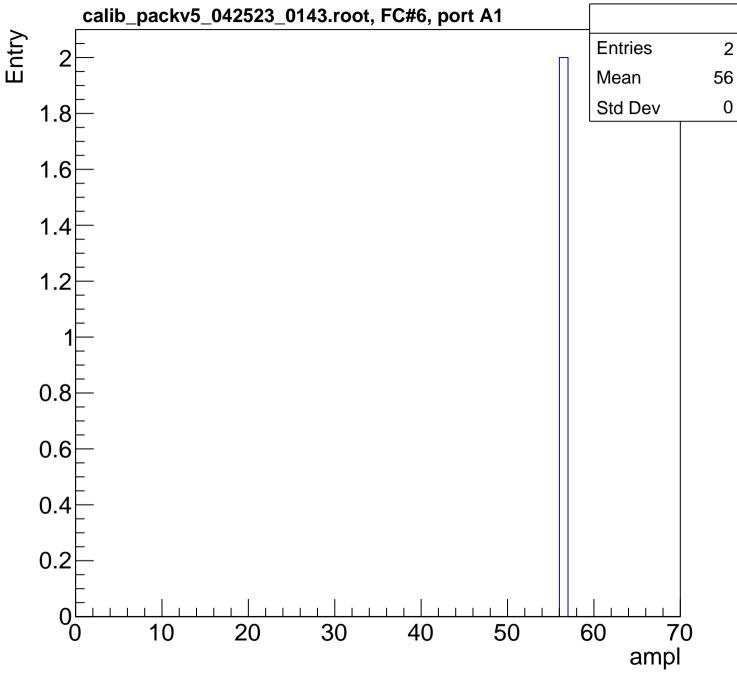










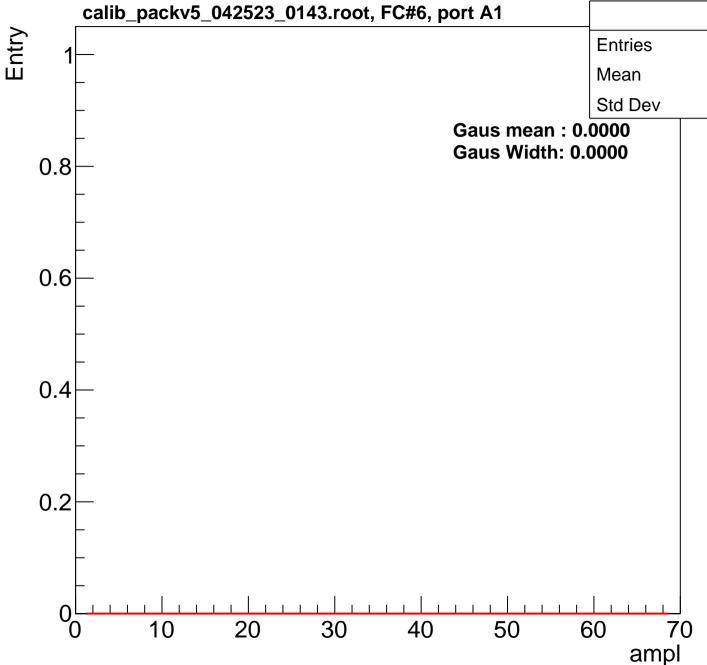


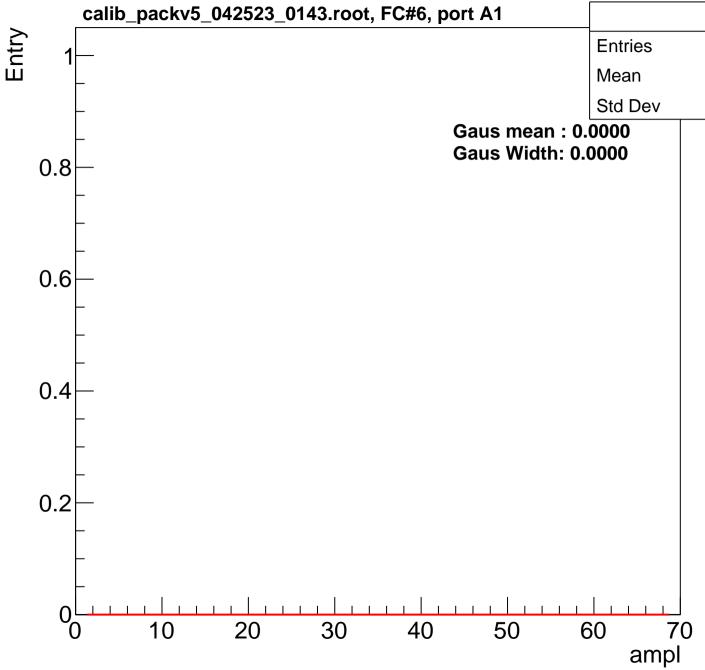












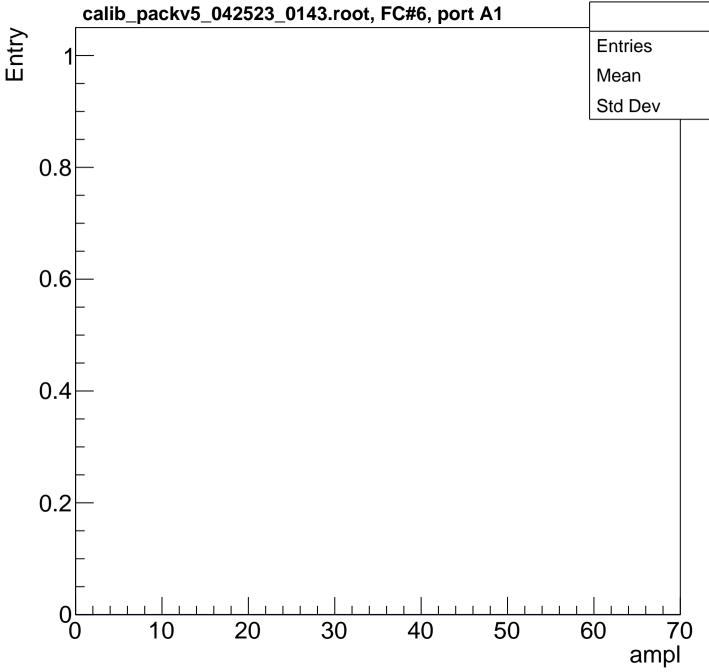






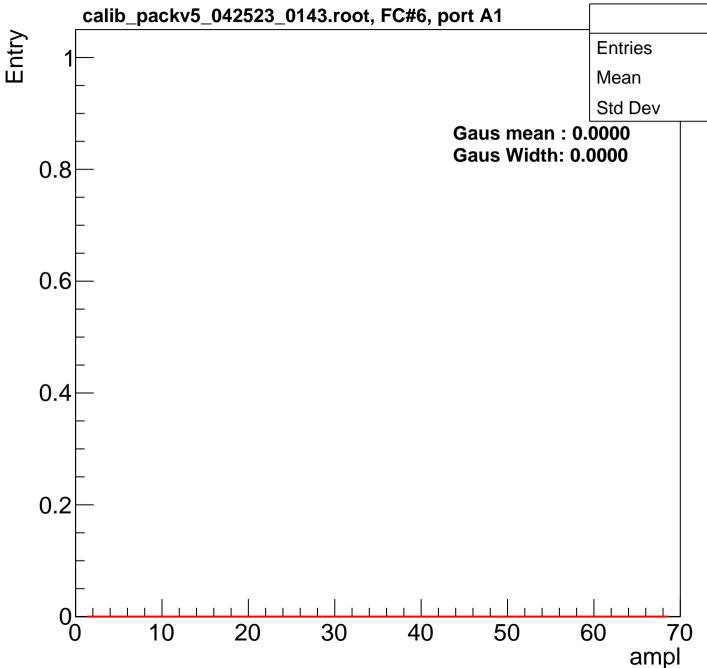












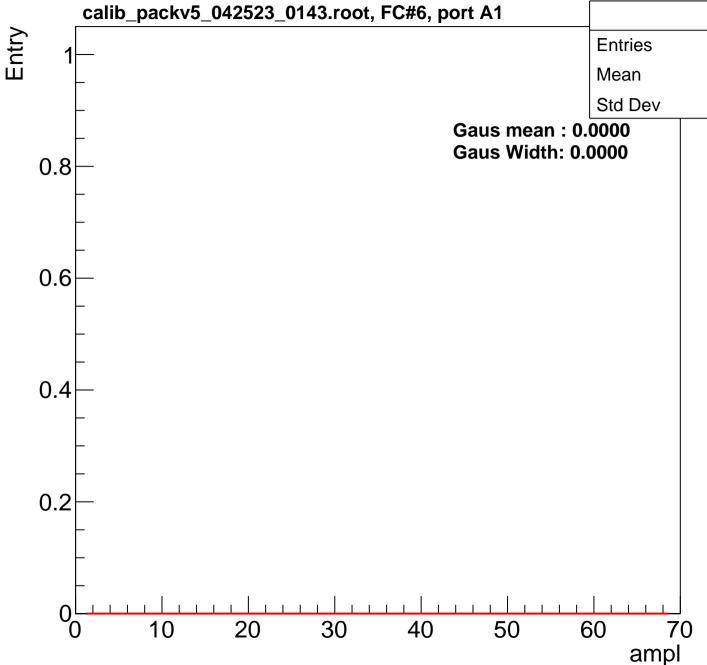














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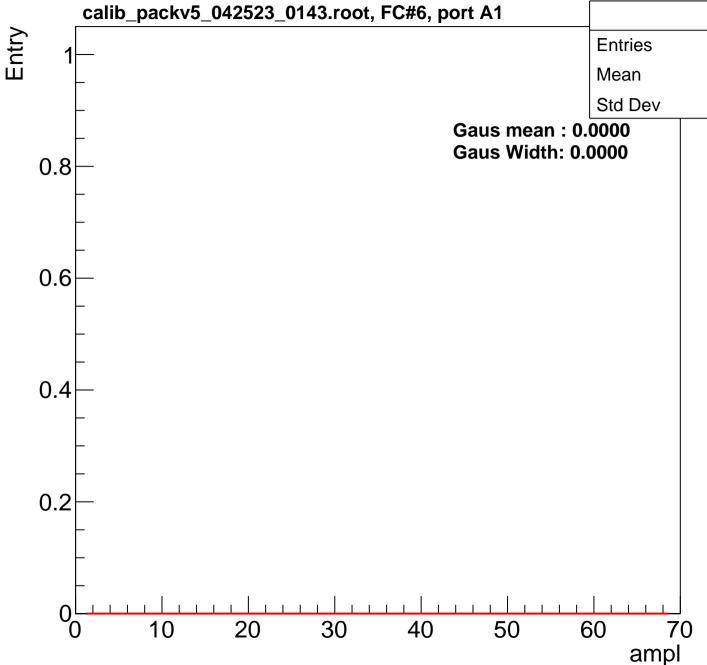




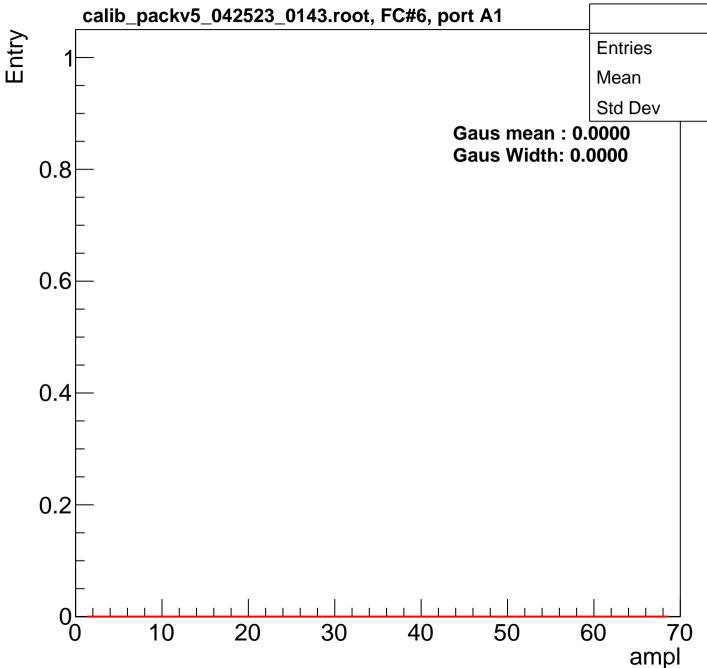












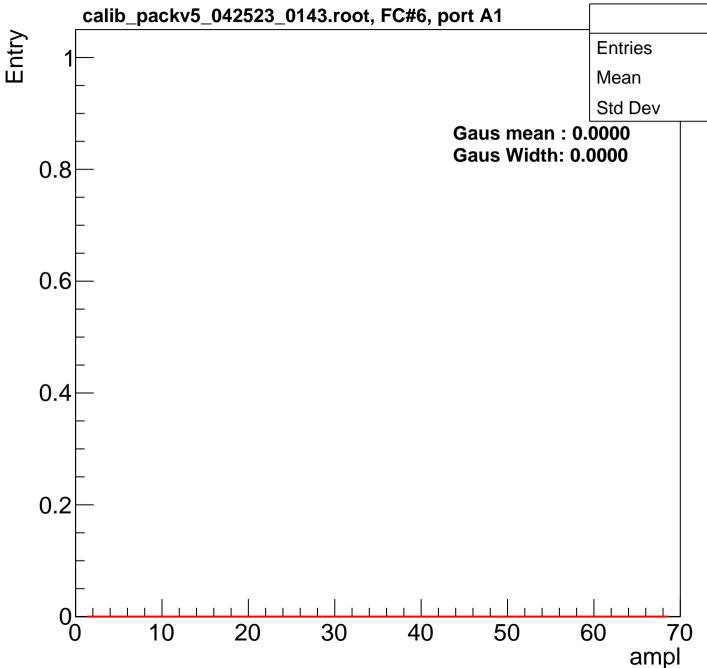


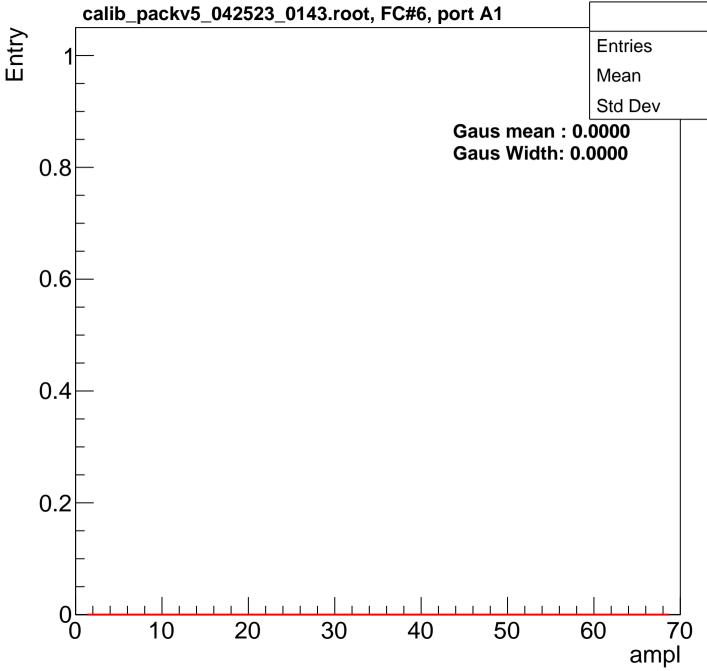












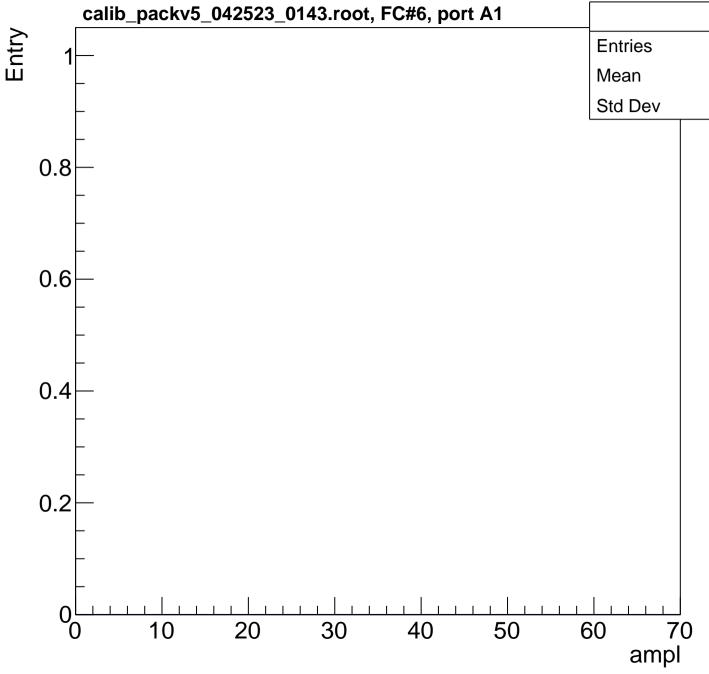


















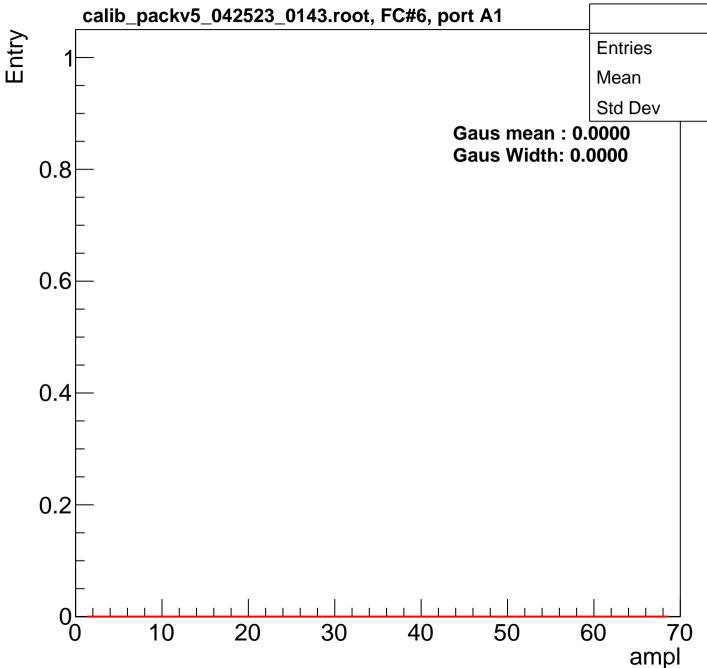




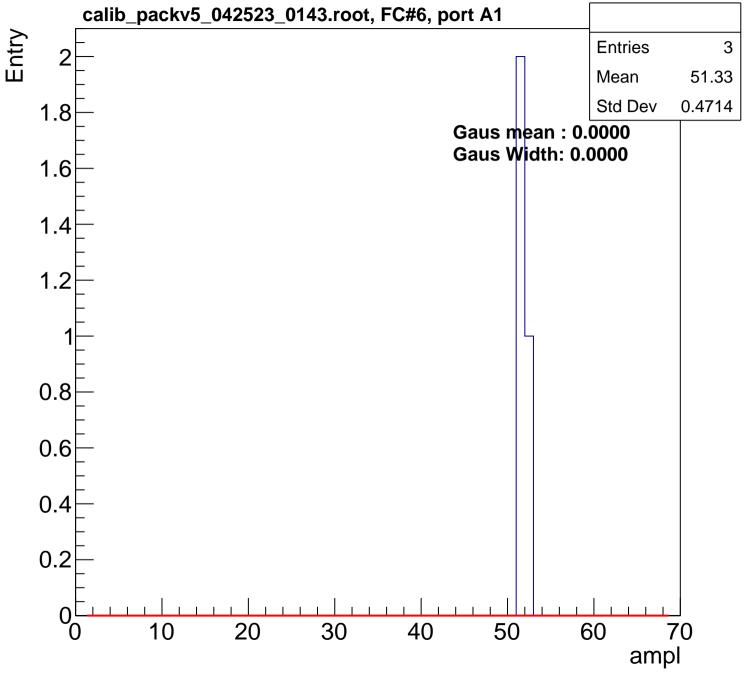


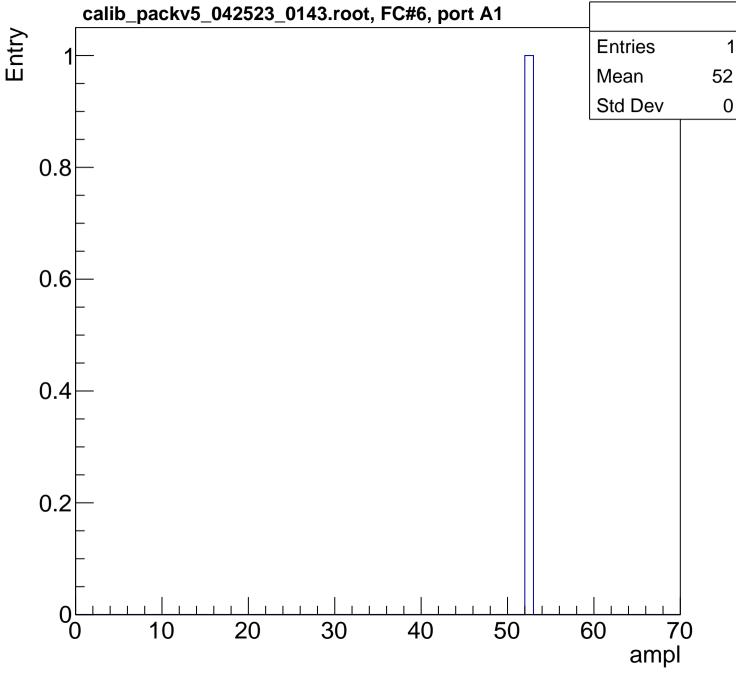








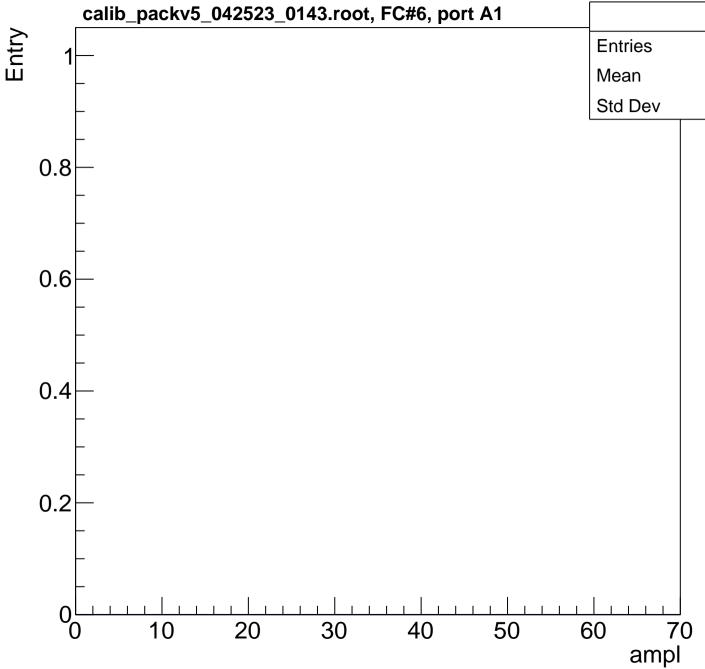




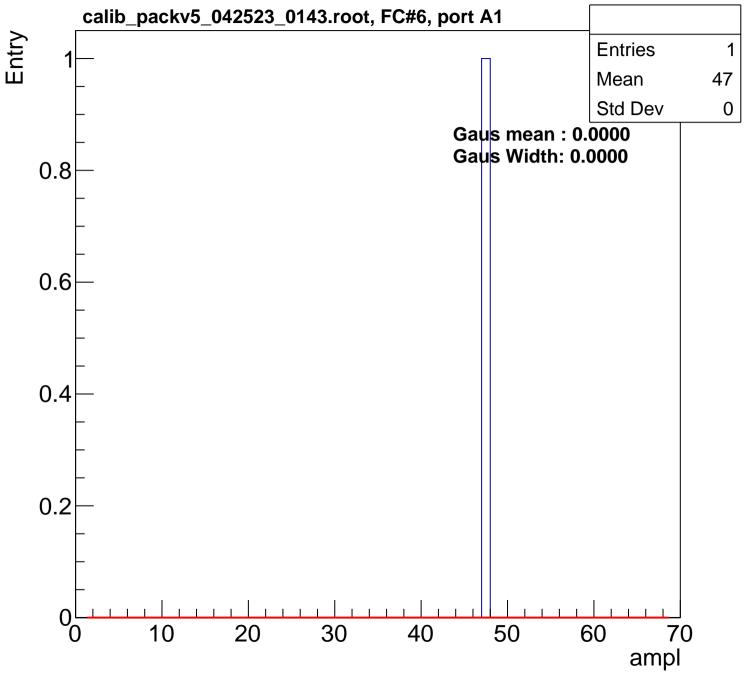


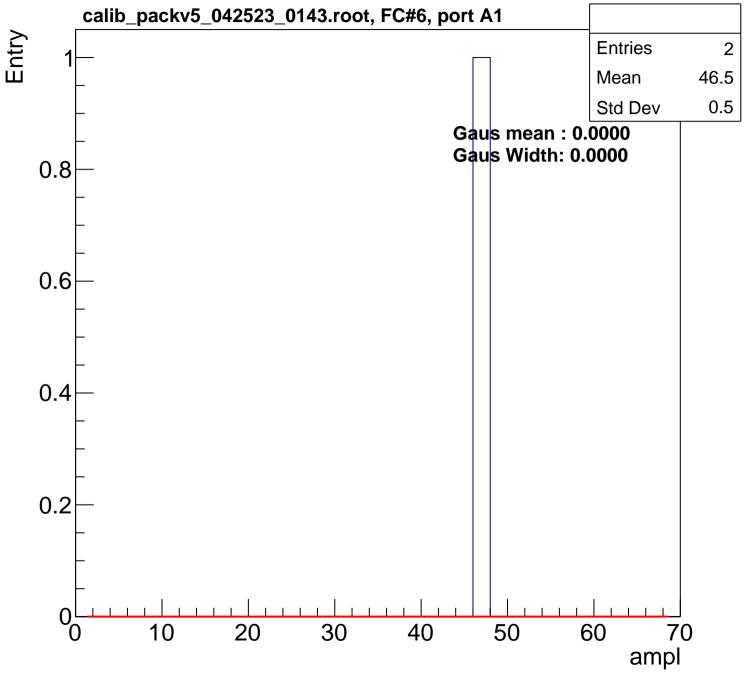


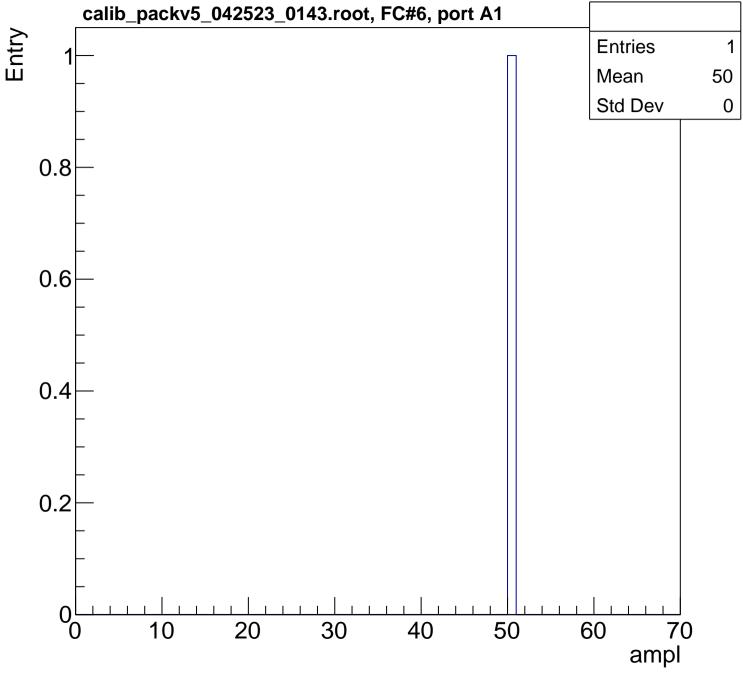


























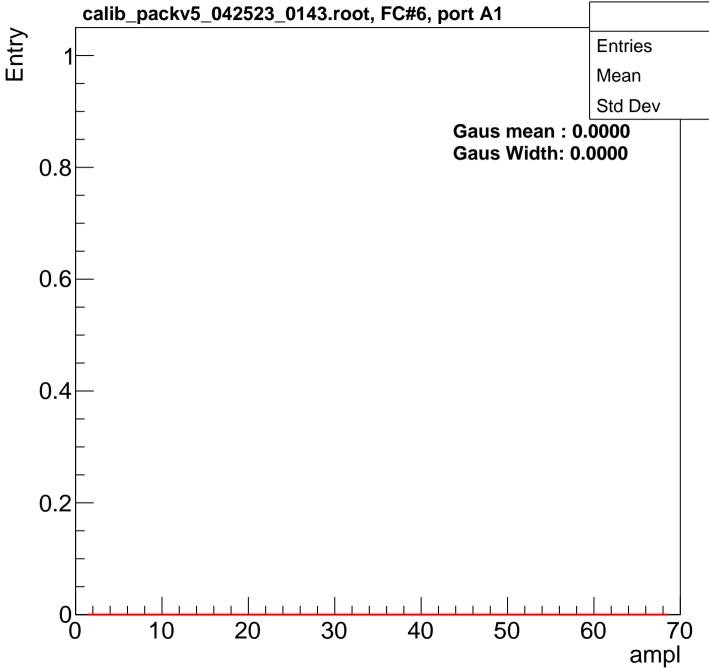




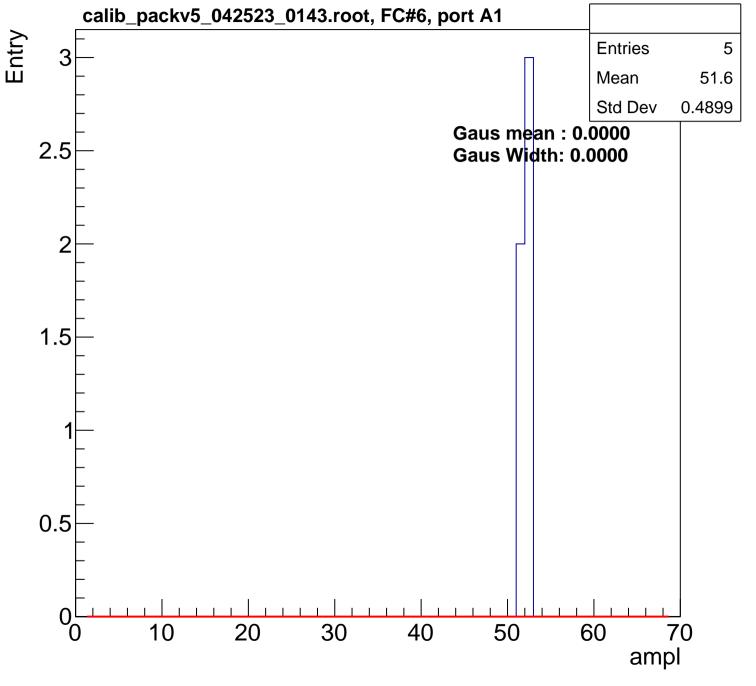


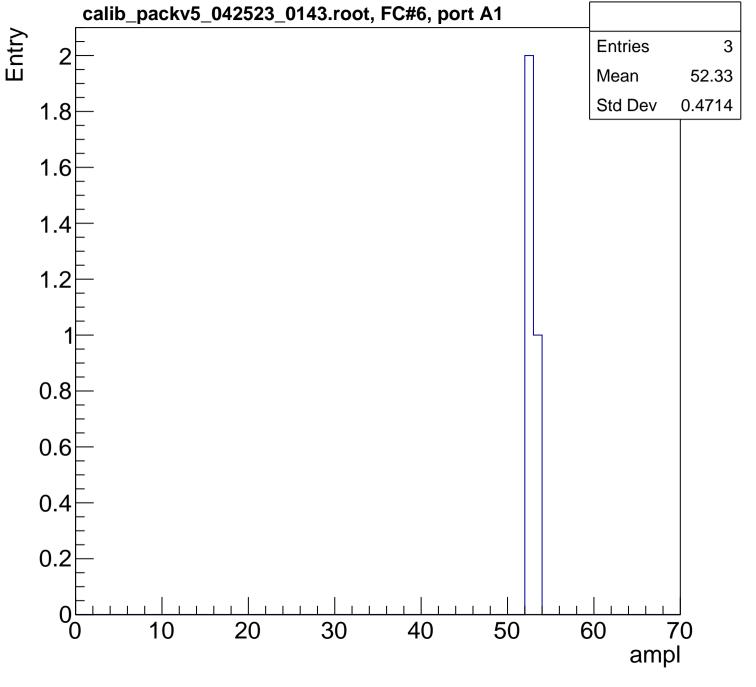










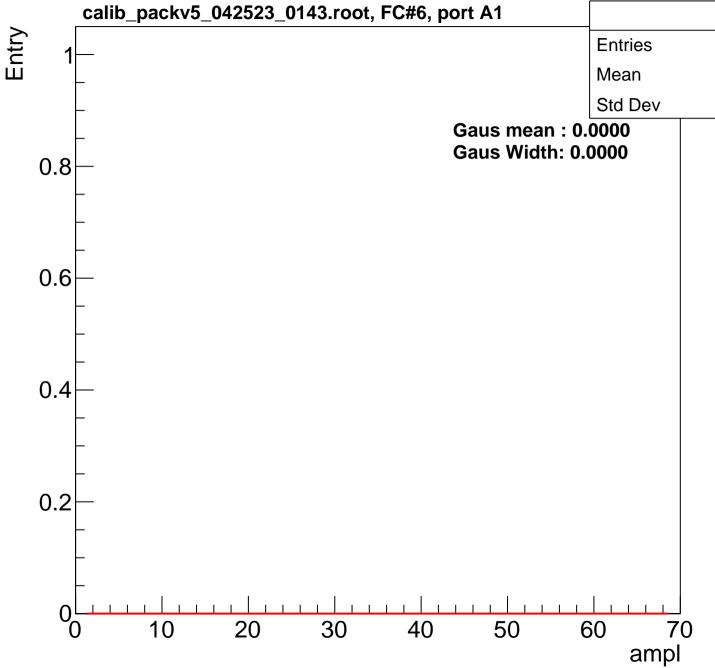




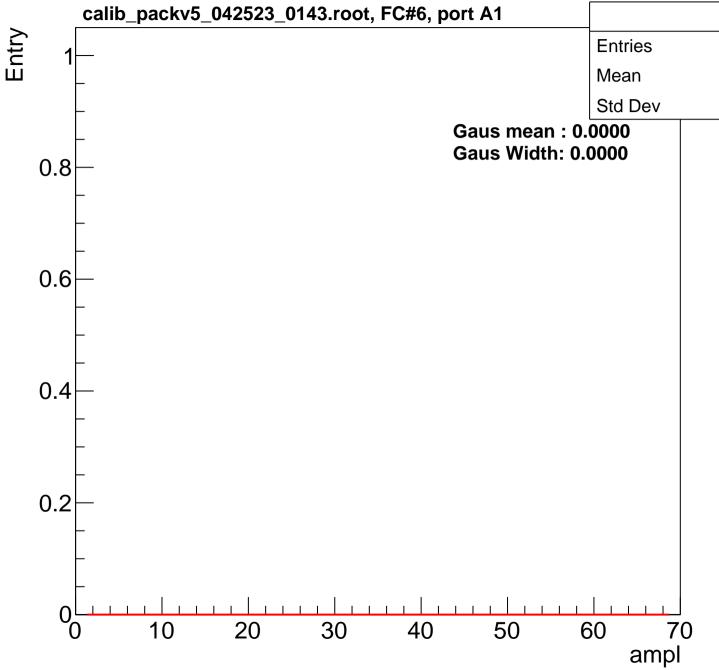


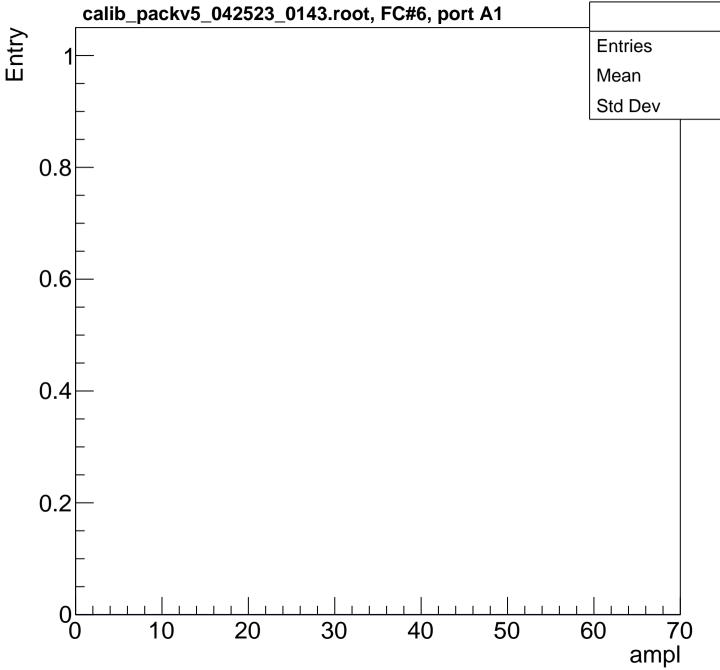












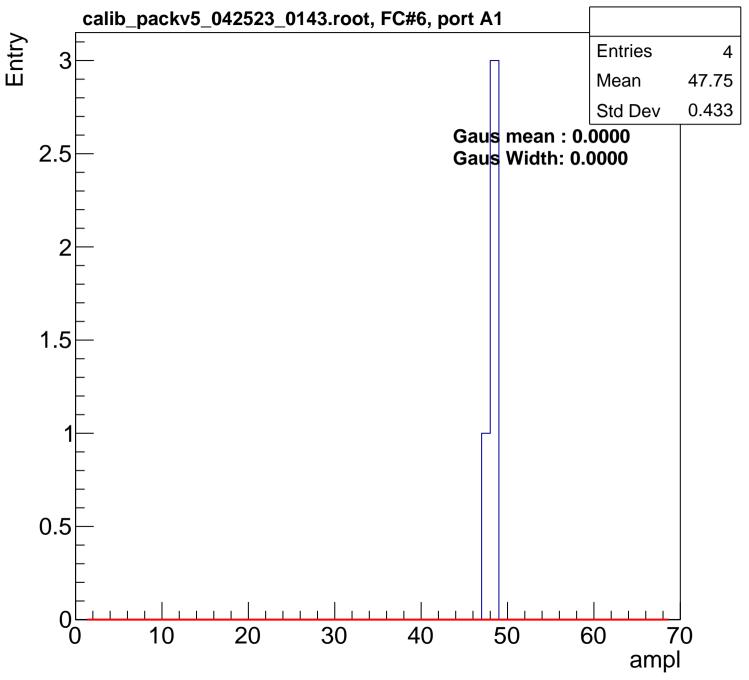


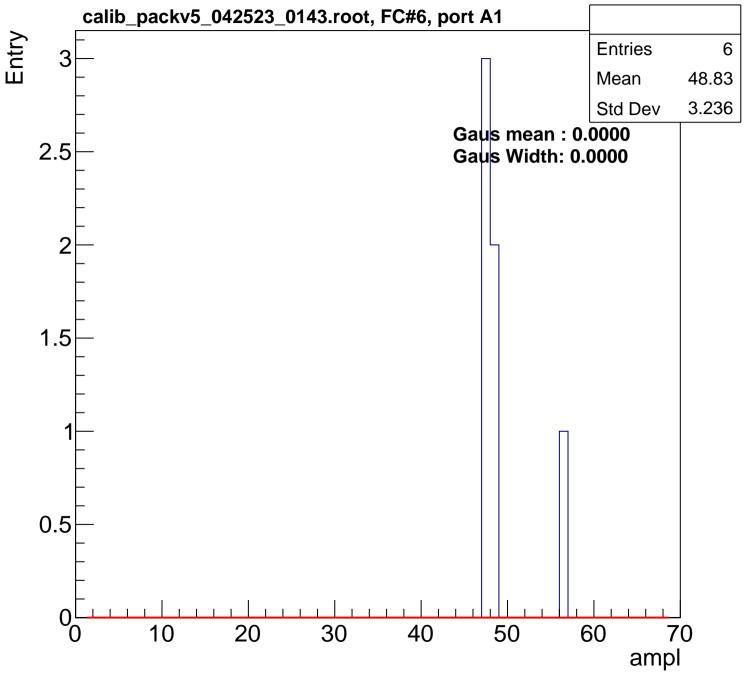














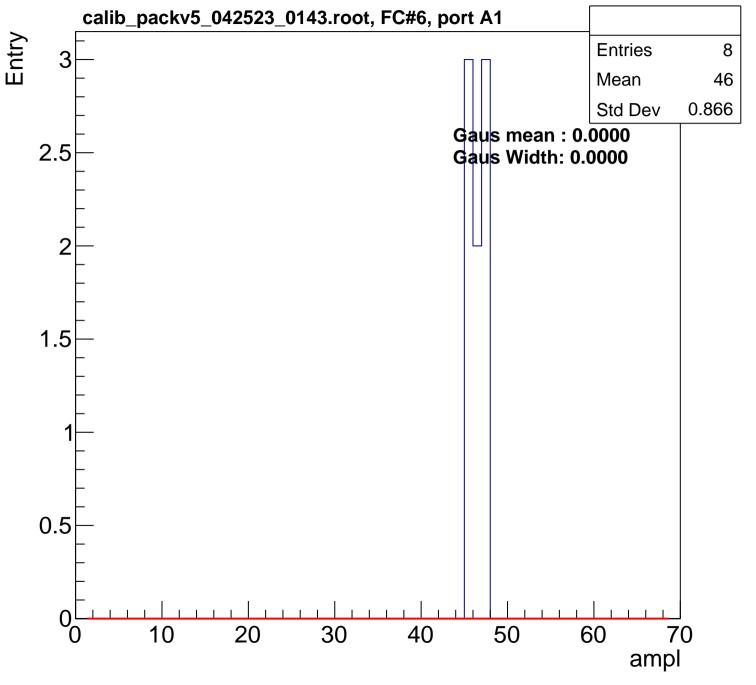


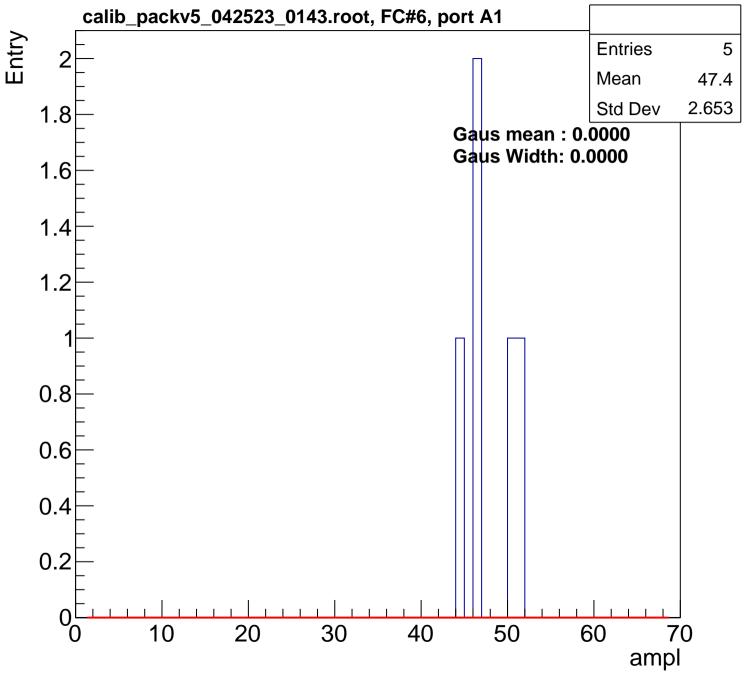


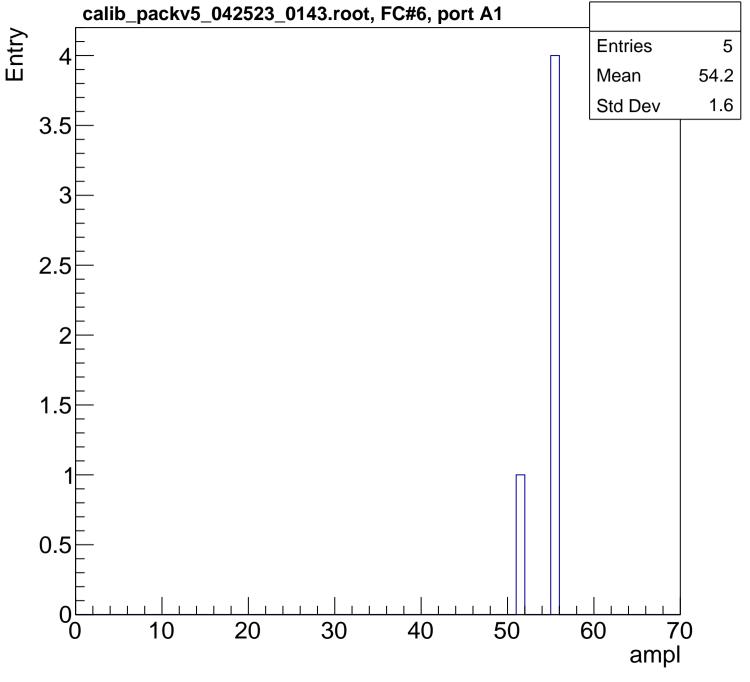














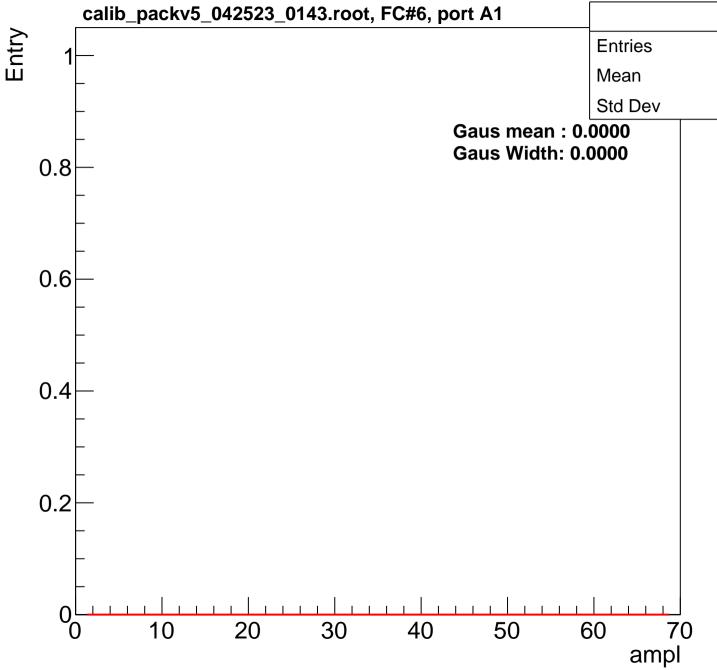
















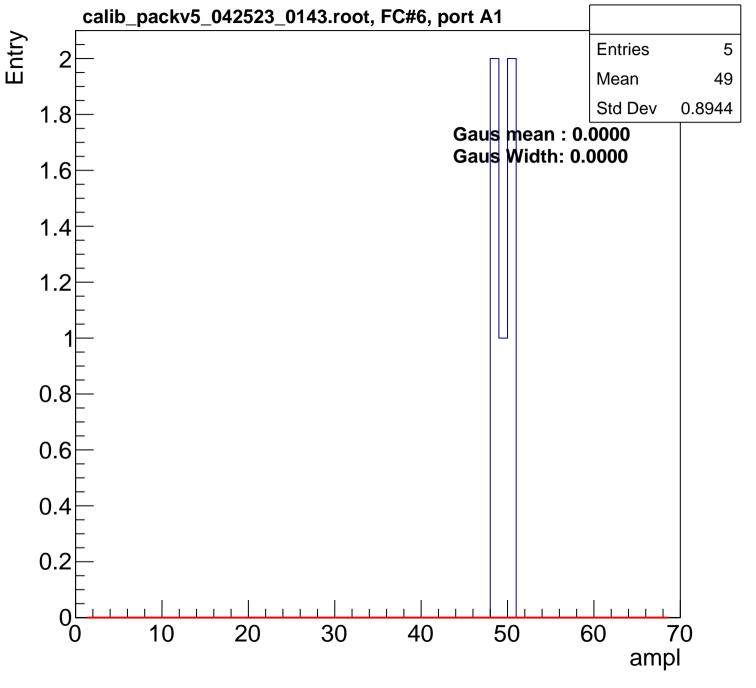


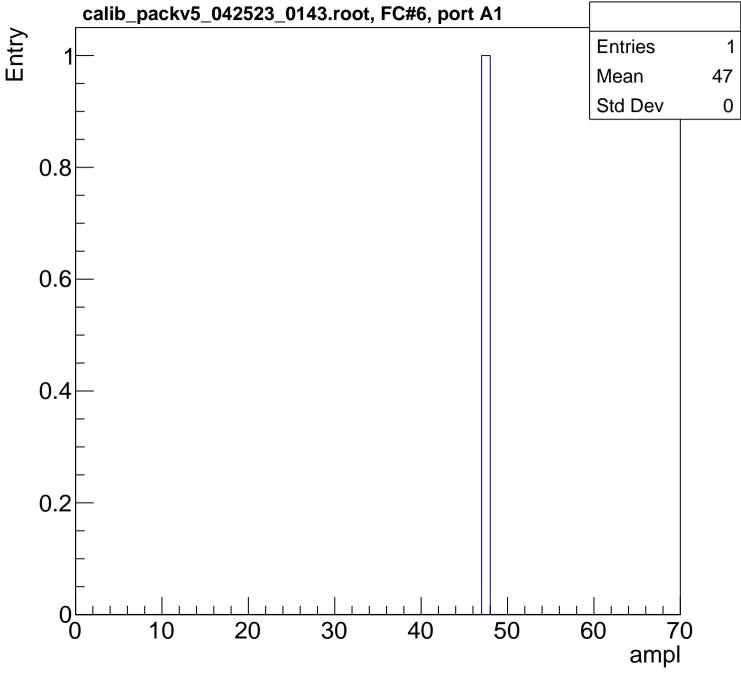










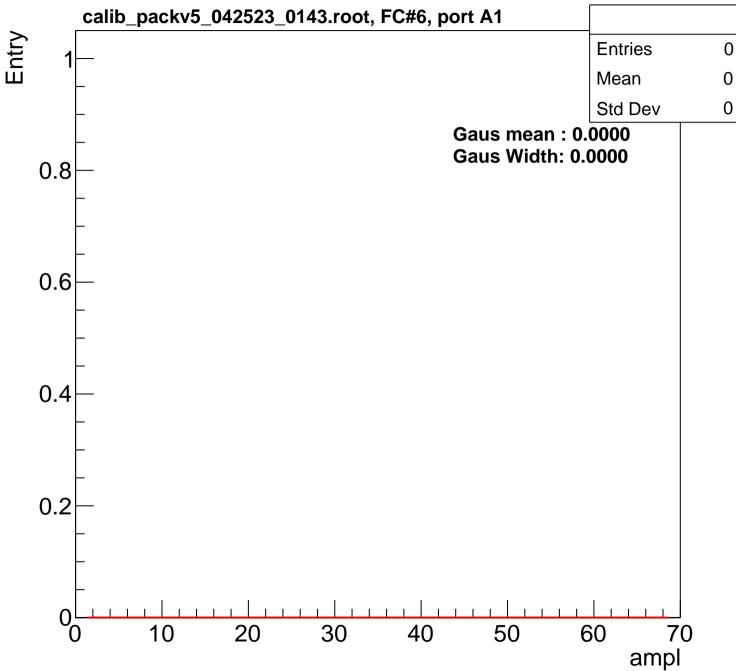


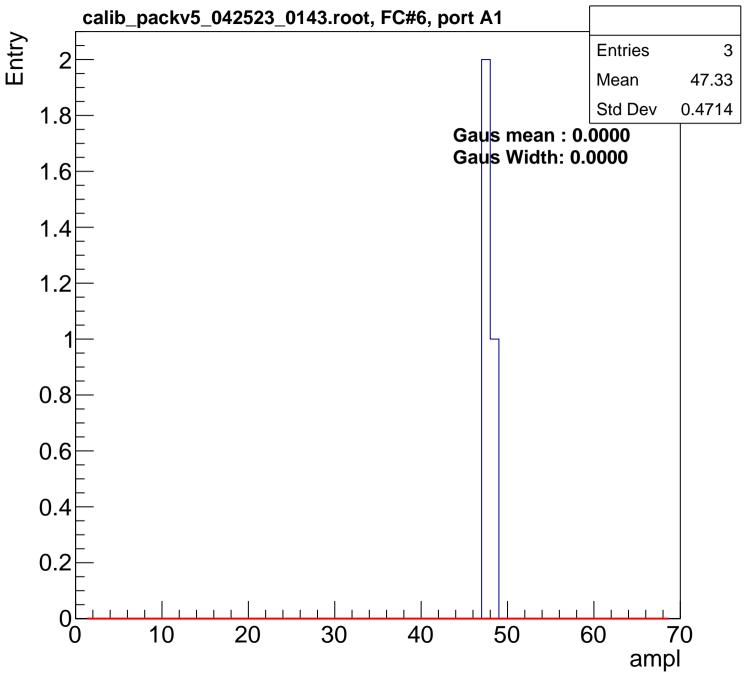


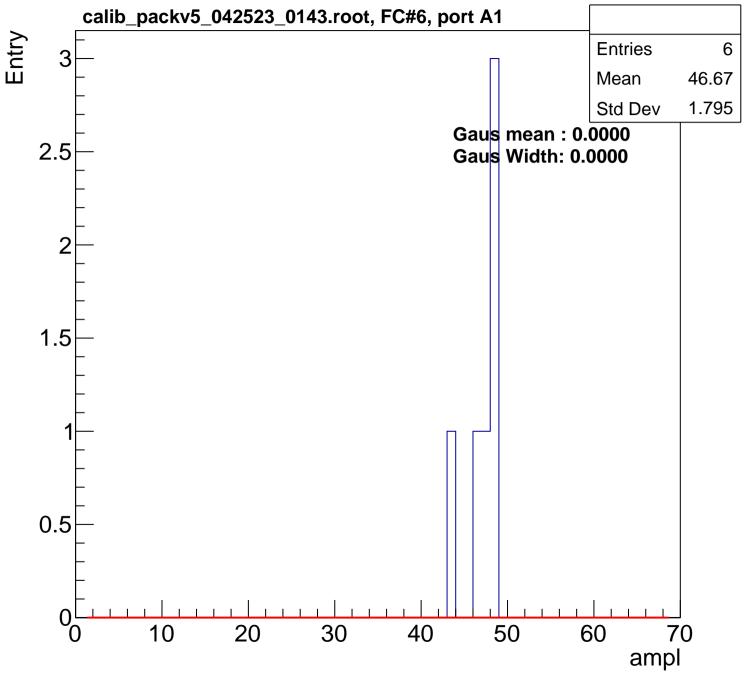


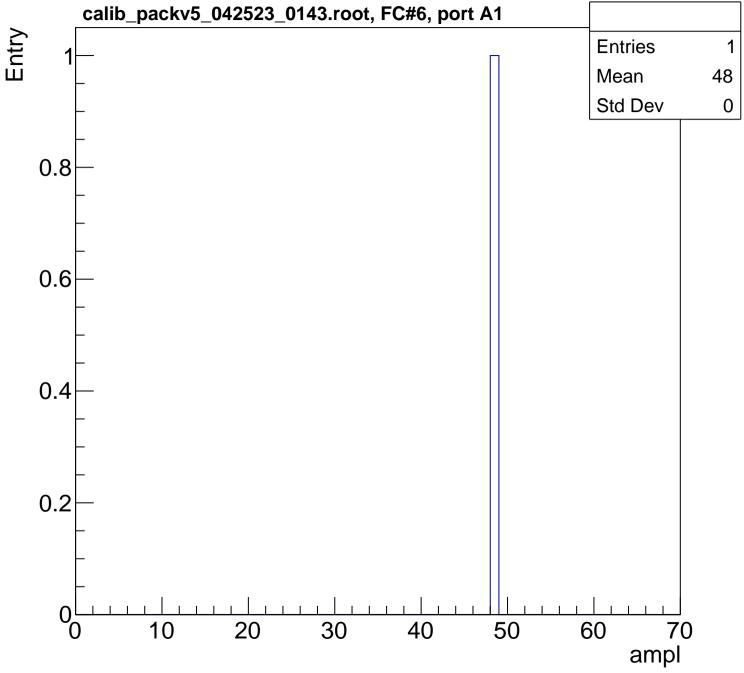














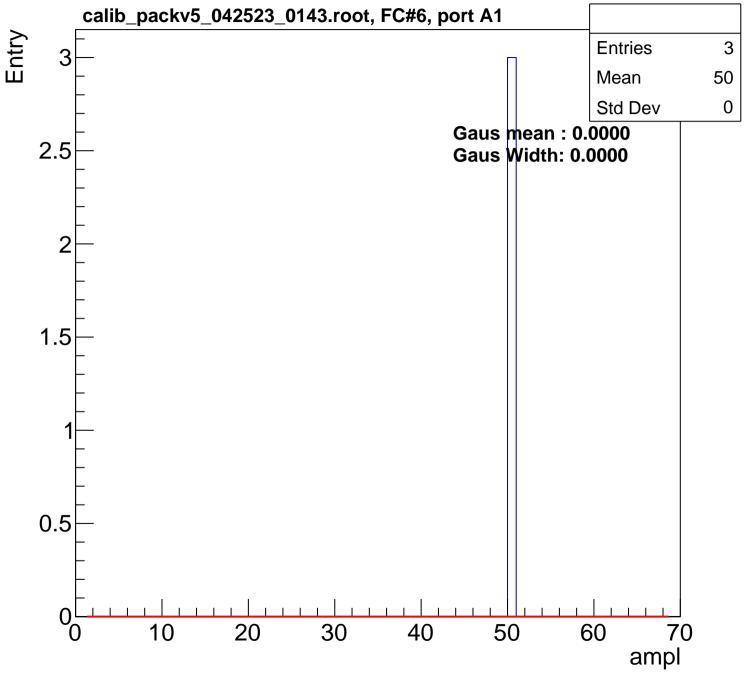


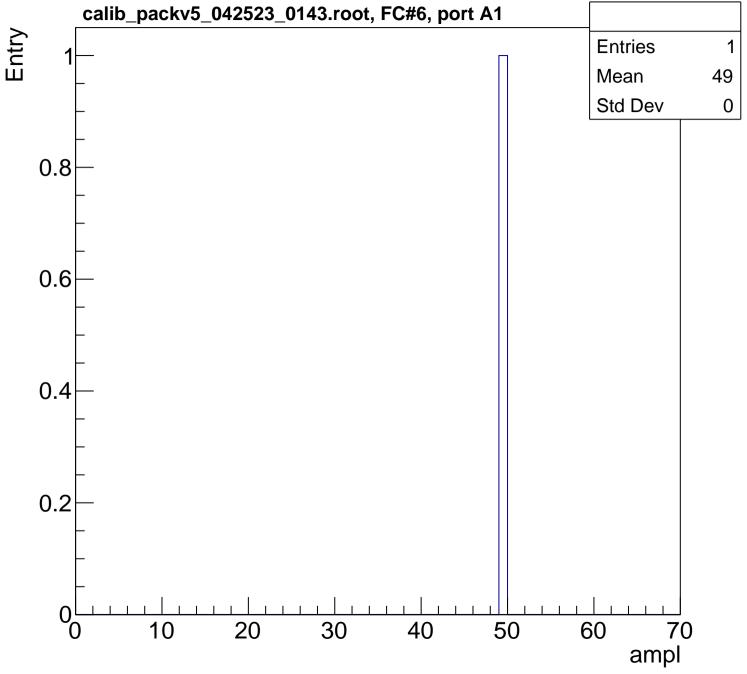




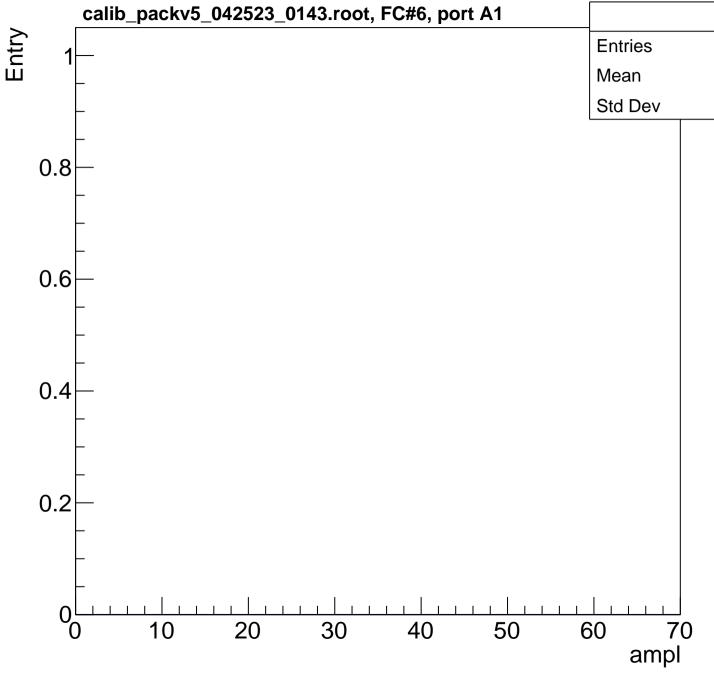








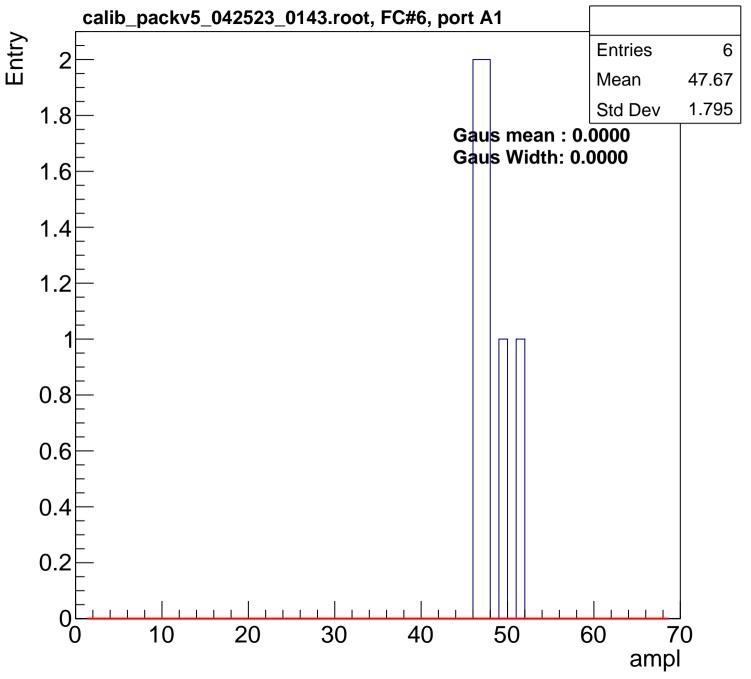


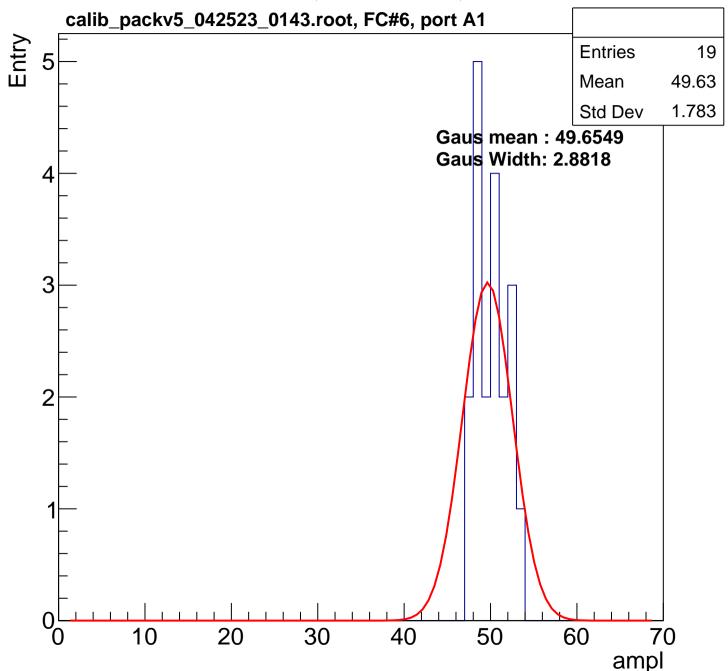


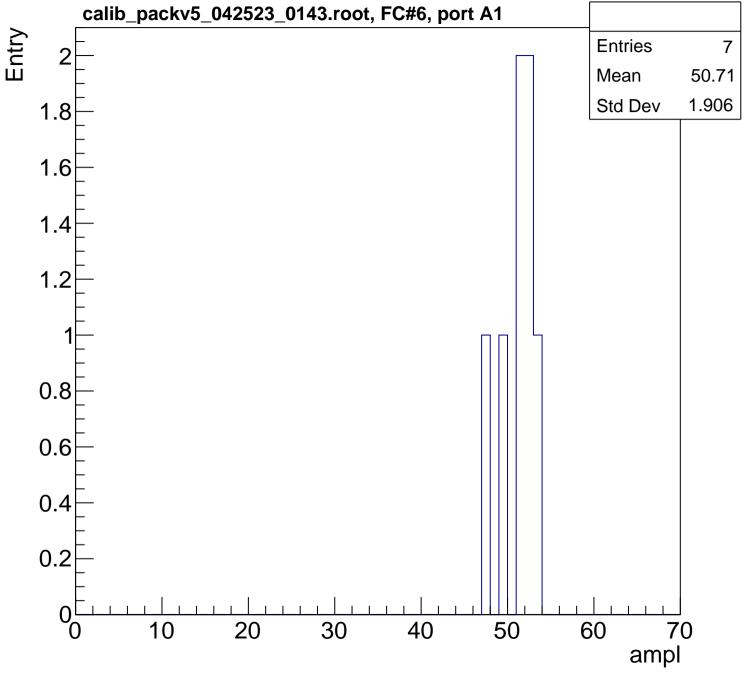










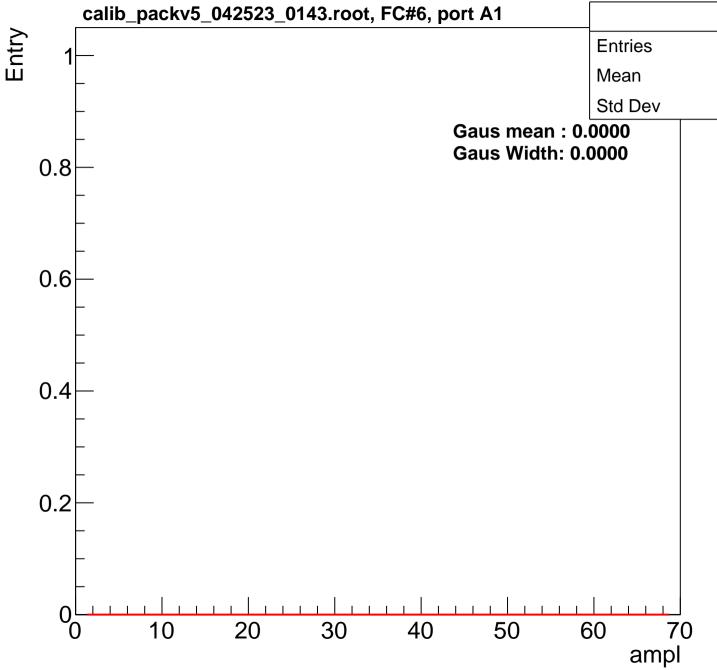


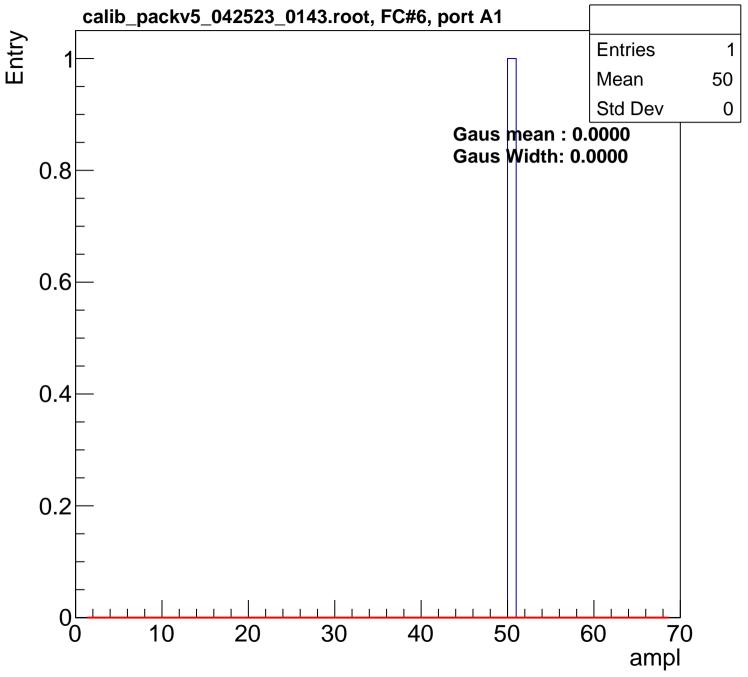


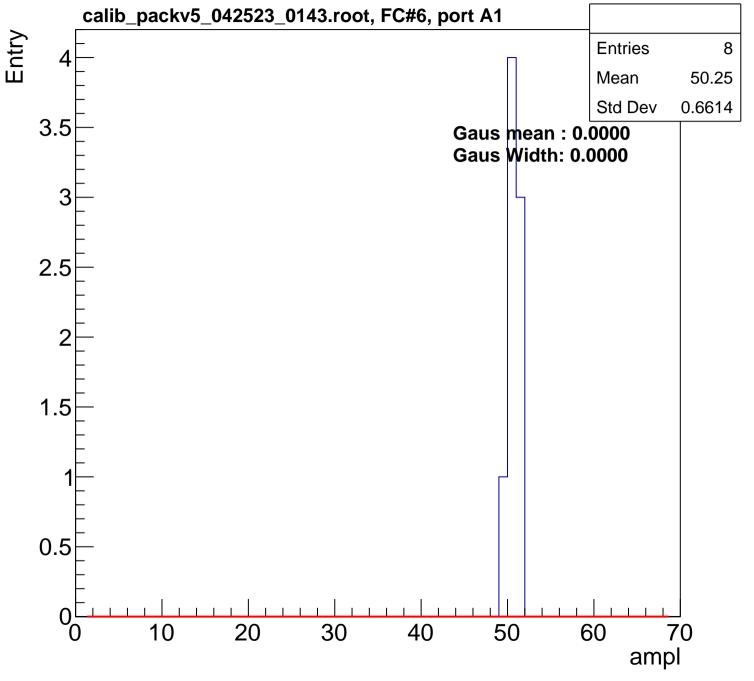


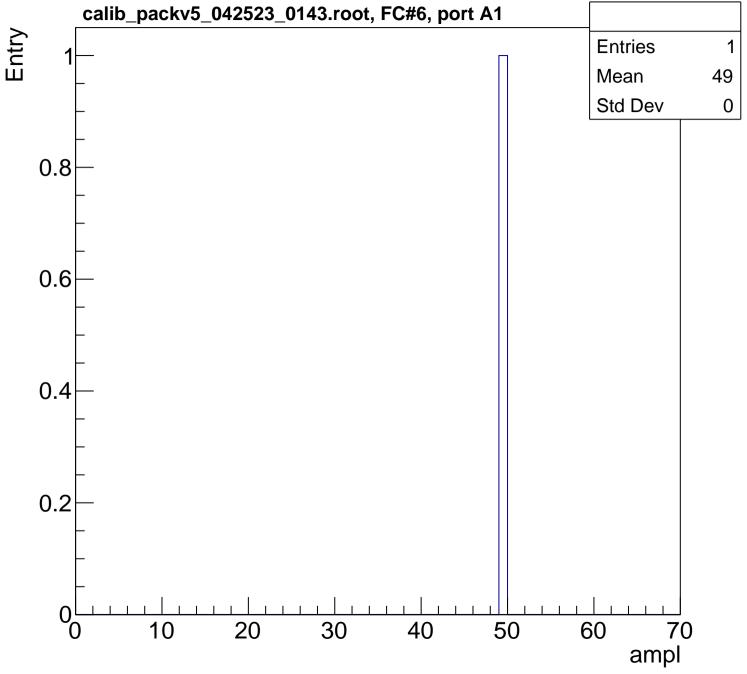










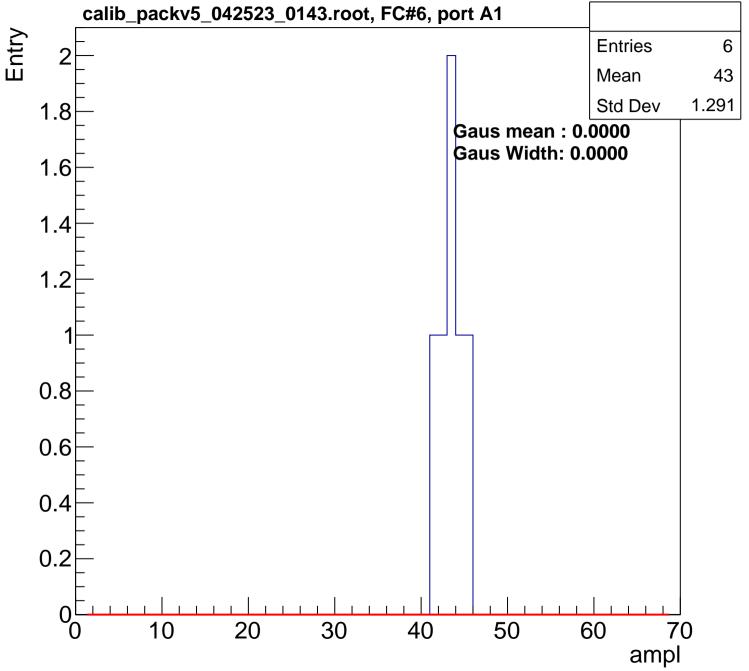


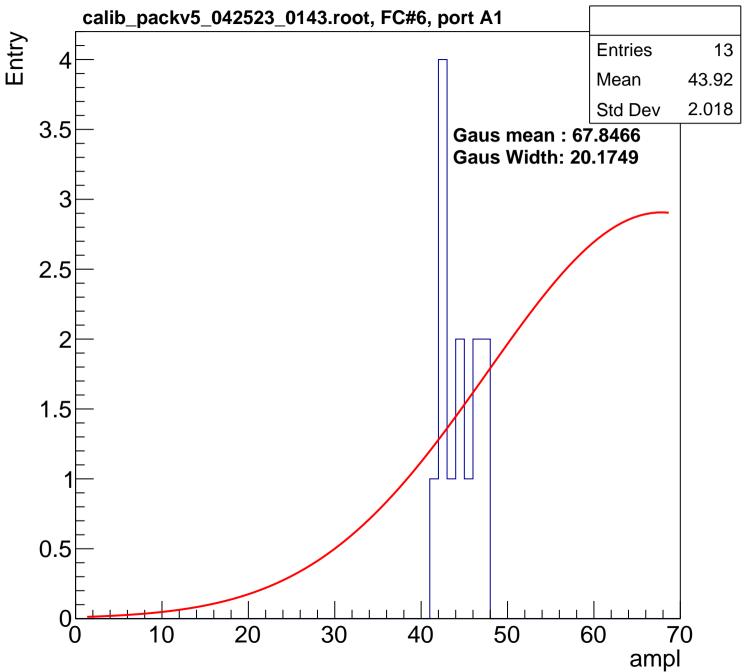


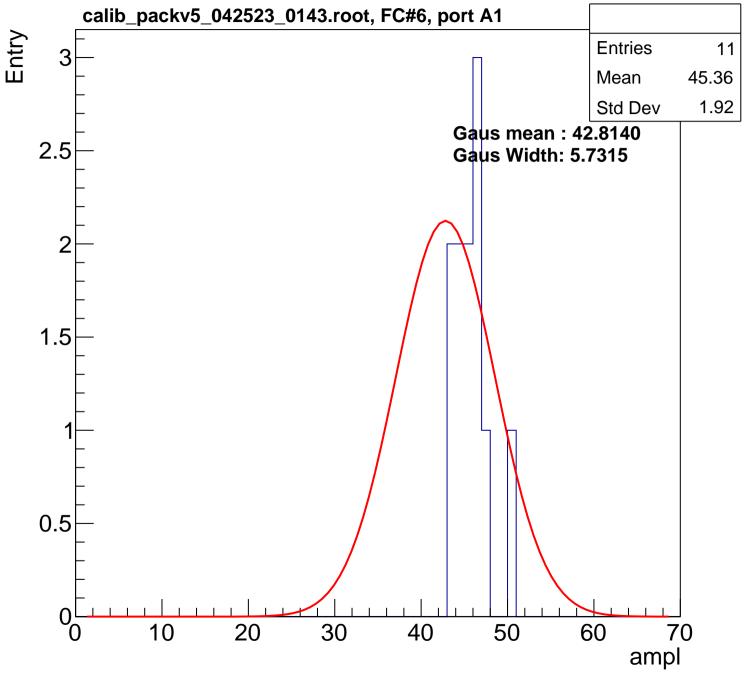


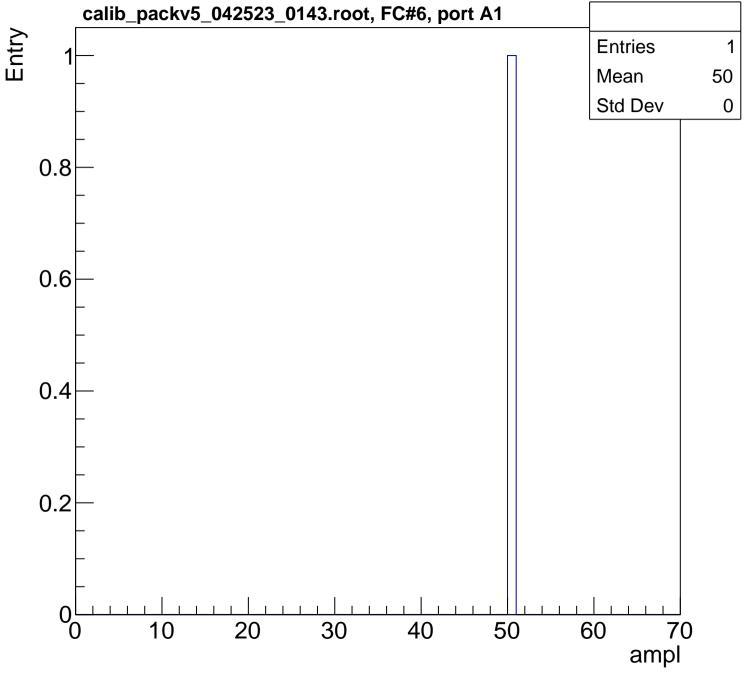












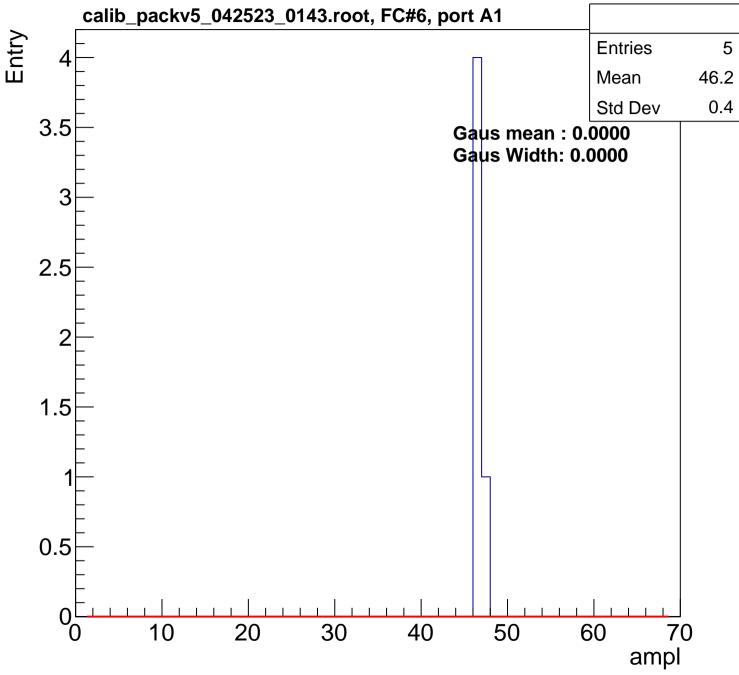


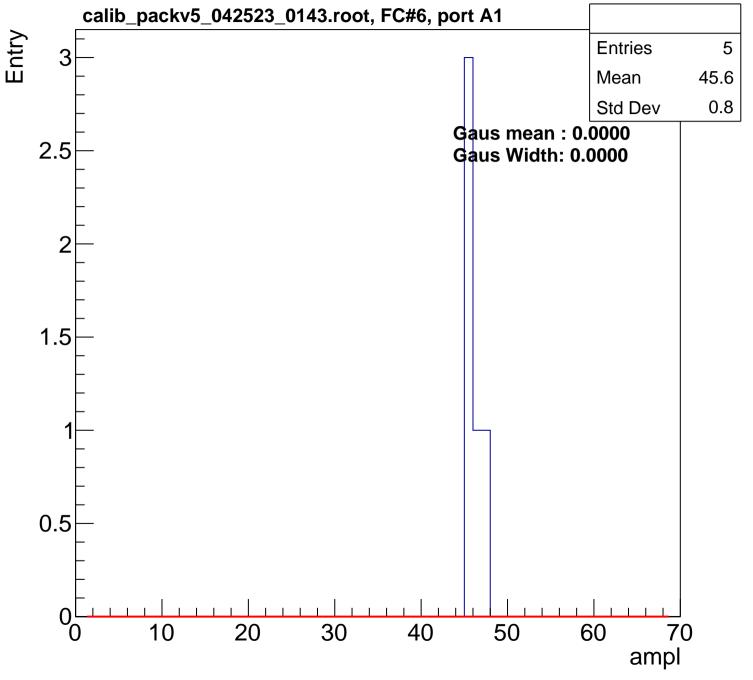


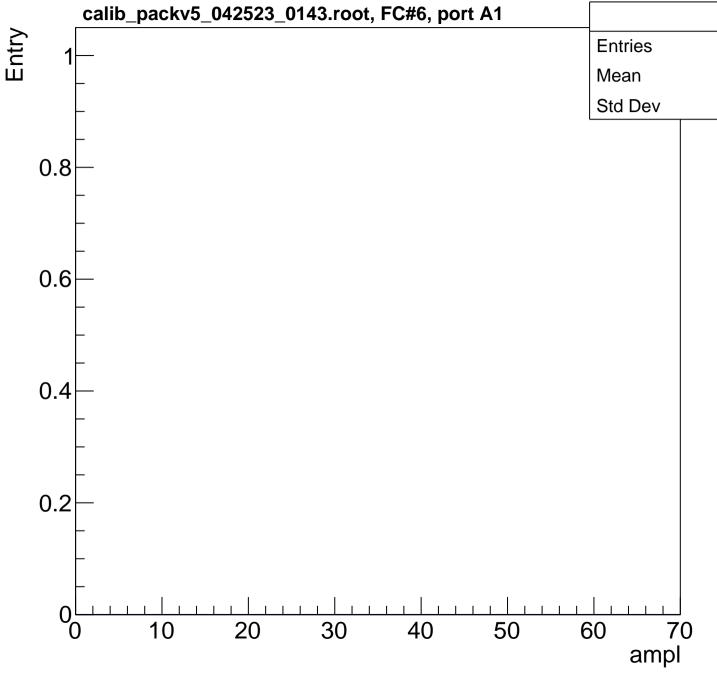










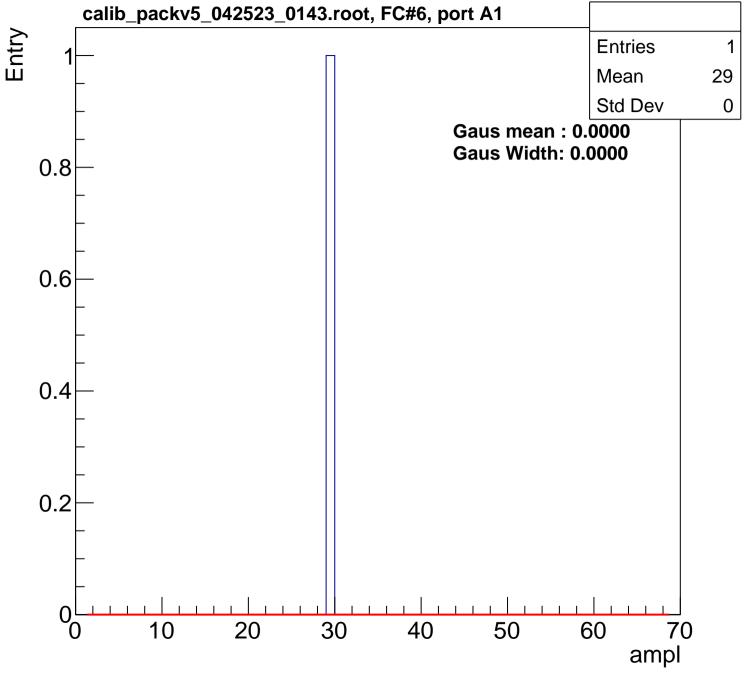


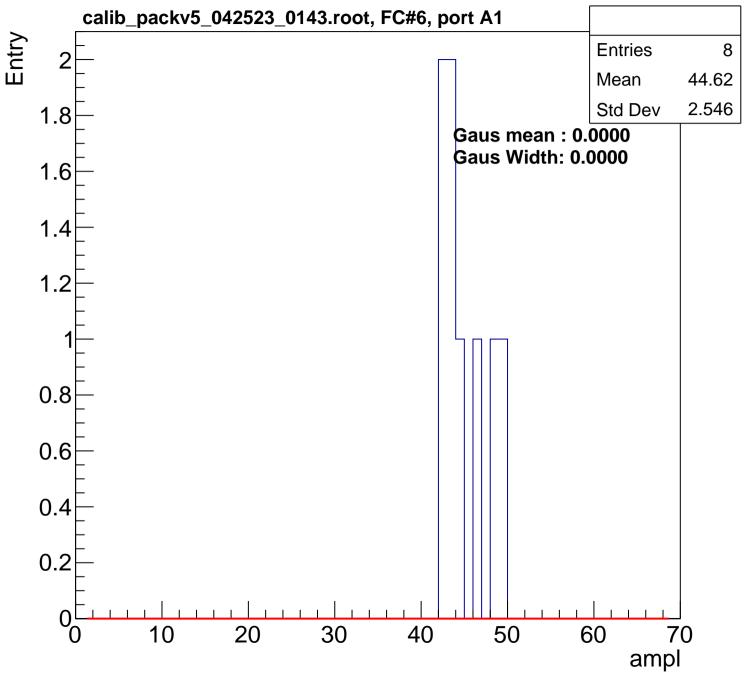


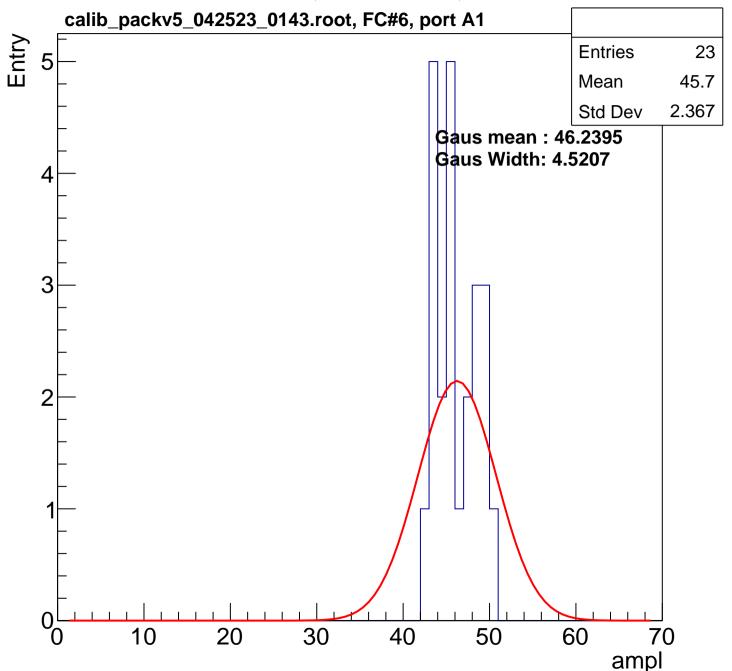


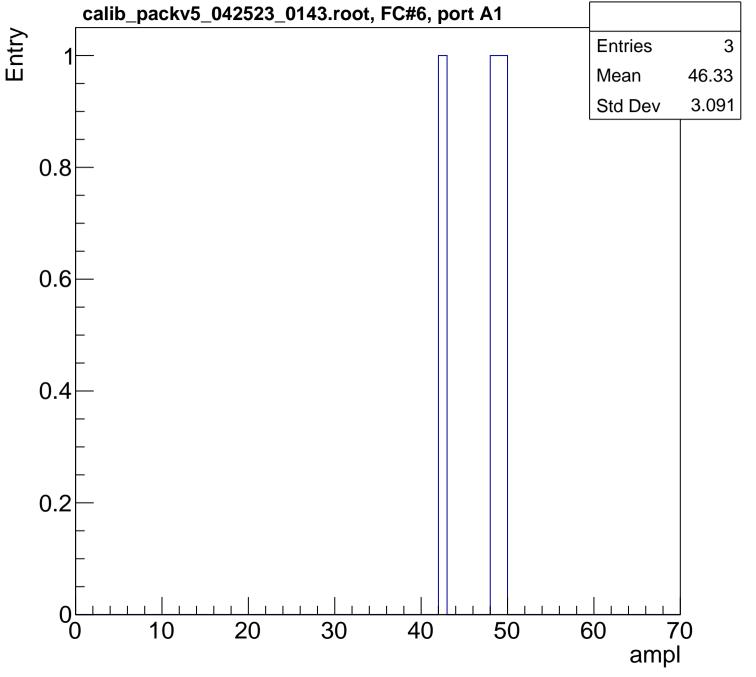








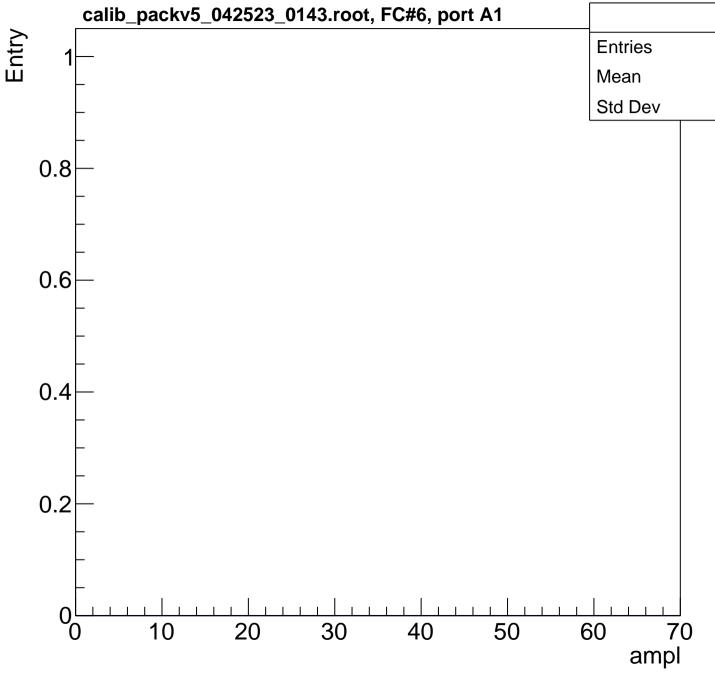


































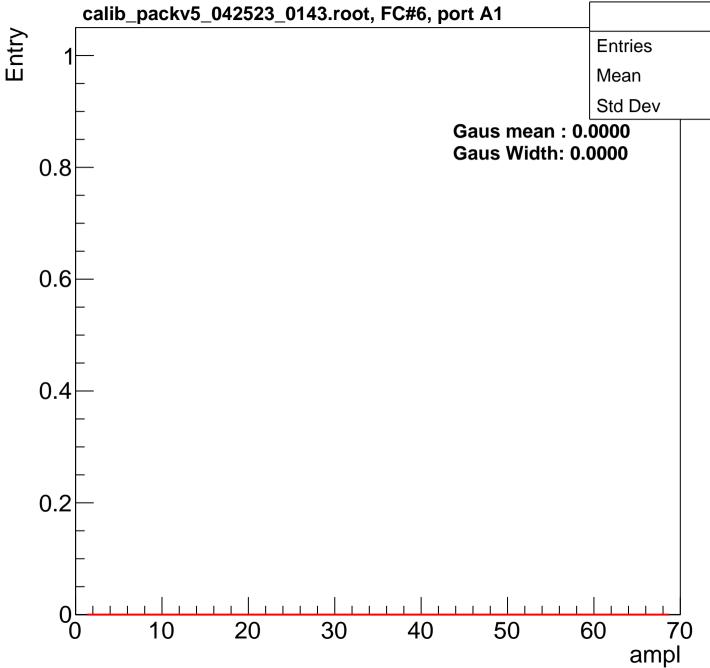


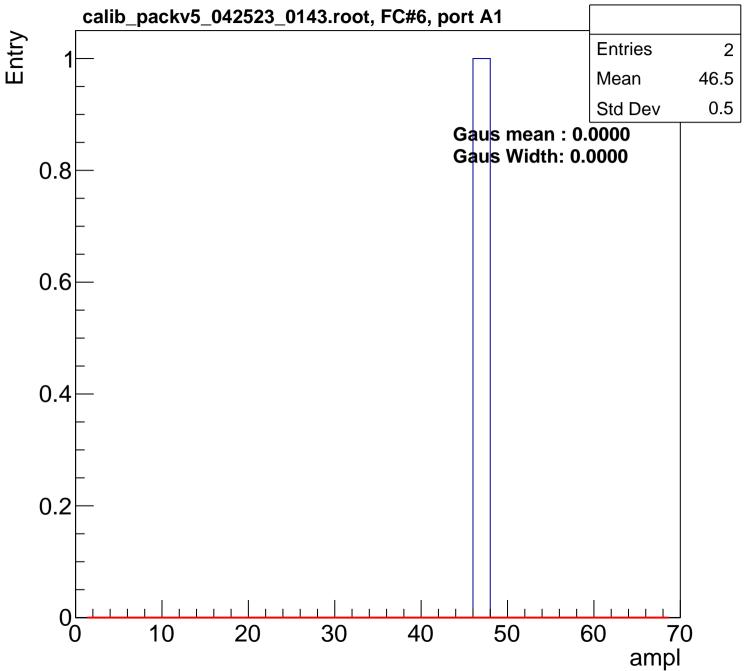


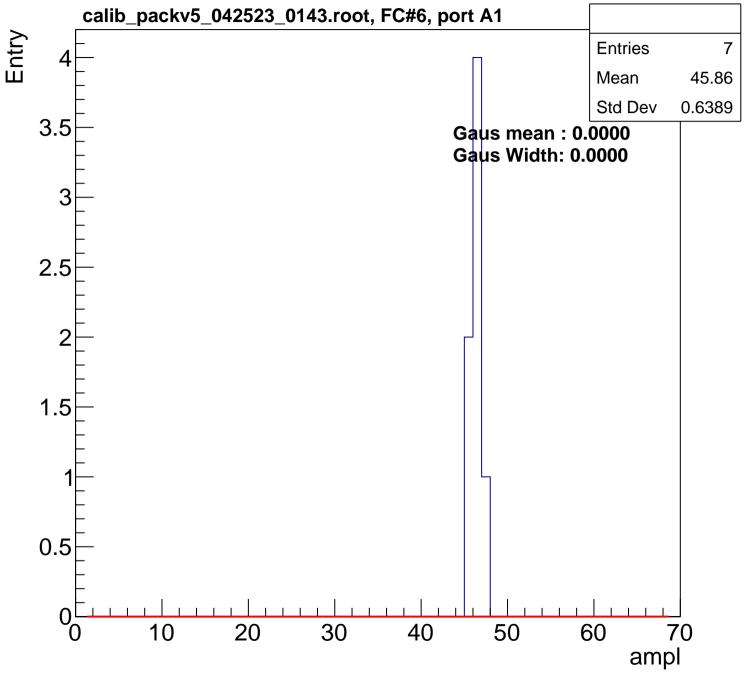














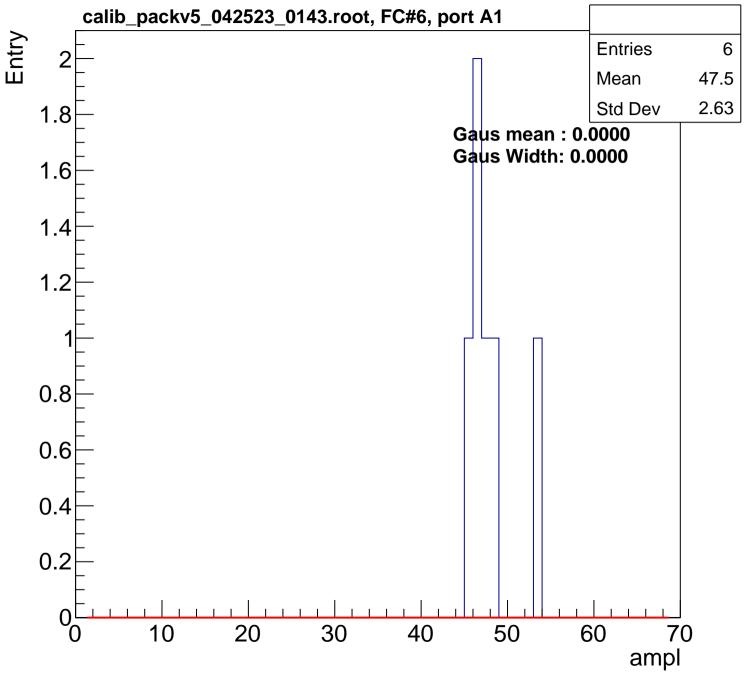


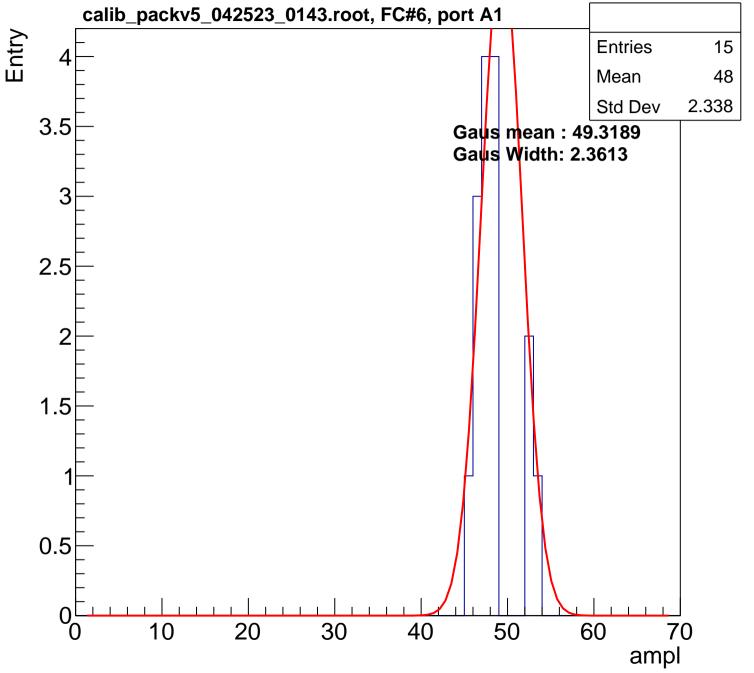


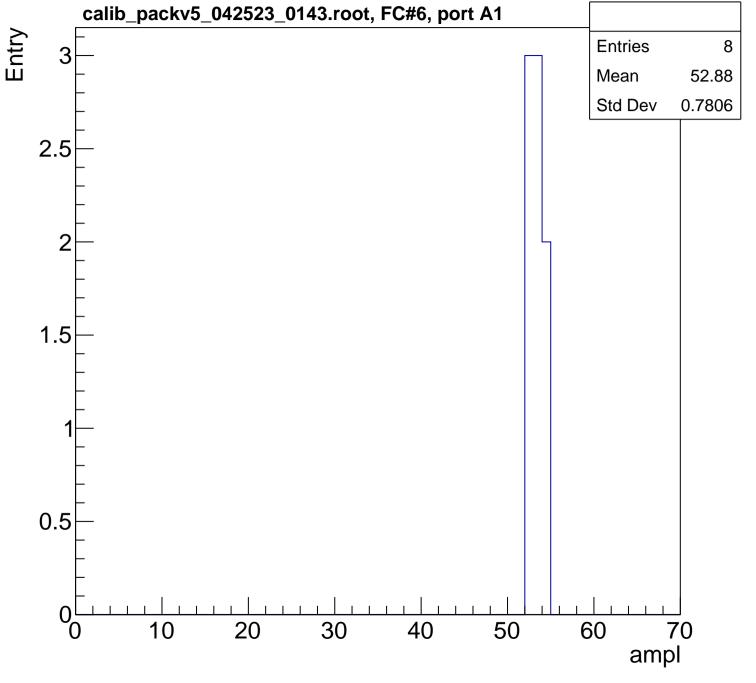










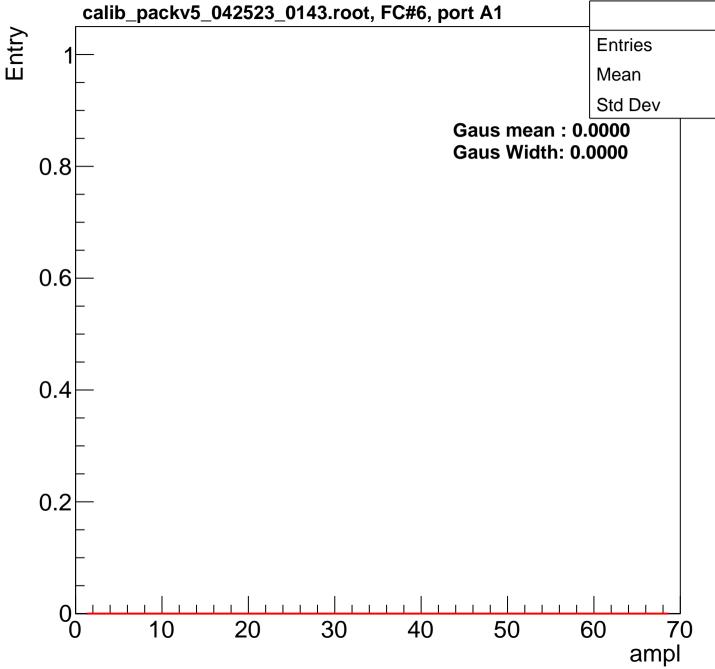


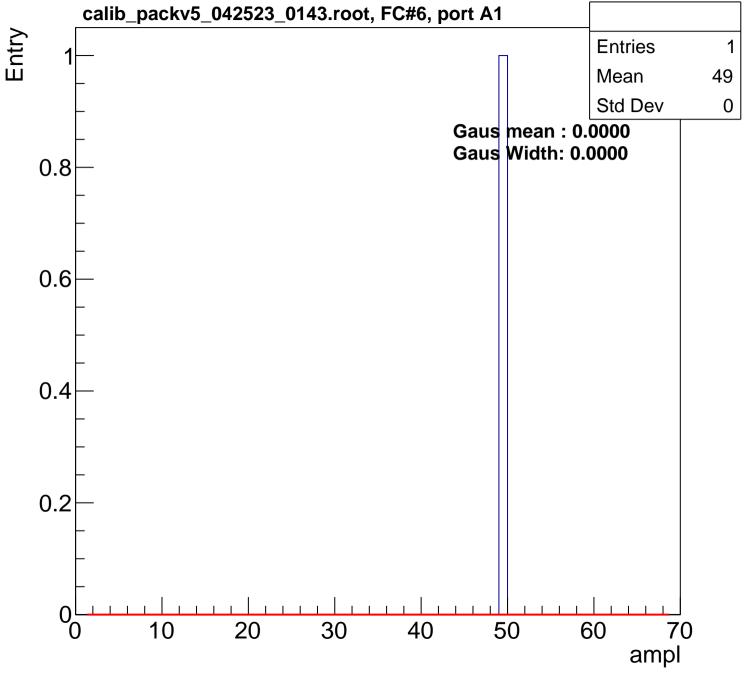


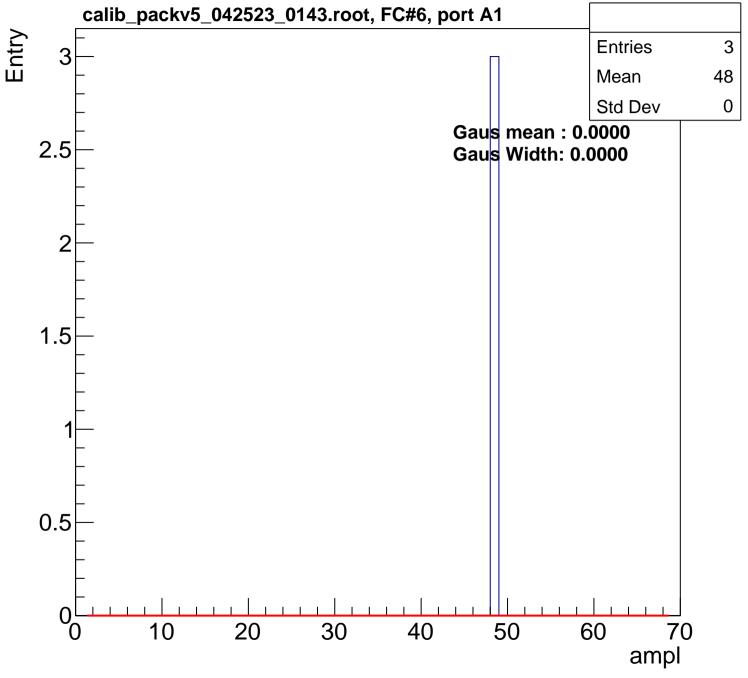


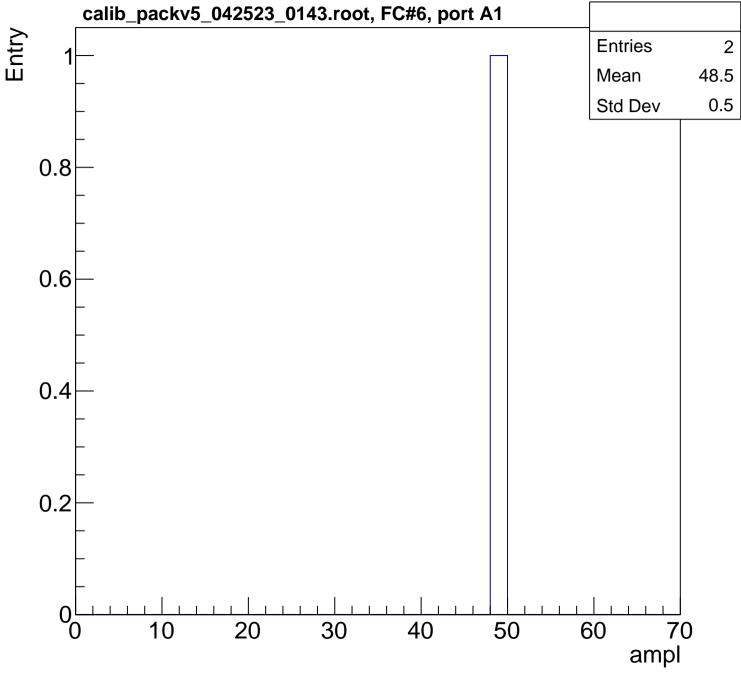










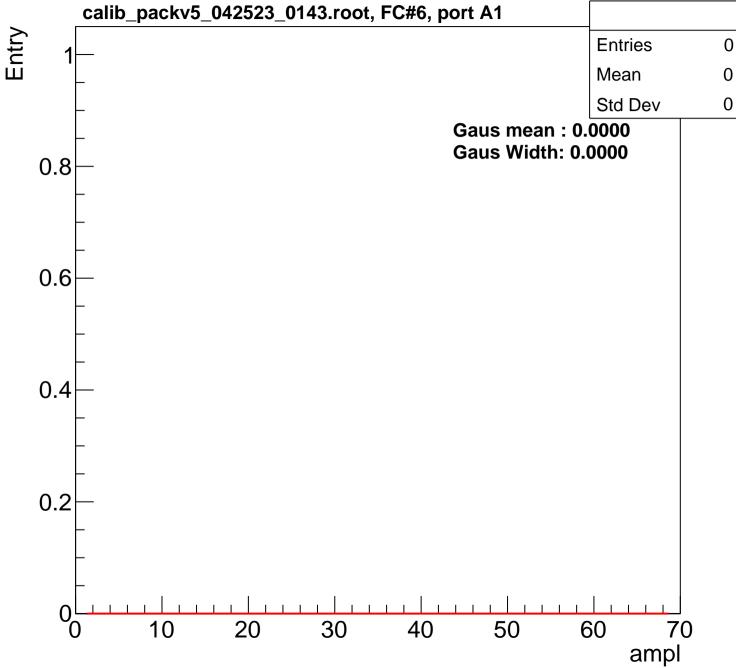


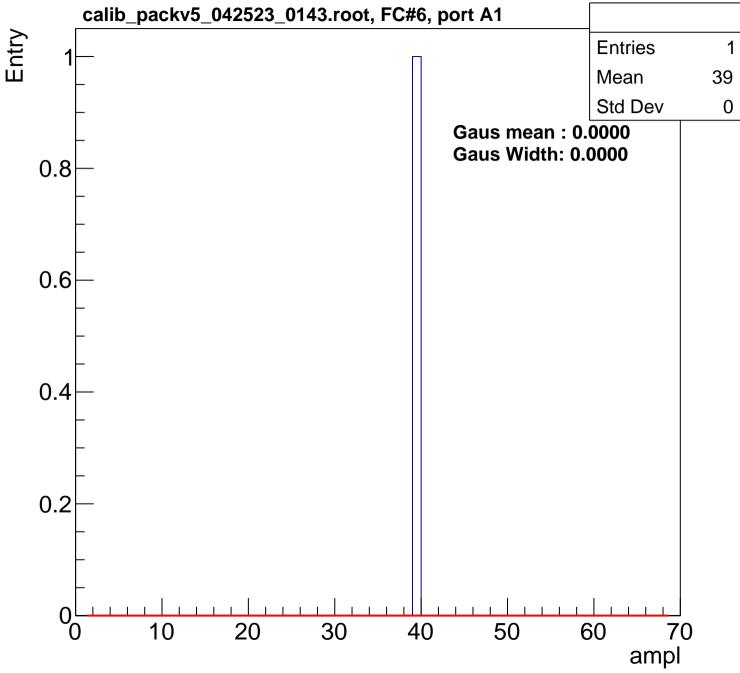


























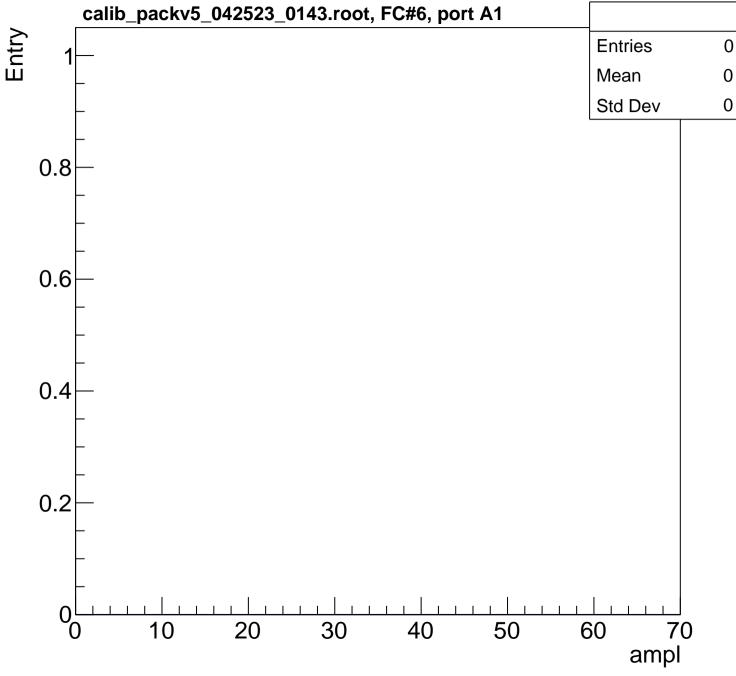




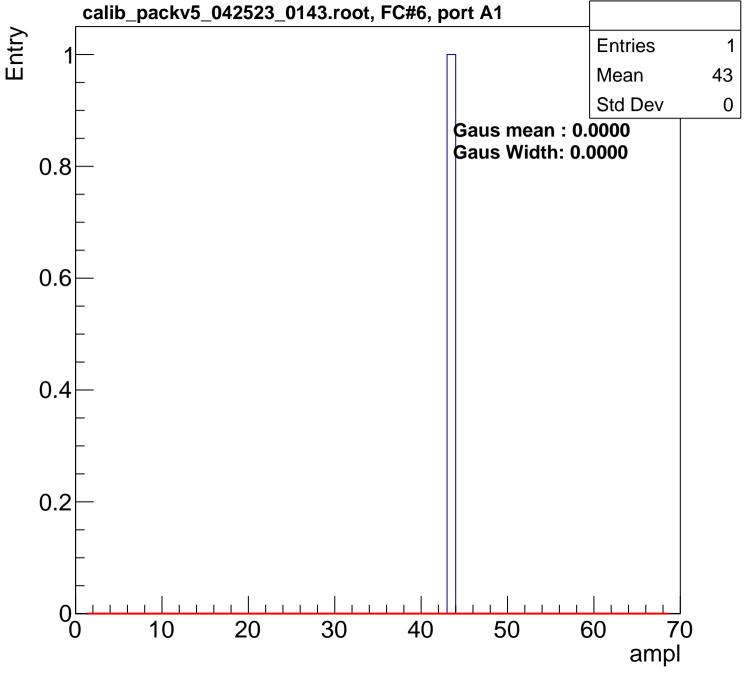


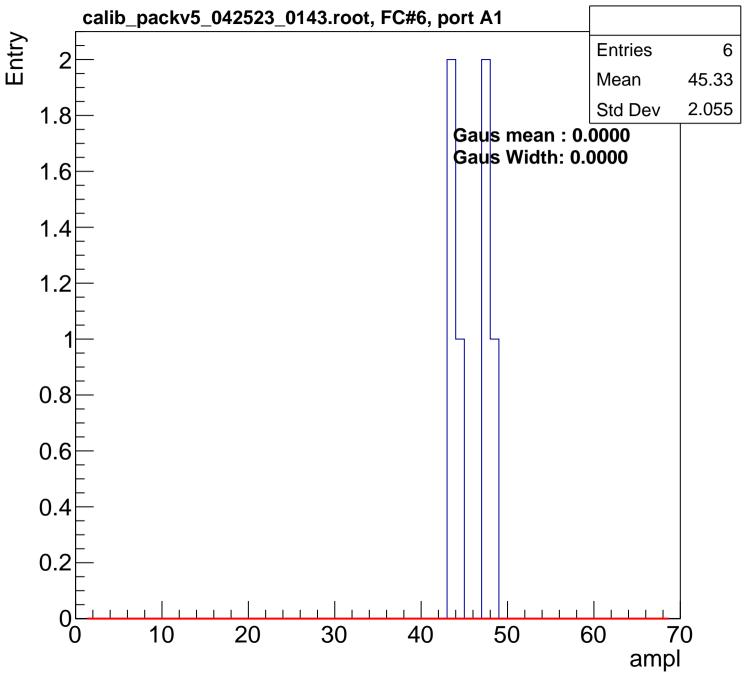


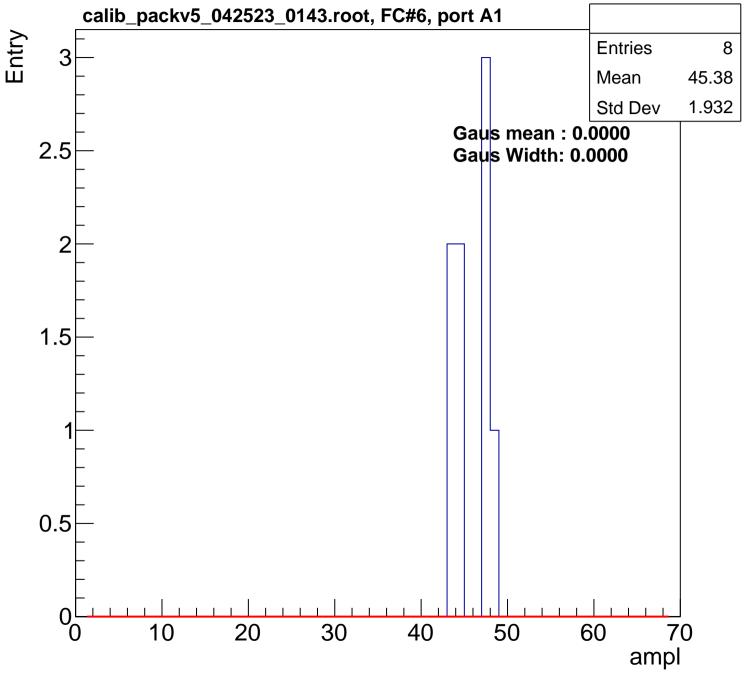












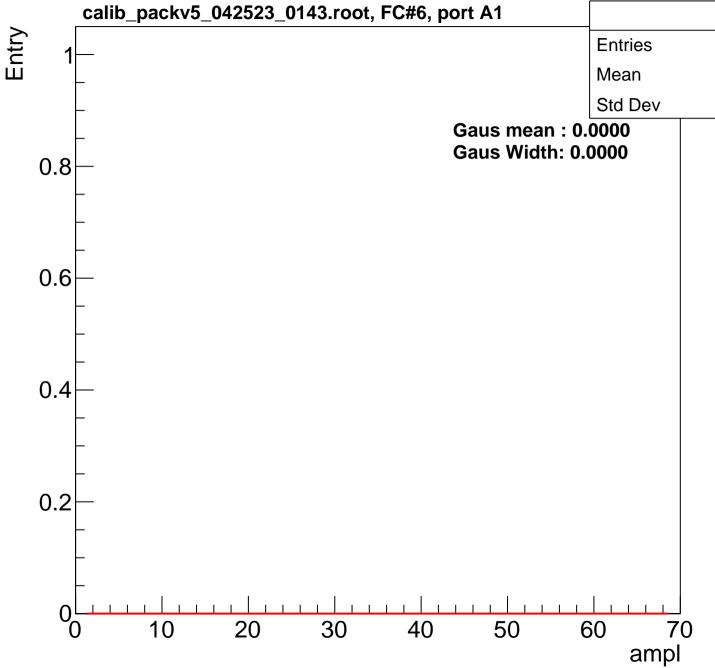


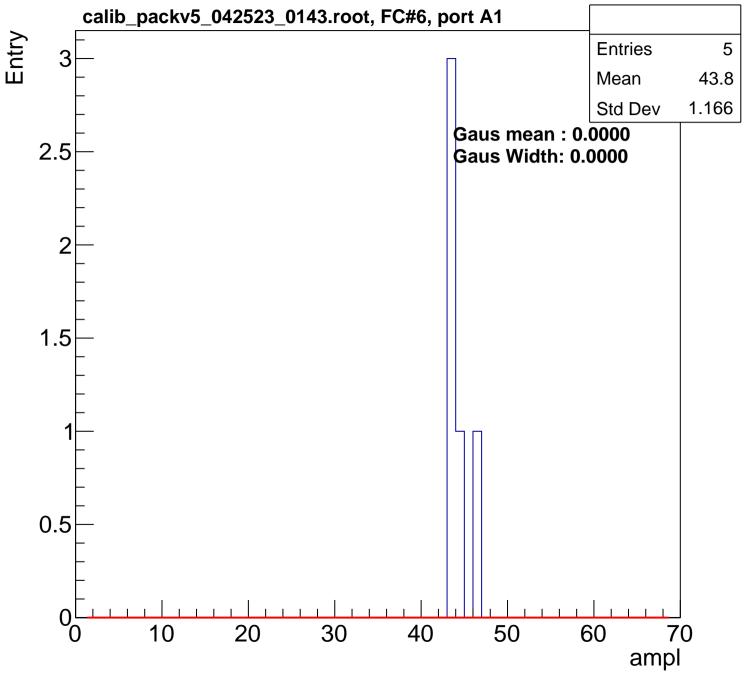


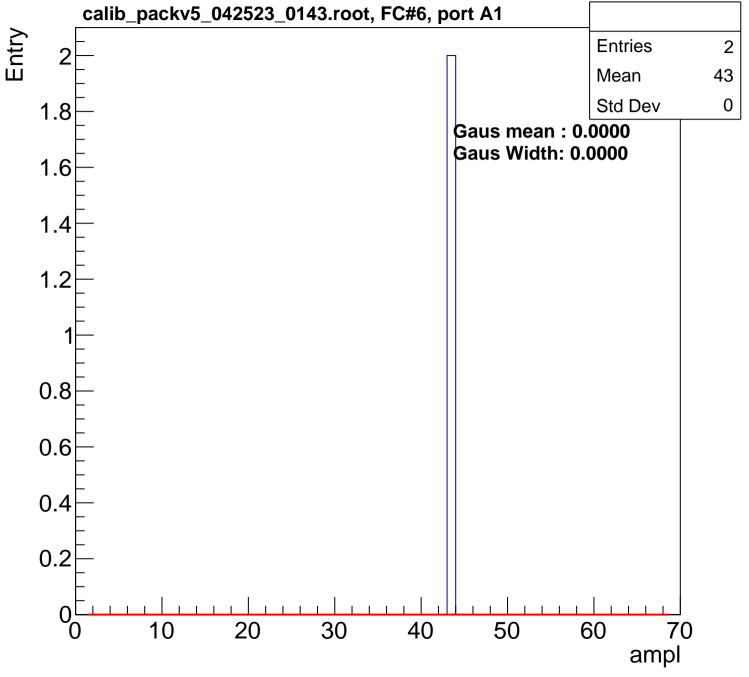












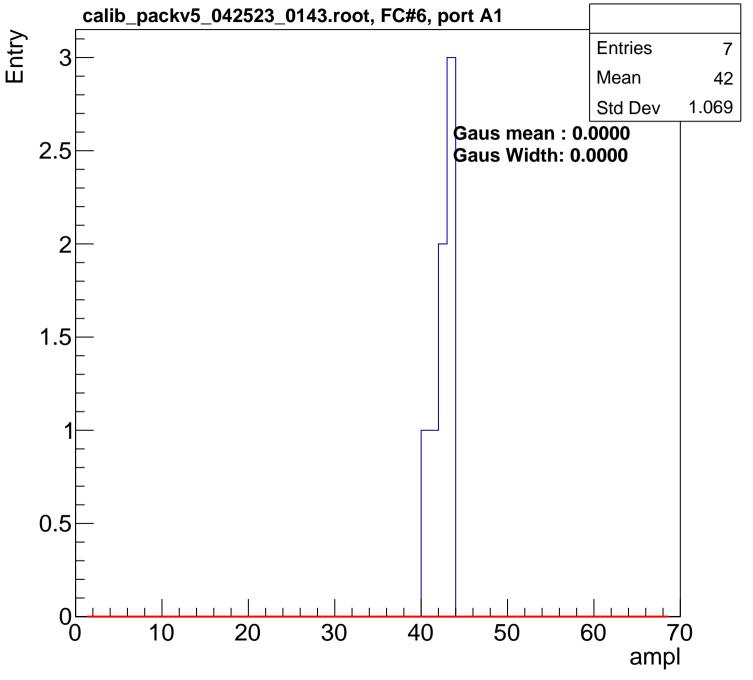


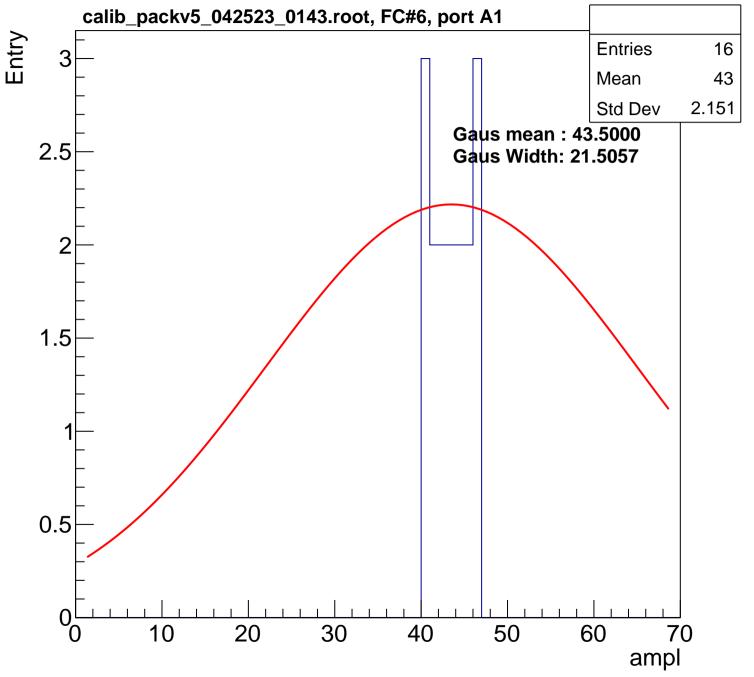


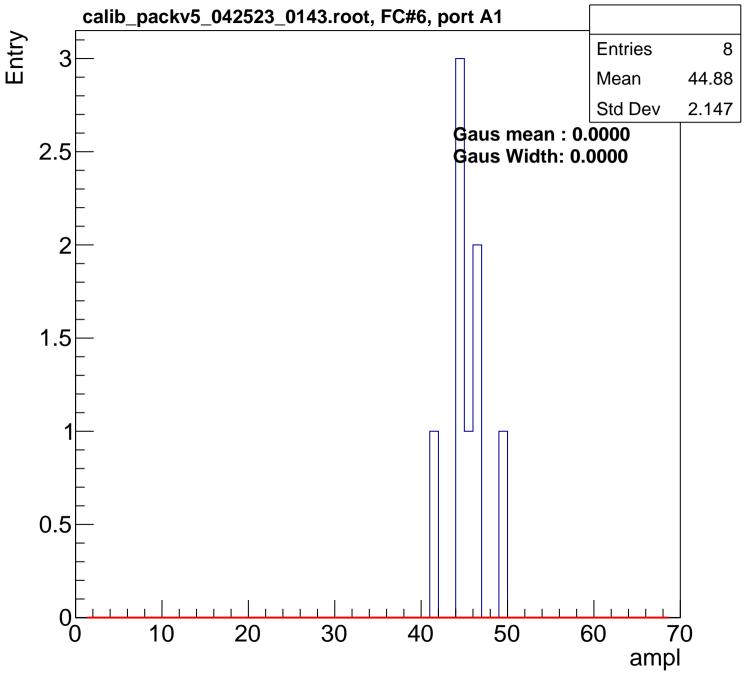


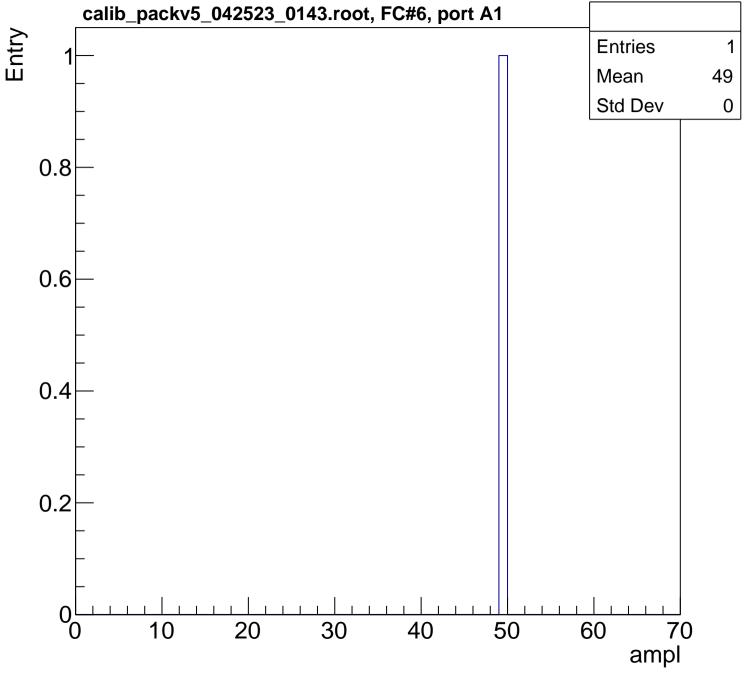
















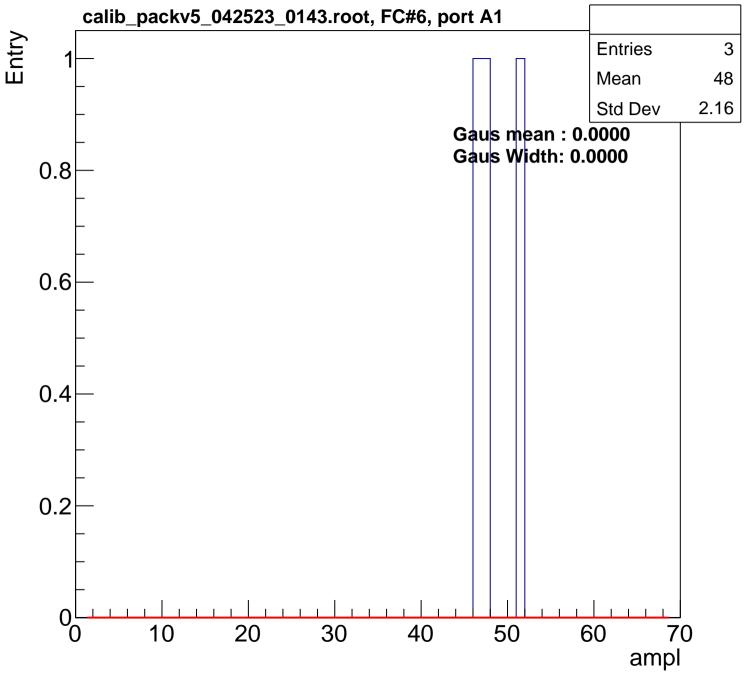


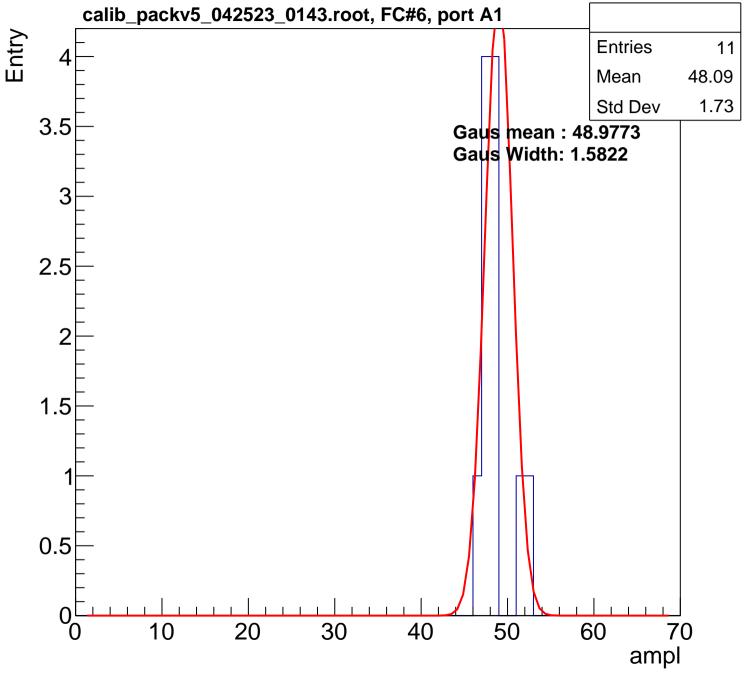


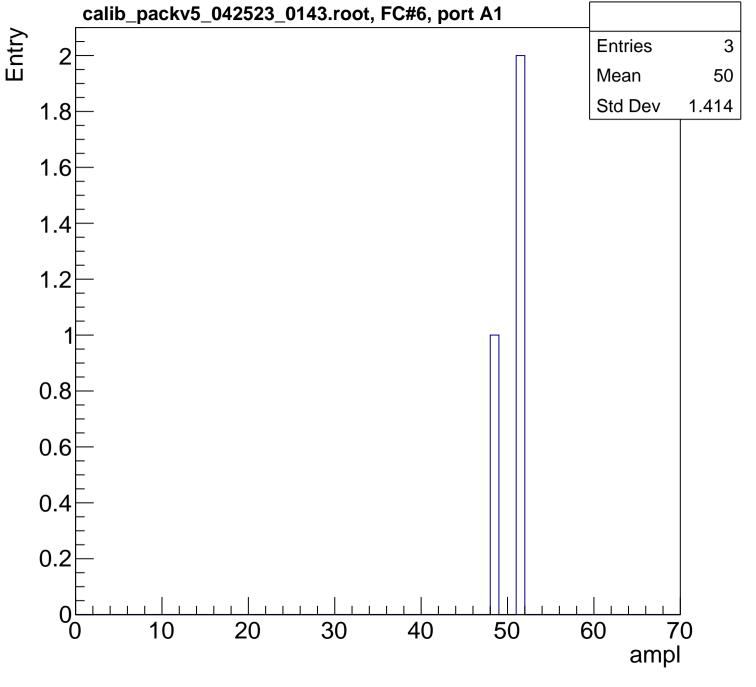
B0L100S, U3-ch35, adc0 calib_packv5_042523_0143.root, FC#6, port A1 Entry **Entries** 1 Mean 0 Std Dev 0 Gaus mean: 0.0000 Gaus Width: 0.0000 8.0 0.6 0.4 0.2 10 20 30 40 50 60

70

ampl





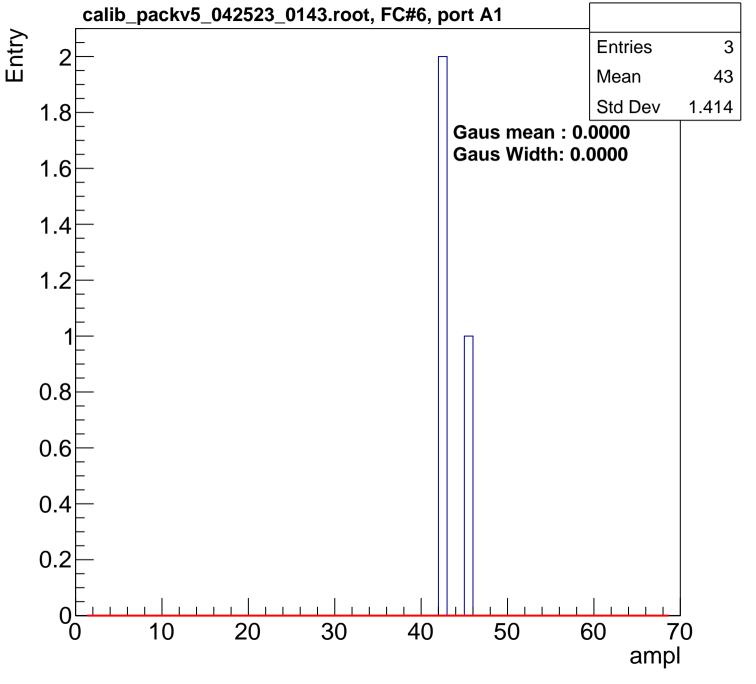


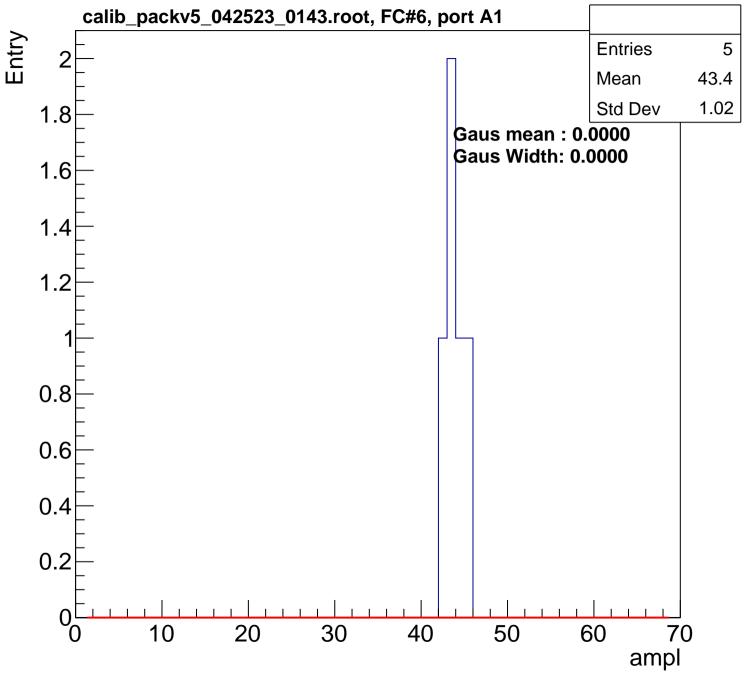


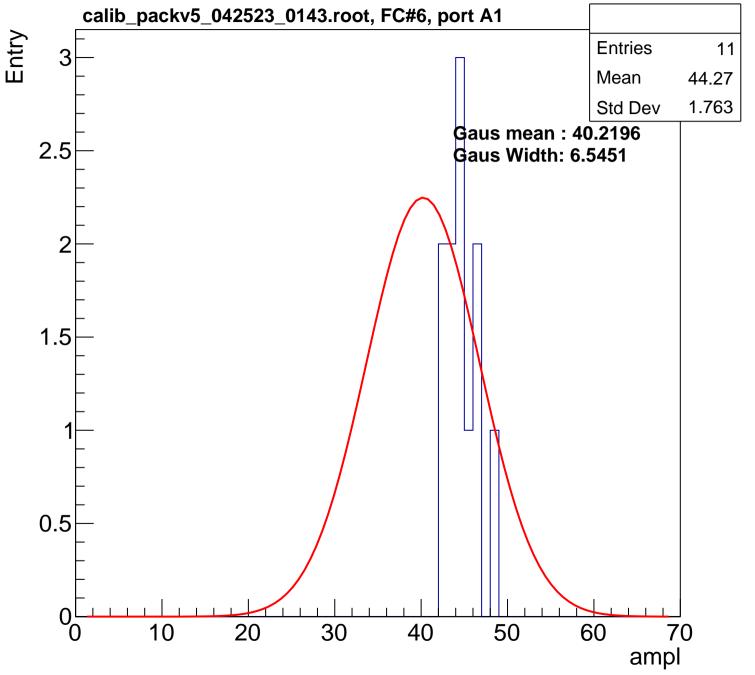


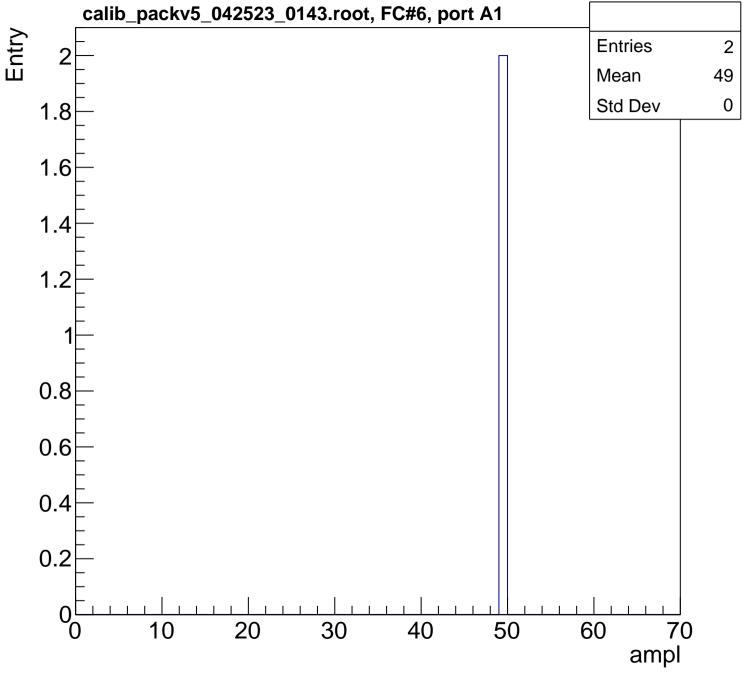






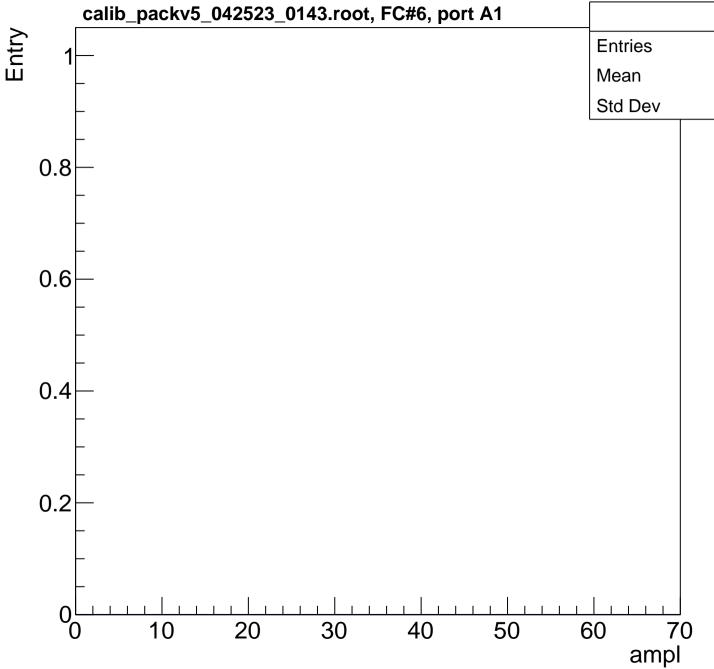






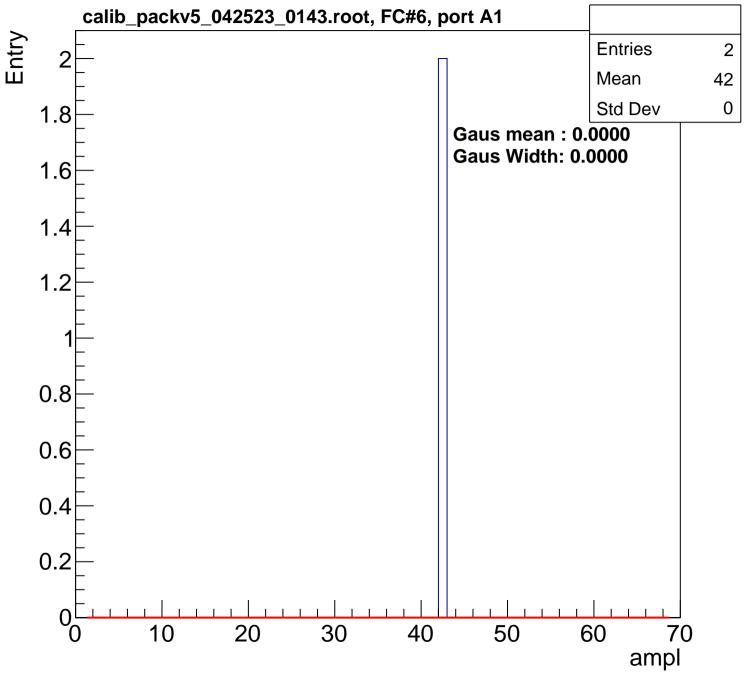


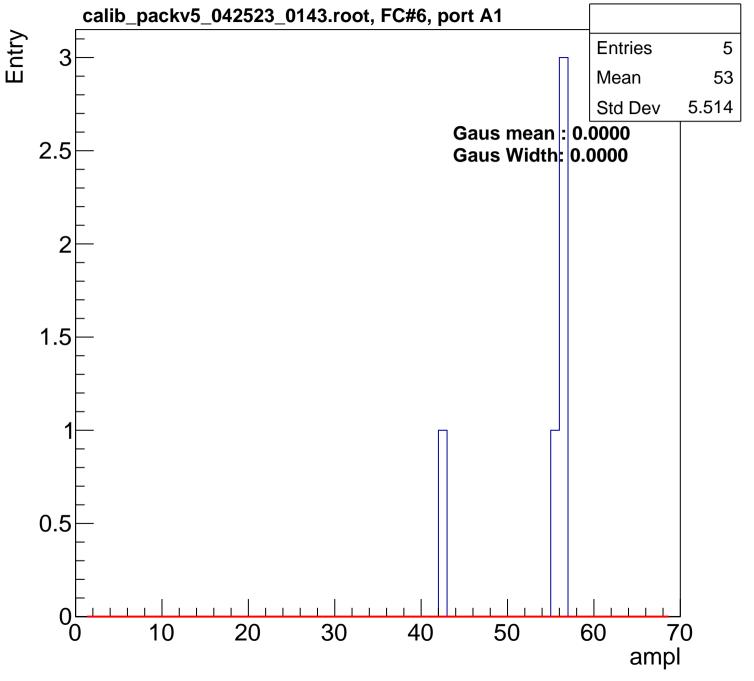


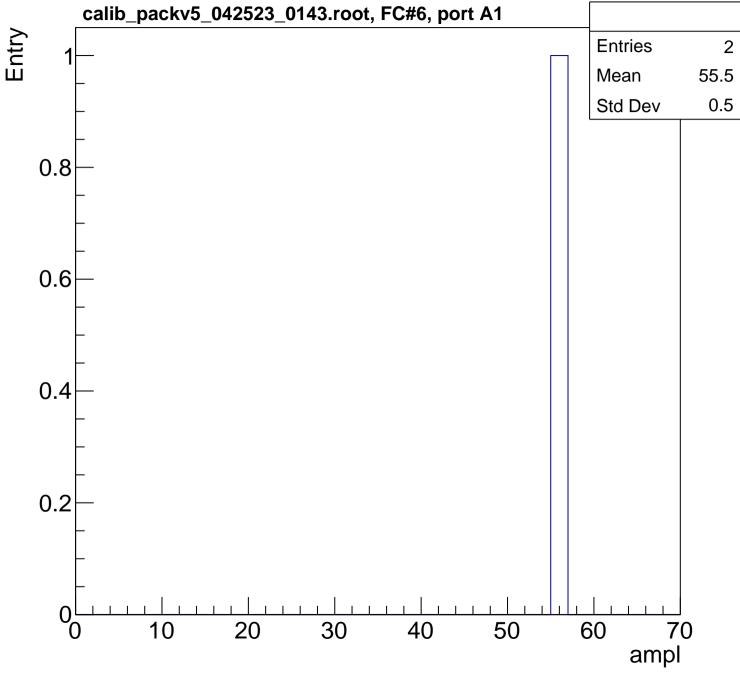


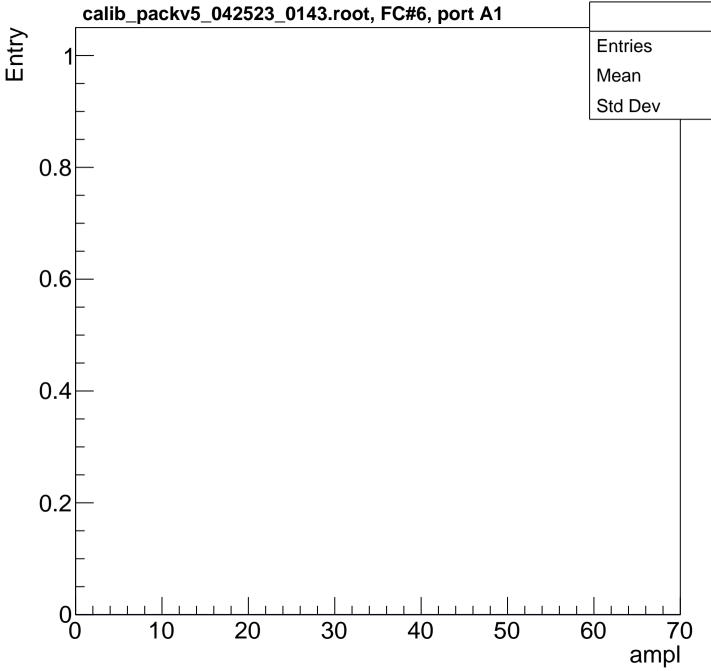








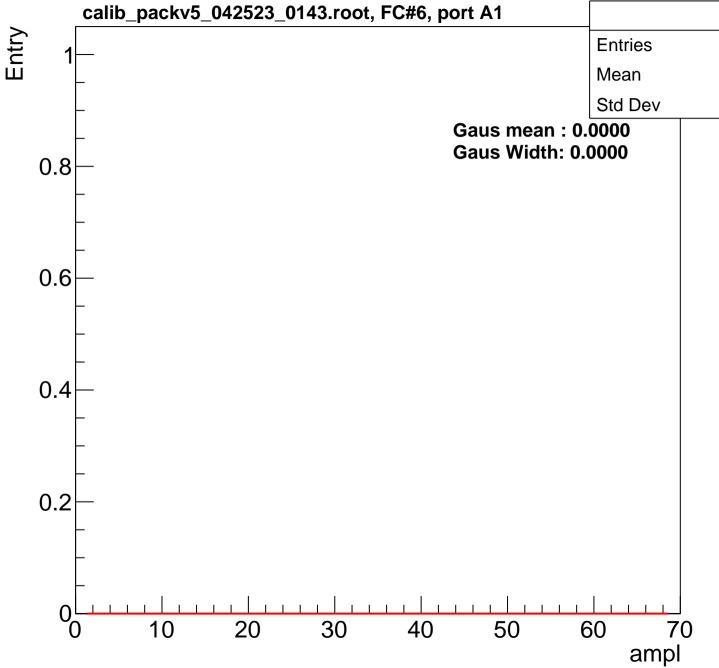


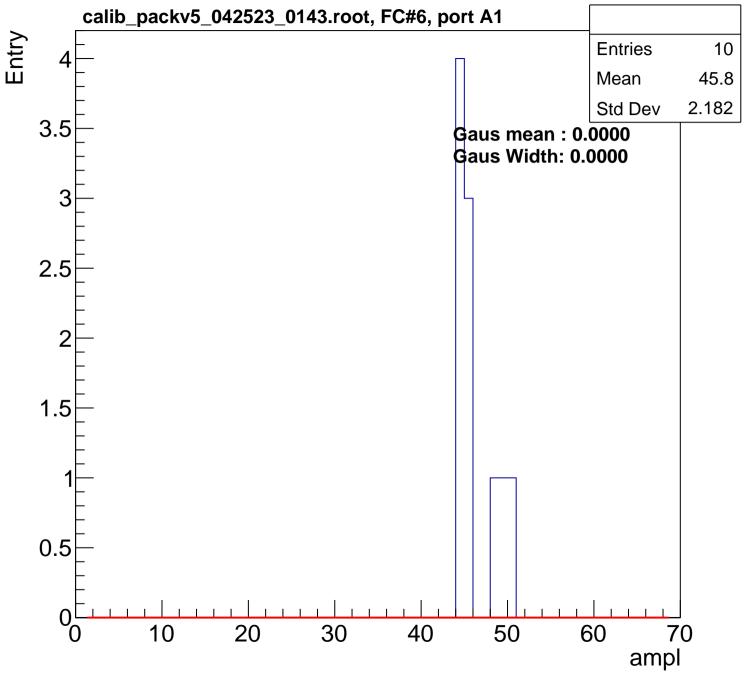


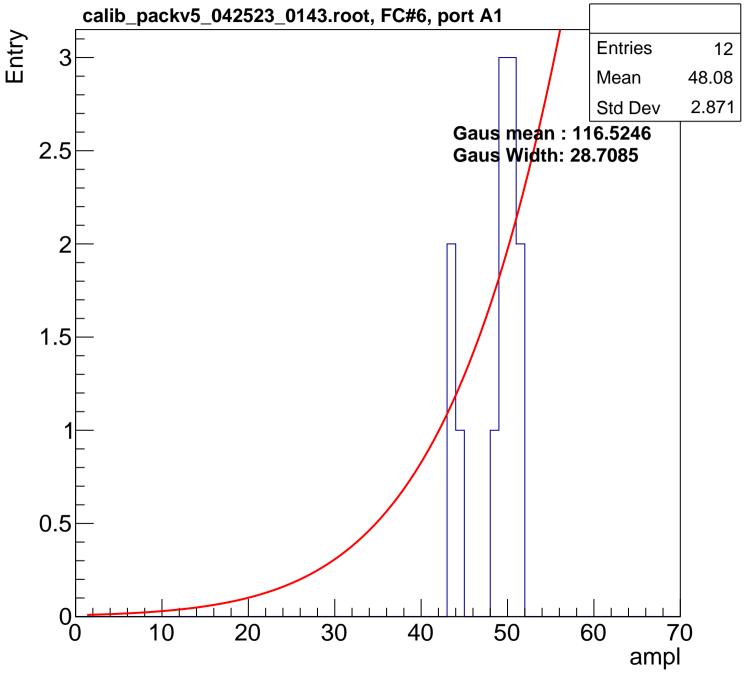


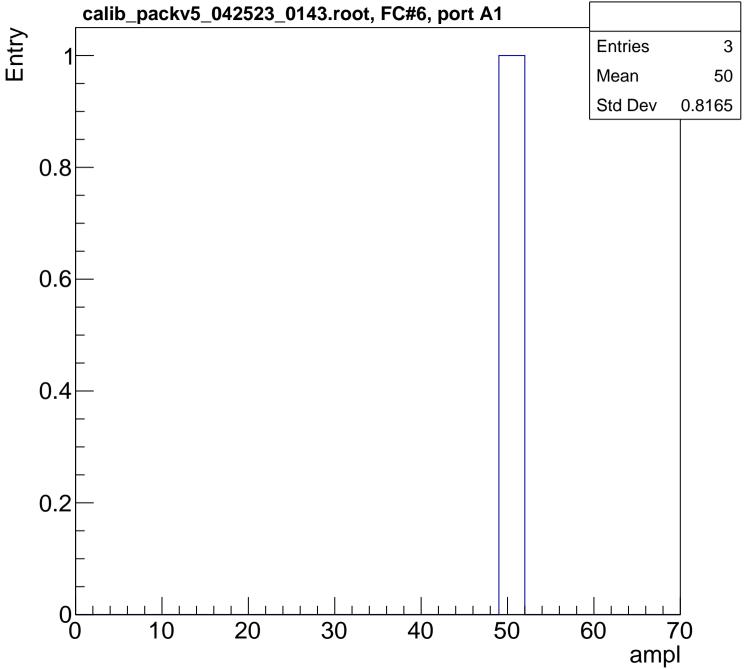










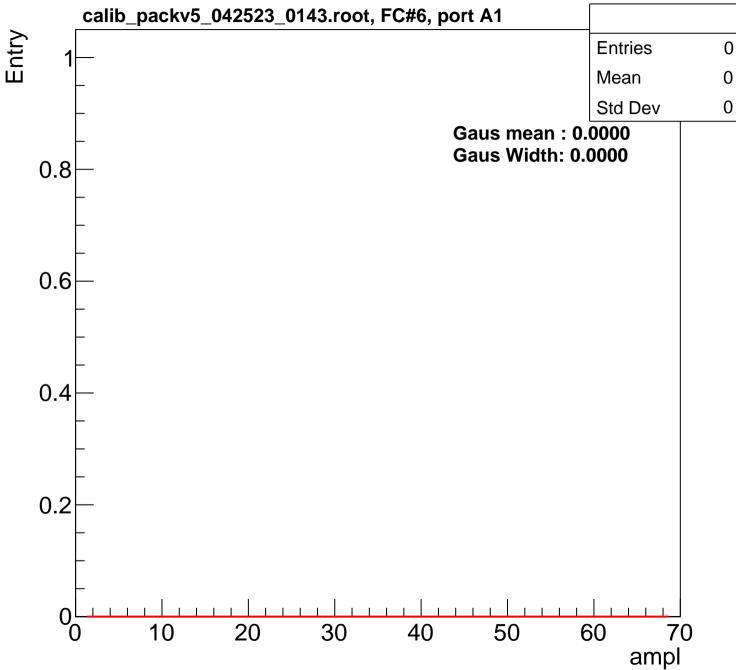






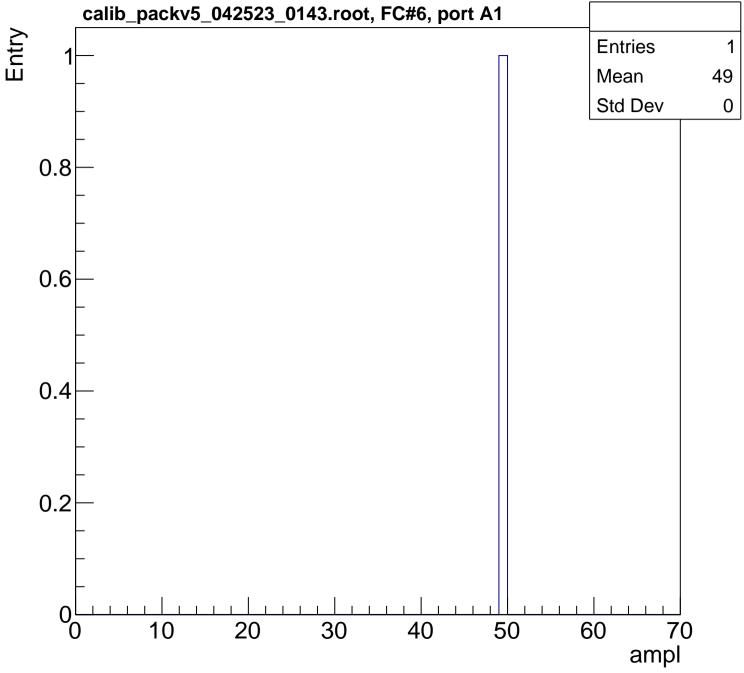










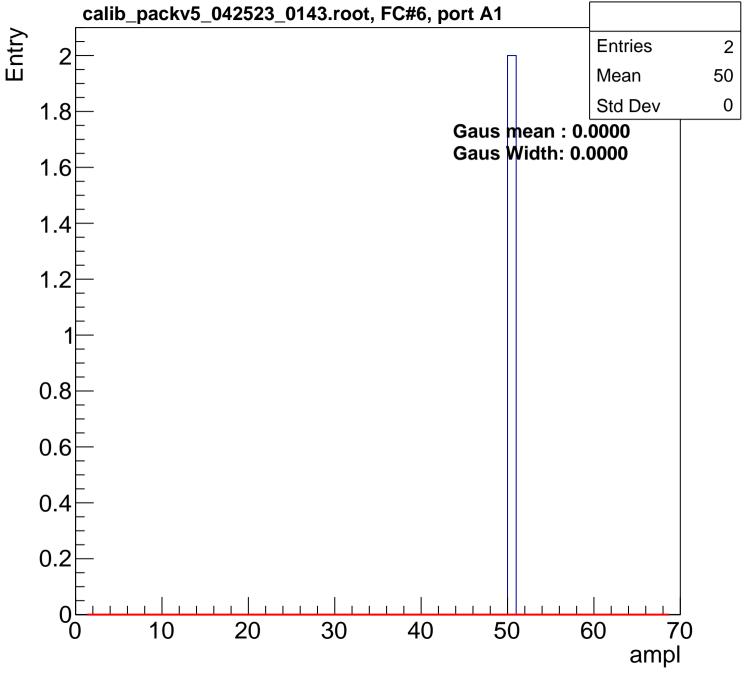


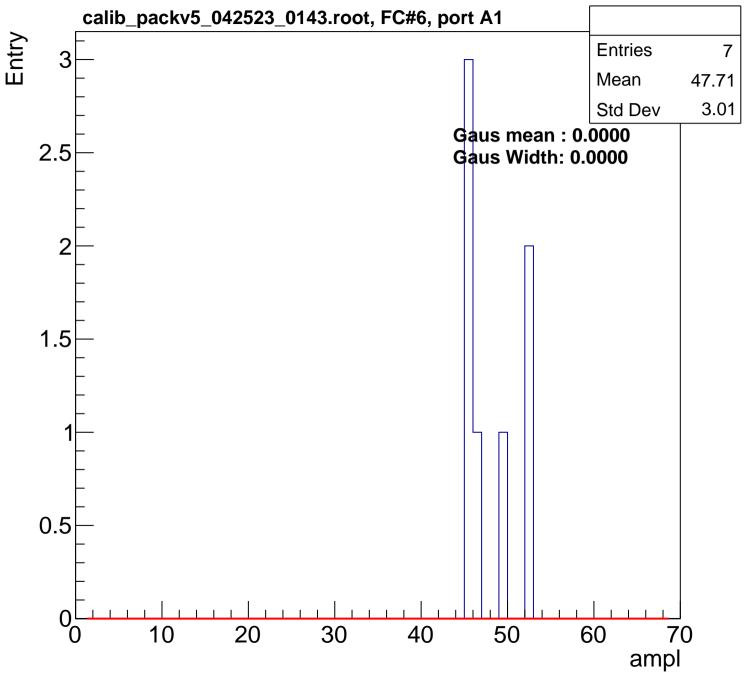


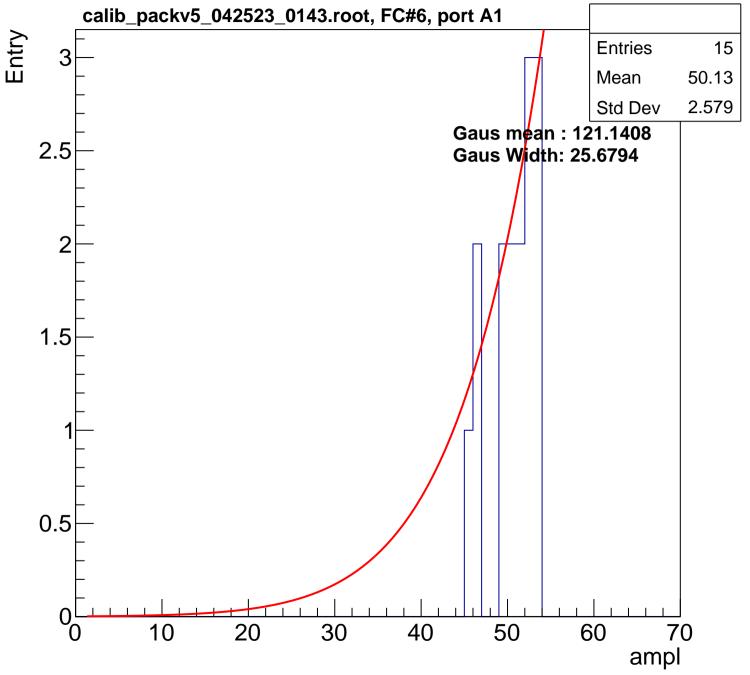


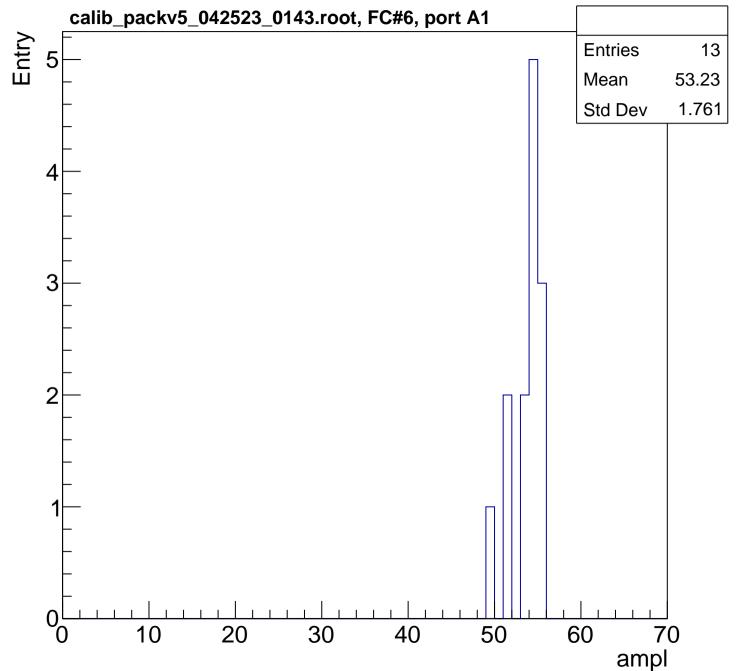


















B0L100S, U3-ch40, adc7 calib_packv5_042523_0143.root, FC#6, port A1 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

































B0L100S, U3-ch43, adc0 calib_packv5_042523_0143.root, FC#6, port A1 Entry **Entries** 1 Mean 0 Std Dev 0 Gaus mean: 0.0000 Gaus Width: 0.0000 8.0 0.6 0.4 0.2 10 20 30 40 50 60 70 ampl







































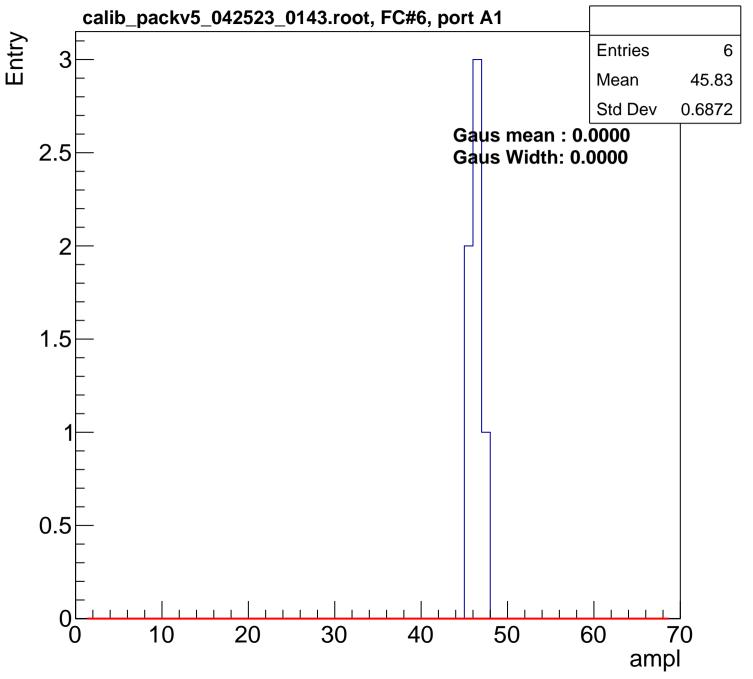


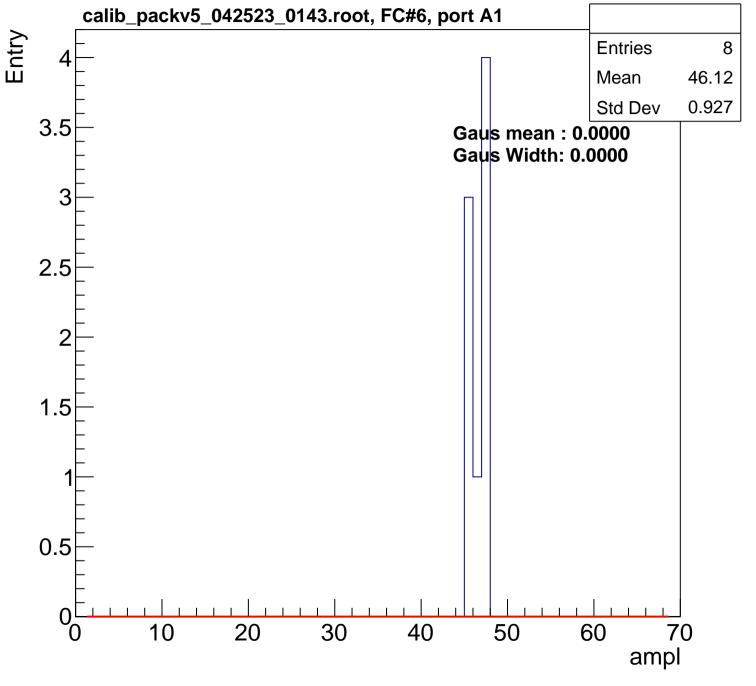


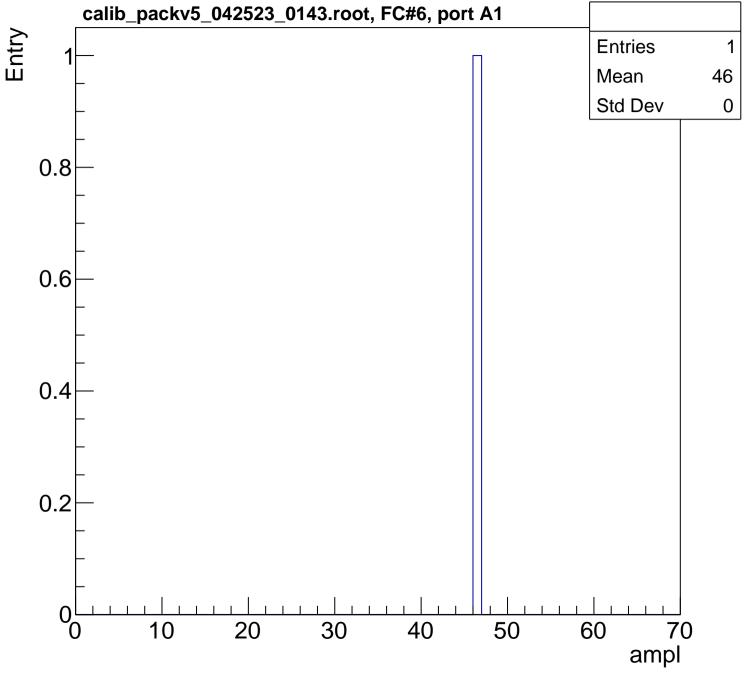


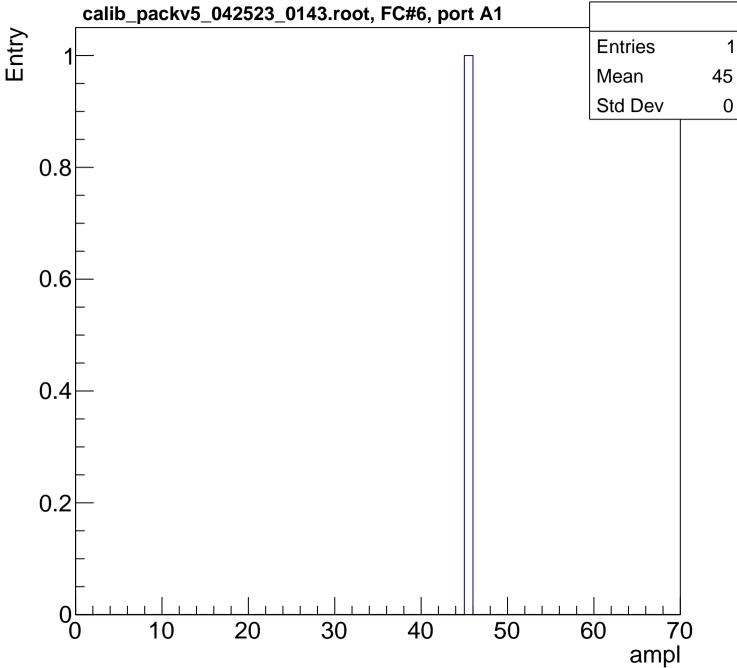








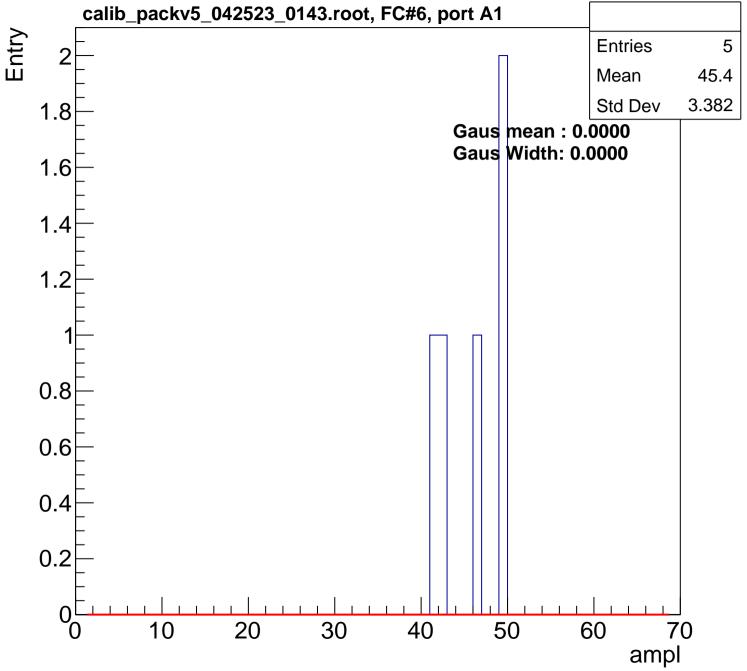


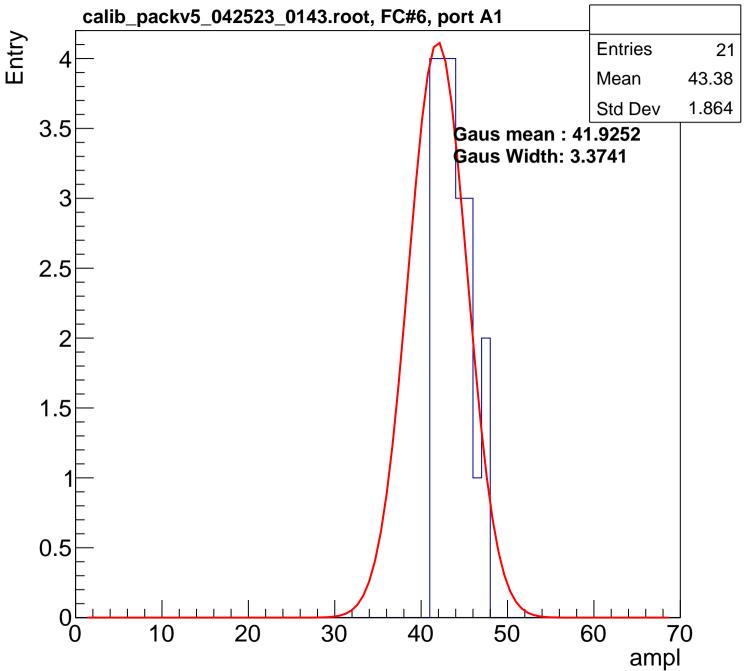


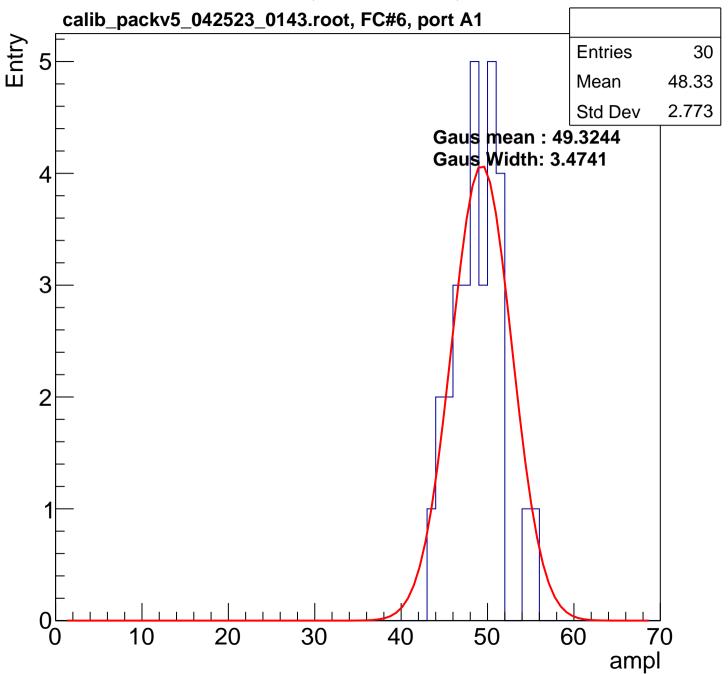


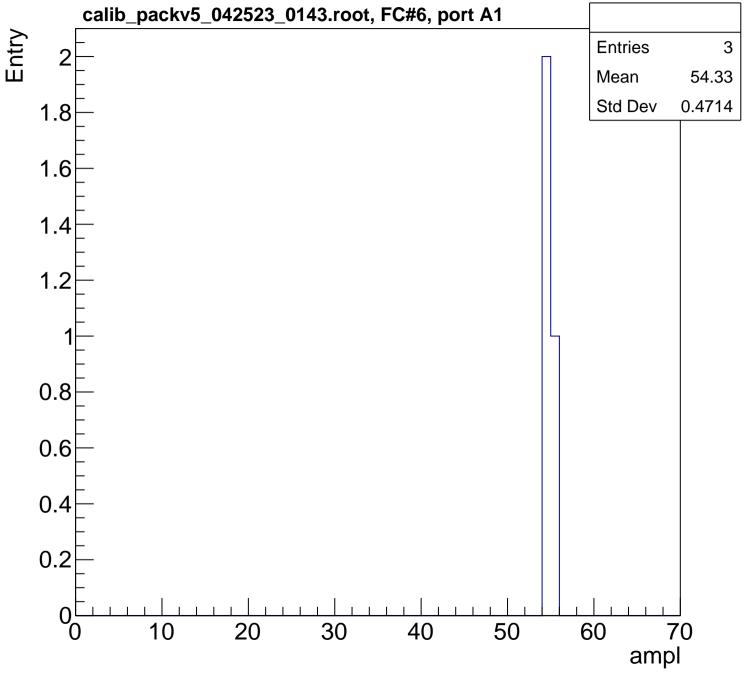














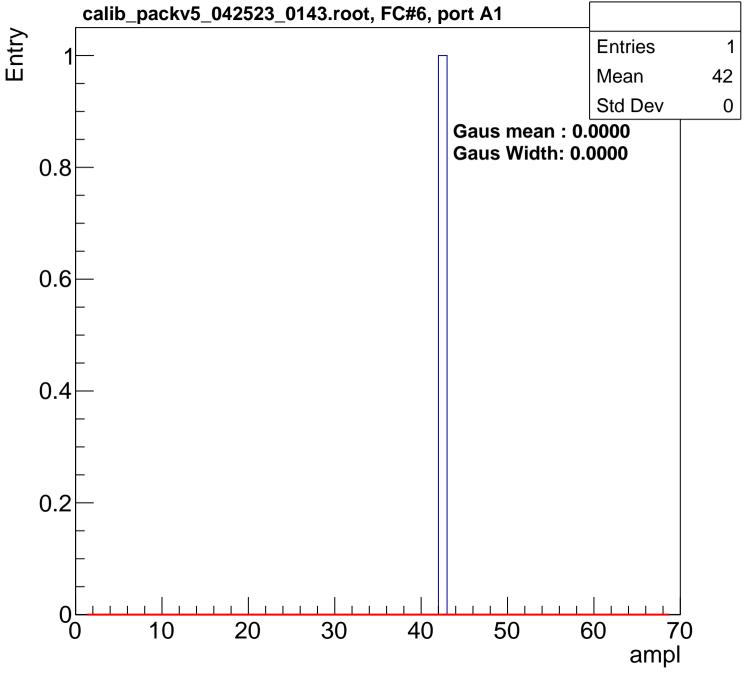
















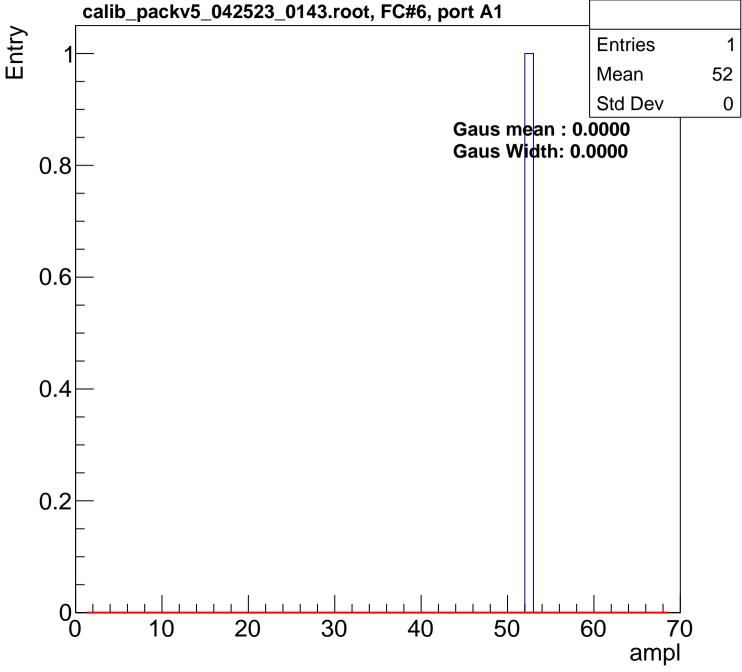












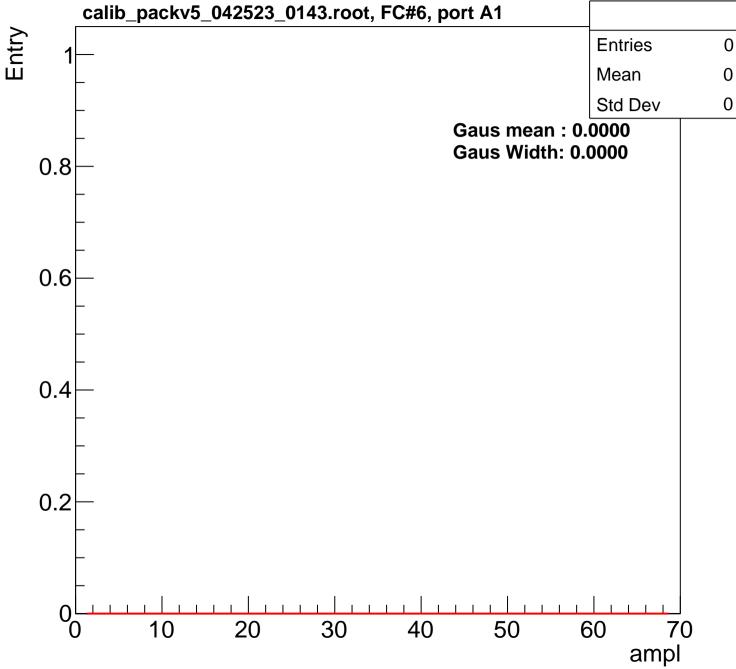


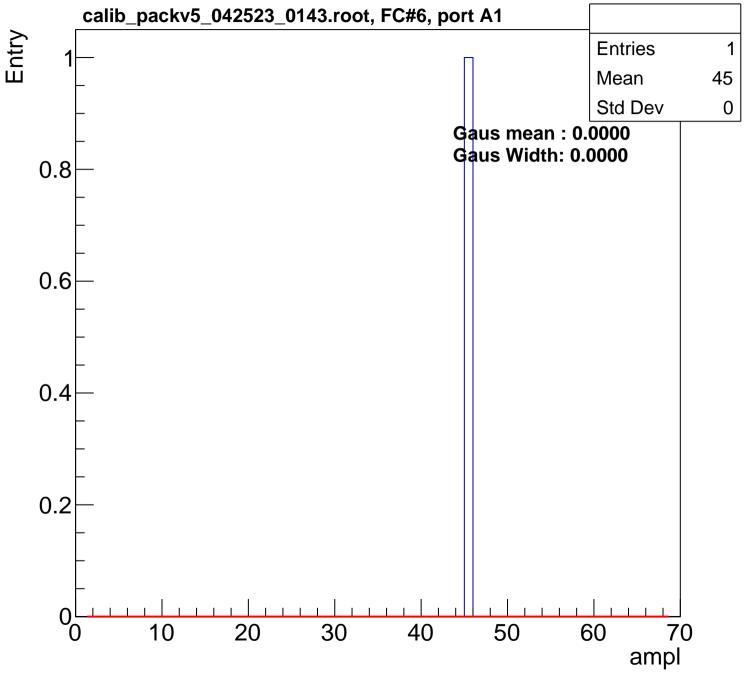




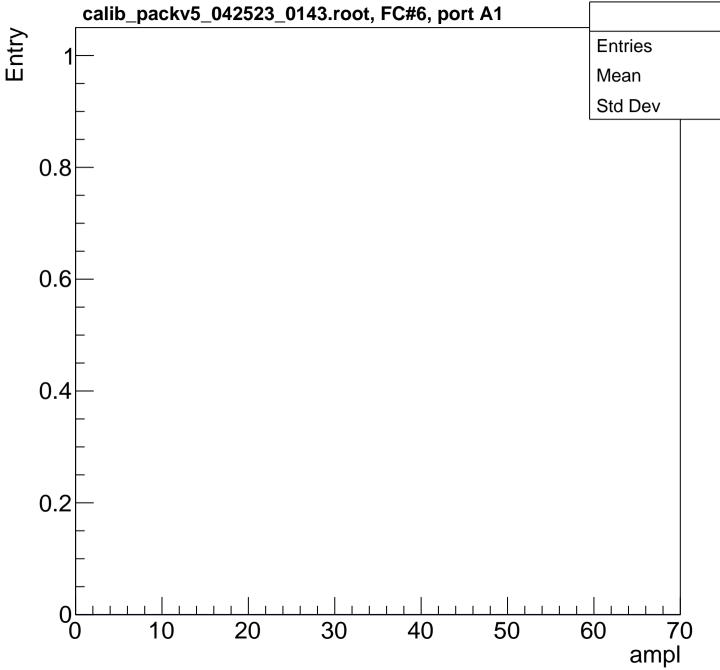








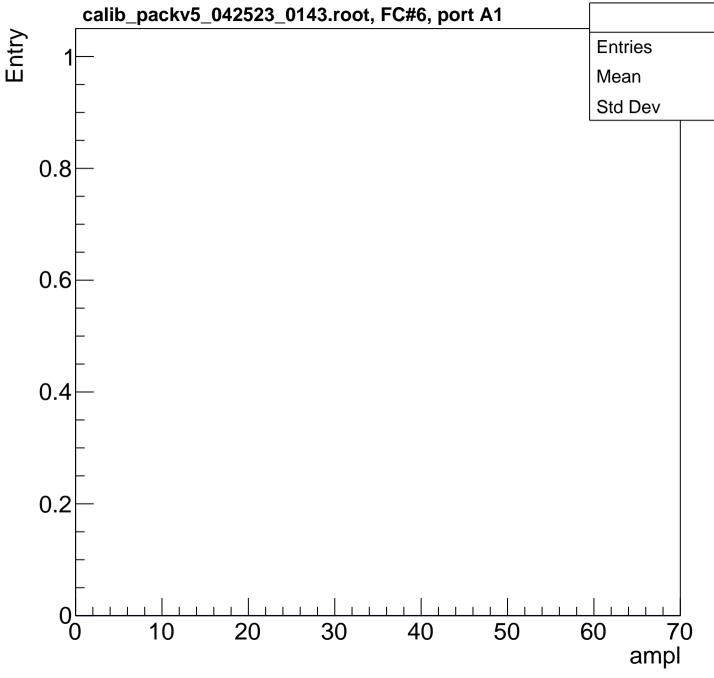


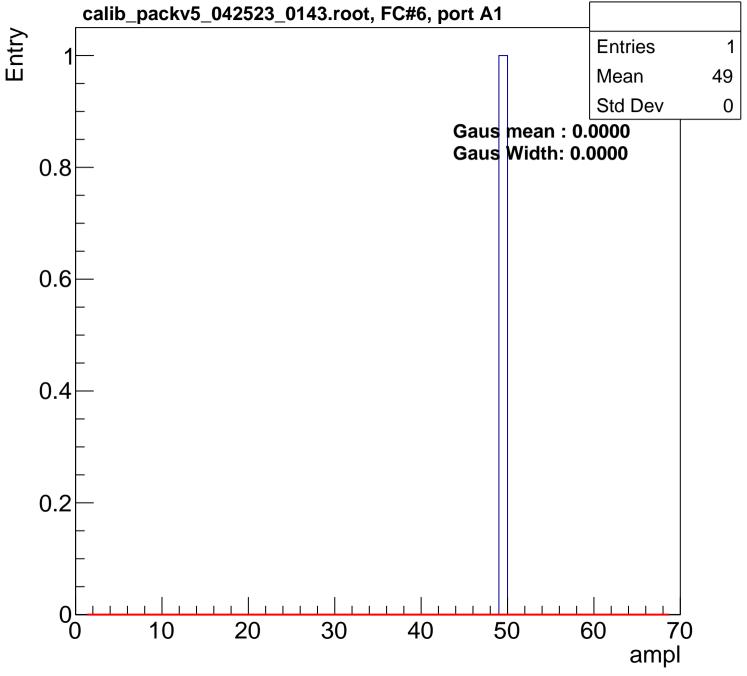


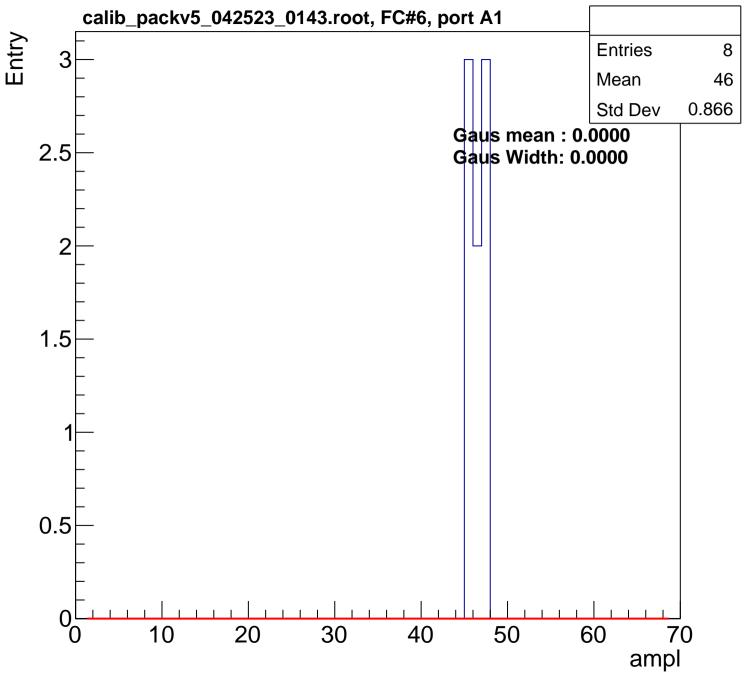


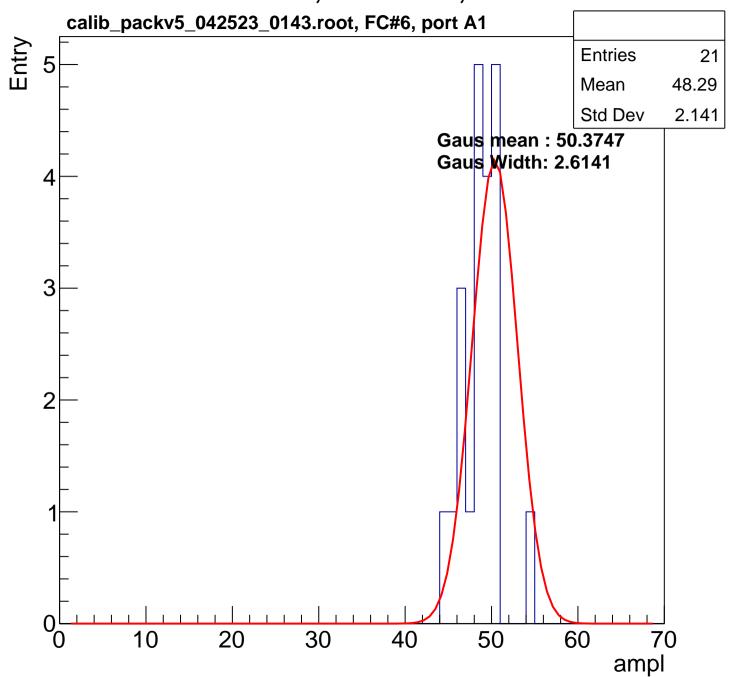


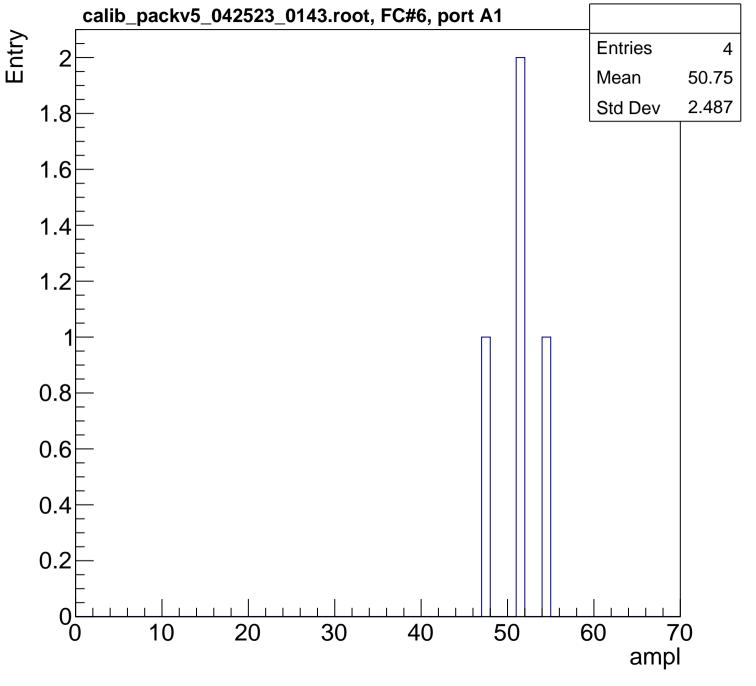






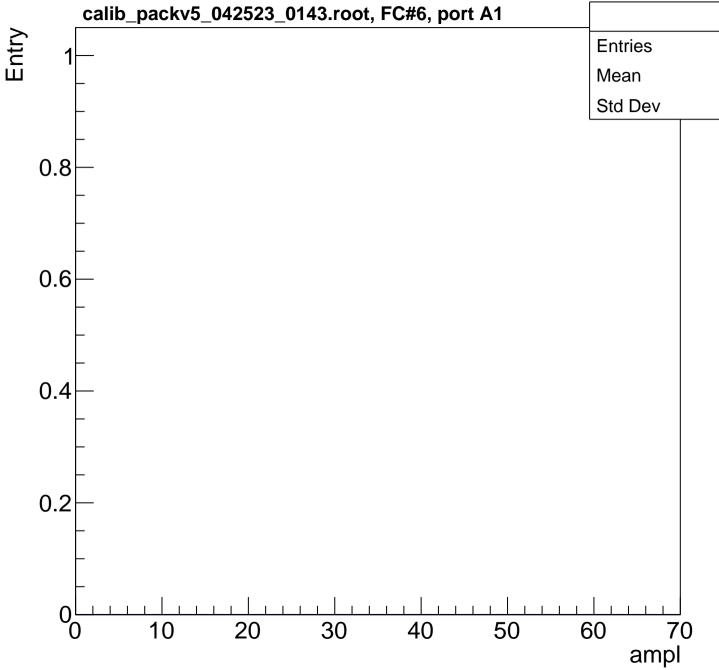






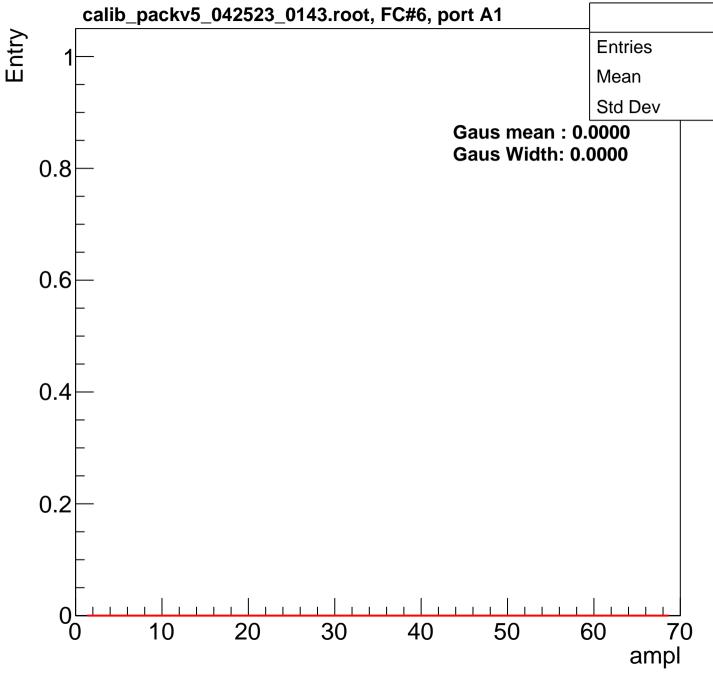


















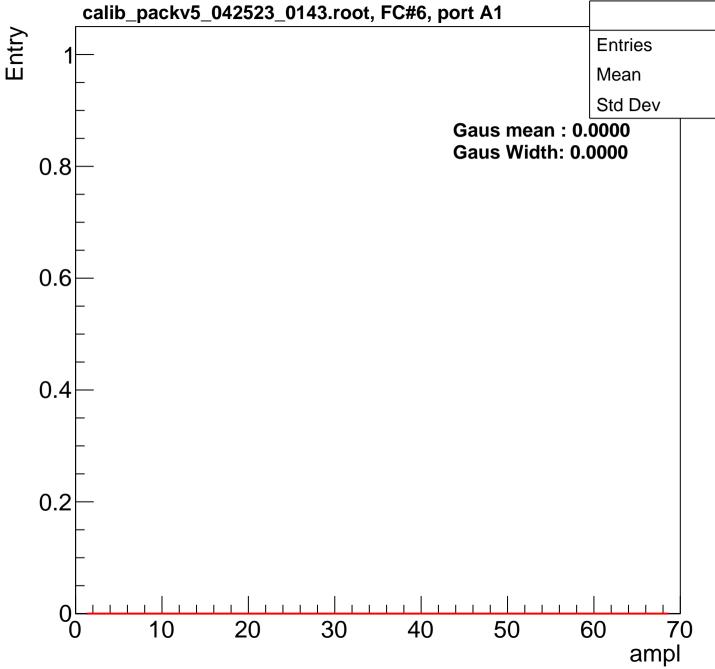














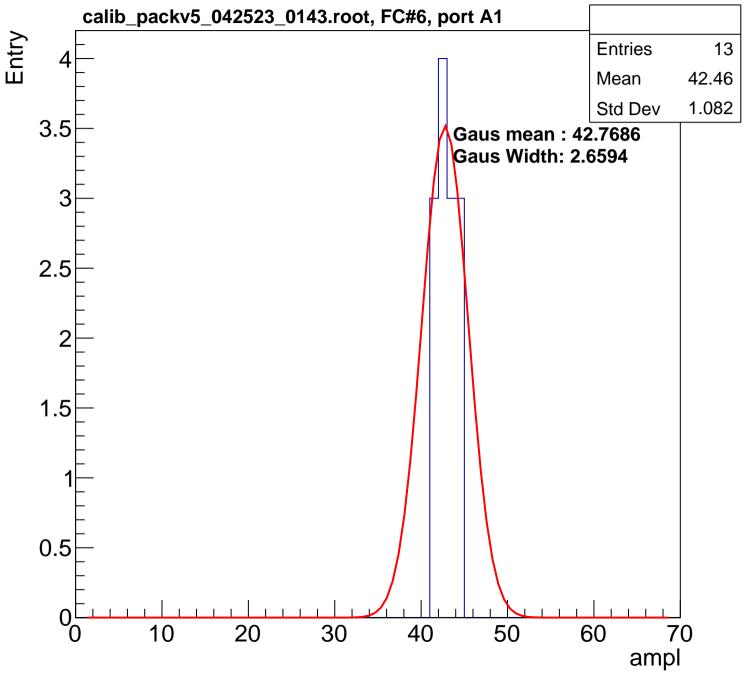


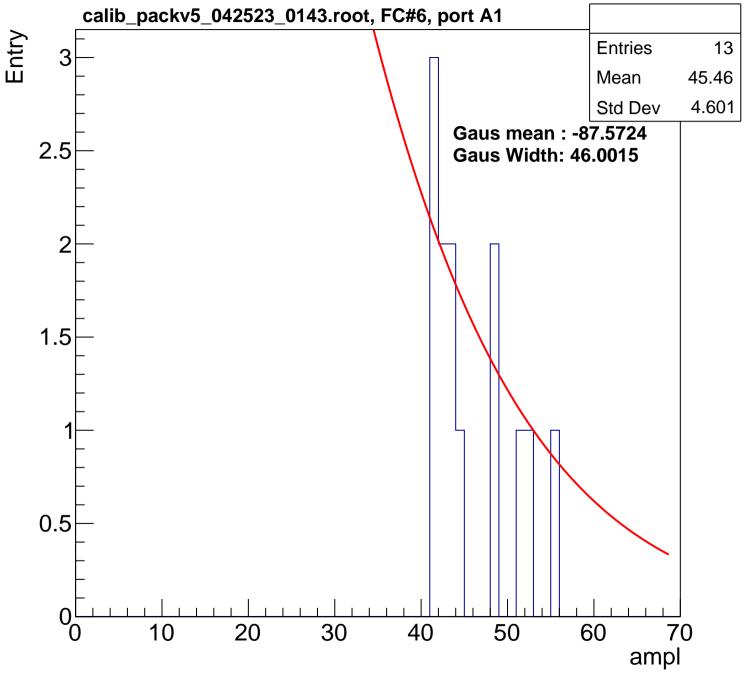


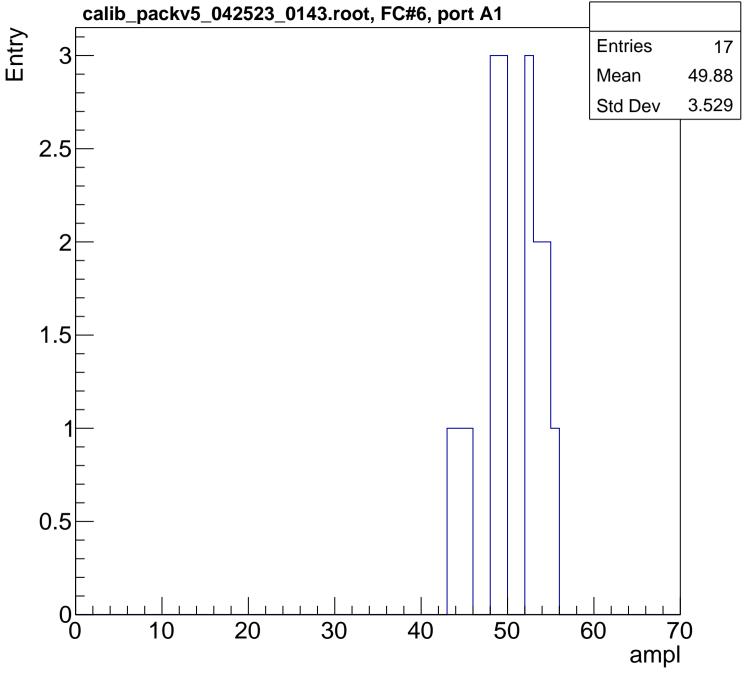


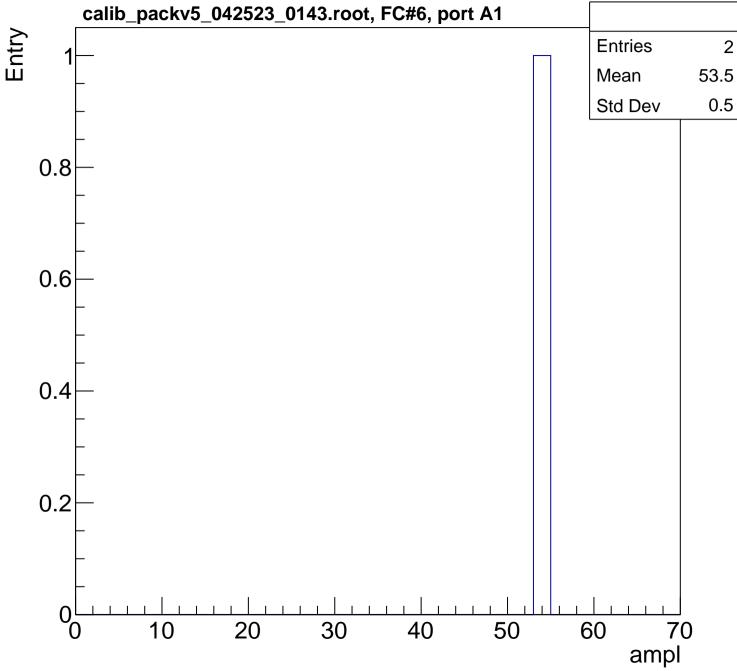










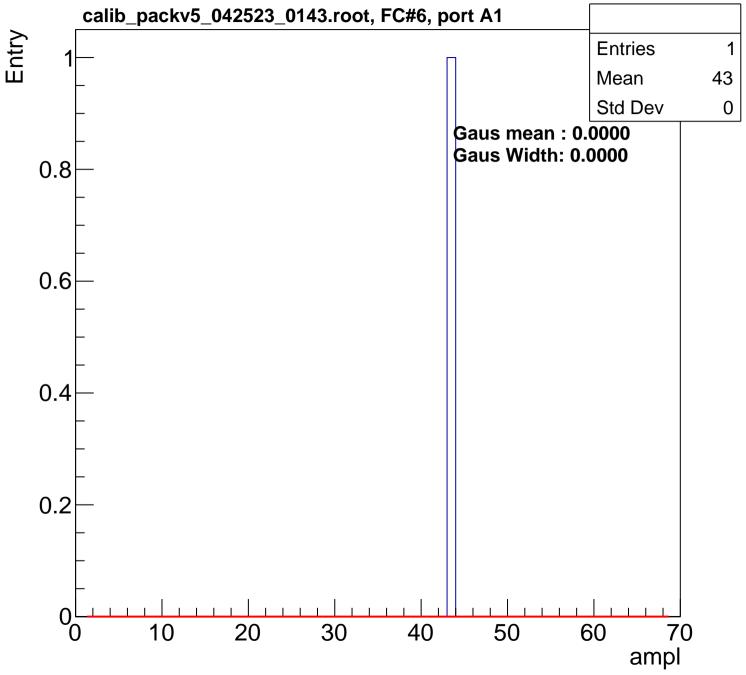


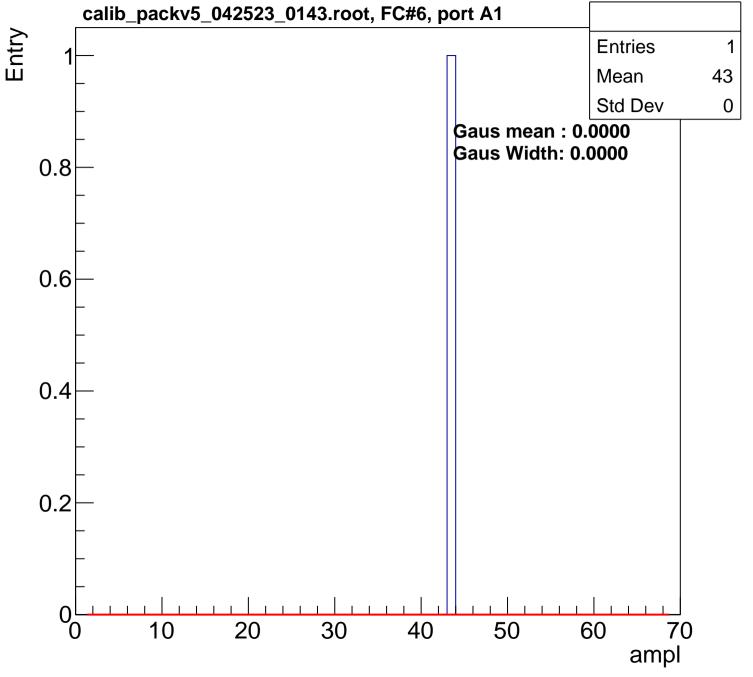












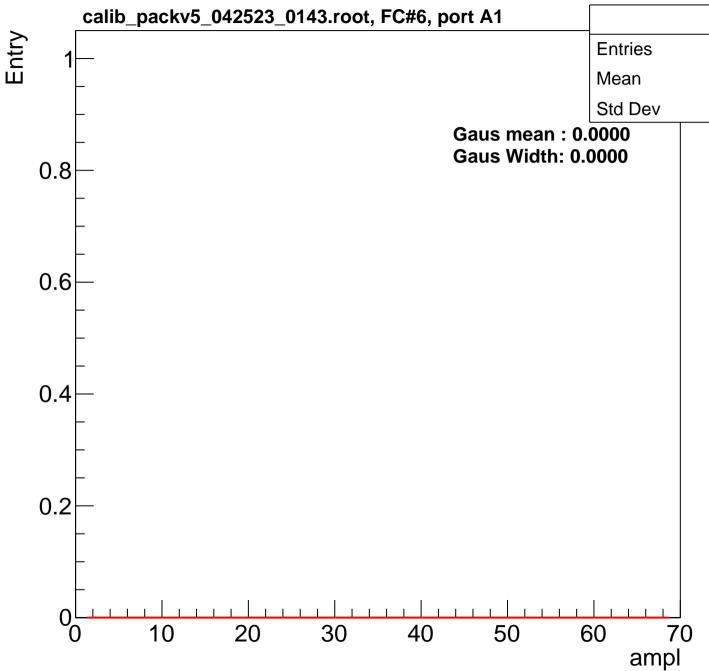


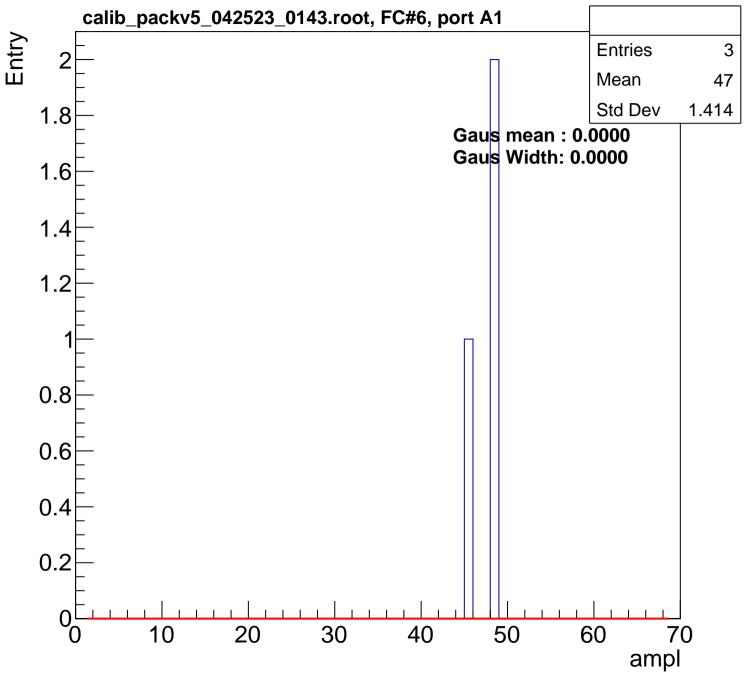


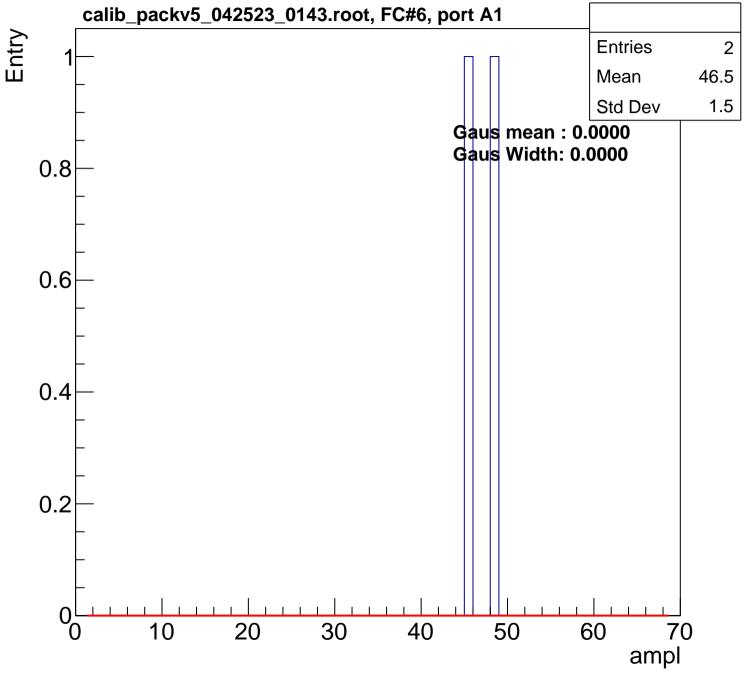


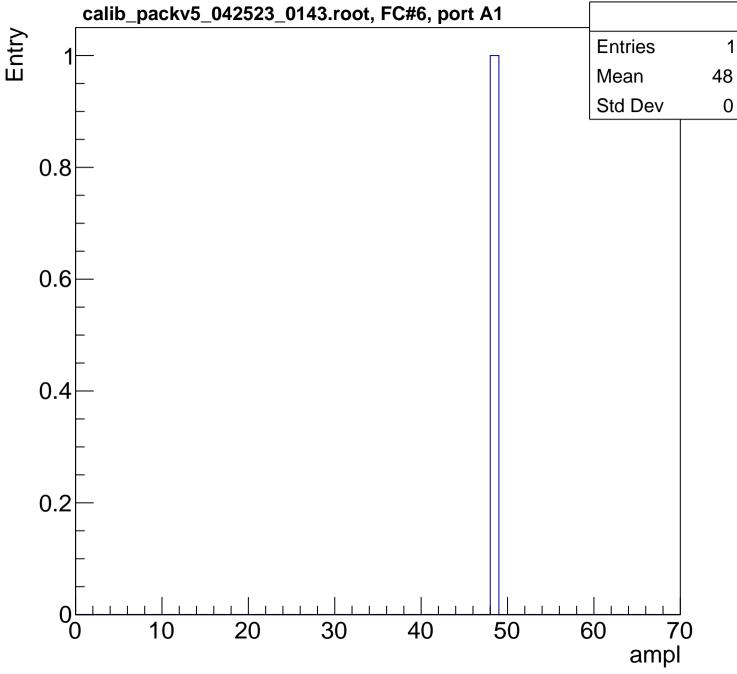






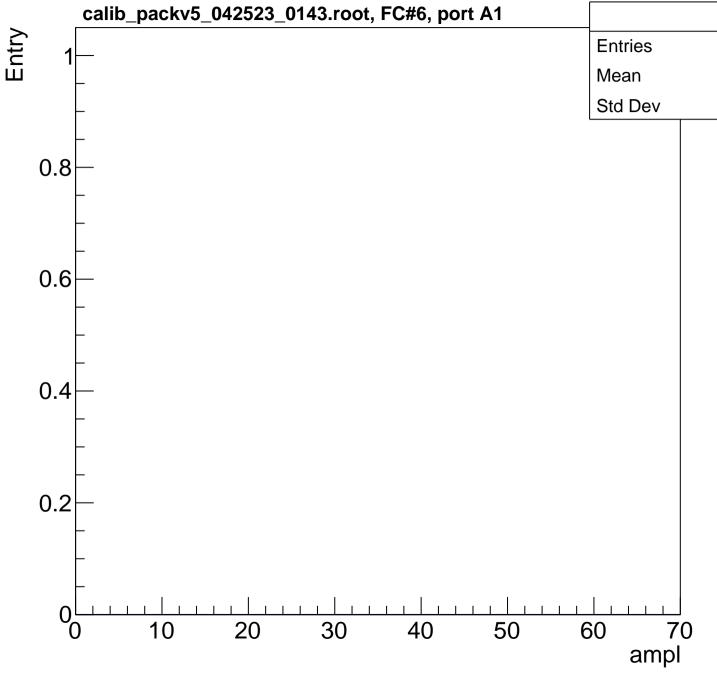






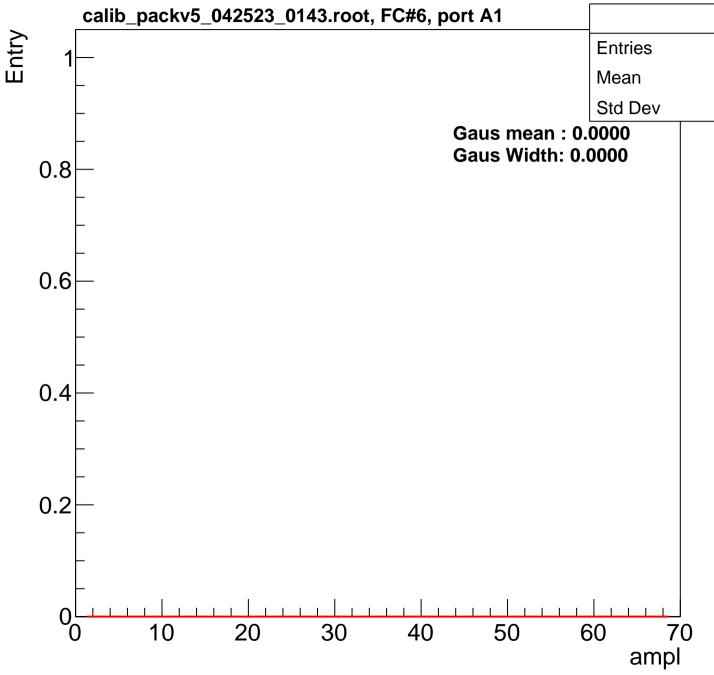




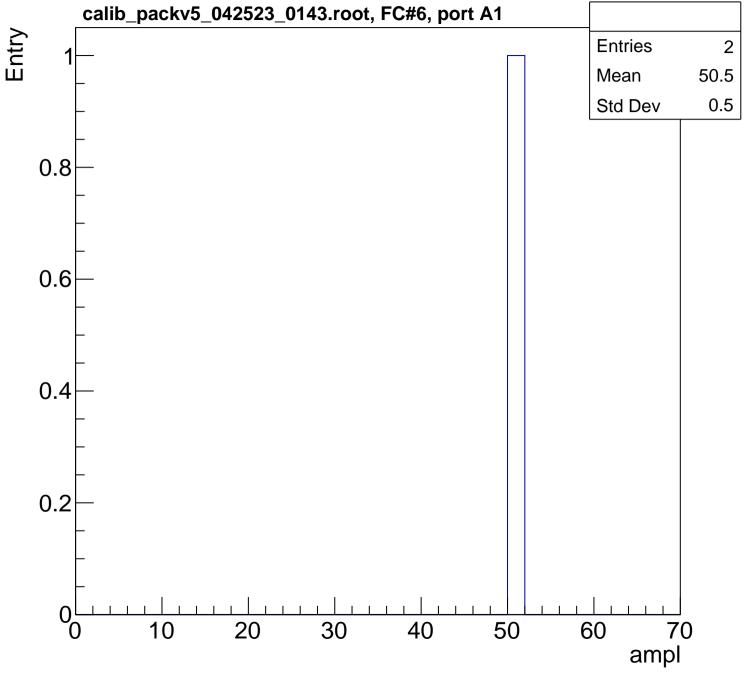












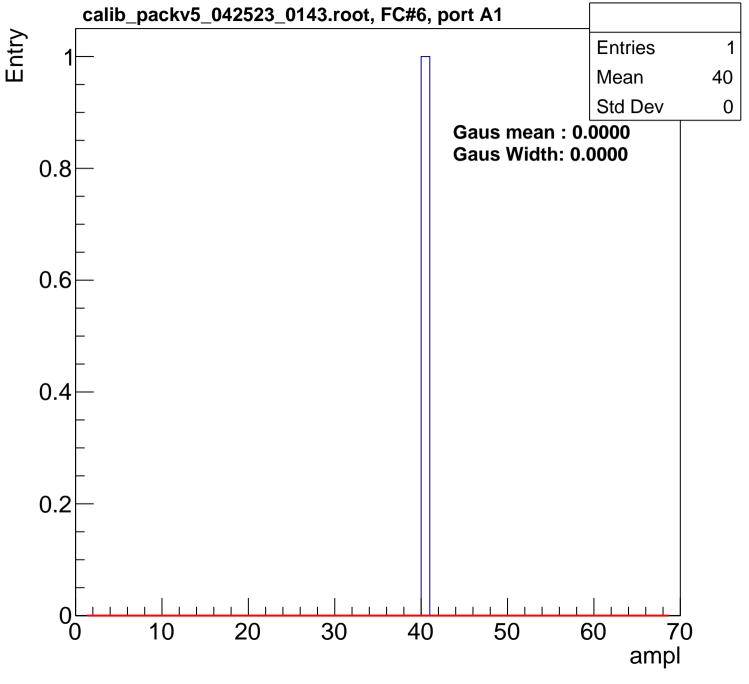


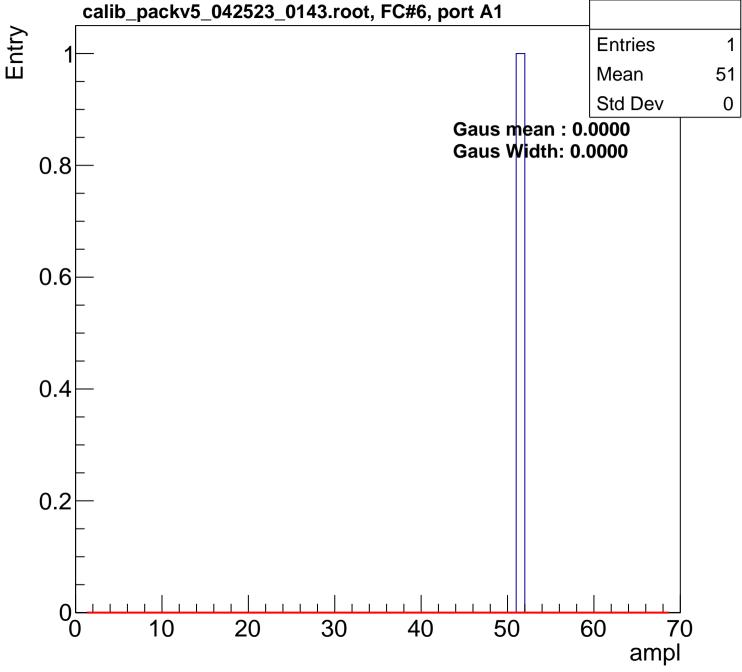




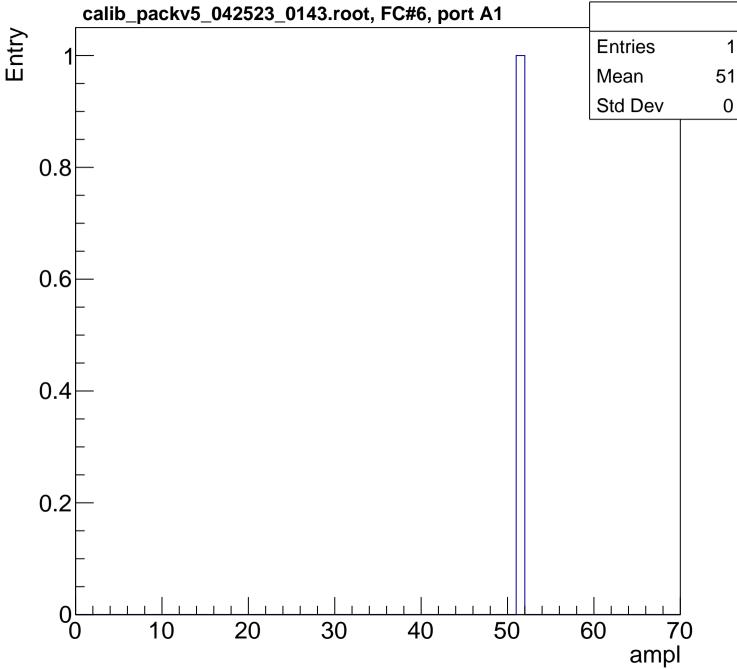










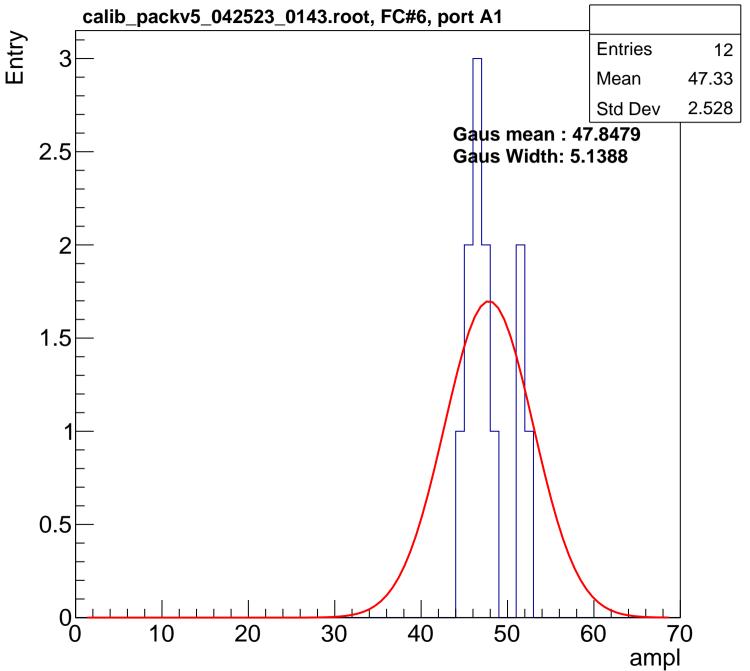


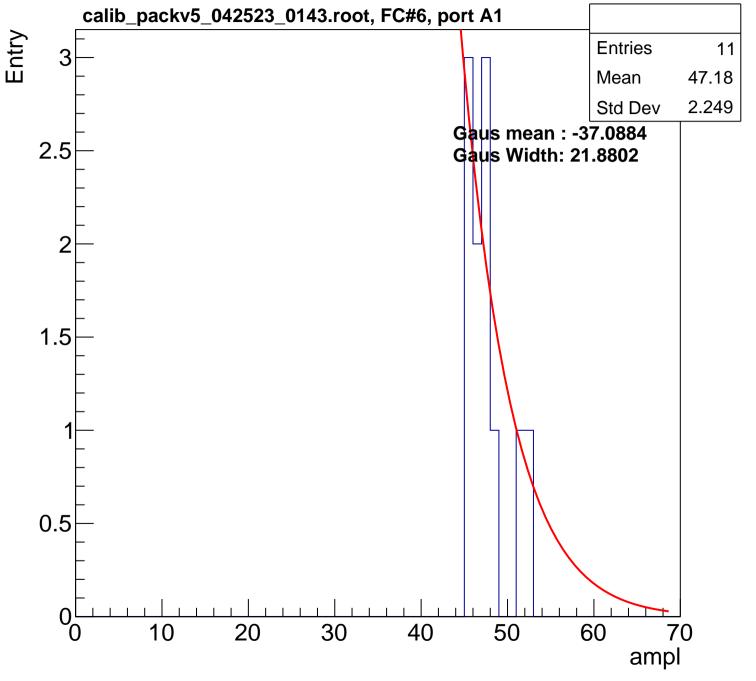


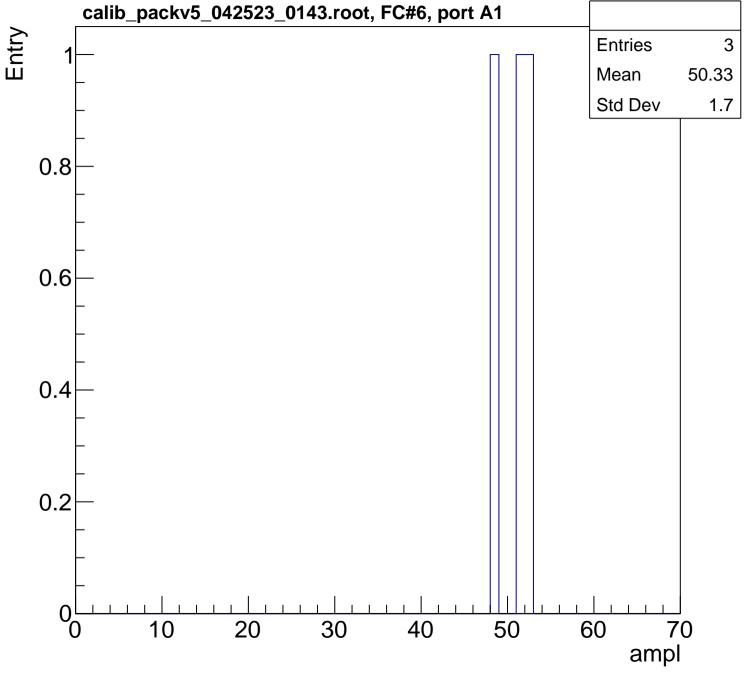










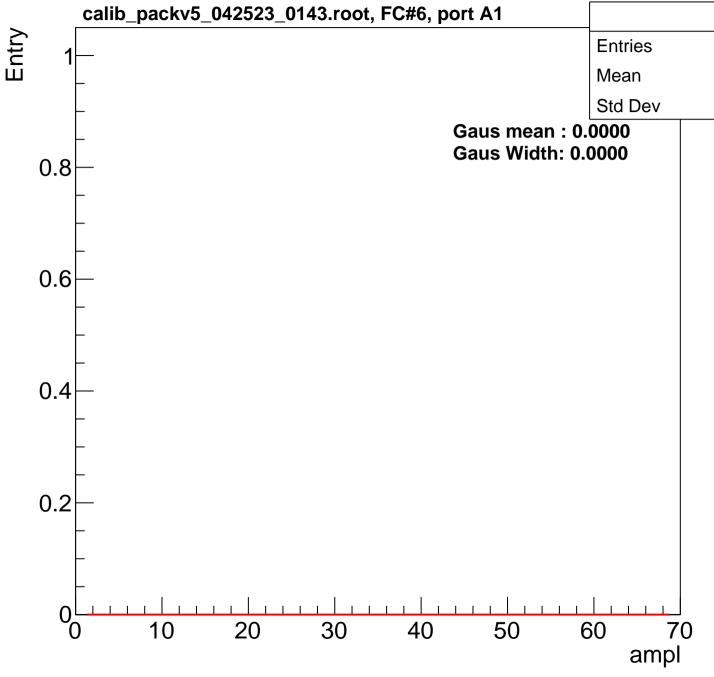


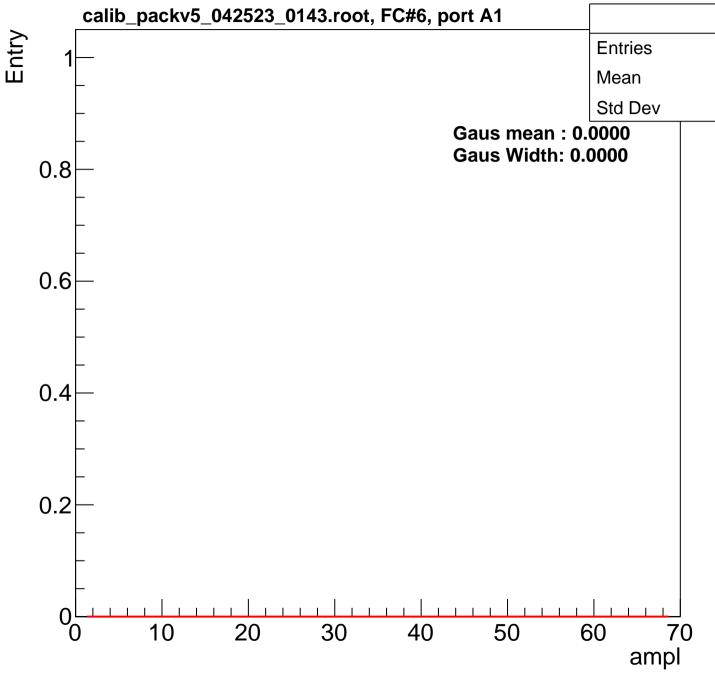




















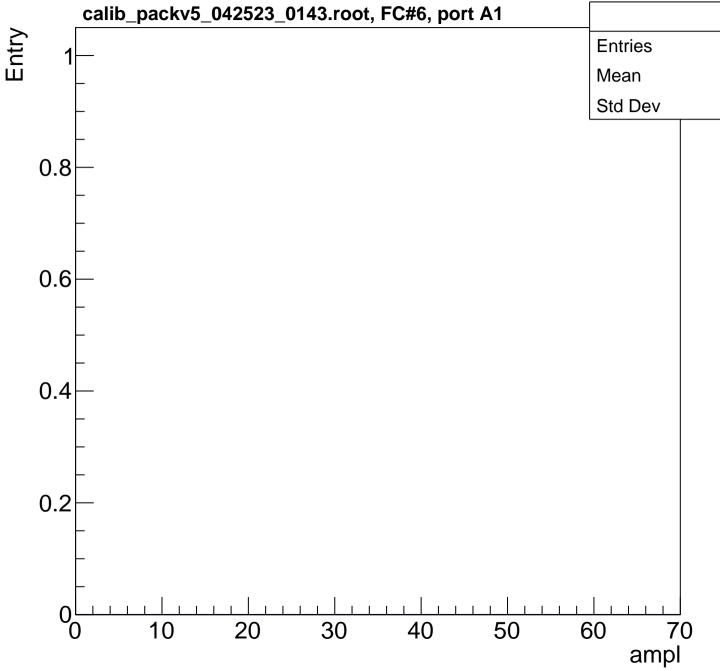










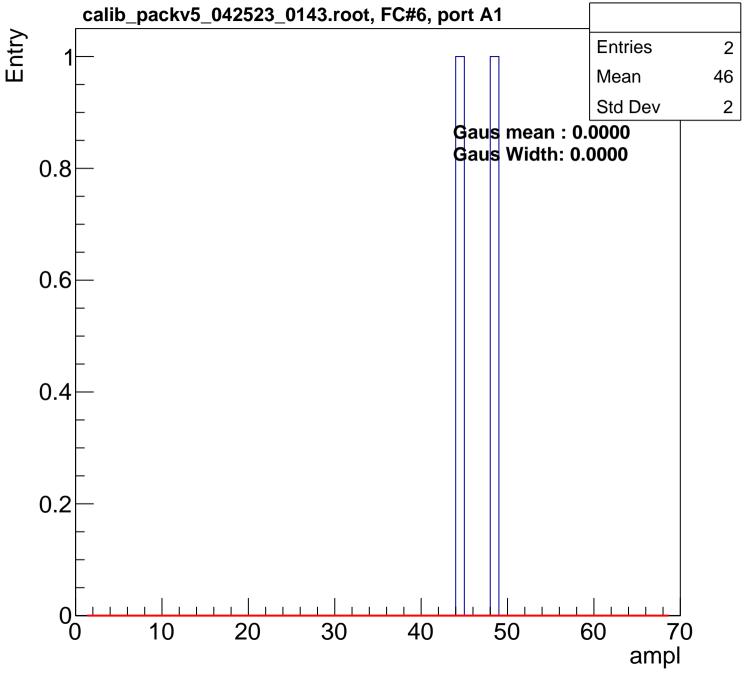


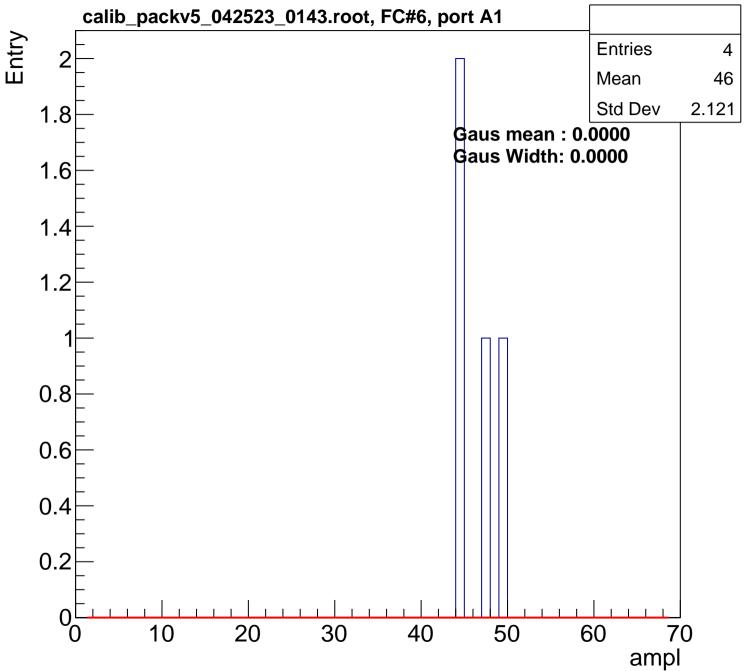


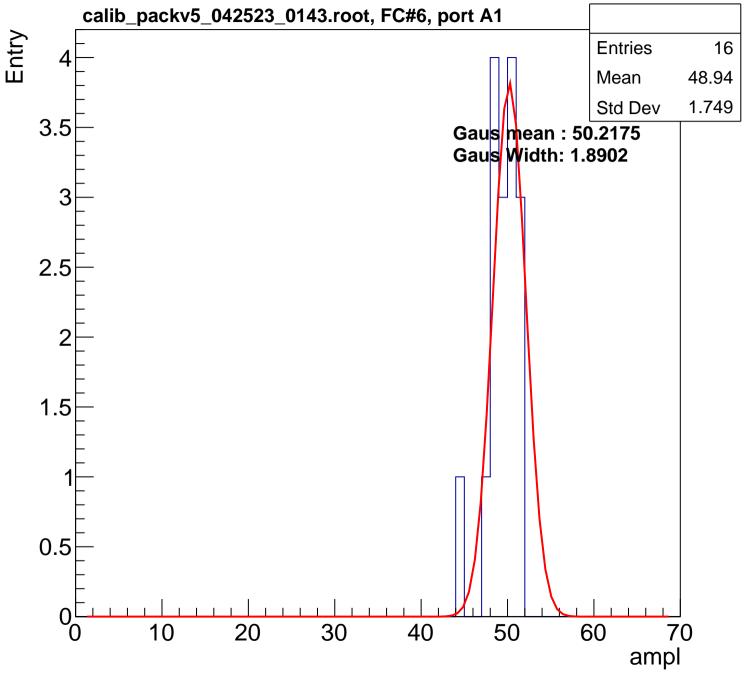


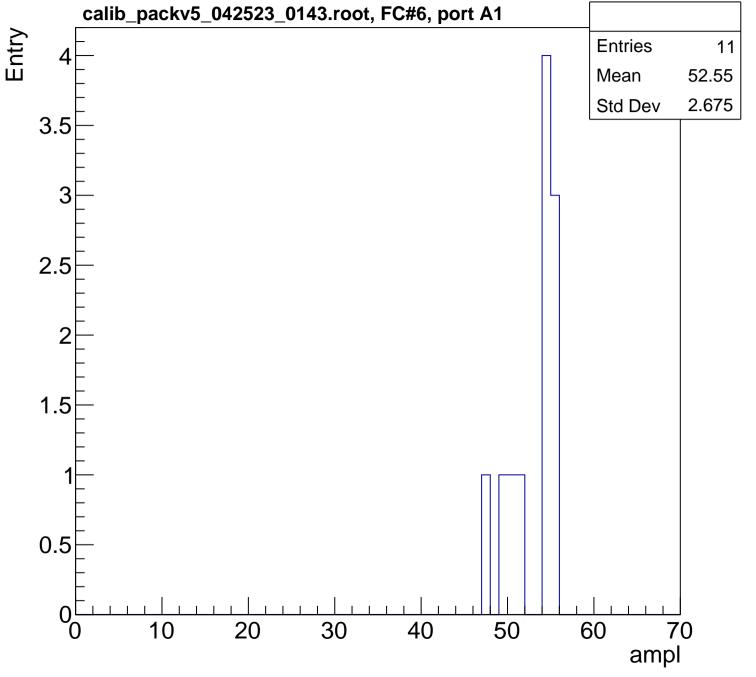


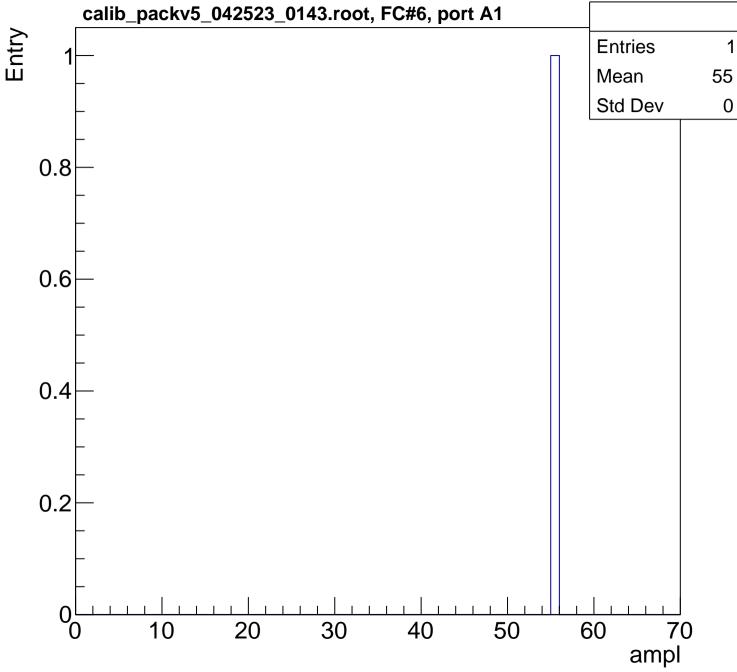








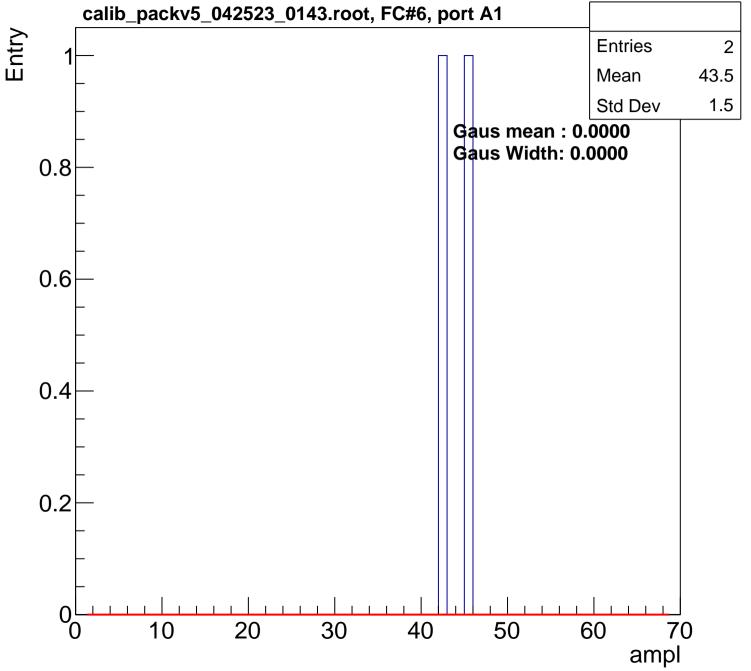


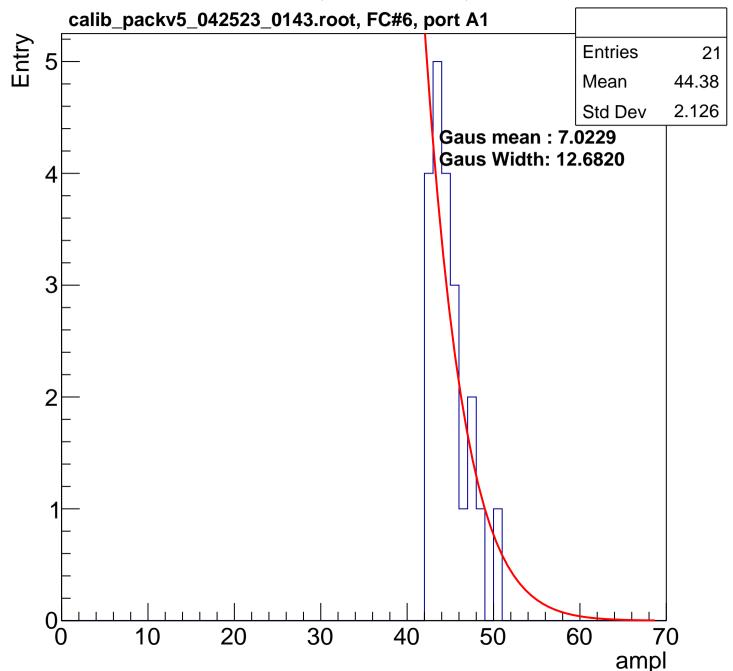


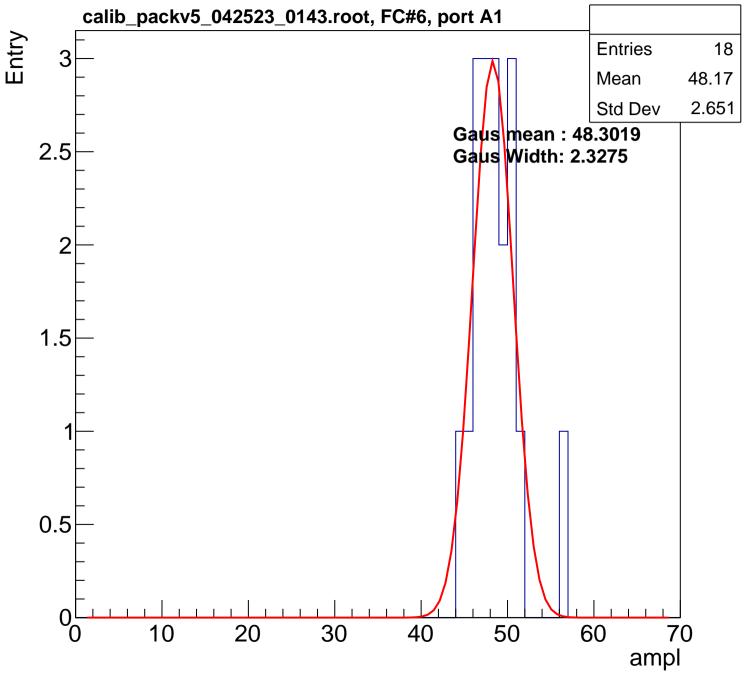


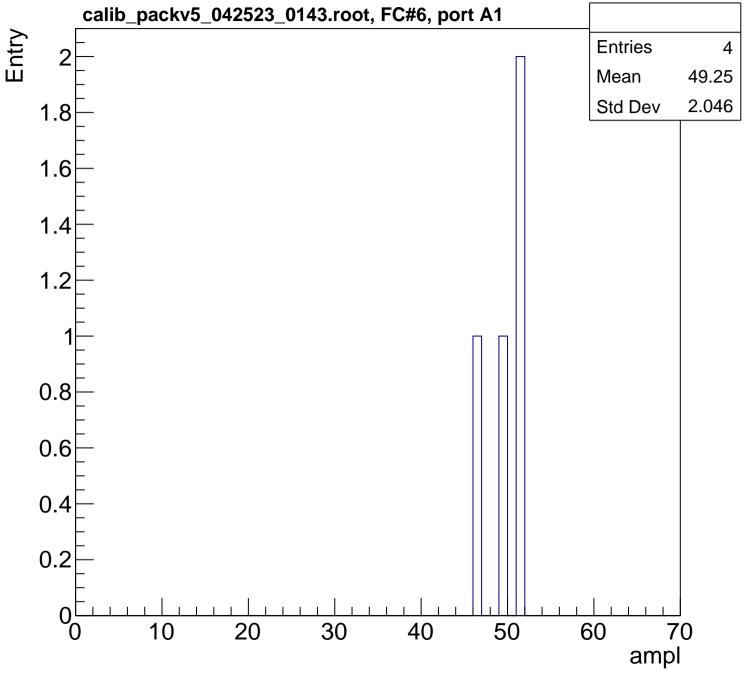










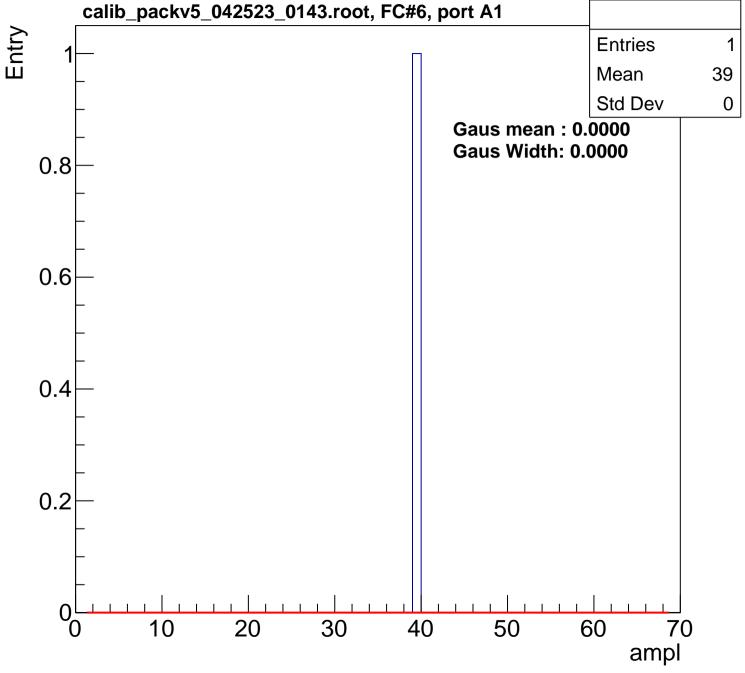


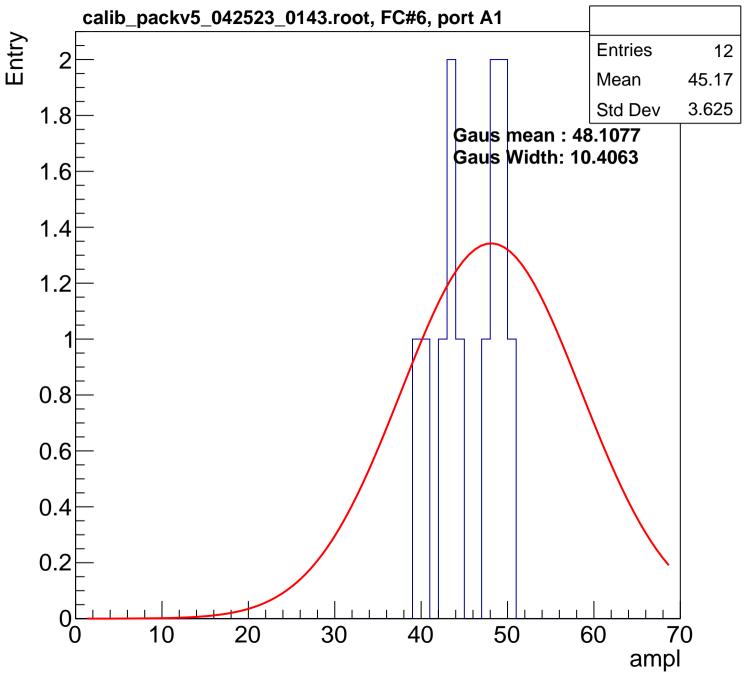


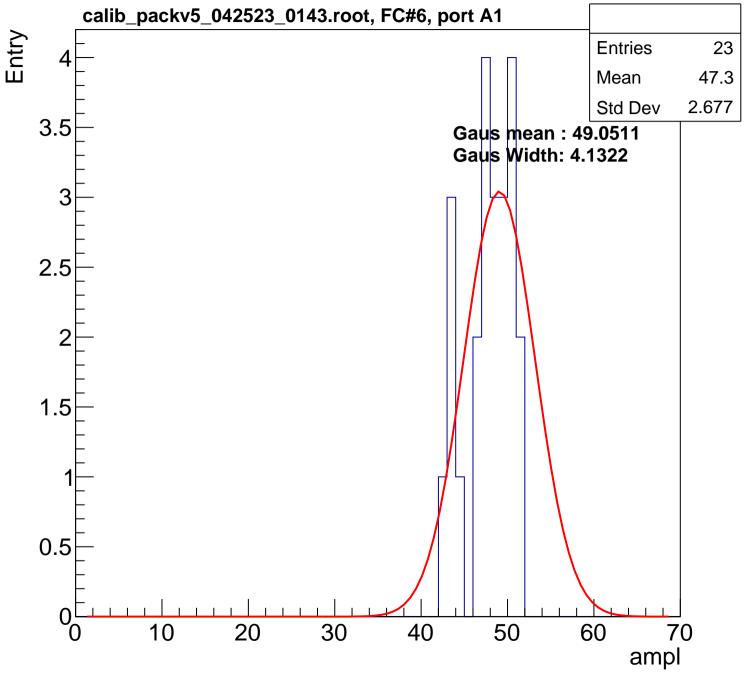


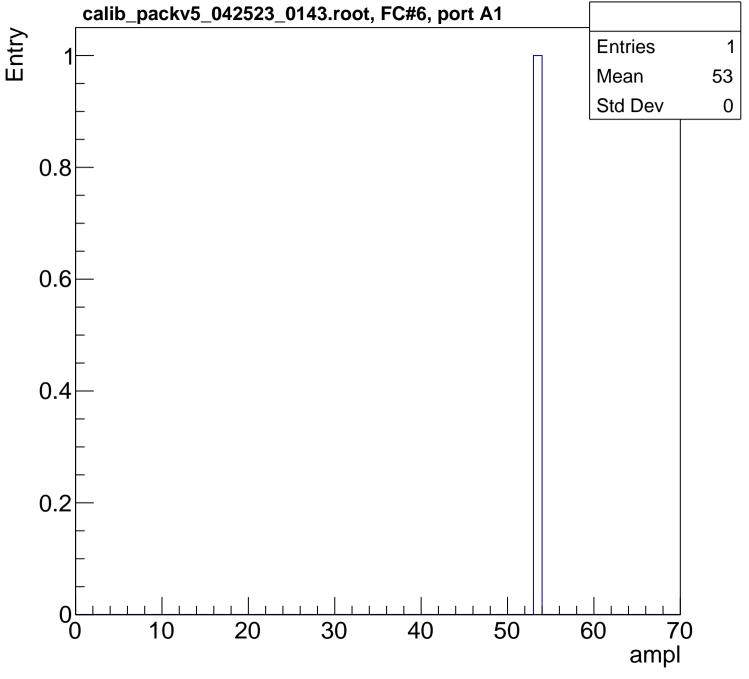












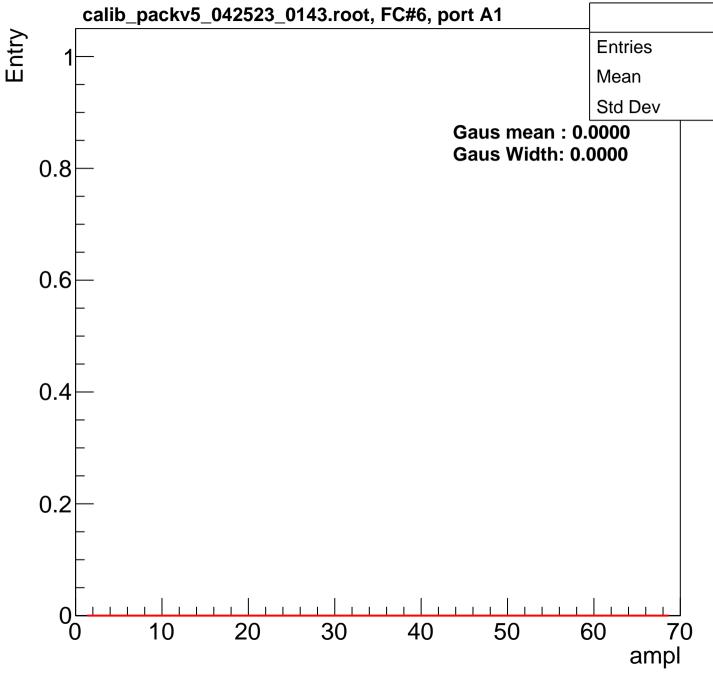














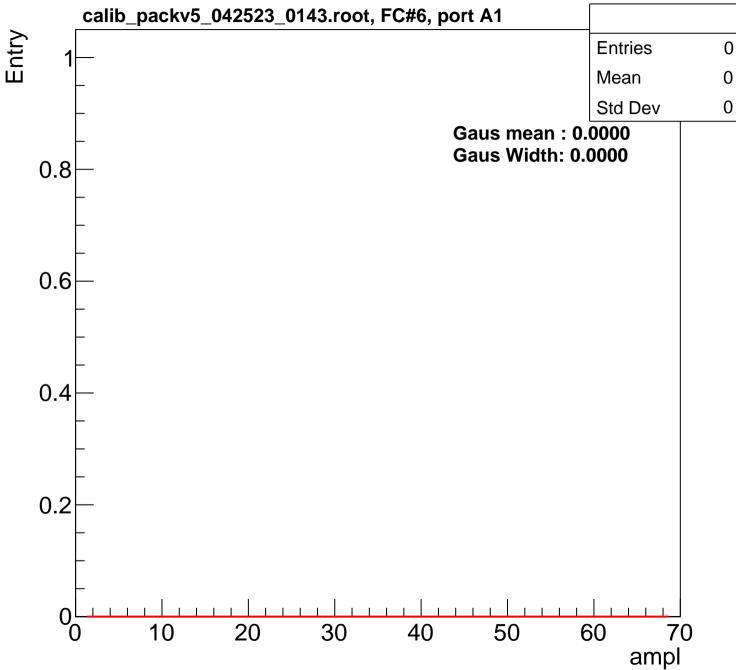


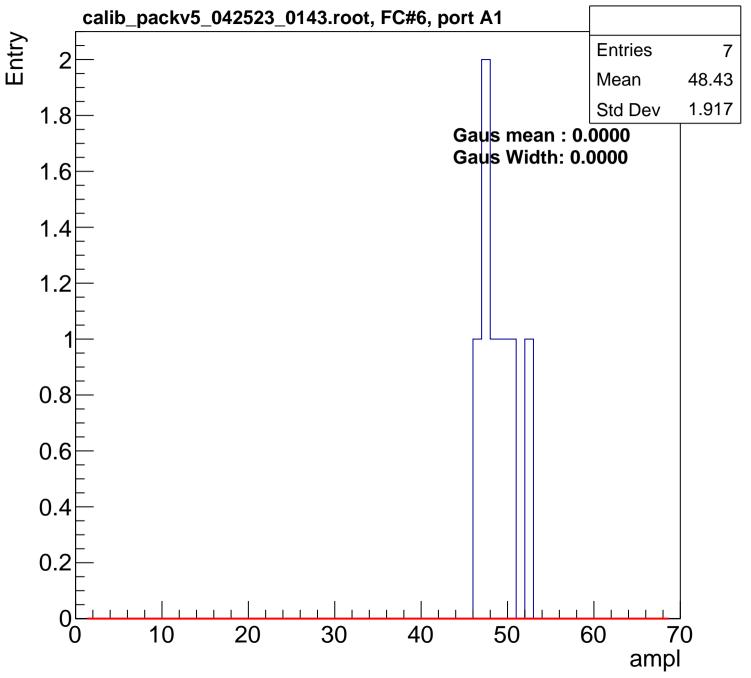


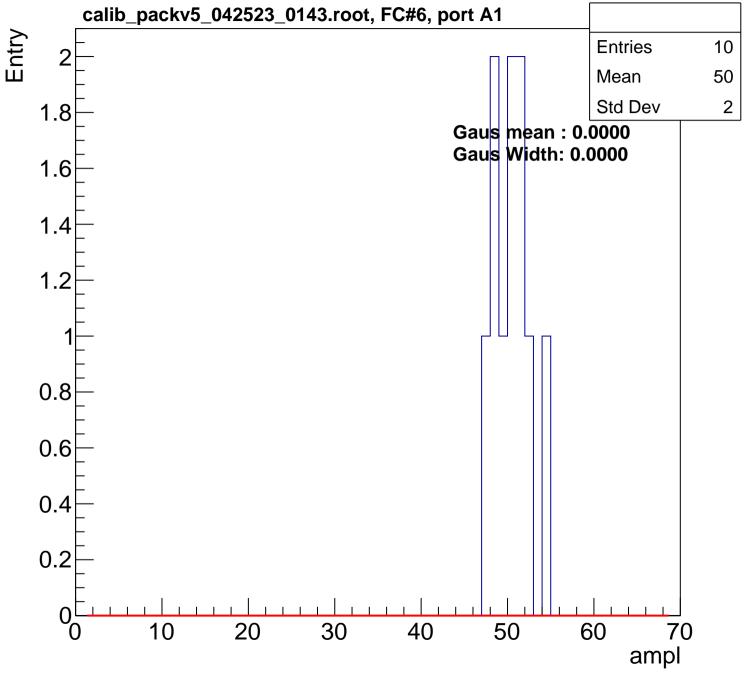


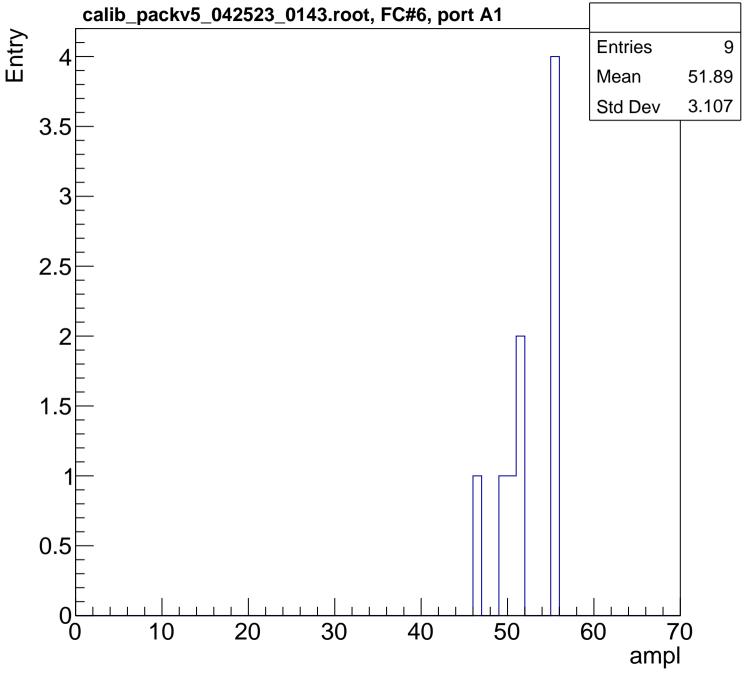












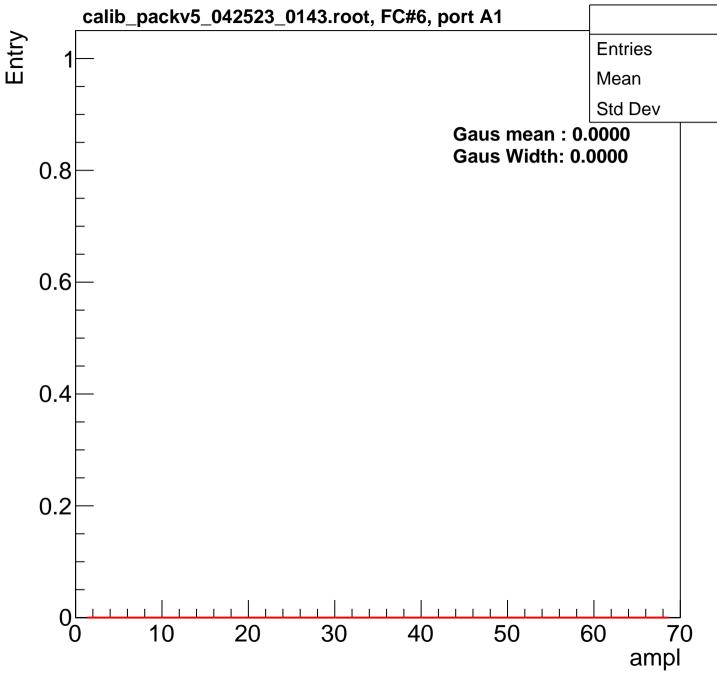














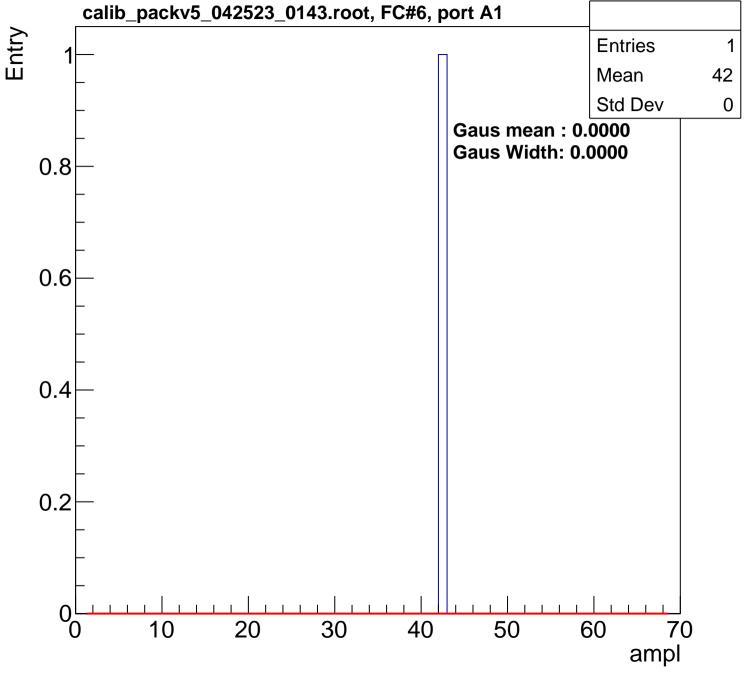


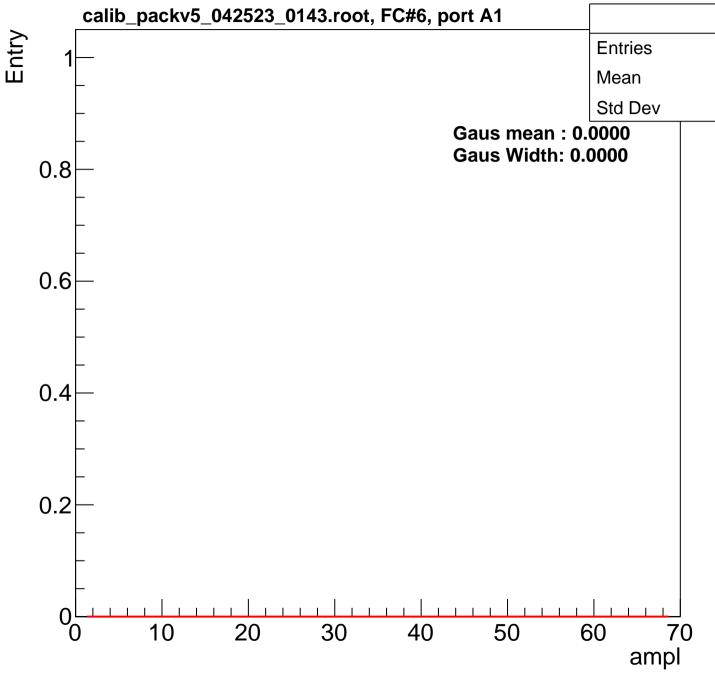














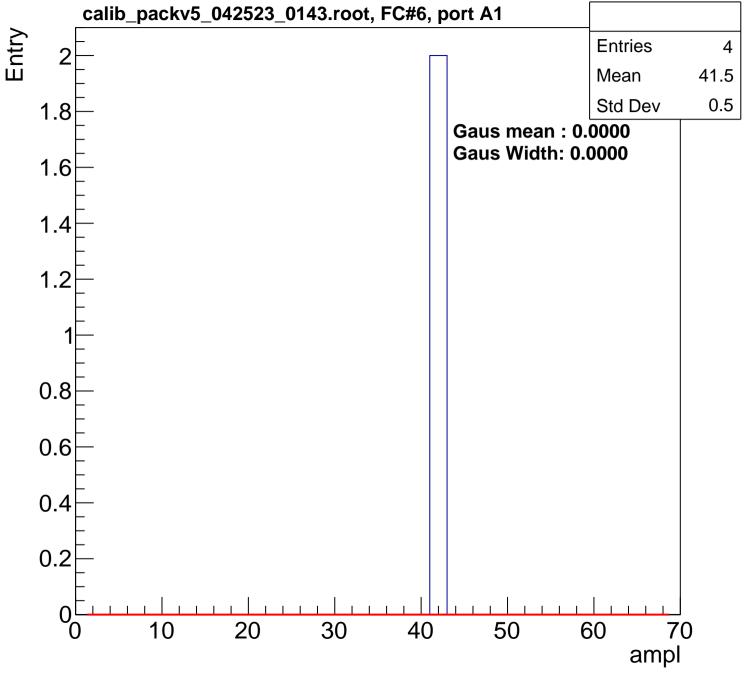


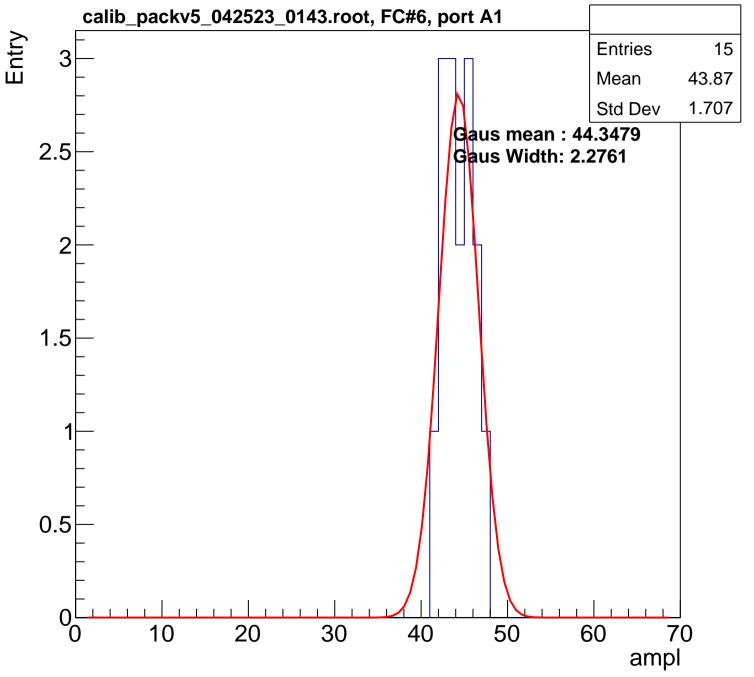


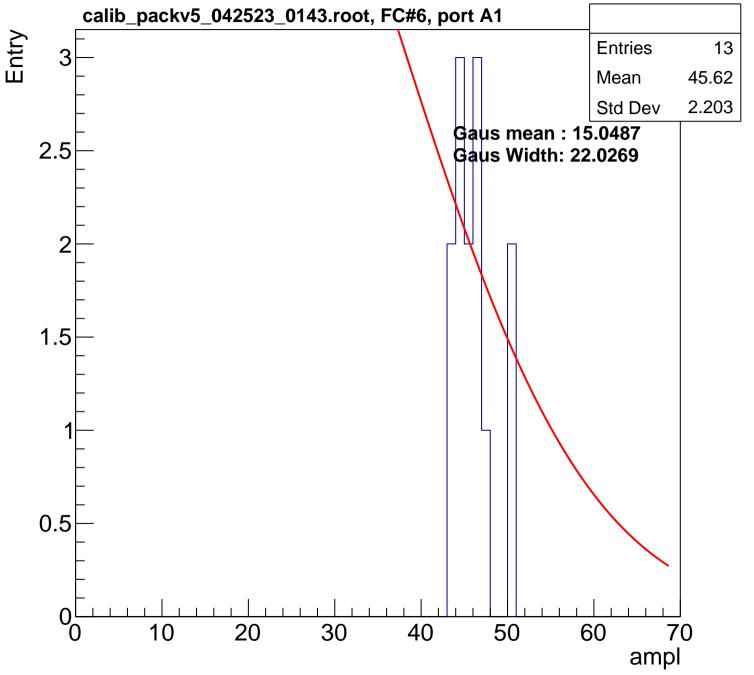


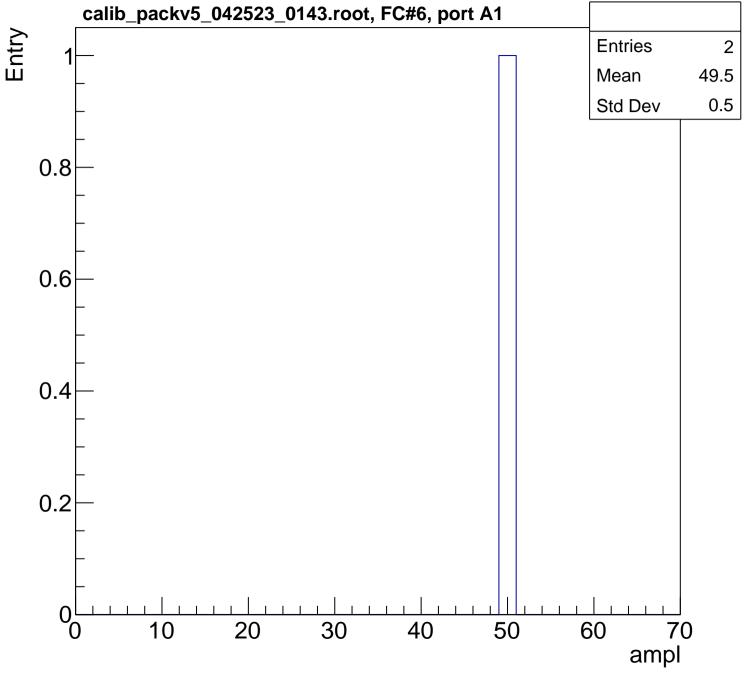


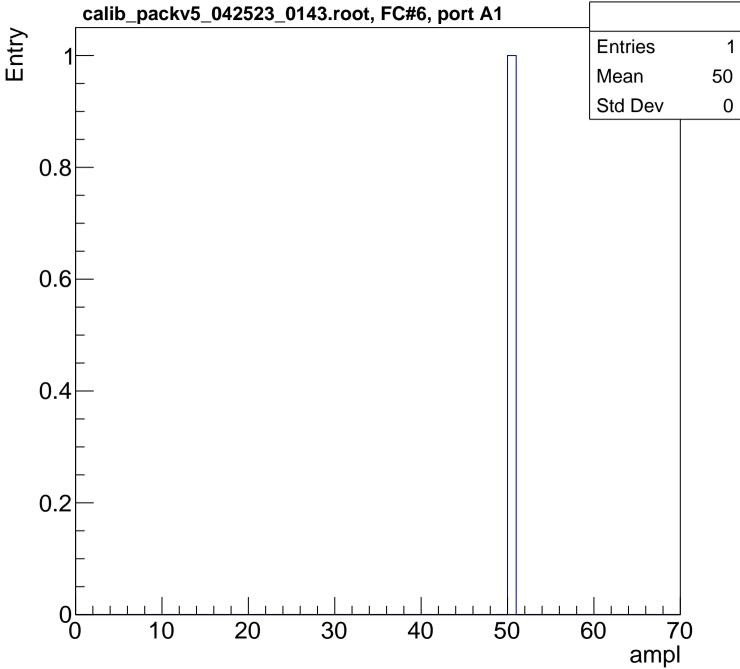








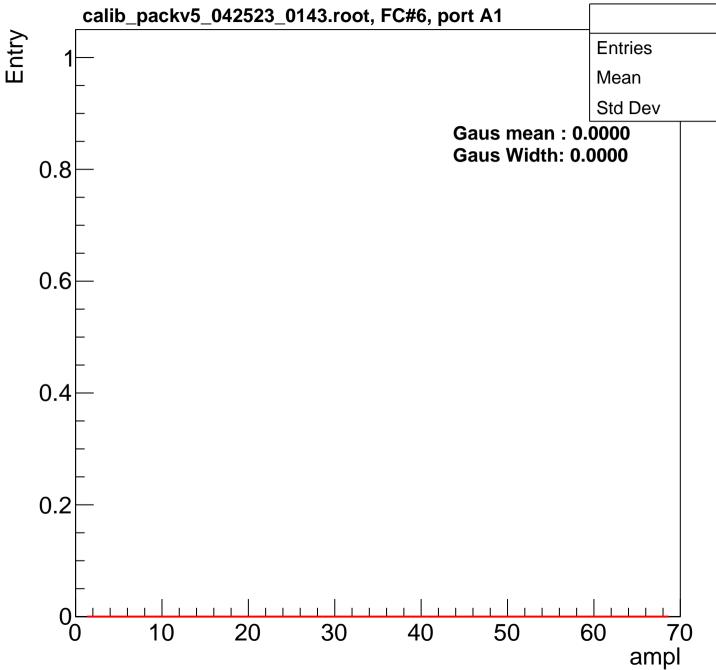


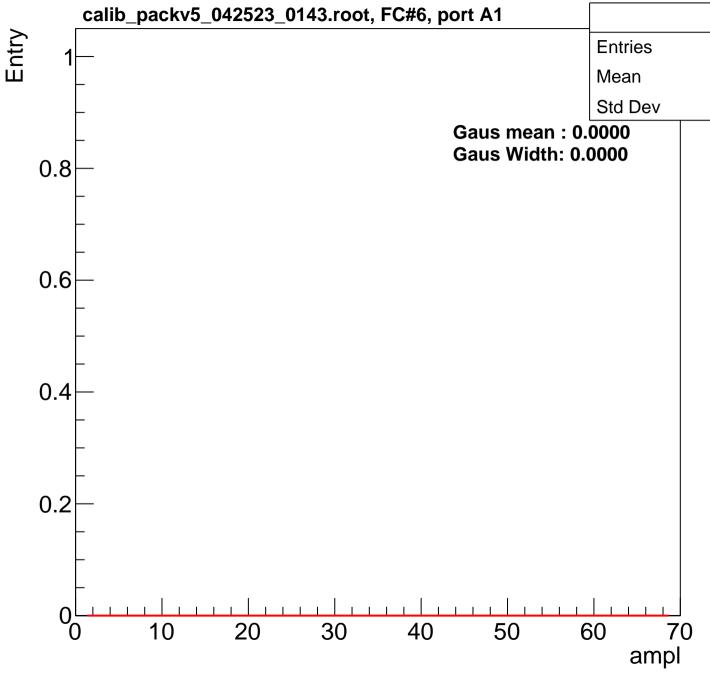
















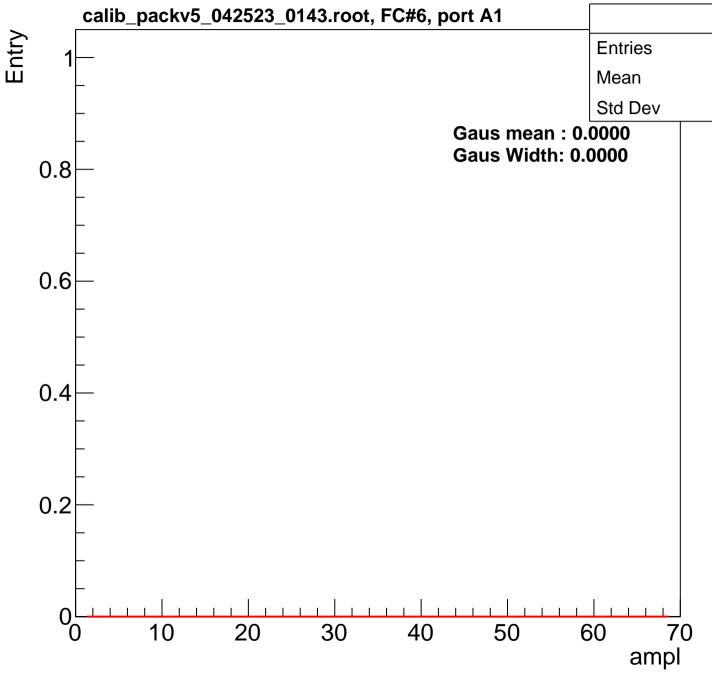


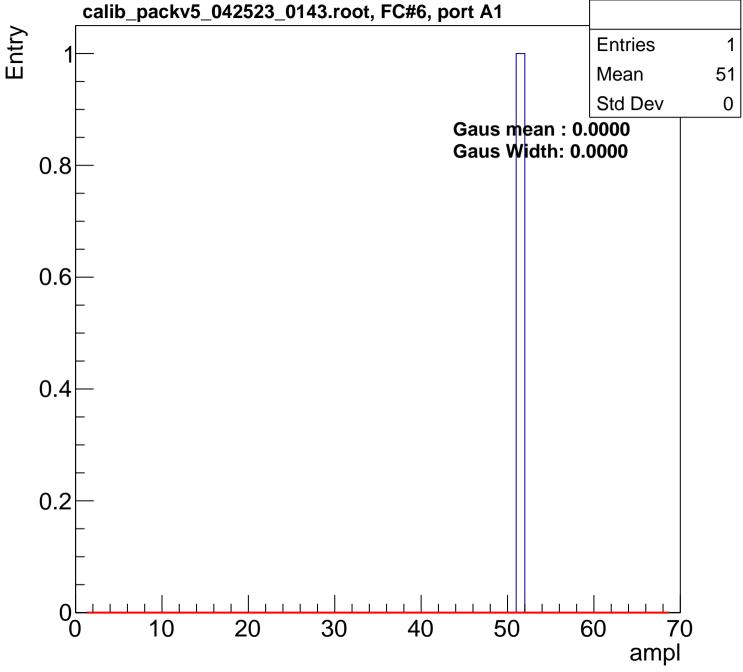












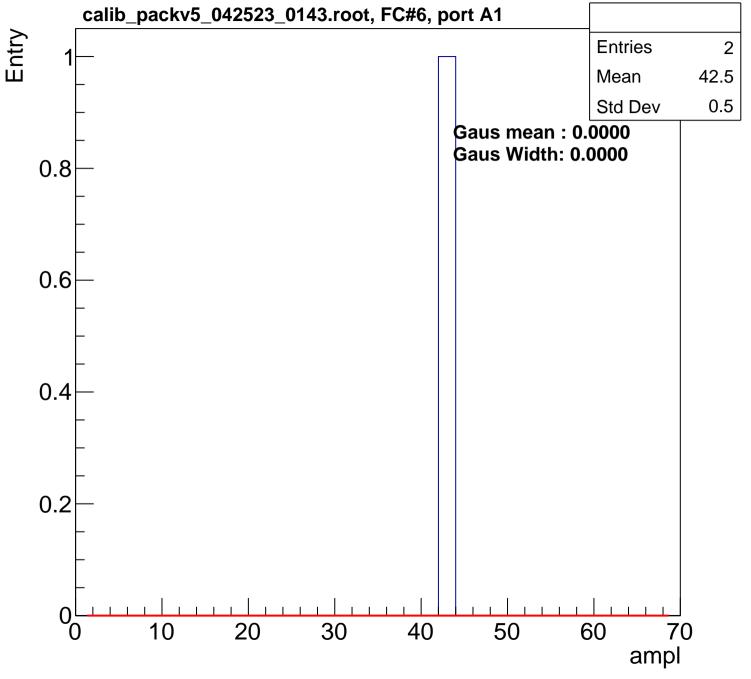


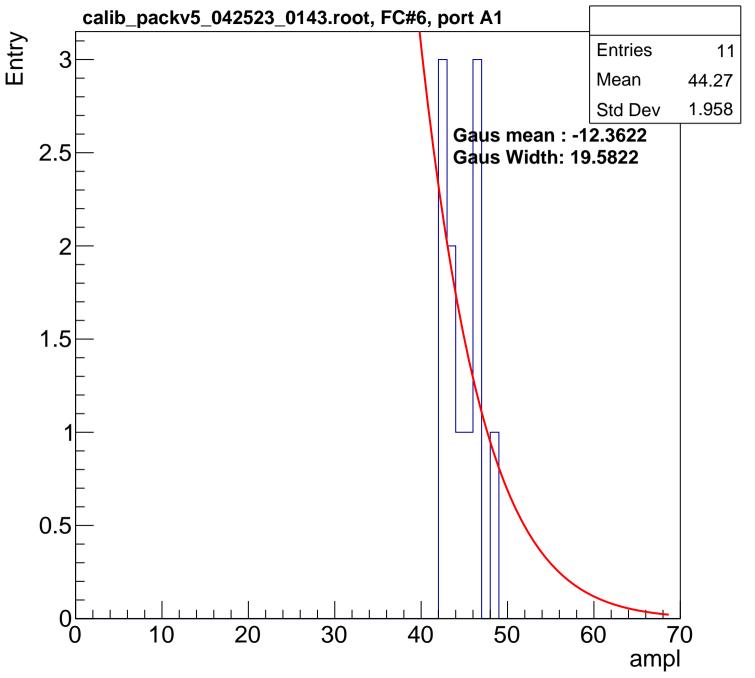


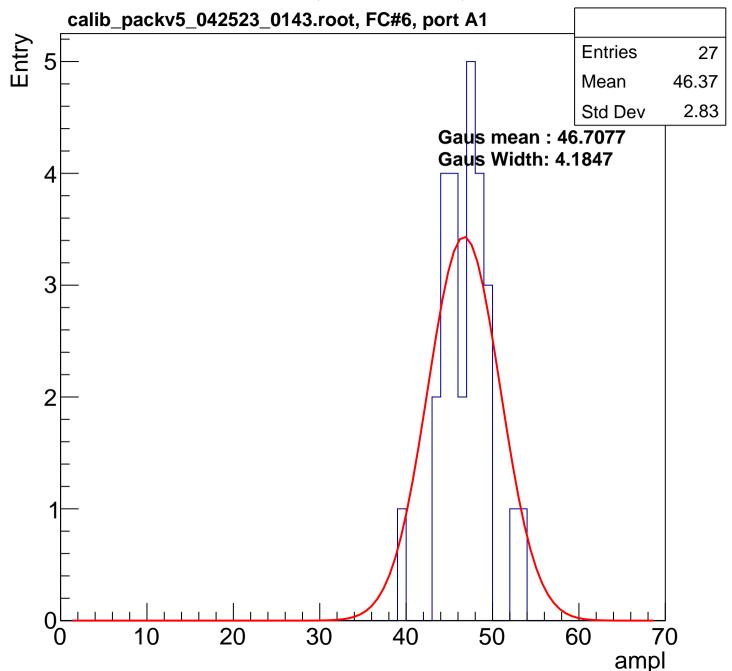


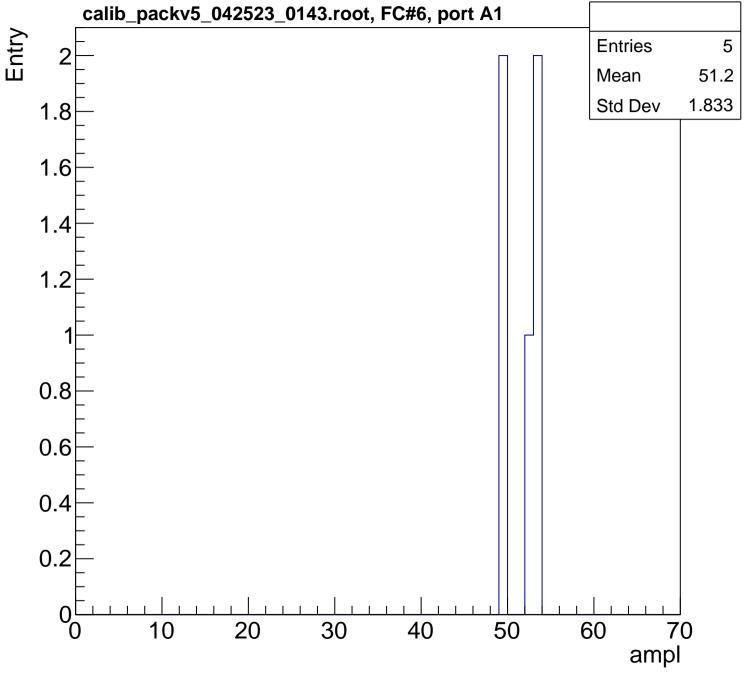










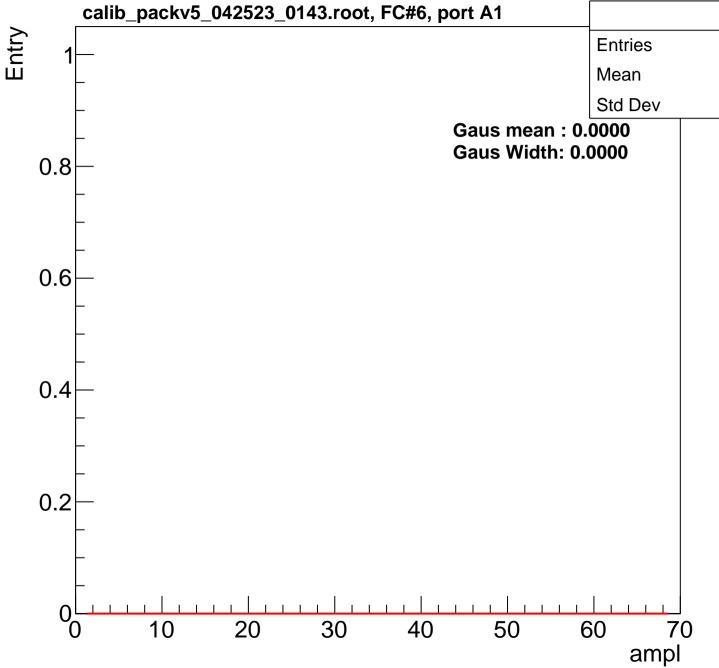




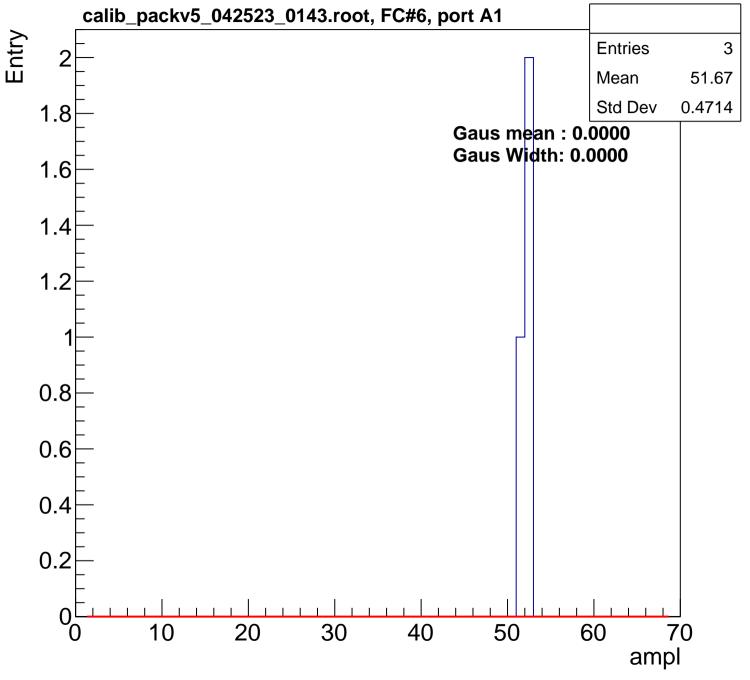


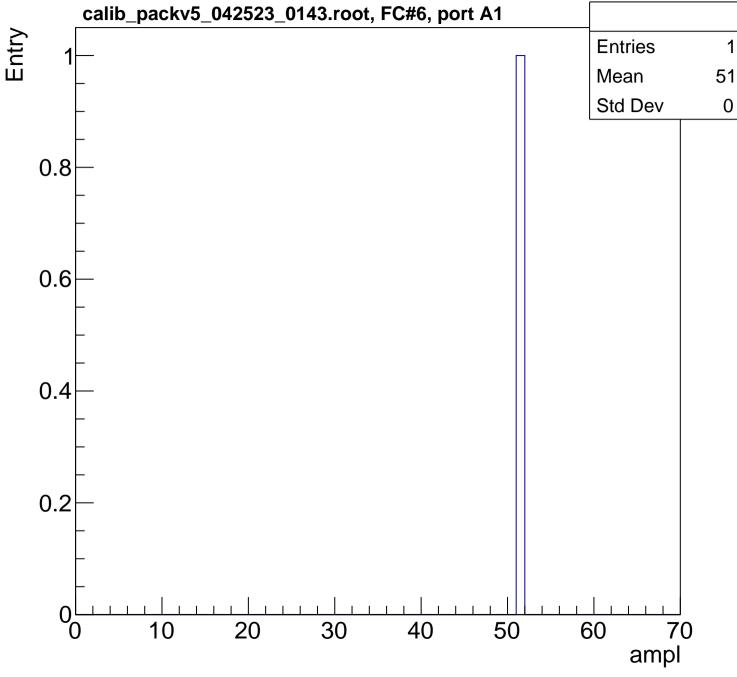




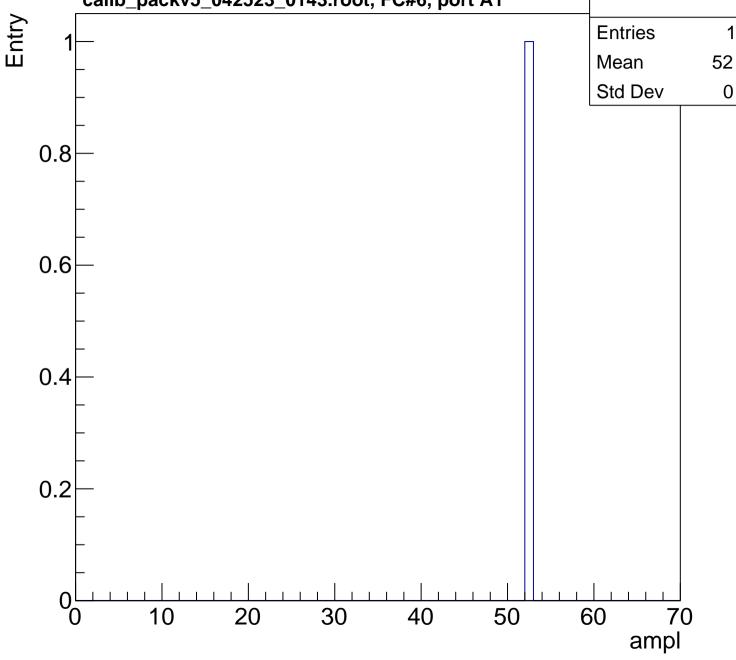








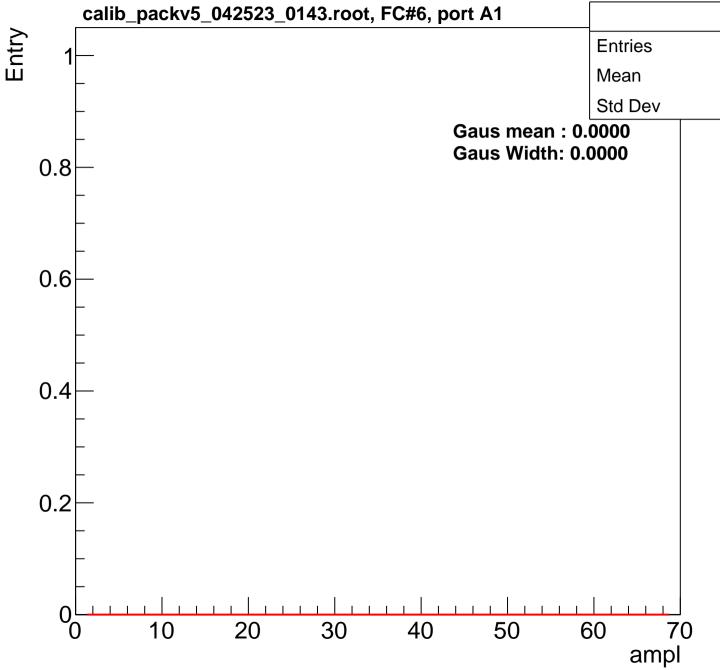
B0L100S, U3-ch73, adc4 calib_packv5_042523_0143.root, FC#6, port A1

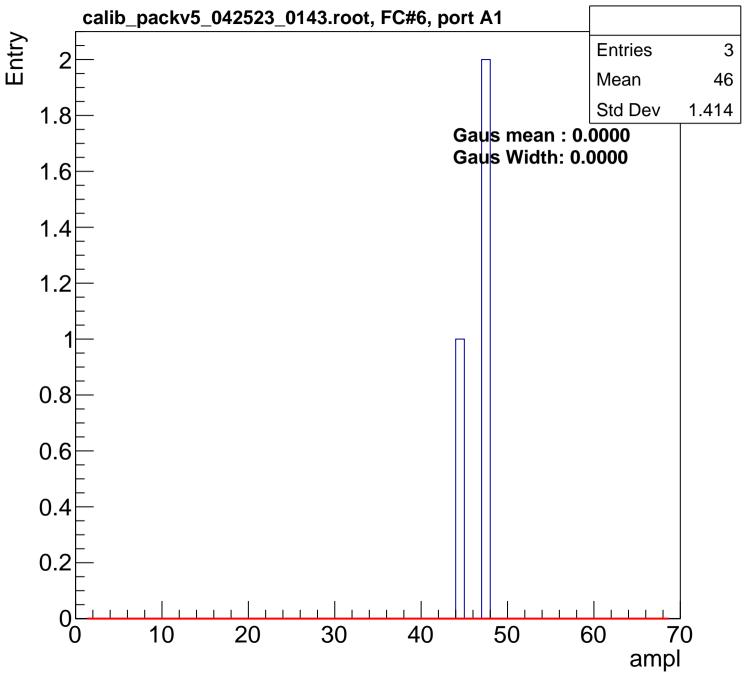


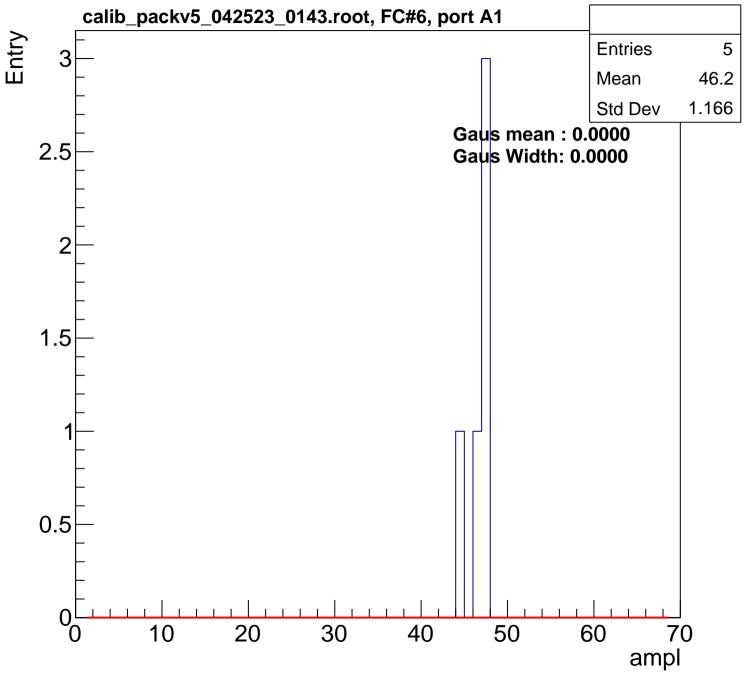












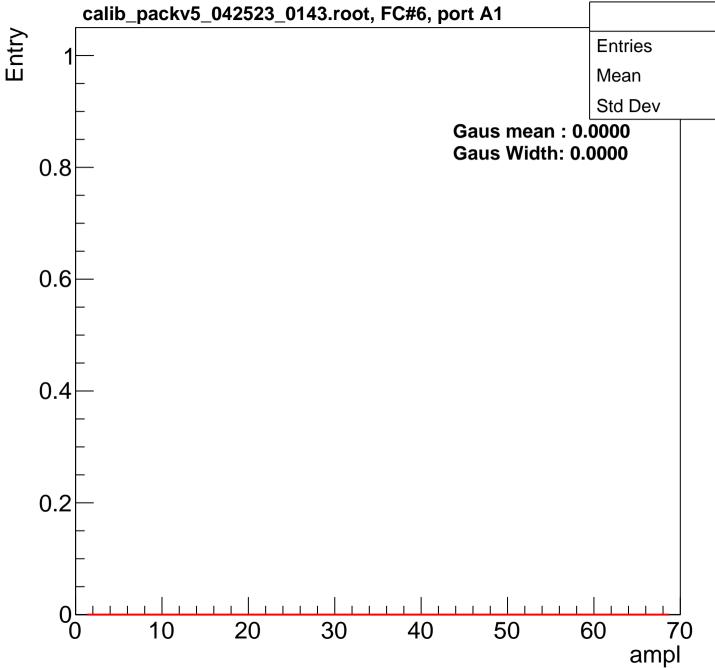
















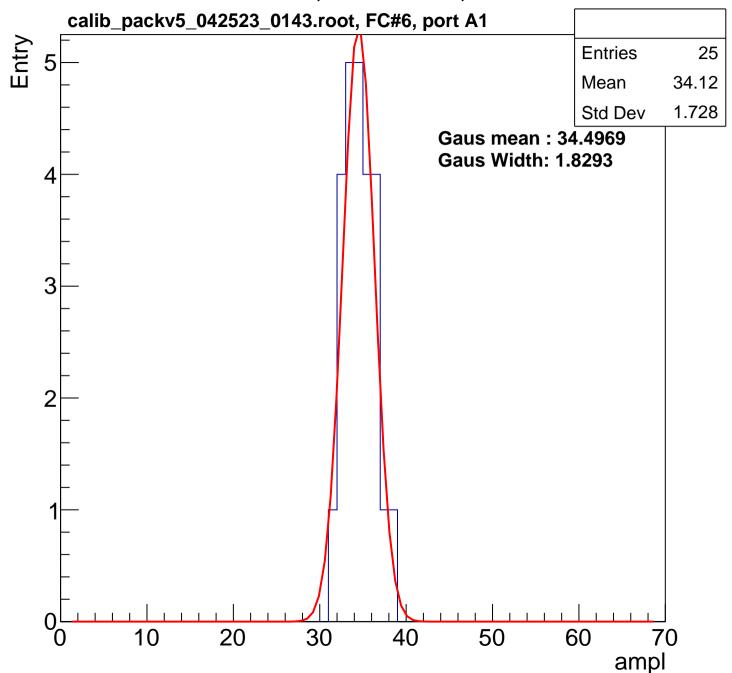


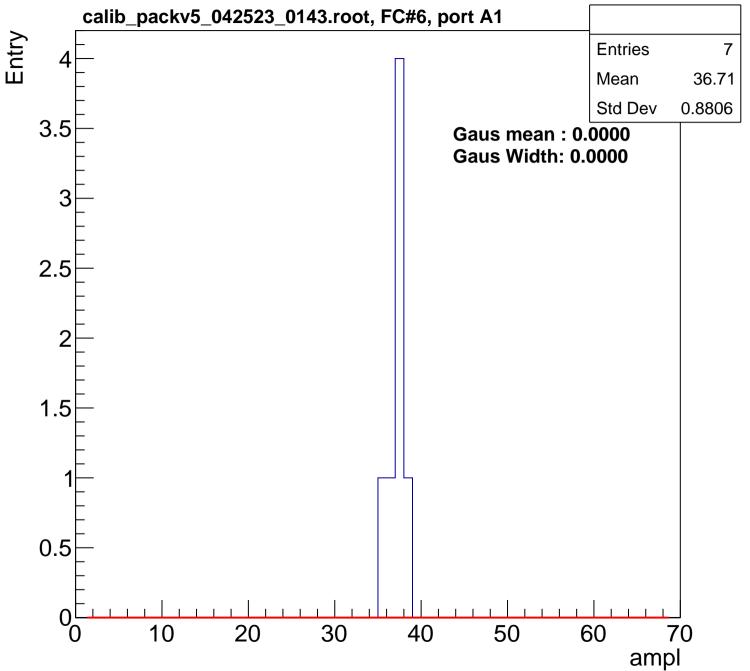
















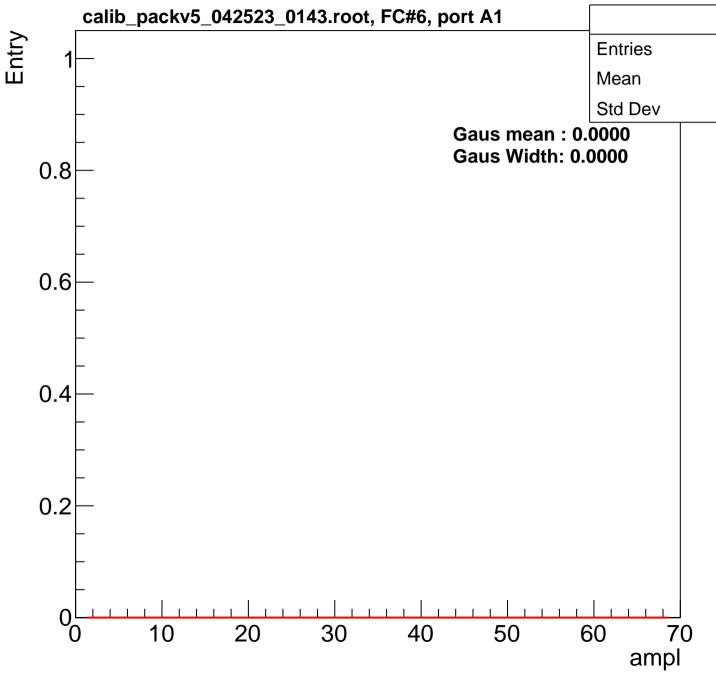














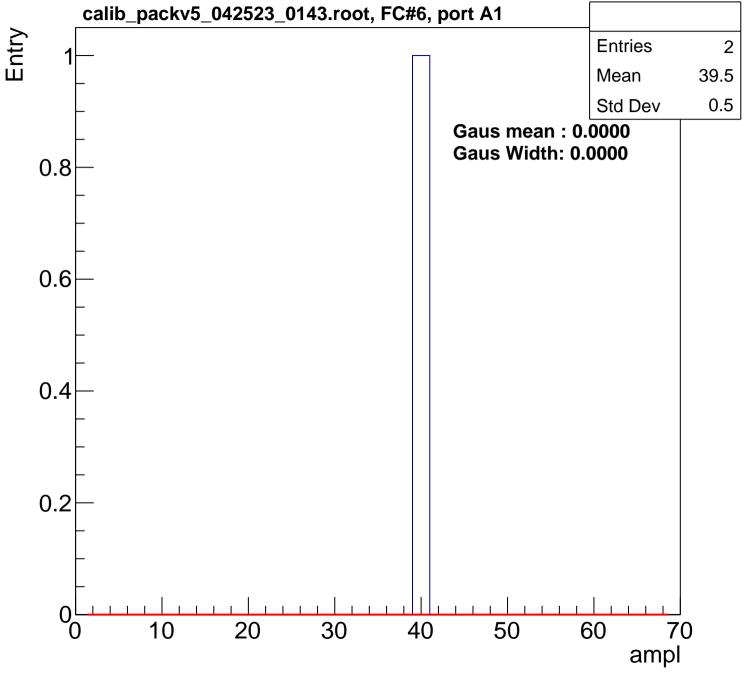


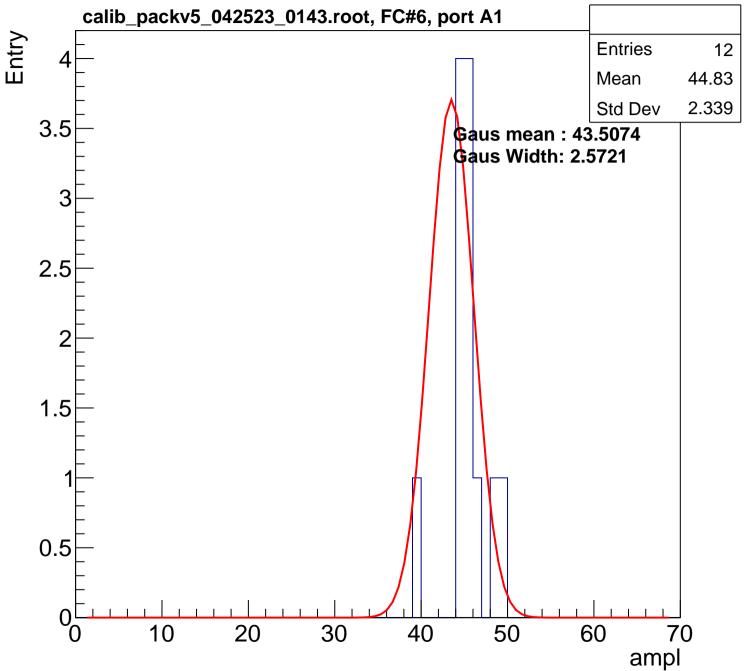


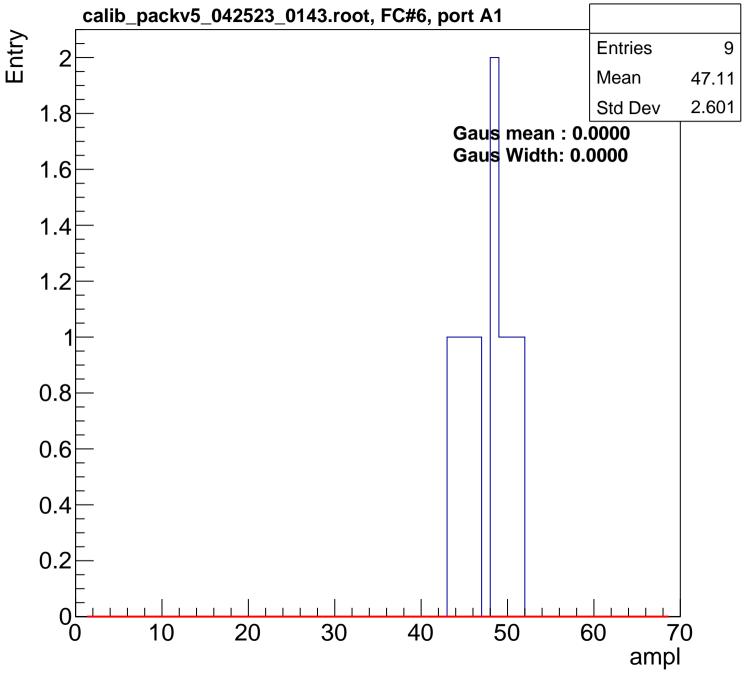


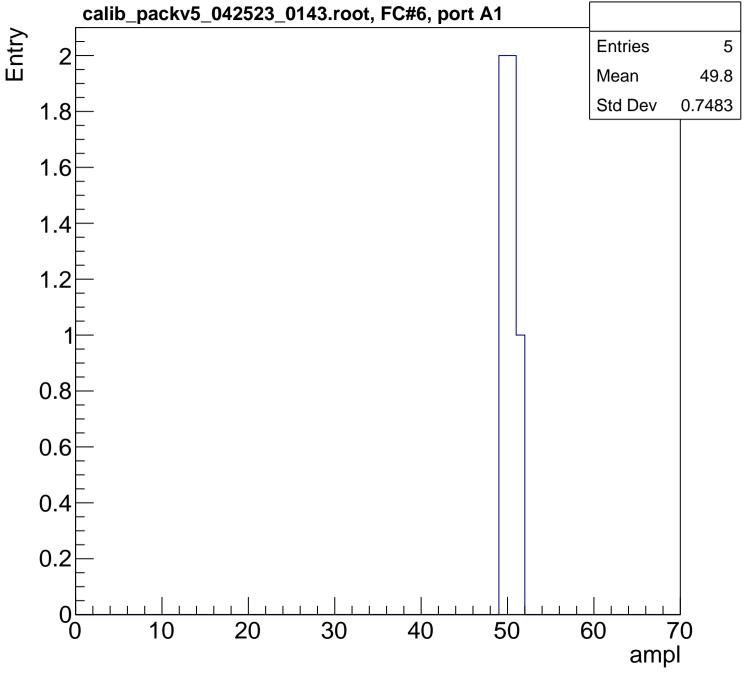










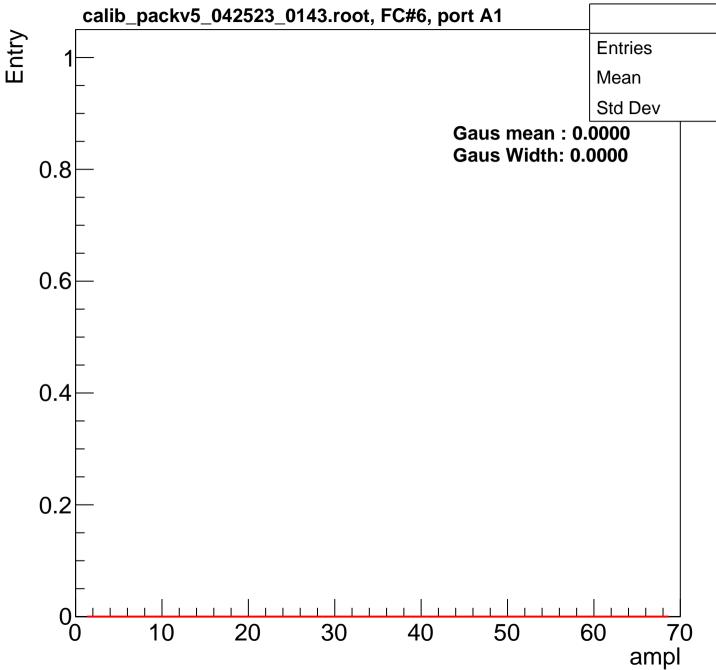


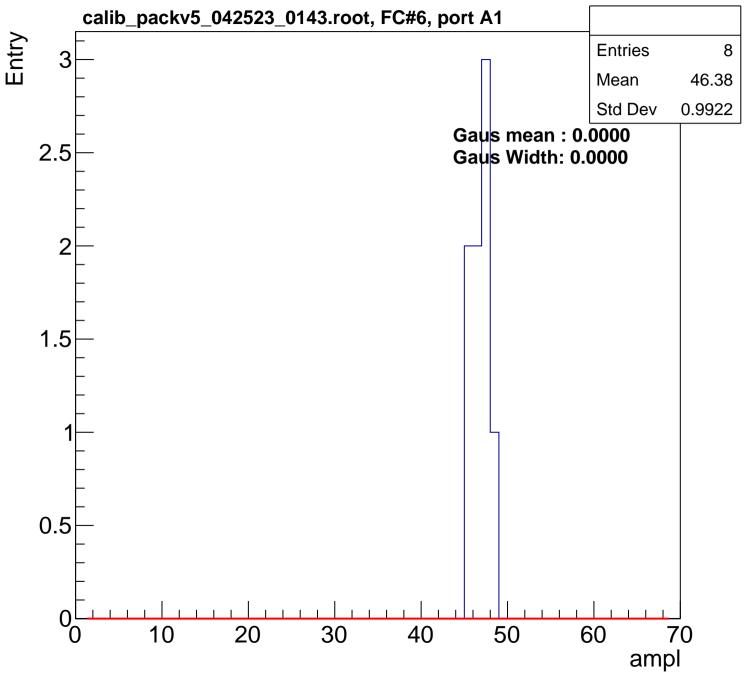


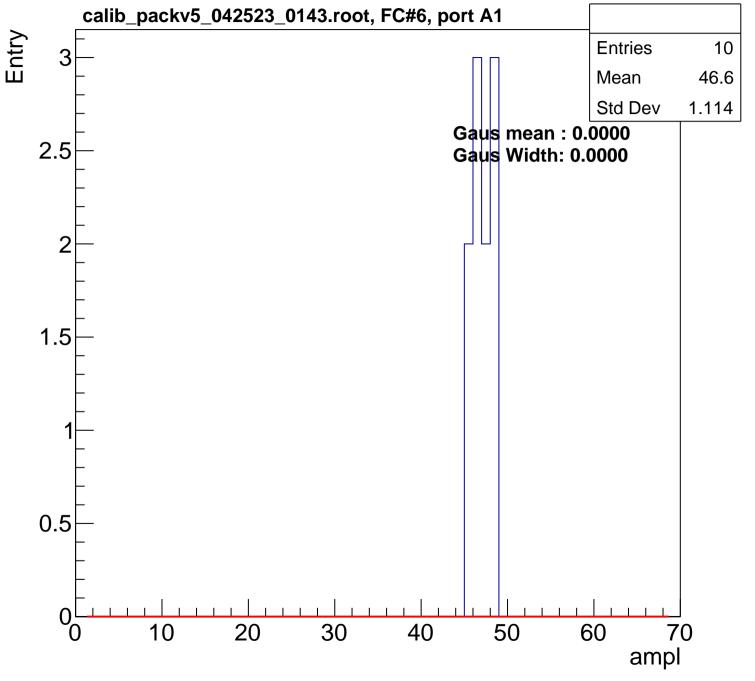


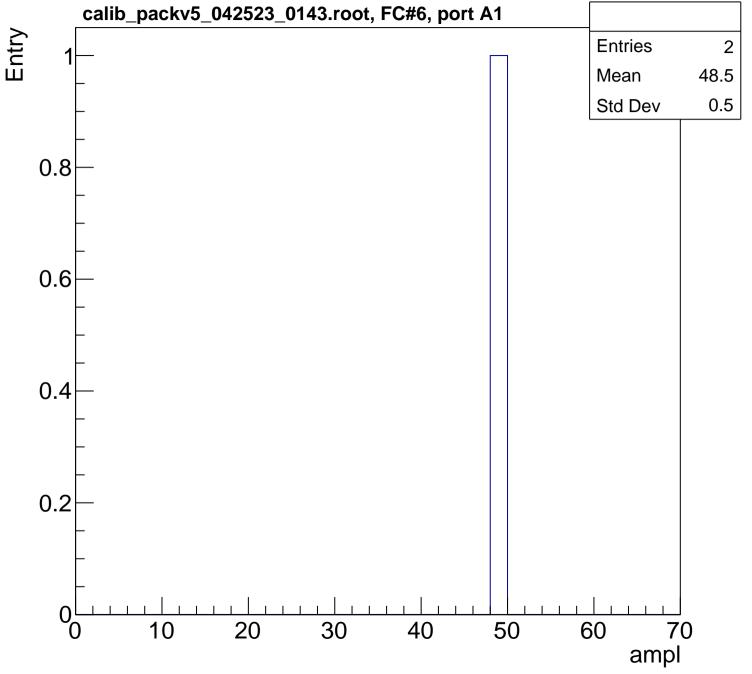














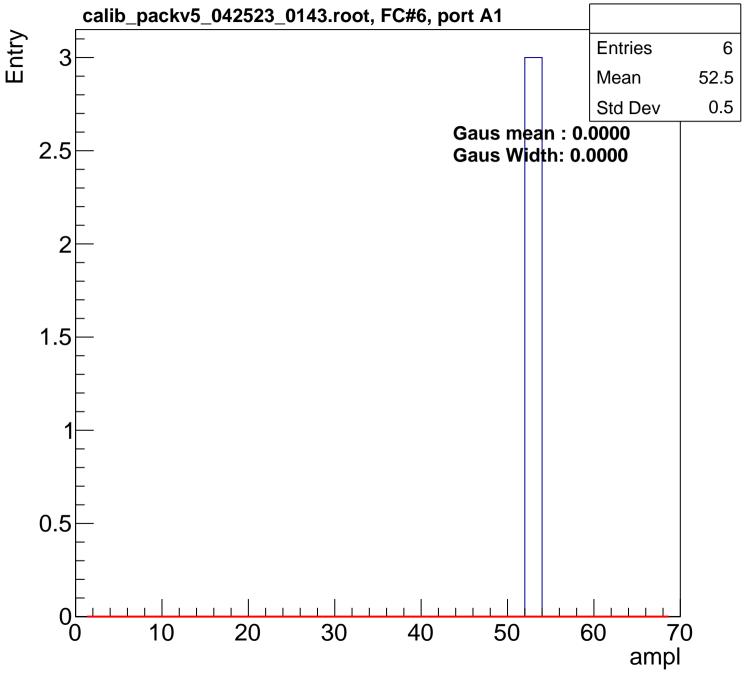


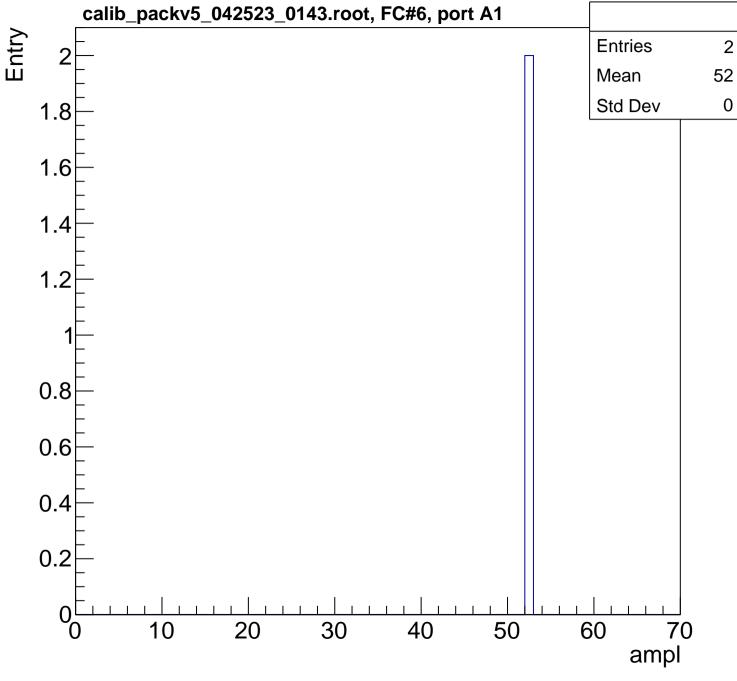












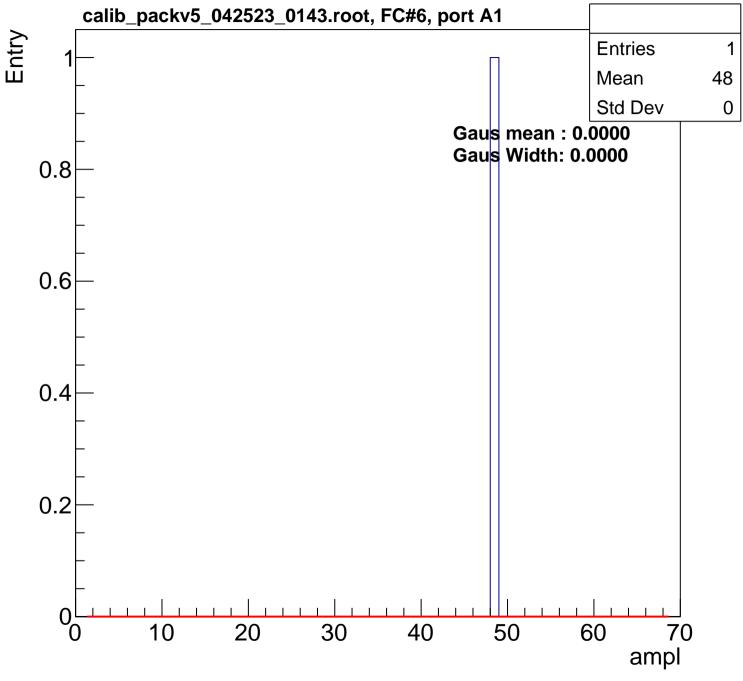


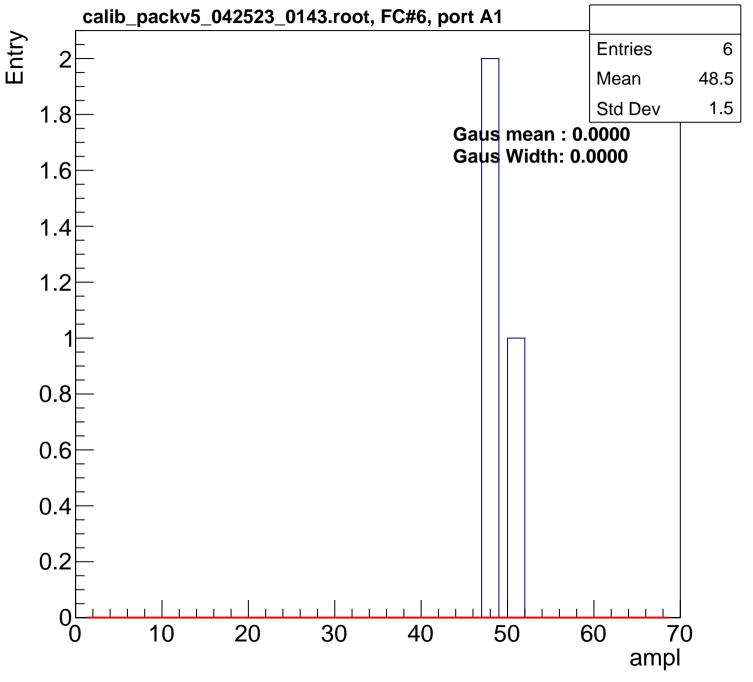








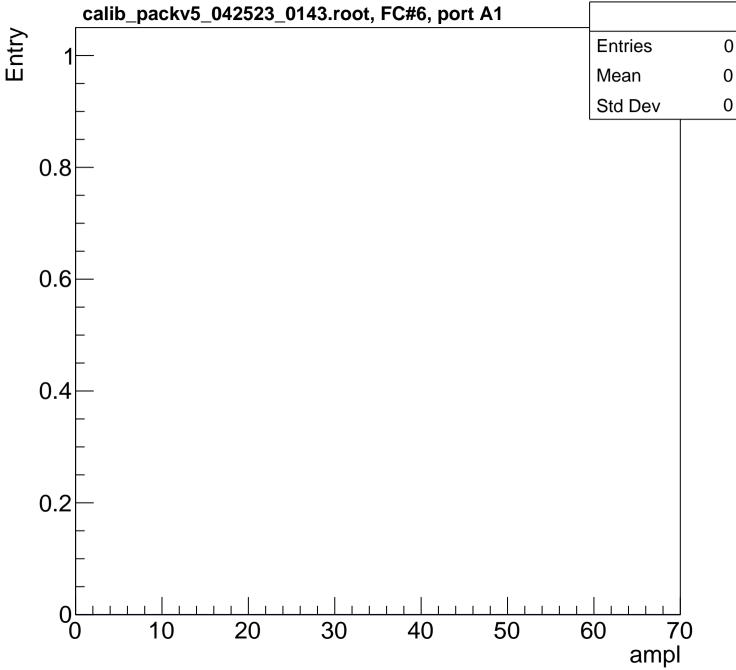




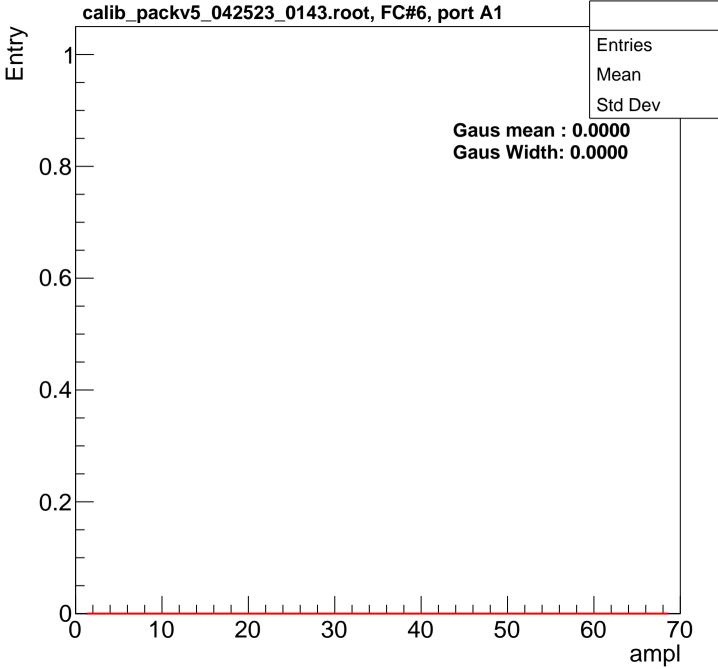


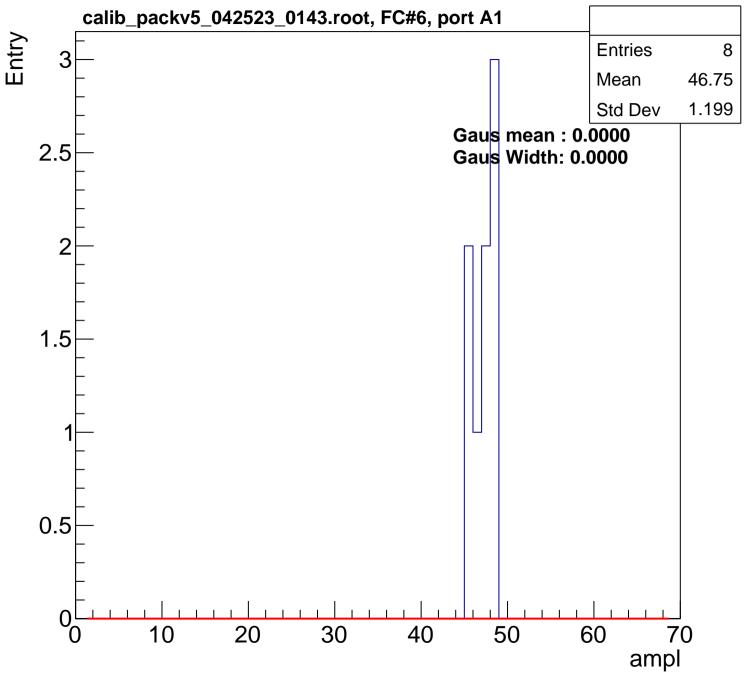


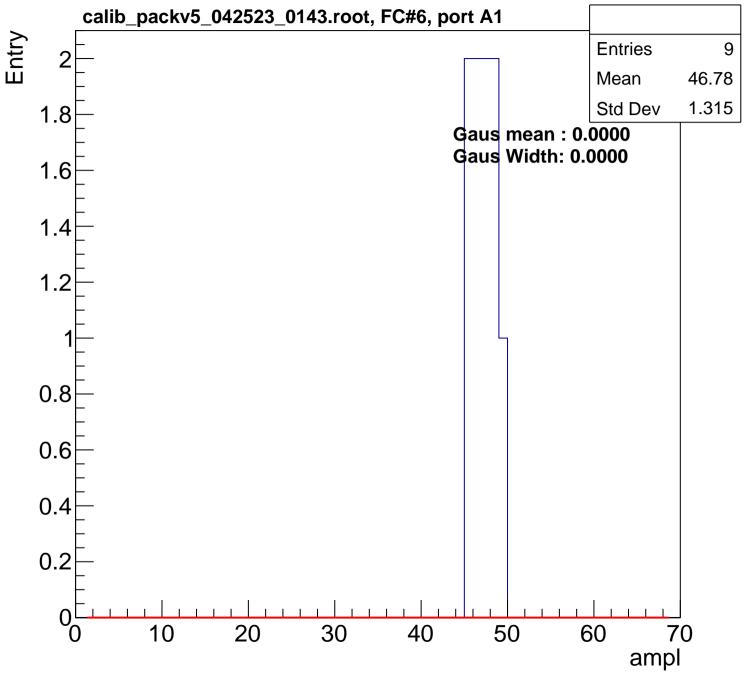


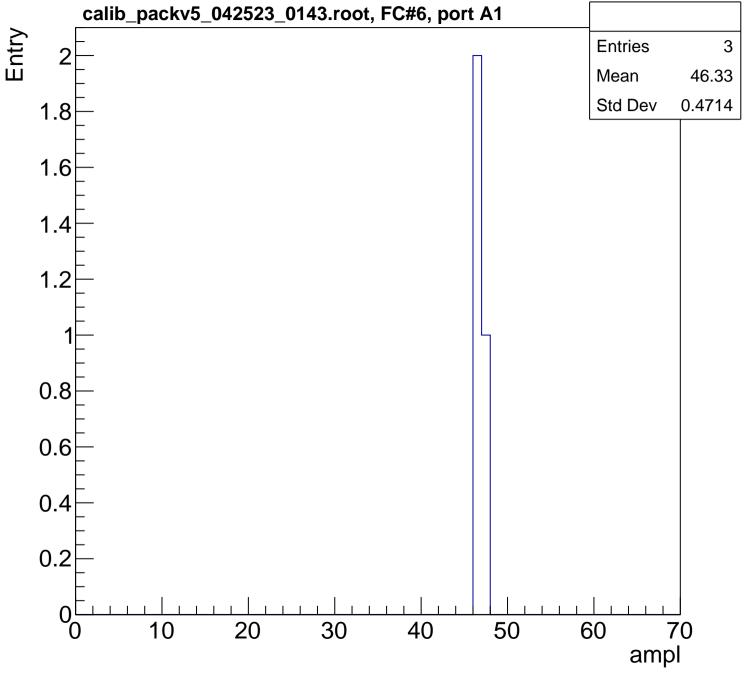










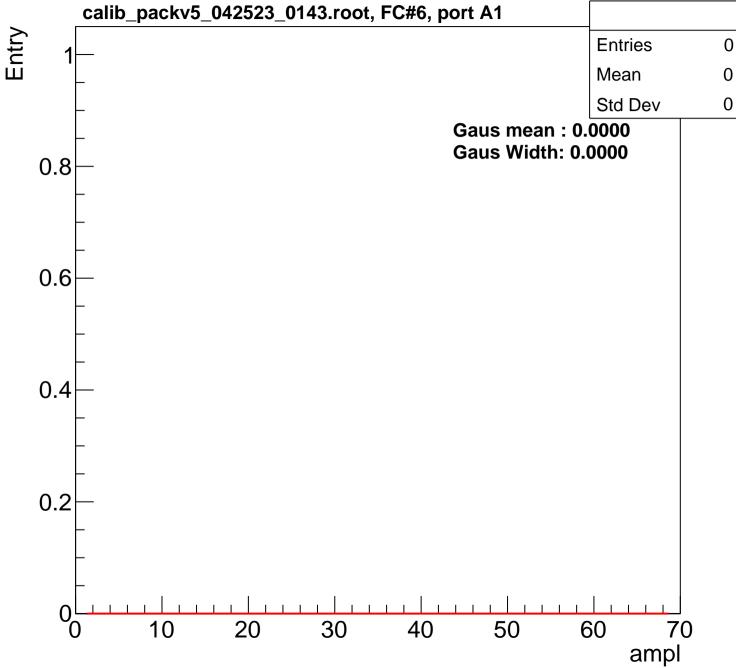
















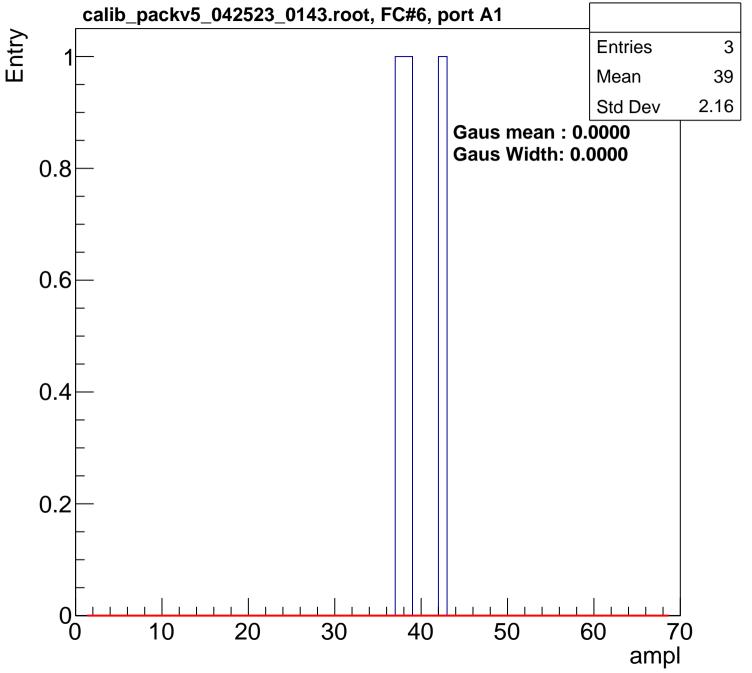


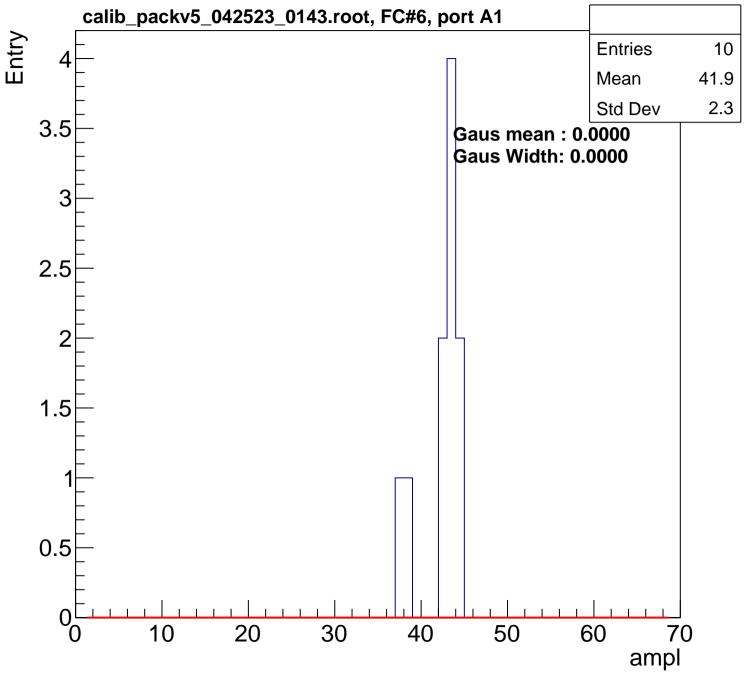


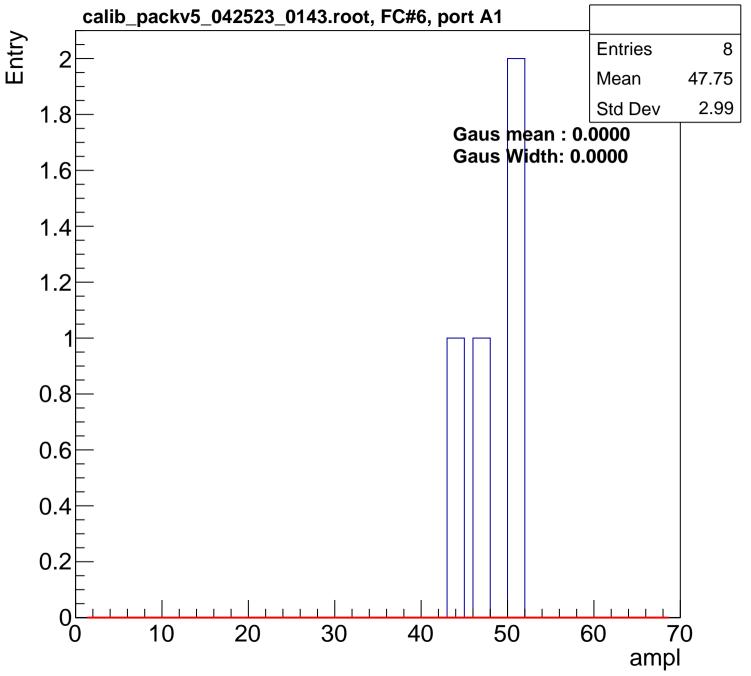


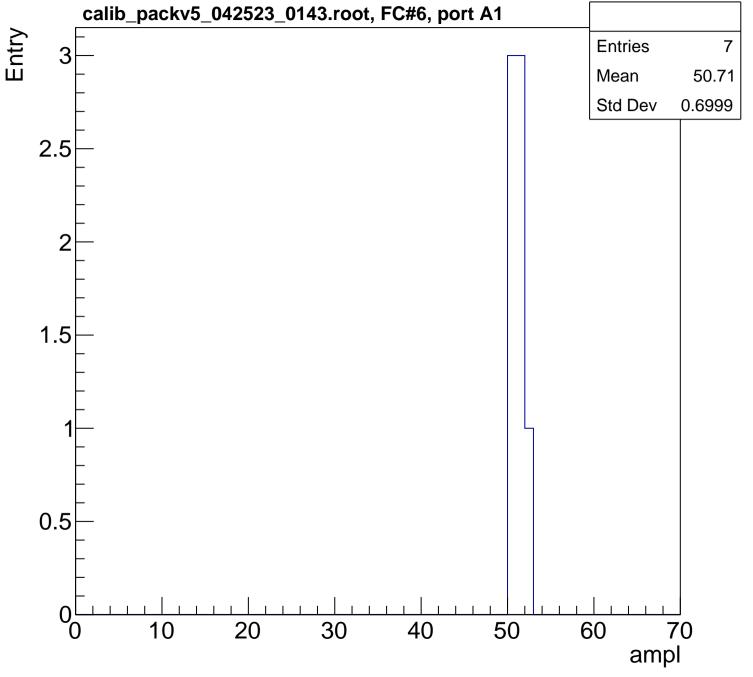




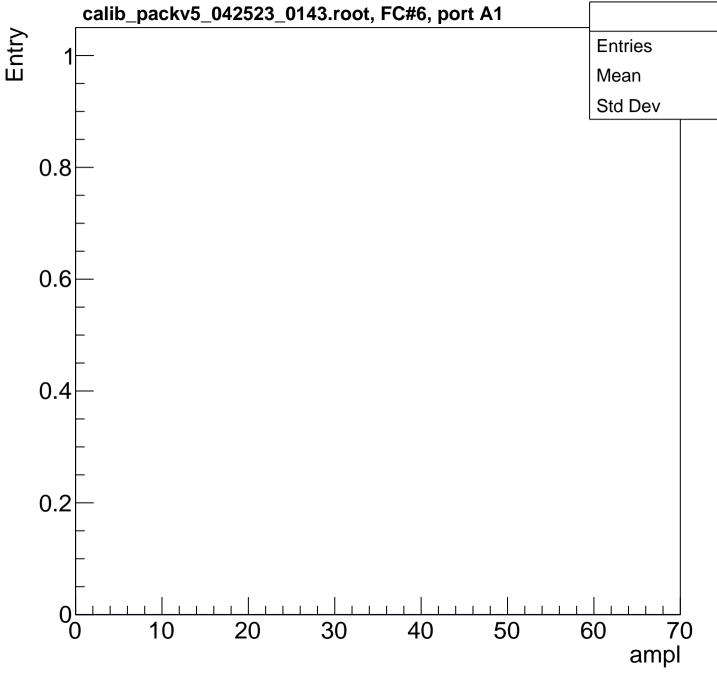






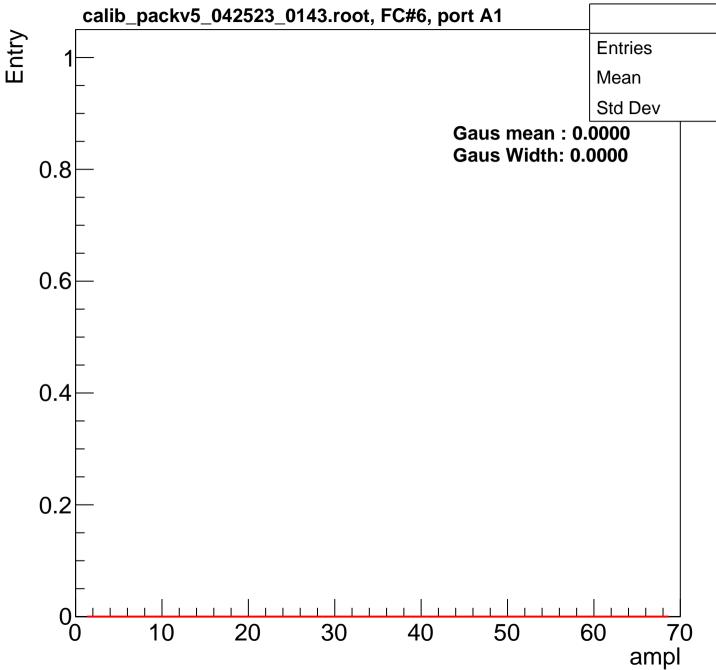


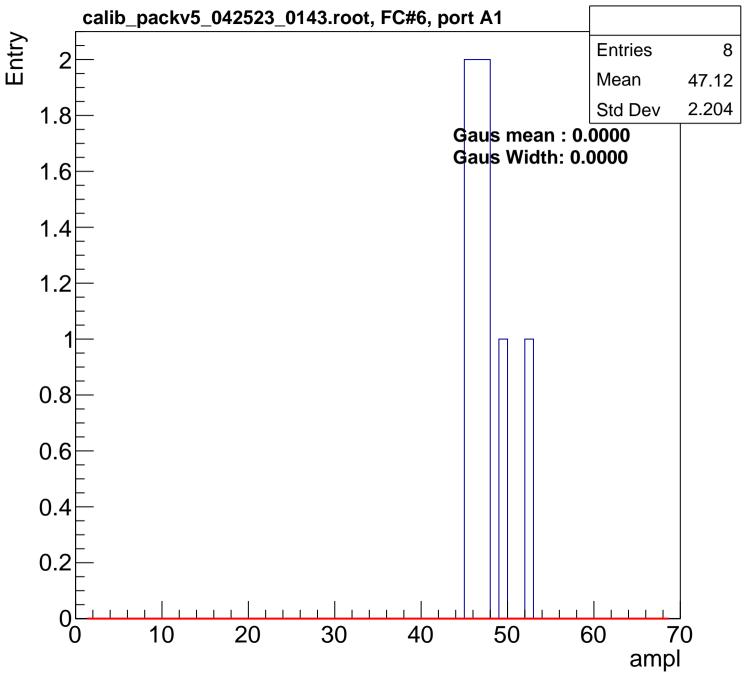


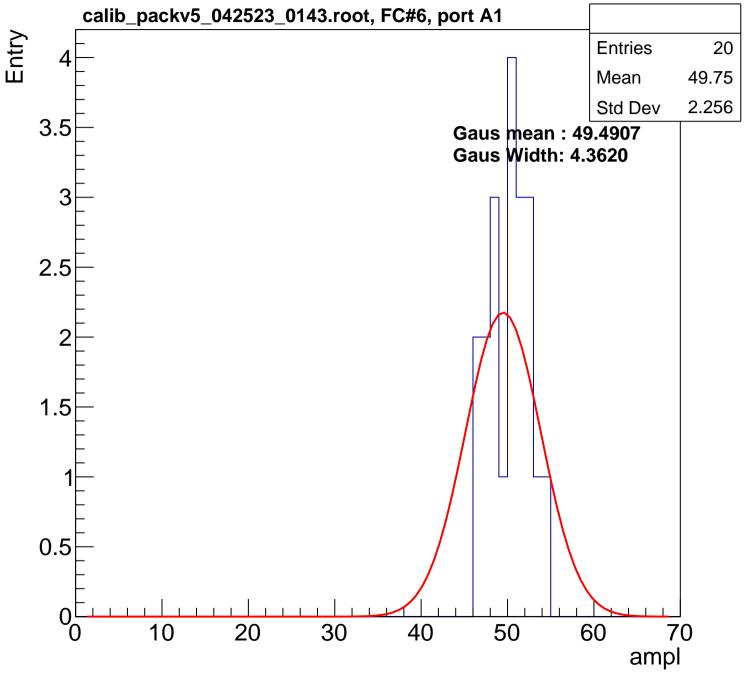


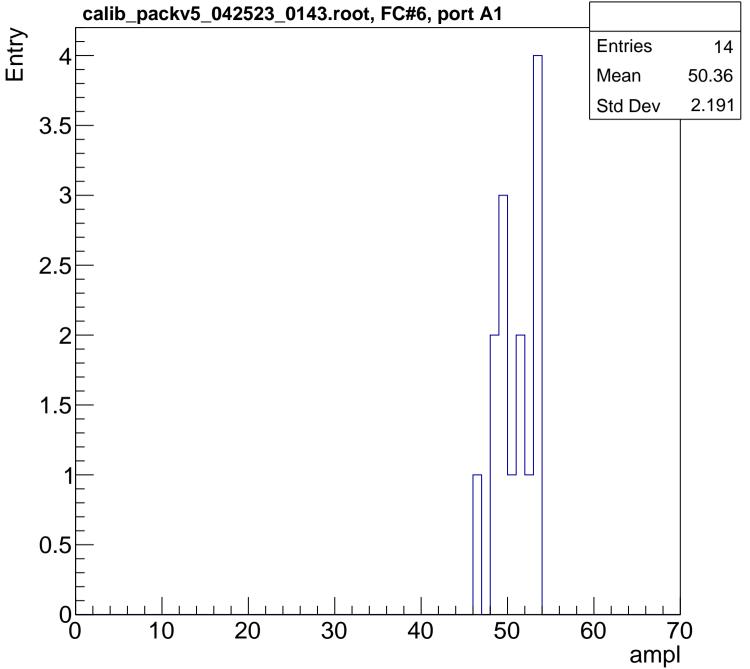










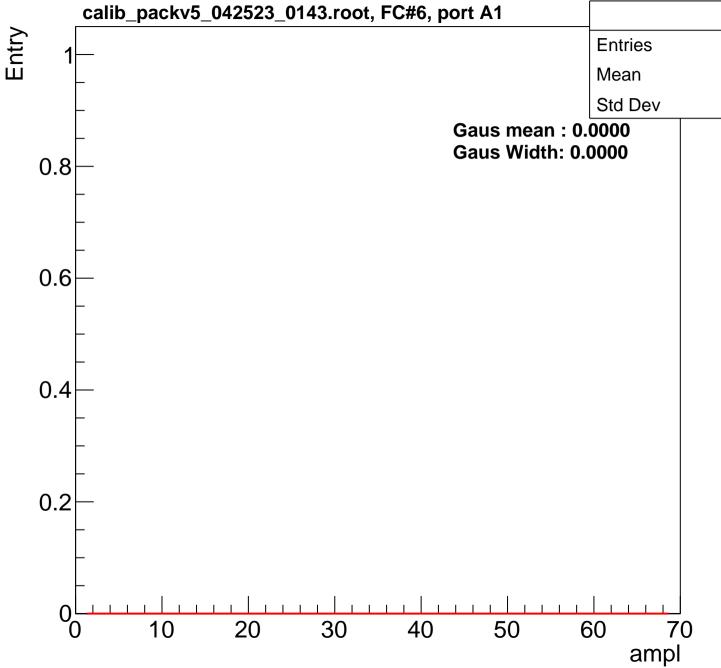












B0L100S, U3-ch86, adc1 calib_packv5_042523_0143.root, FC#6, port A1 Entry **Entries** Mean 52 Std Dev 0 Gaus mean: 0.0000 **Gaus Width: 0.0000** 8.0 0.6 0.4 0.2 10 20 30 40 50 60 70 ampl

