

B1L100S, U14-ch0, adc0

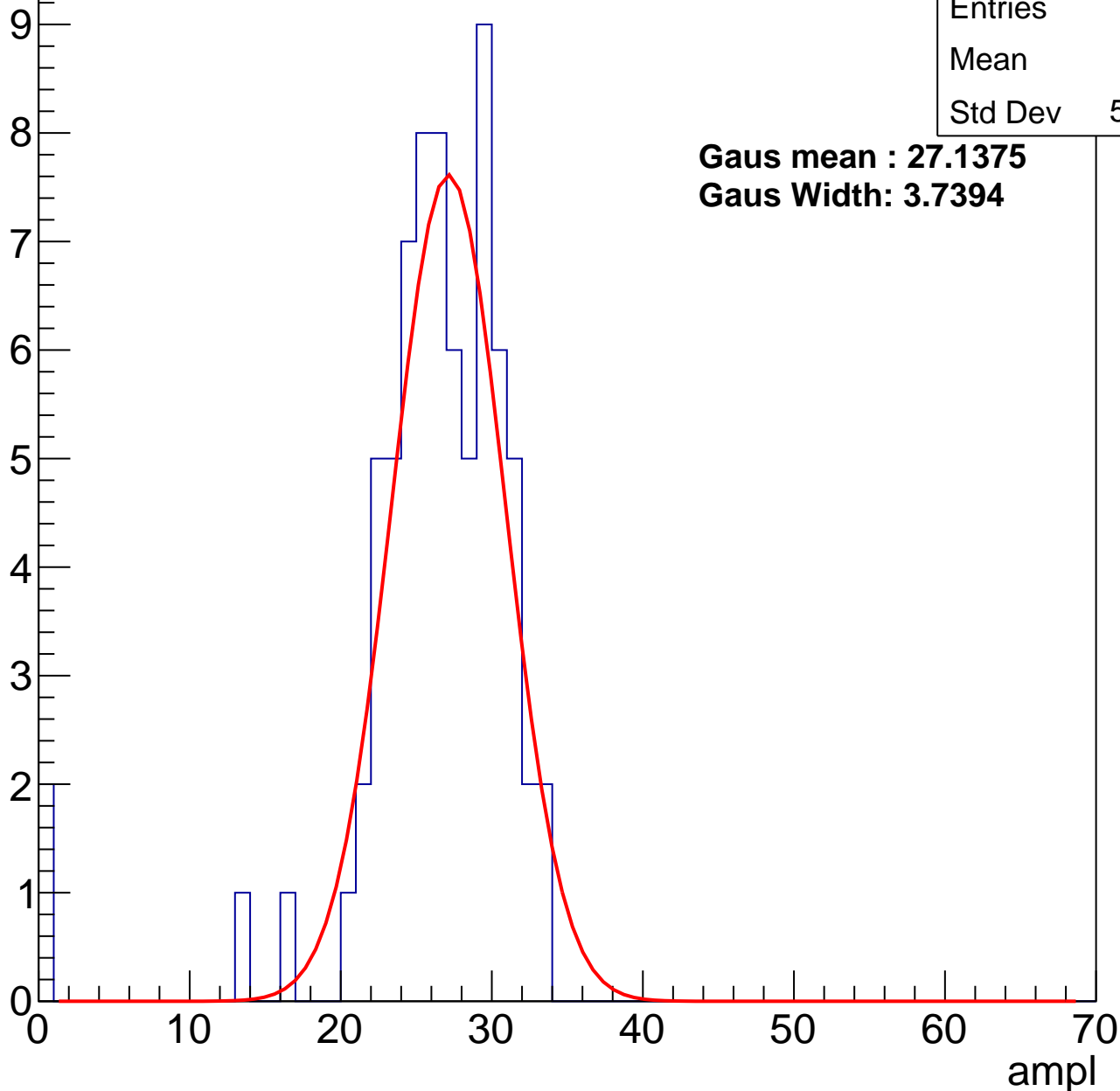
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	25.6
Std Dev	5.602

Gaus mean : 27.1375

Gaus Width: 3.7394



B1L100S, U14-ch0, adc1

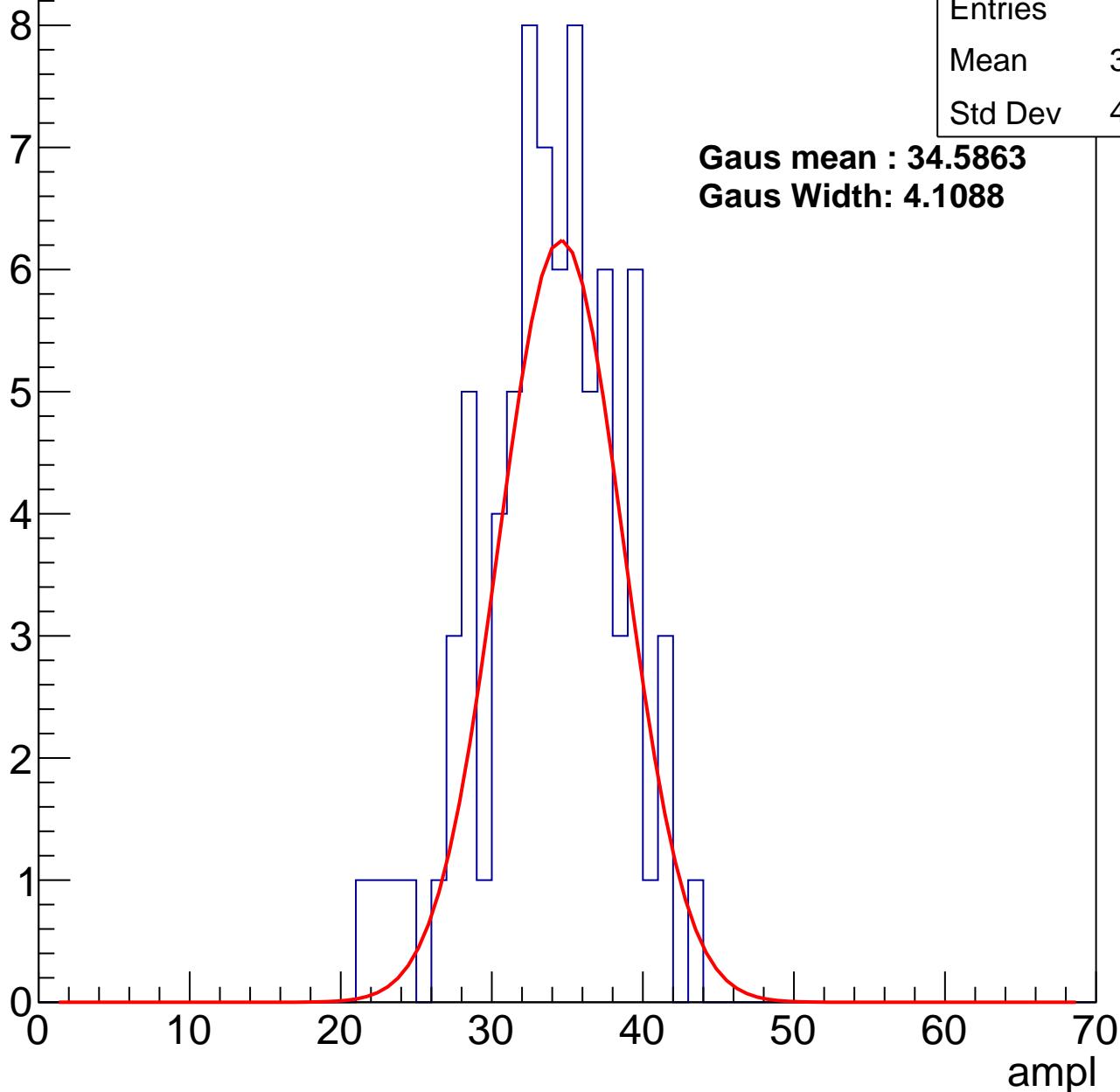
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	33.35
Std Dev	4.572

Gaus mean : 34.5863

Gaus Width: 4.1088



B1L100S, U14-ch0, adc2

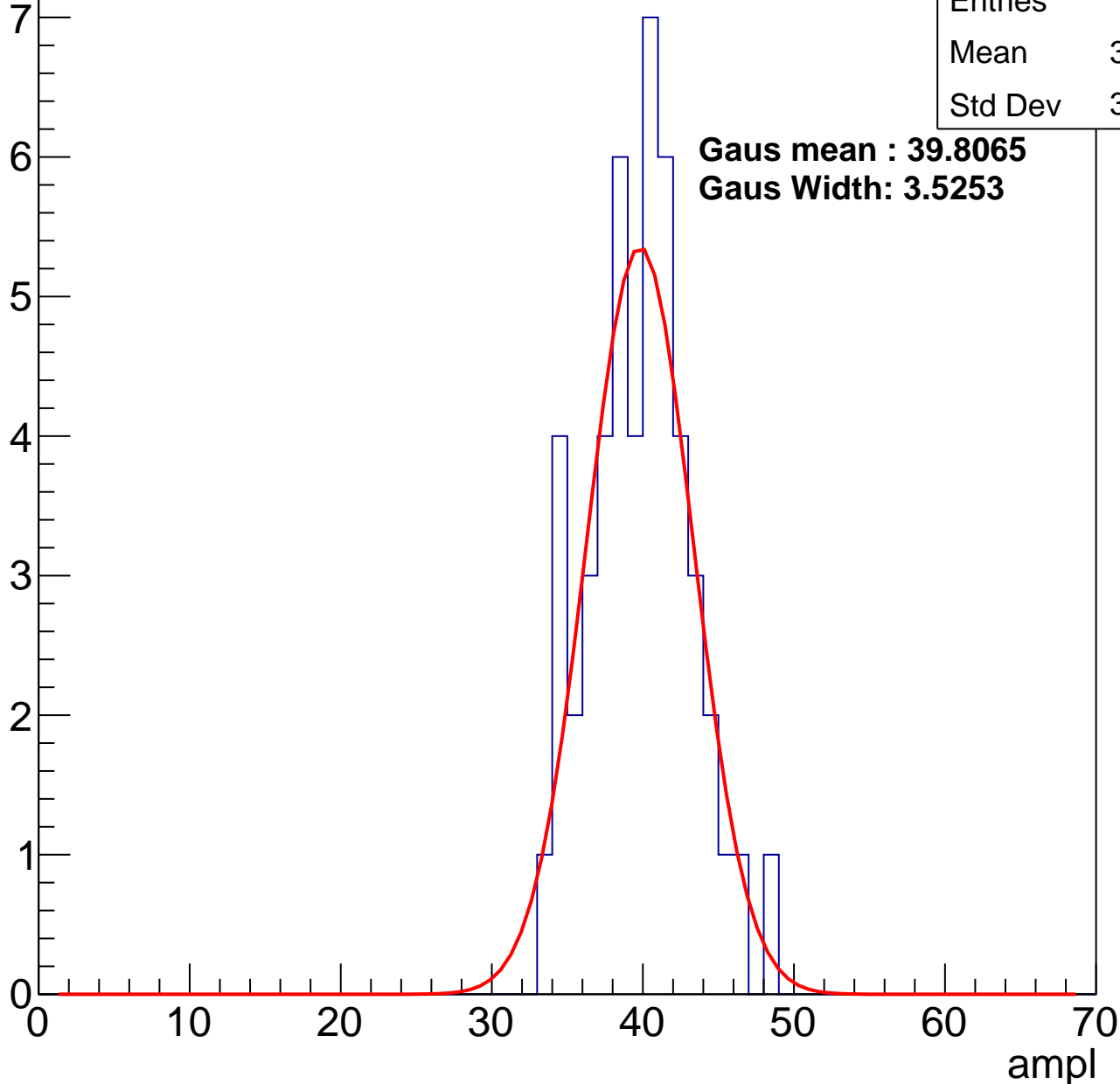
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	39.37
Std Dev	3.324

Gaus mean : 39.8065

Gaus Width: 3.5253

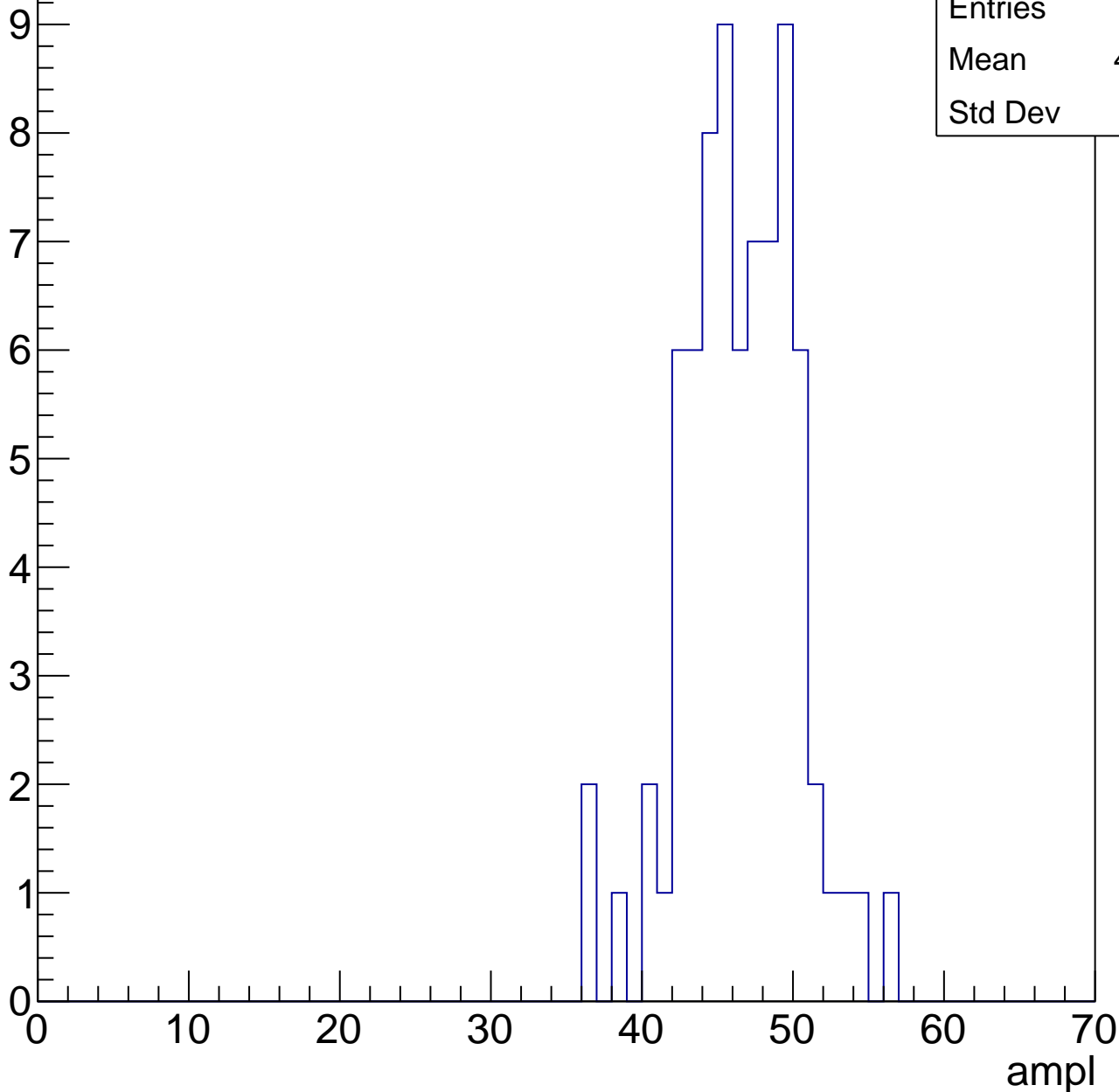


B1L100S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	46.01
Std Dev	3.74

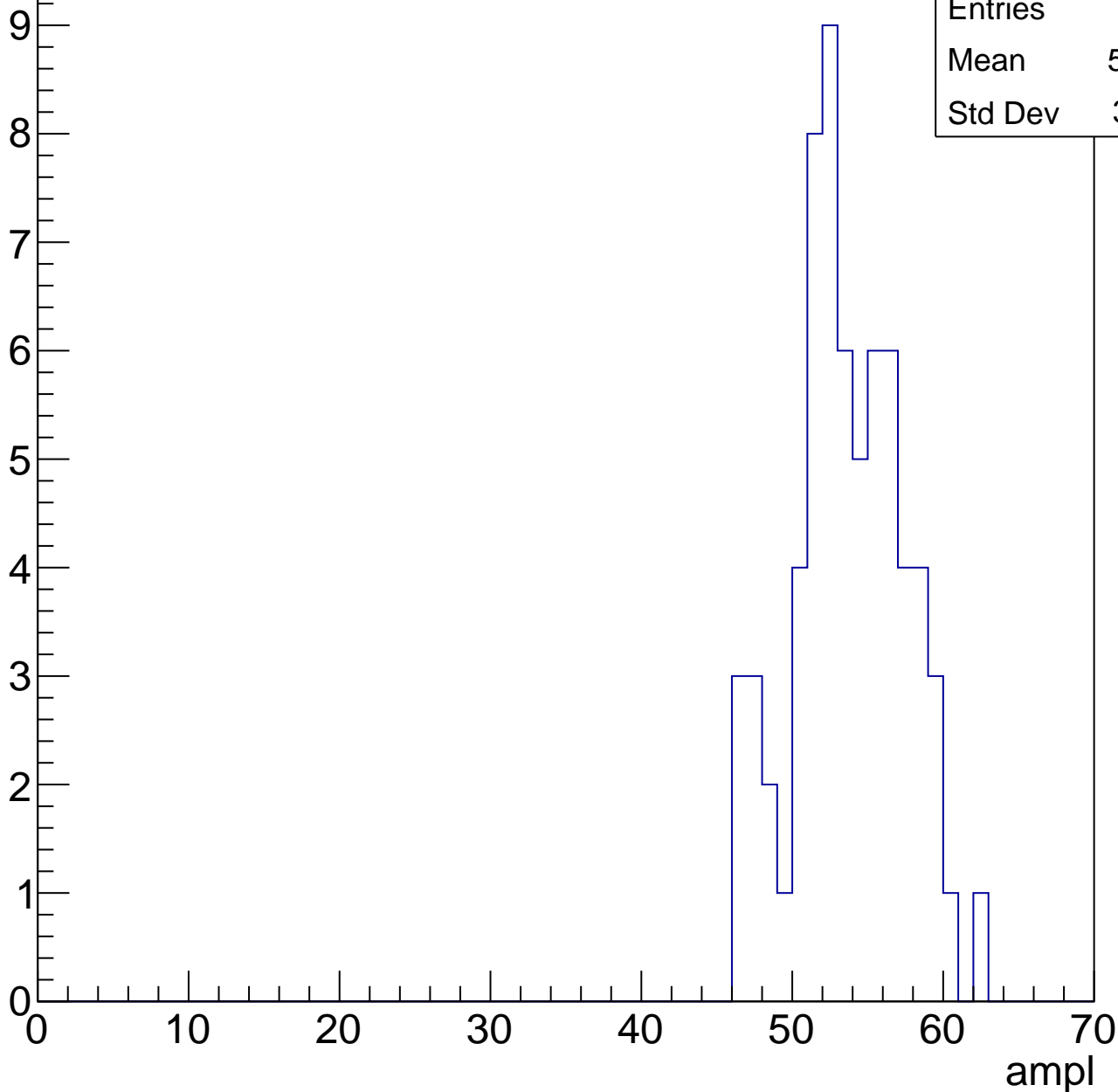


B1L100S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	53.23
Std Dev	3.651

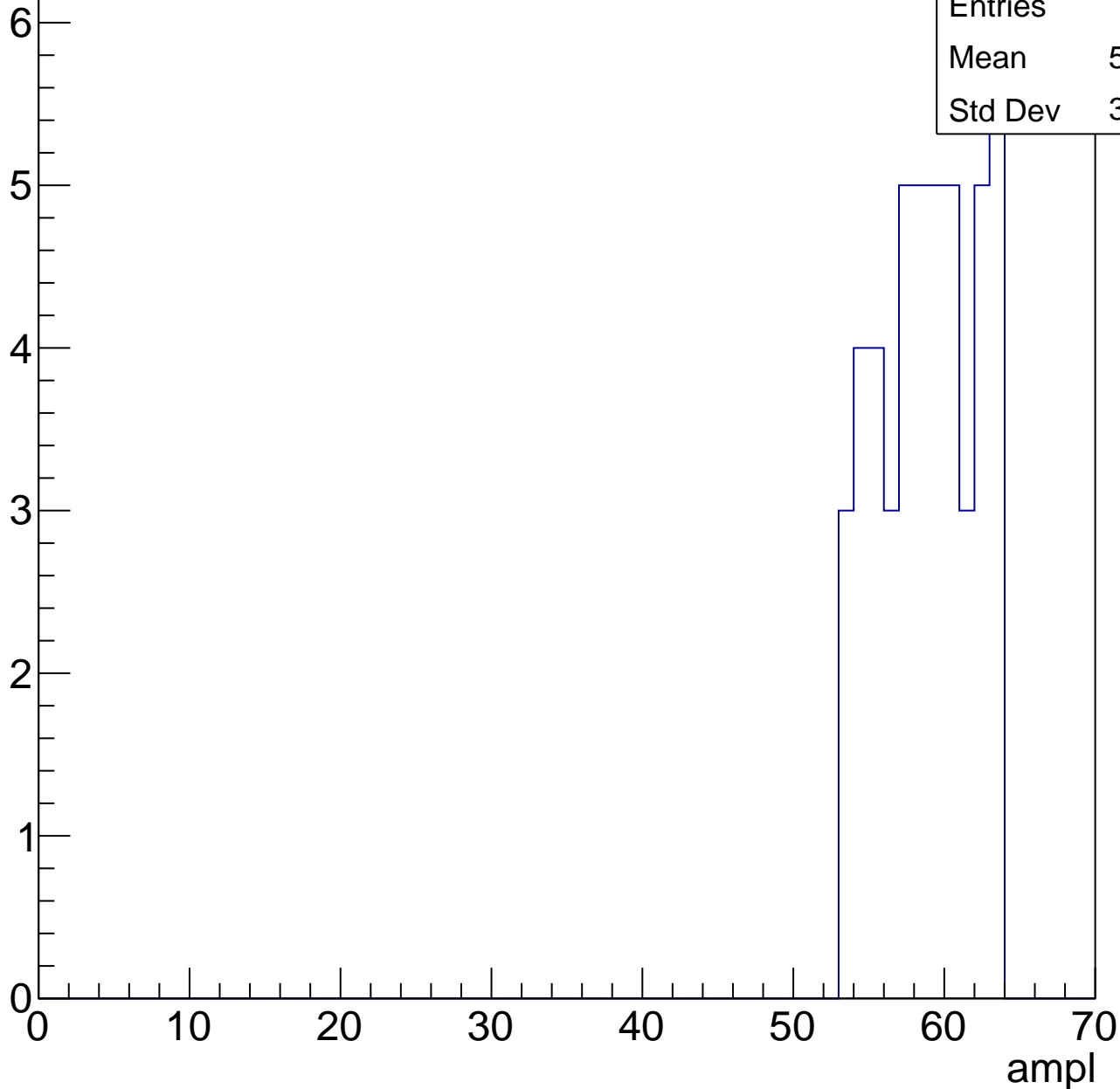


B1L100S, U14-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.42
Std Dev	3.115

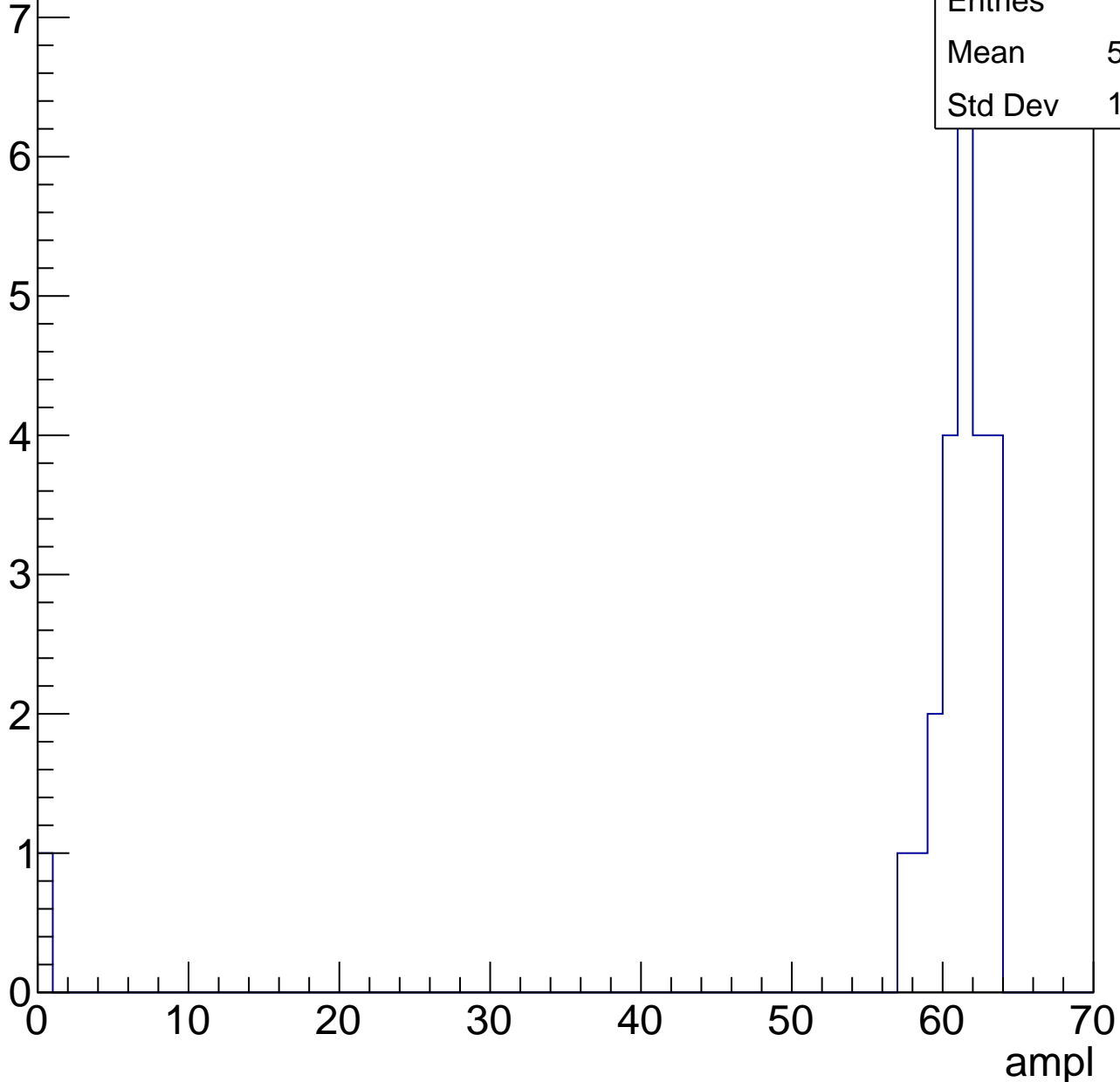


B1L100S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.33
Std Dev	12.26



B1L100S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch1, adc0

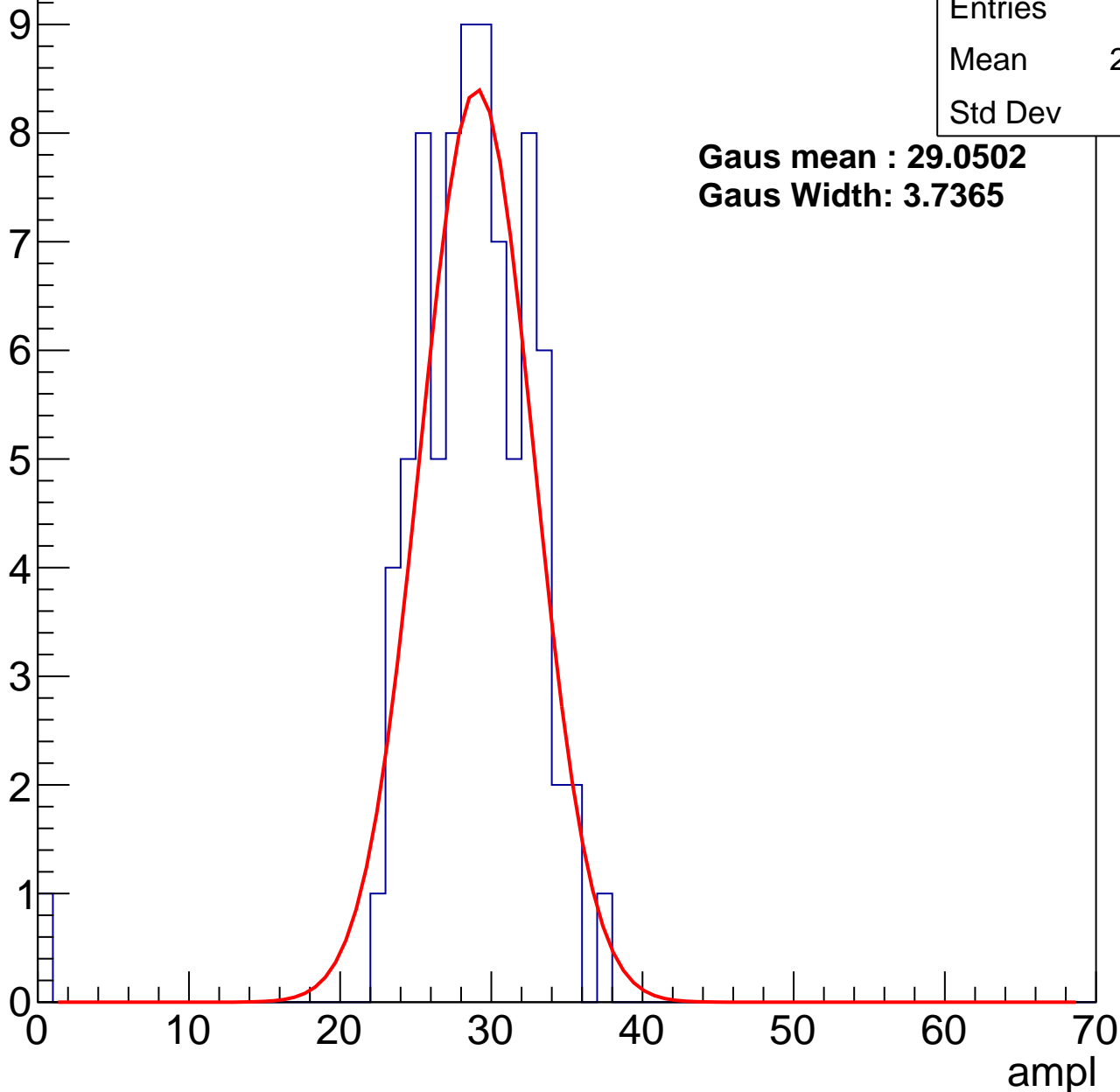
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	28.23
Std Dev	4.59

Gaus mean : 29.0502

Gaus Width: 3.7365



B1L100S, U14-ch1, adc1

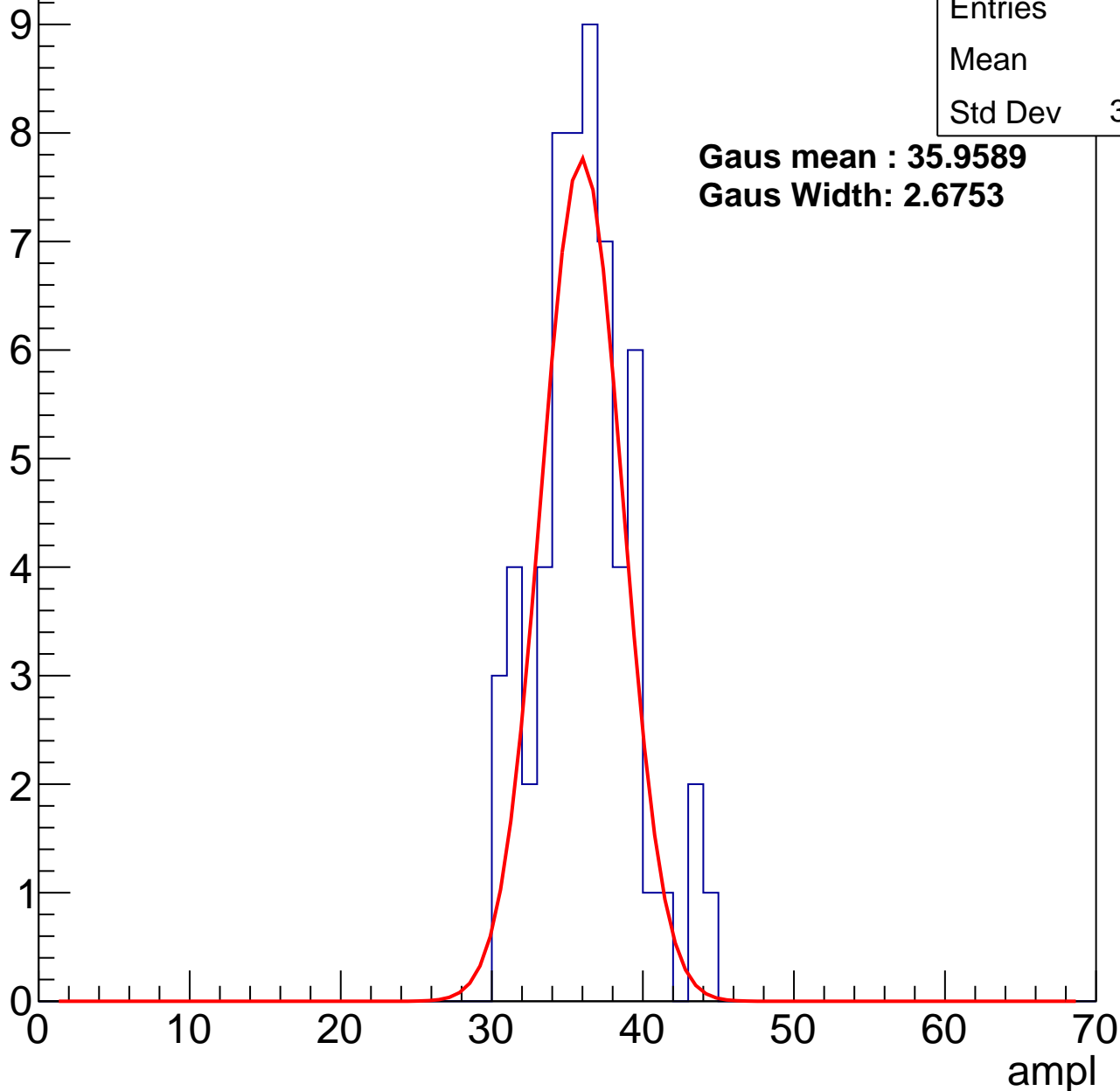
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	35.7
Std Dev	3.137

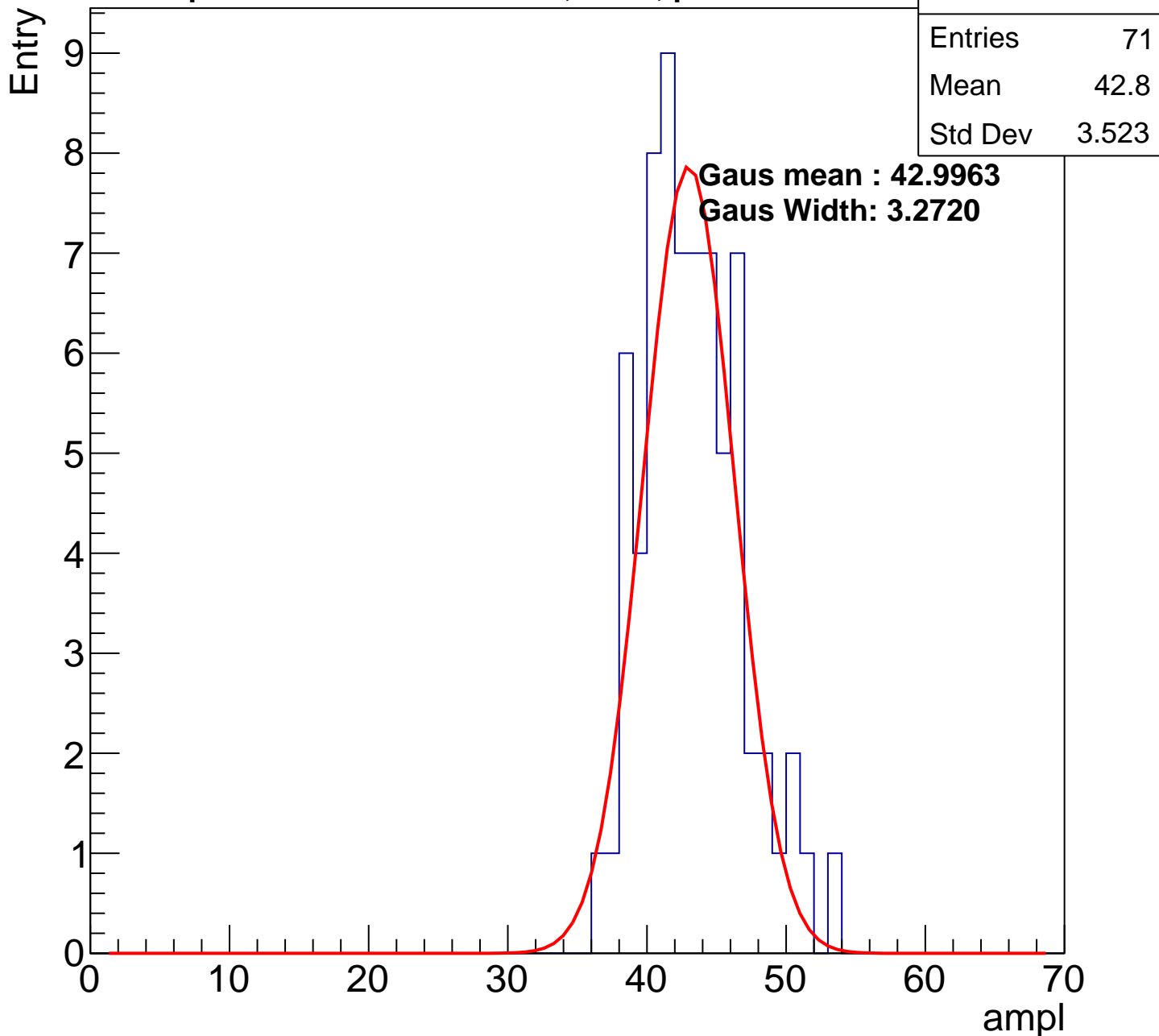
Gaus mean : 35.9589

Gaus Width: 2.6753



B1L100S, U14-ch1, adc2

calib_packv5_042523_0143.root, FC#4, port A2

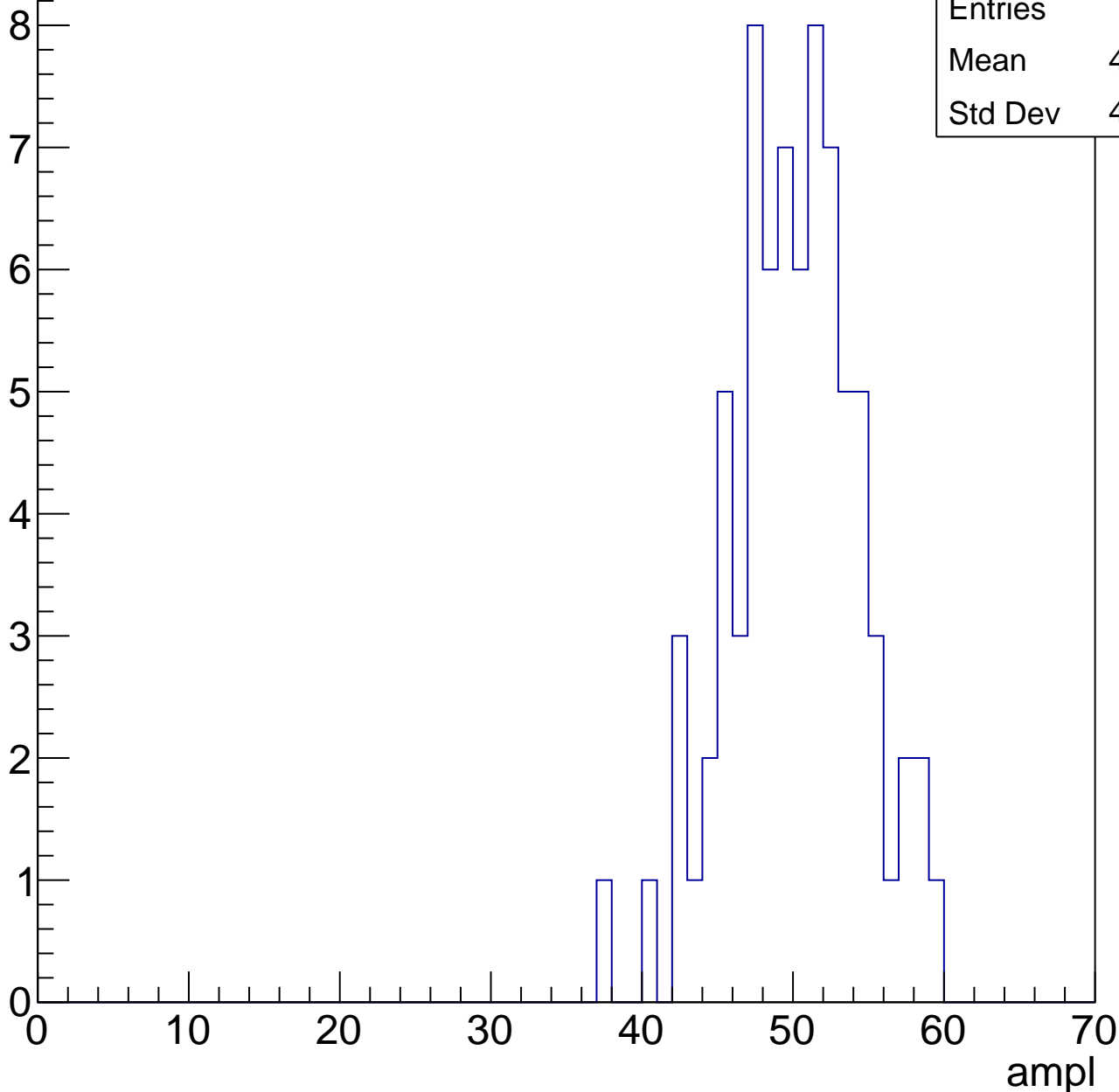


B1L100S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	49.62
Std Dev	4.304



B1L100S, U14-ch1, adc4

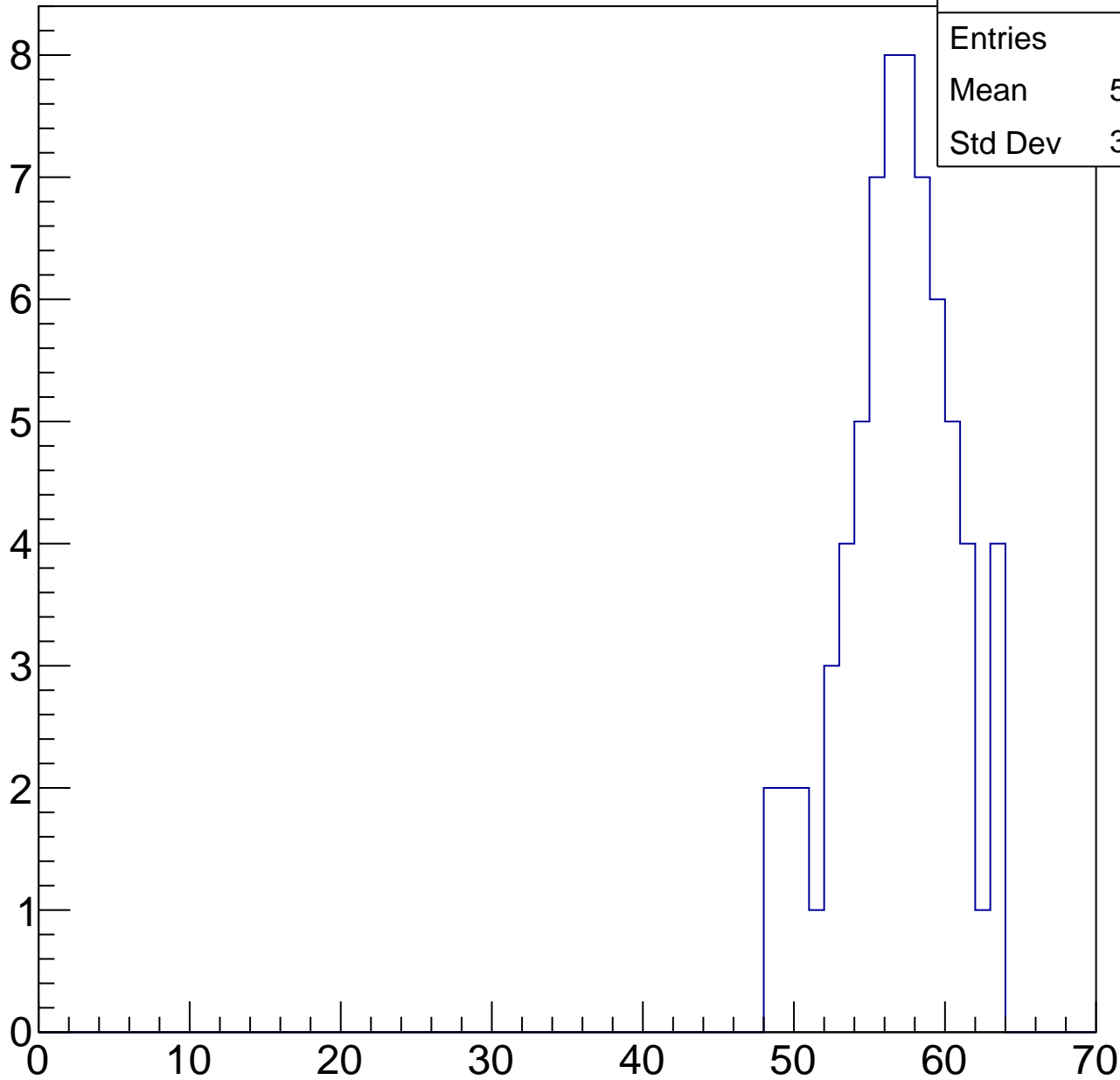
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

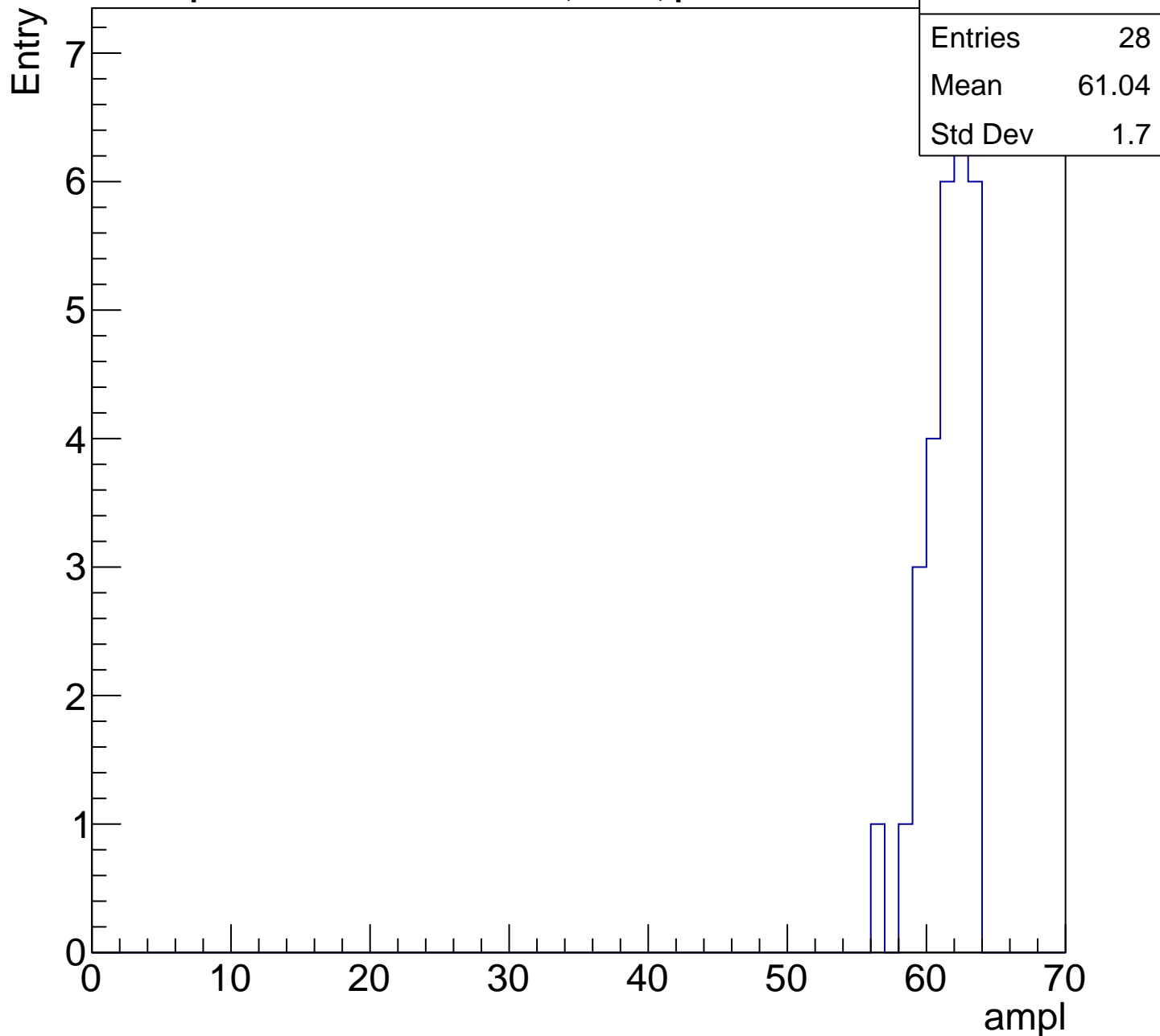
Entries	69
Mean	56.38
Std Dev	3.664

ampl



B1L100S, U14-ch1, adc5

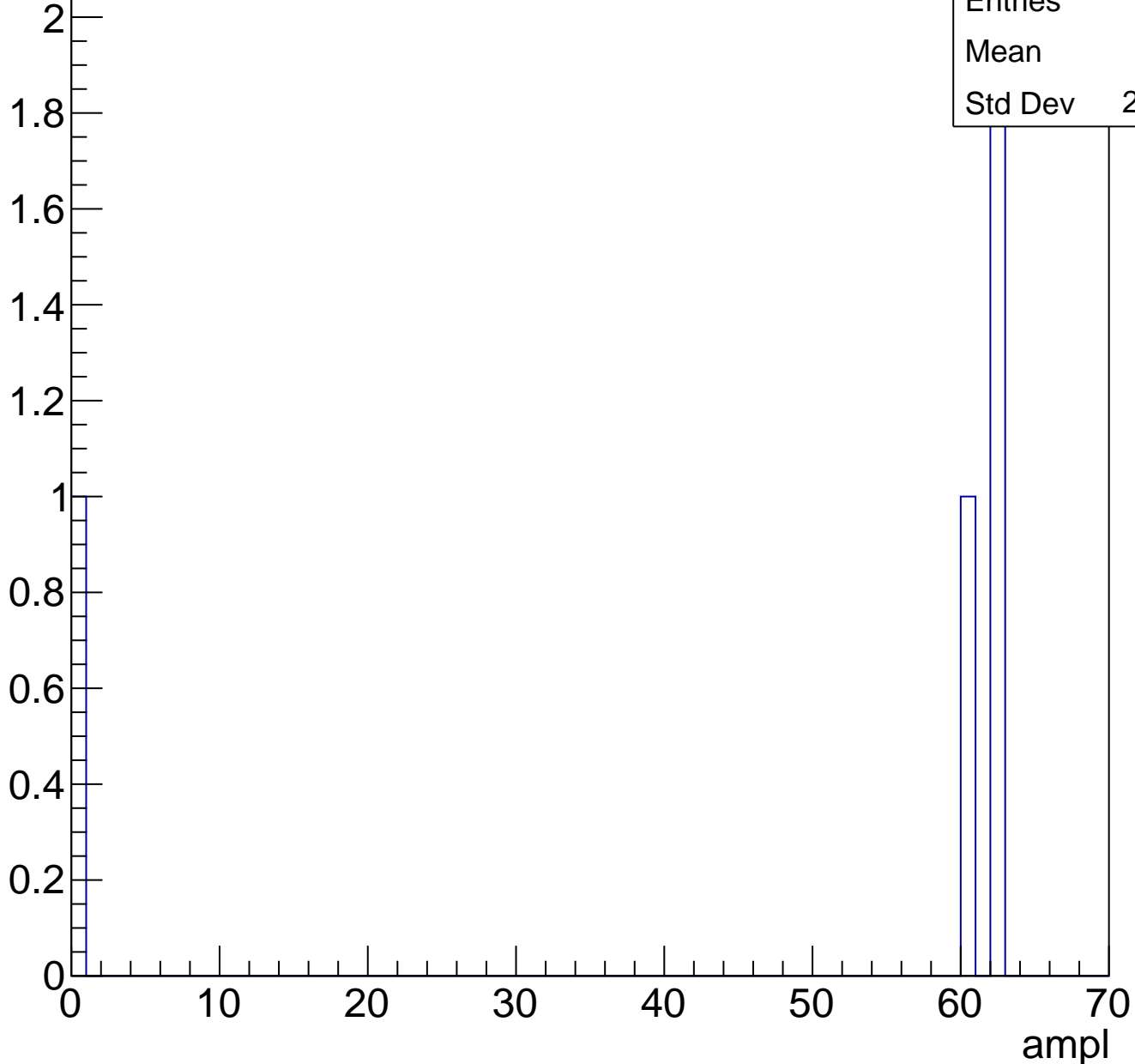
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch2, adc0

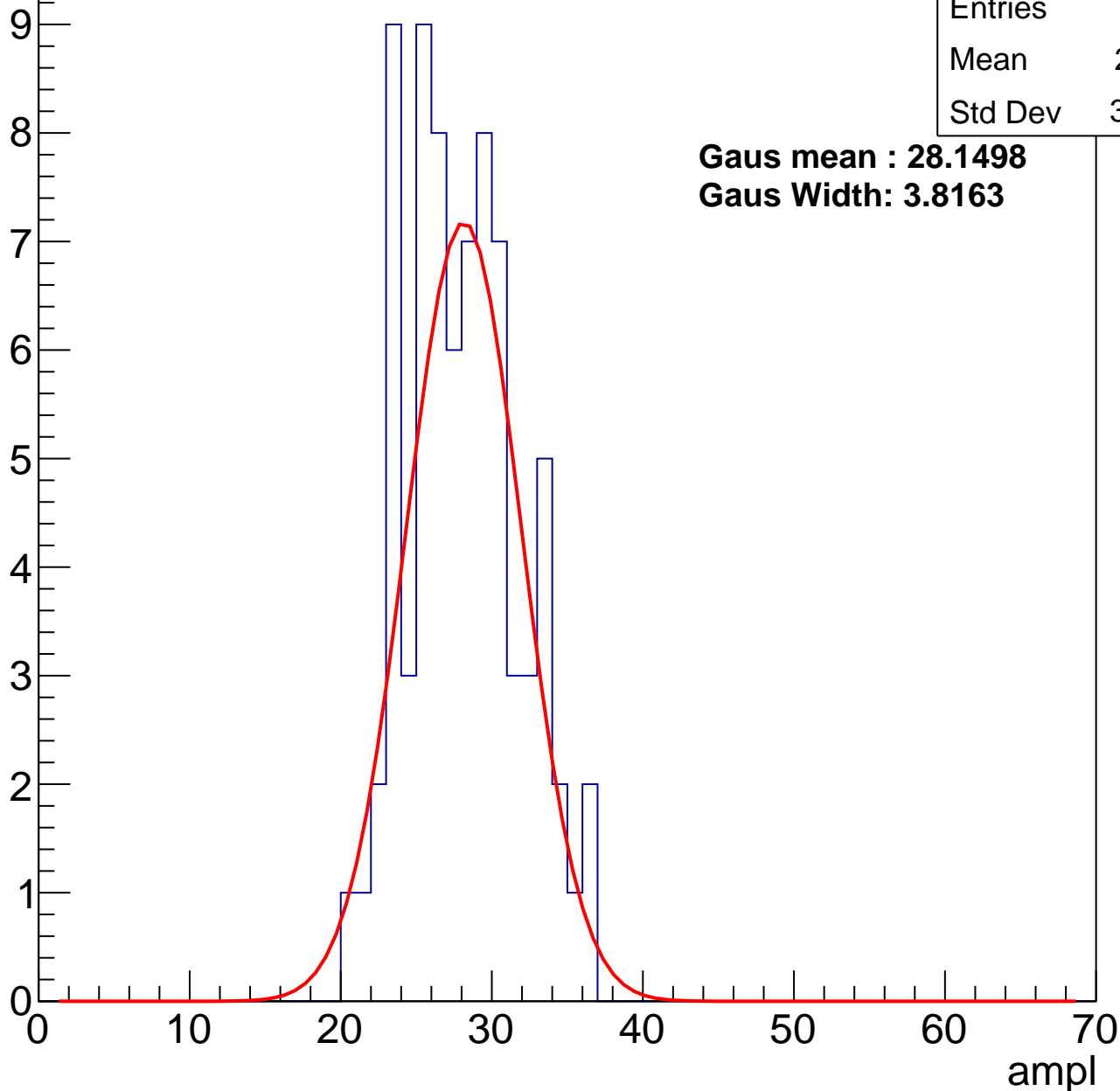
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	27.61
Std Dev	3.693

Gaus mean : 28.1498

Gaus Width: 3.8163



B1L100S, U14-ch2, adc1

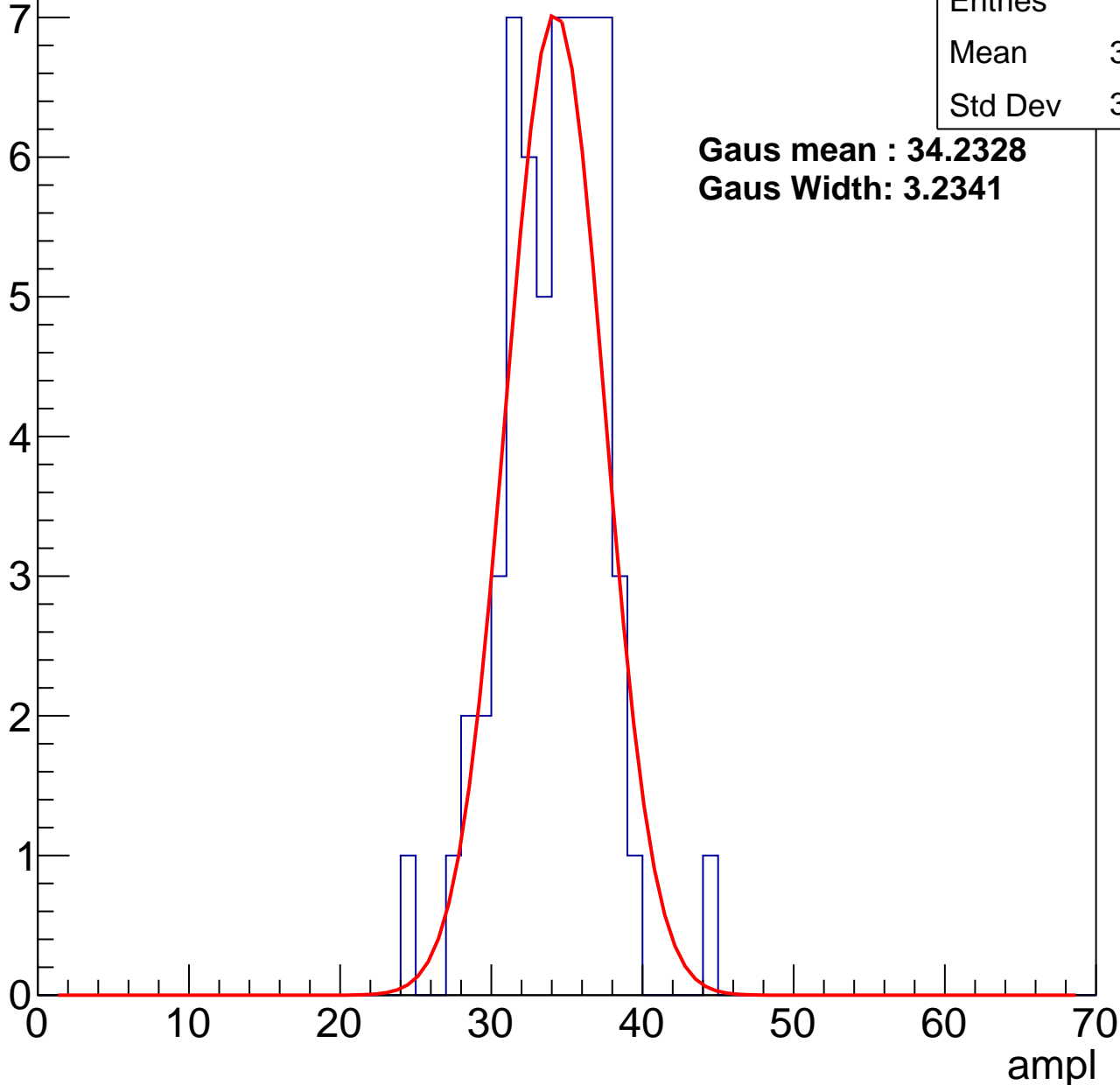
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	33.67
Std Dev	3.355

Gaus mean : 34.2328

Gaus Width: 3.2341



B1L100S, U14-ch2, adc2

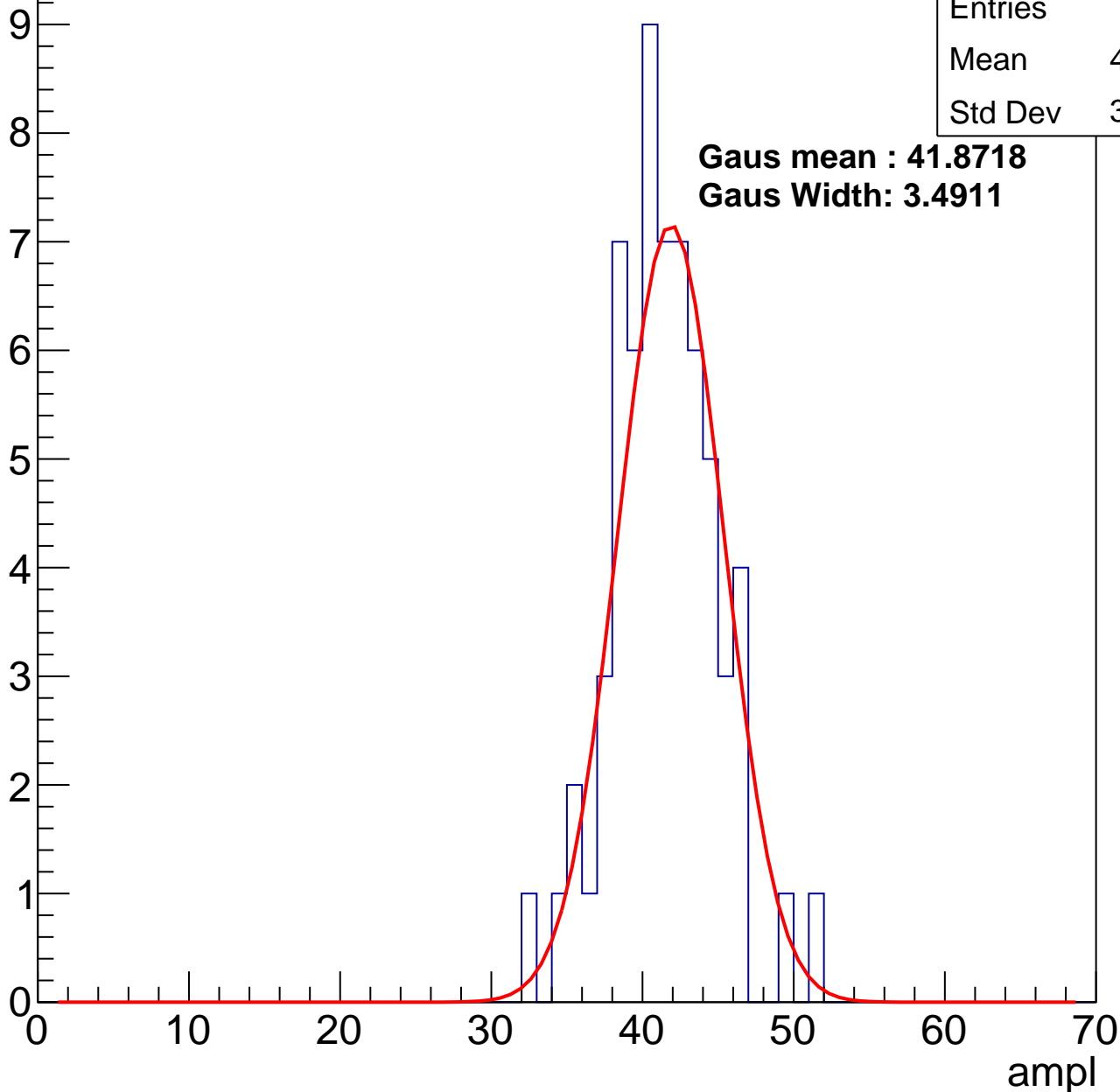
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	40.95
Std Dev	3.443

Gaus mean : 41.8718

Gaus Width: 3.4911

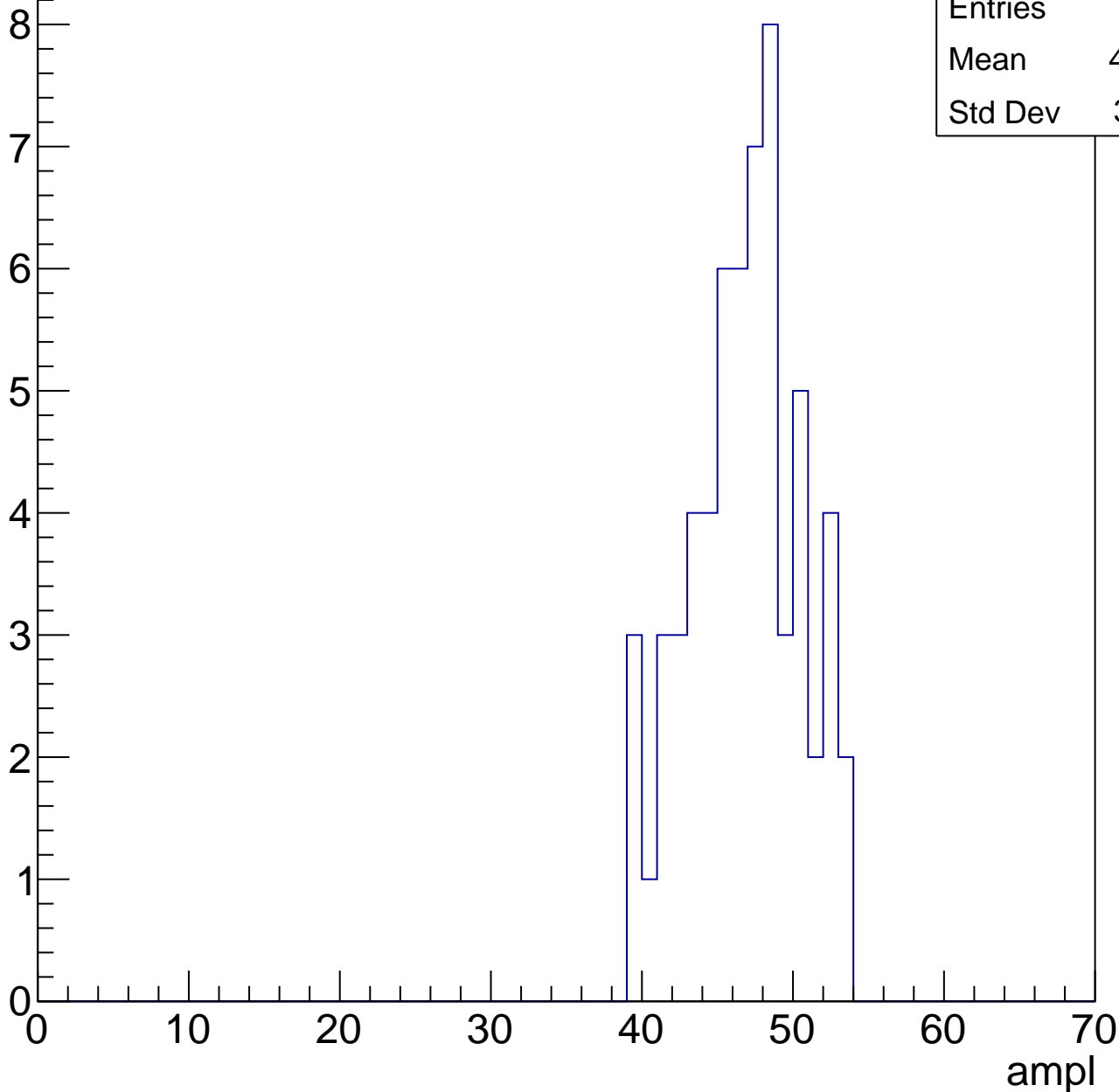


B1L100S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	46.33
Std Dev	3.611

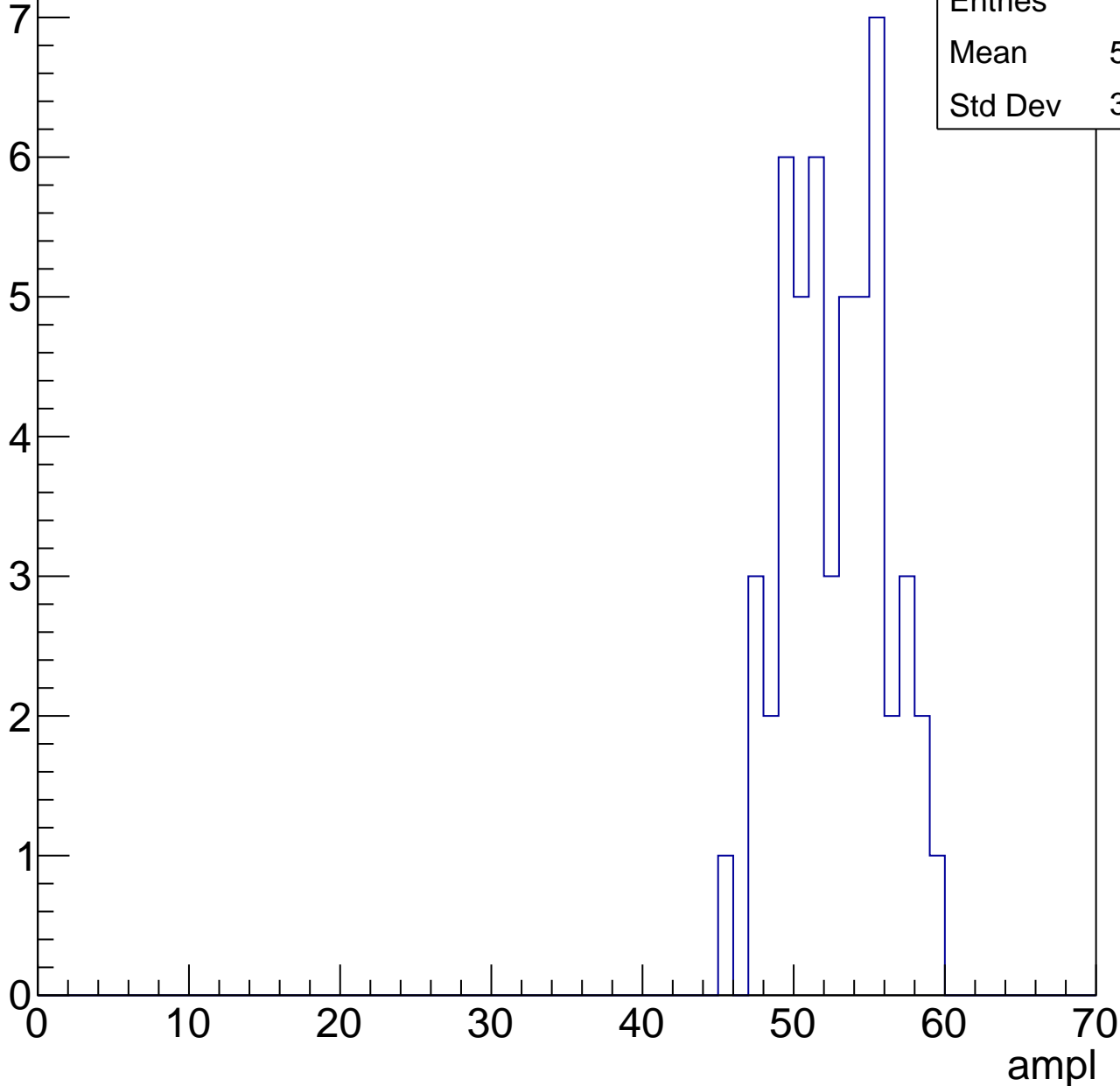


B1L100S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

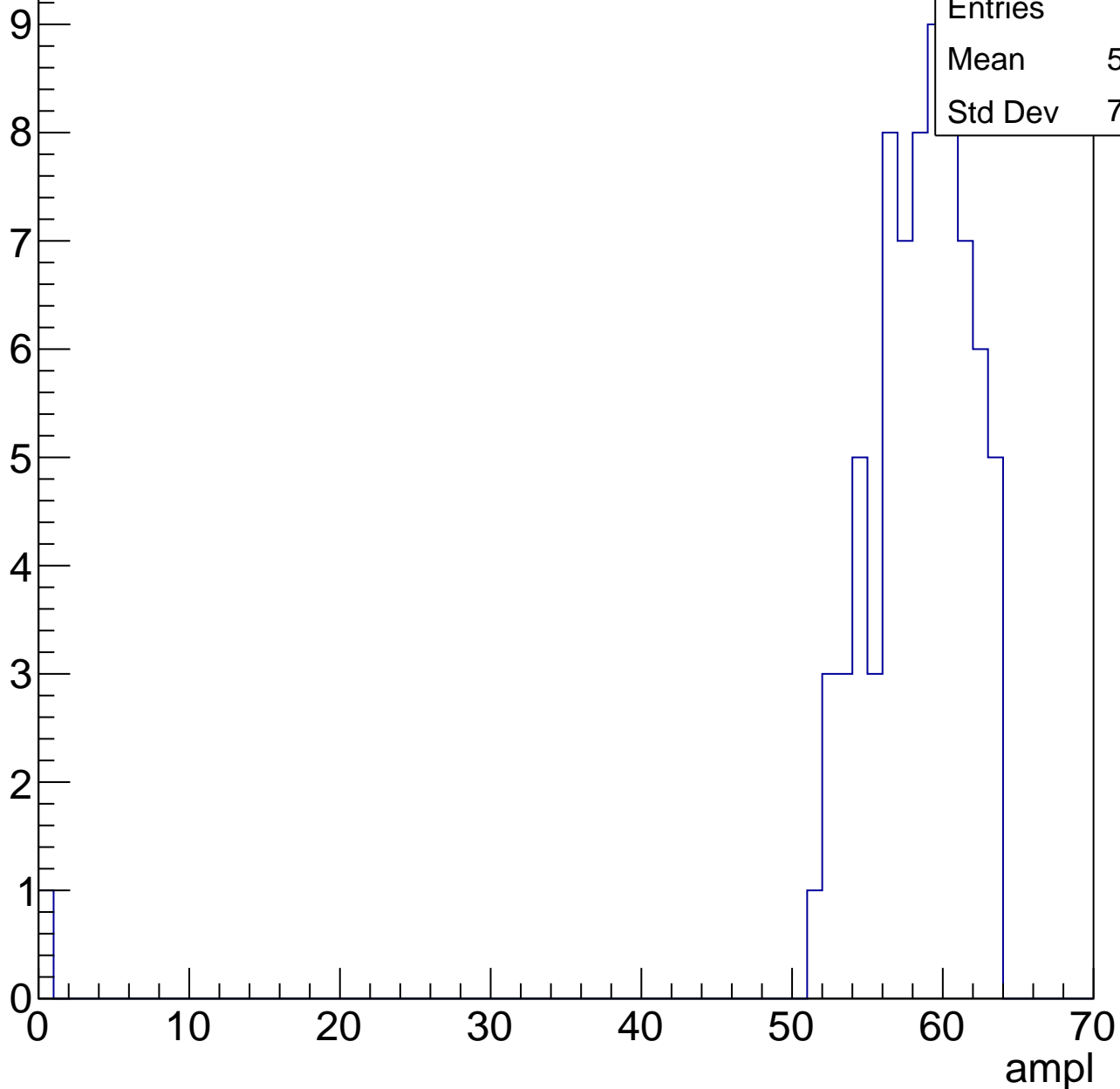
Entries	51
Mean	52.27
Std Dev	3.278



B1L100S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

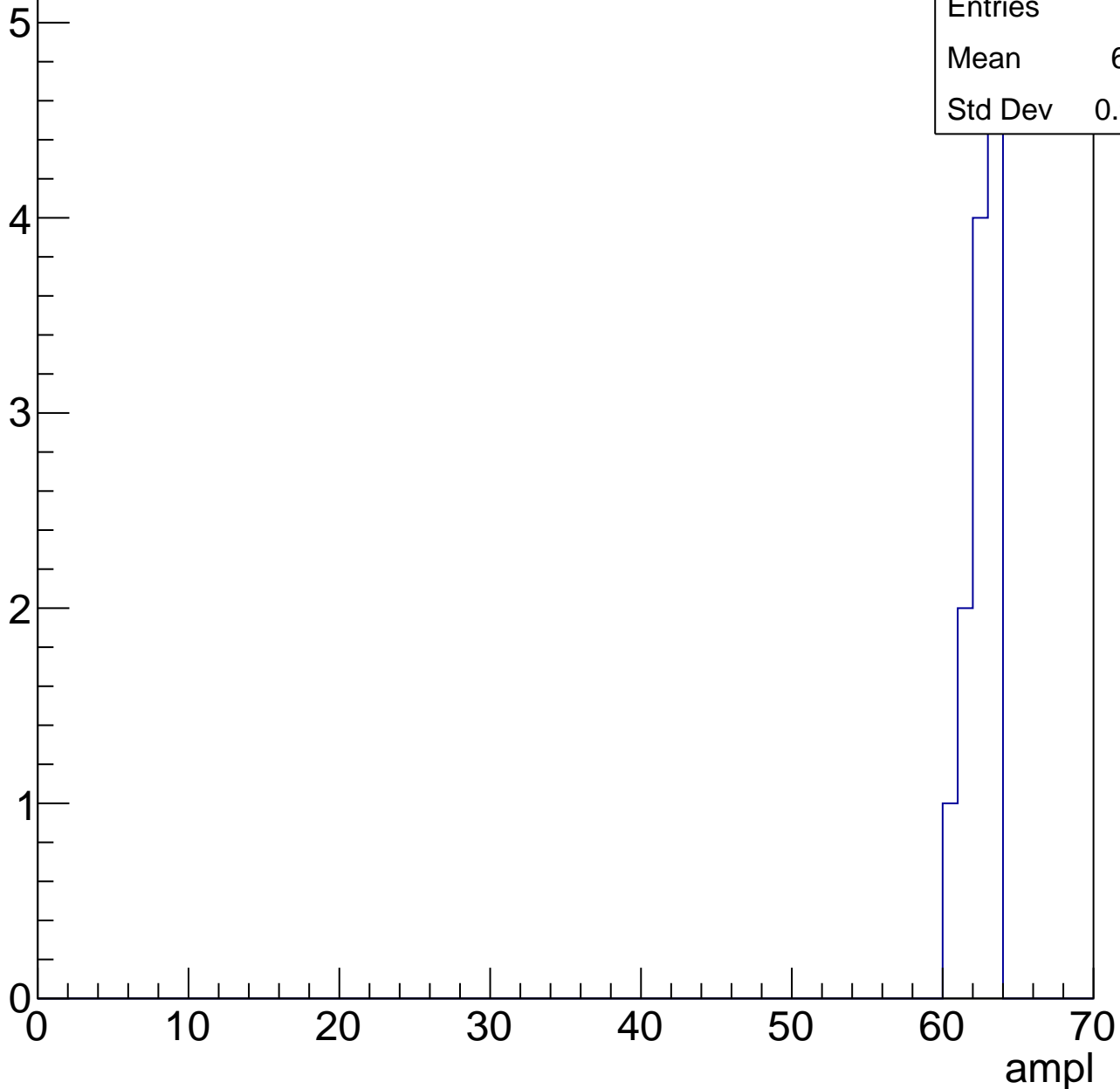


B1L100S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

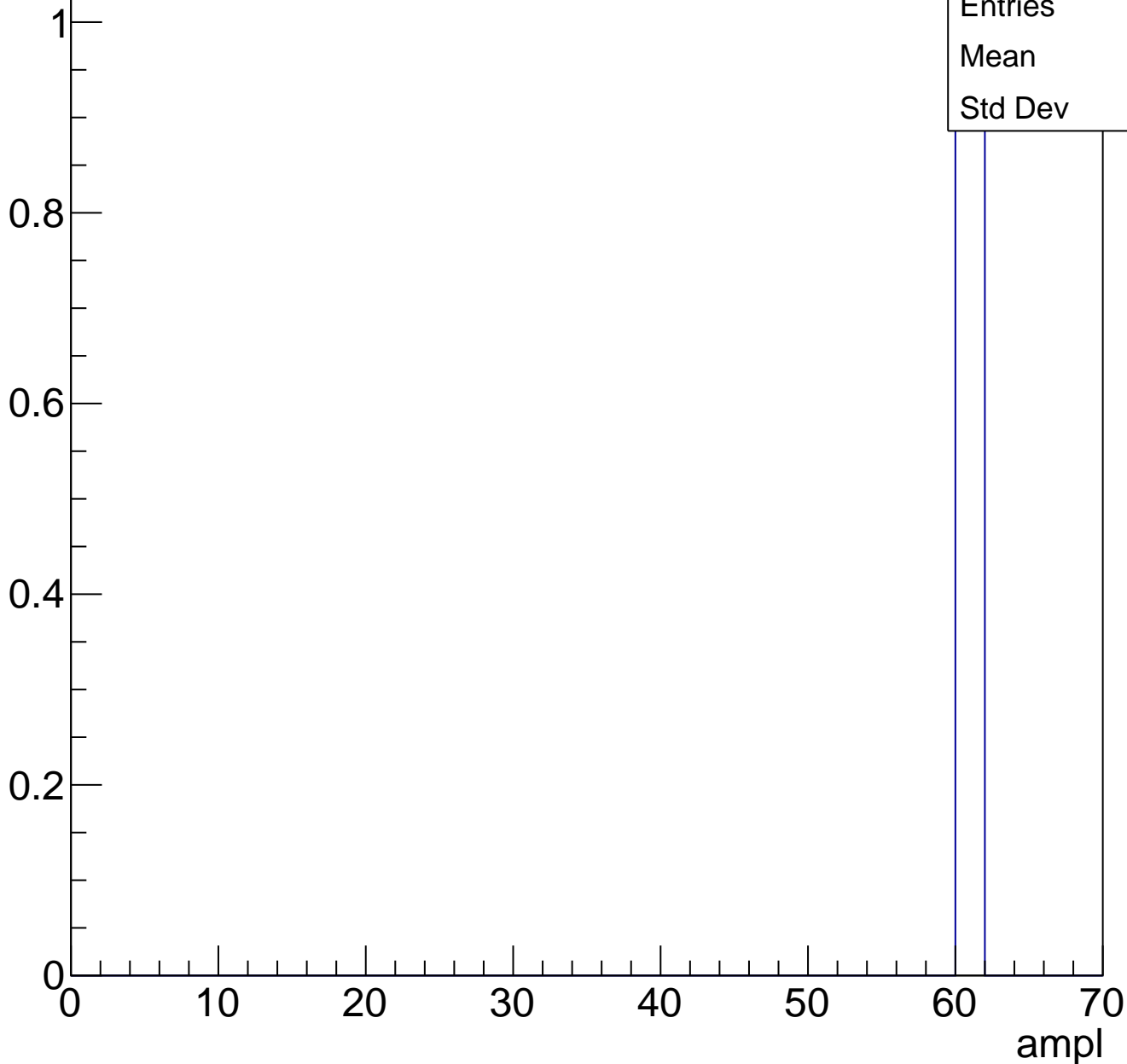
Entries	12
Mean	62.08
Std Dev	0.9538



B1L100S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch3, adc0

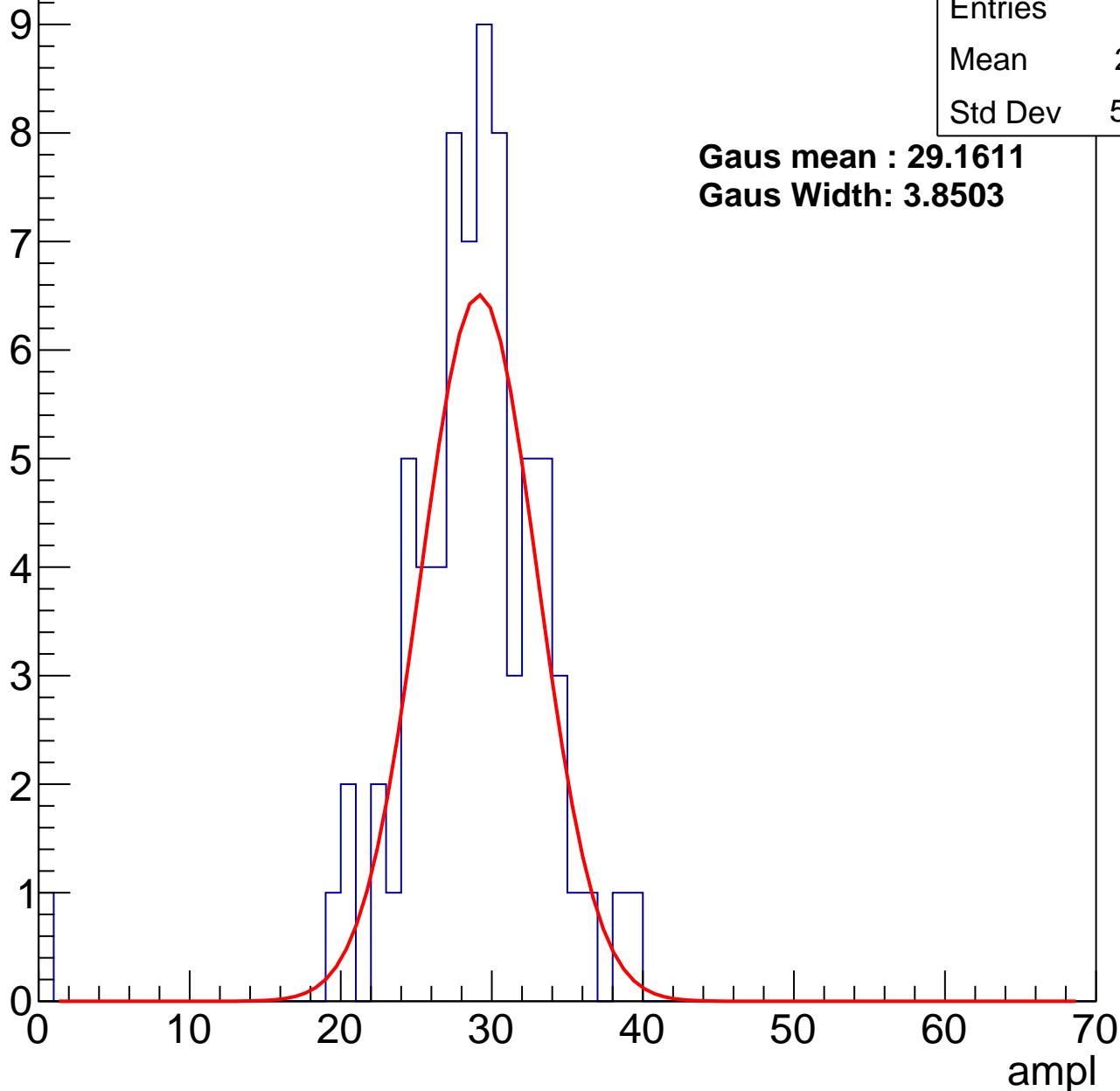
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.21
Std Dev	5.193

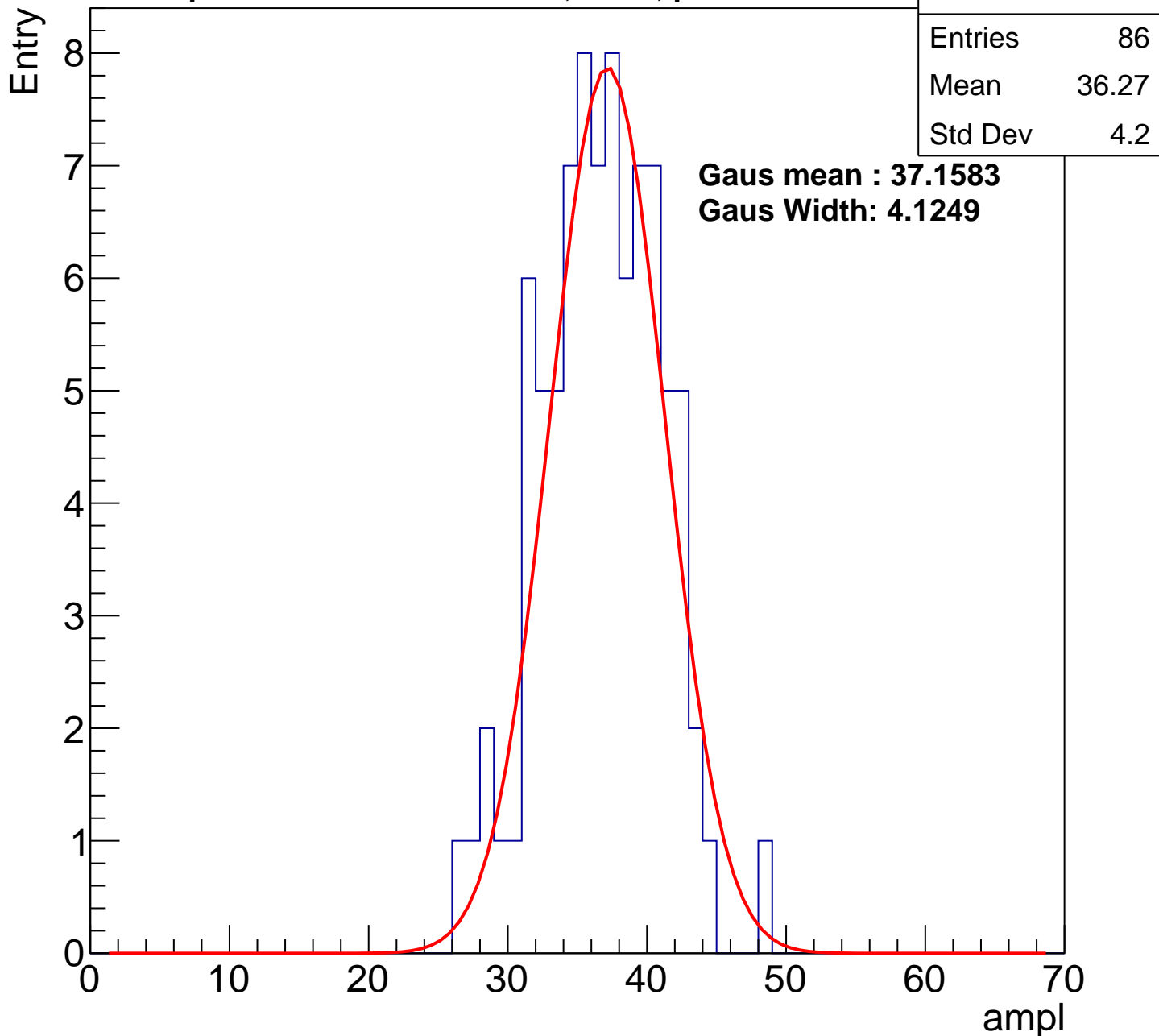
Gaus mean : 29.1611

Gaus Width: 3.8503



B1L100S, U14-ch3, adc1

calib_packv5_042523_0143.root, FC#4, port A2

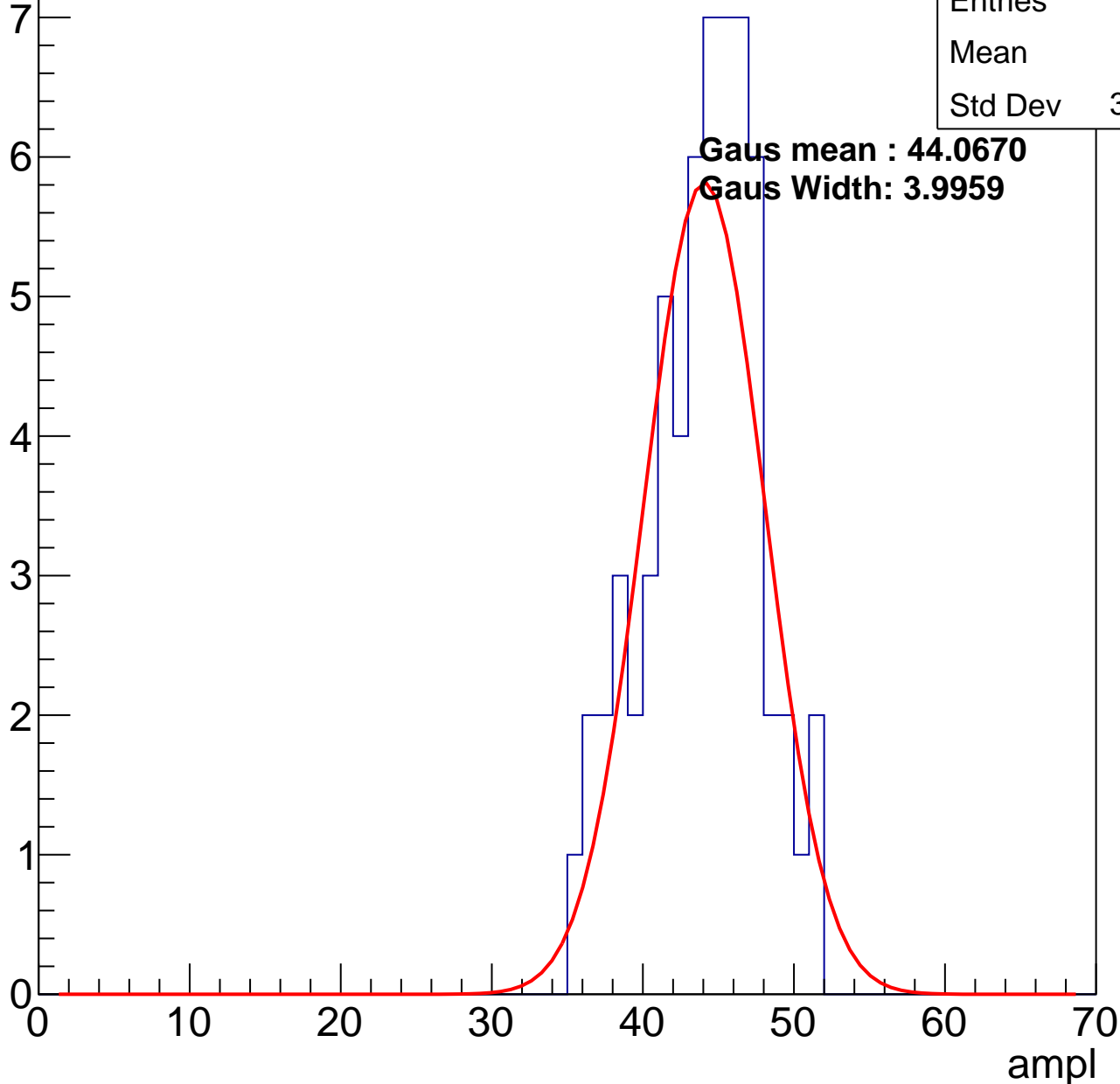


B1L100S, U14-ch3, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.5
Std Dev	3.745

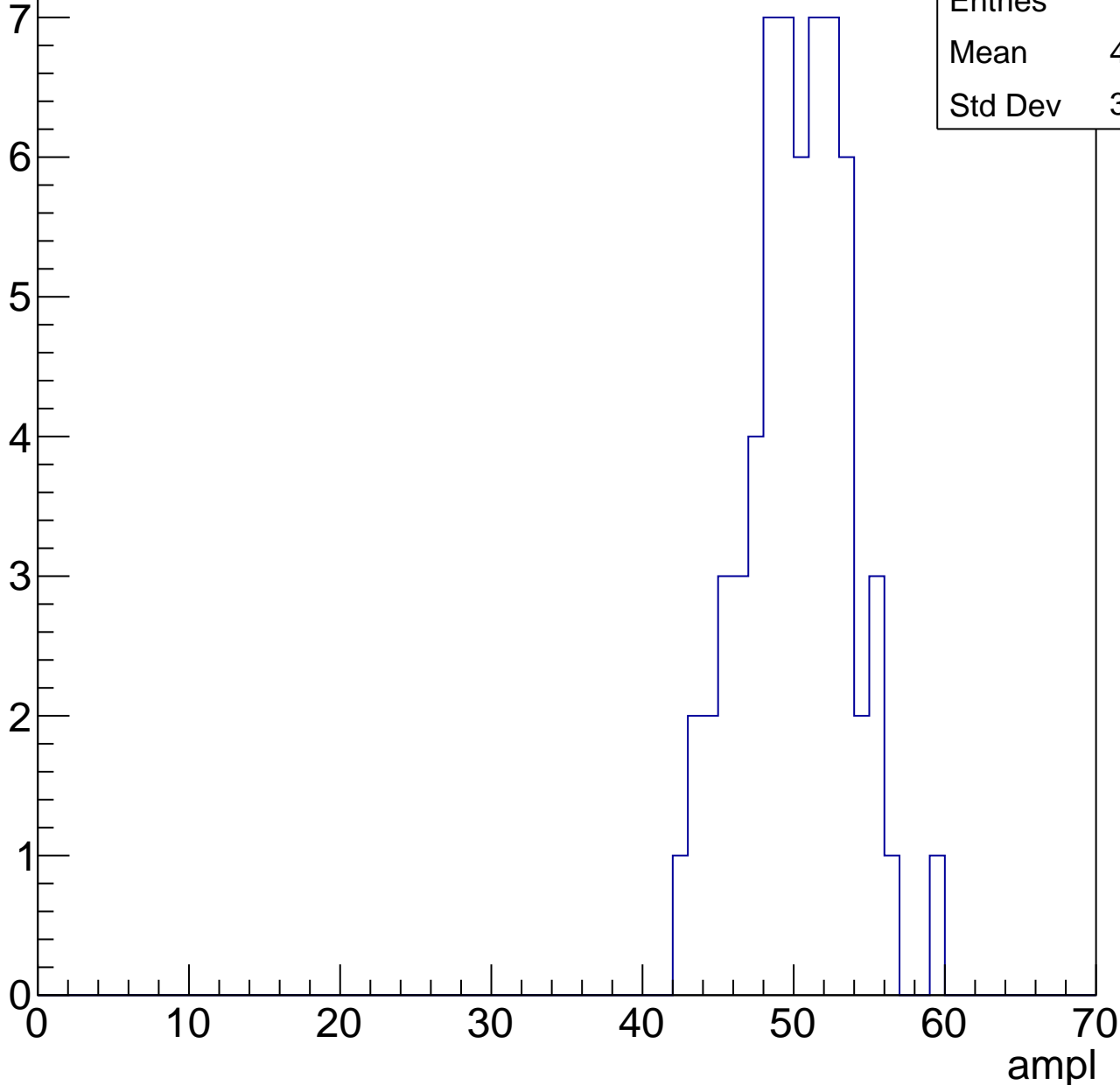


B1L100S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

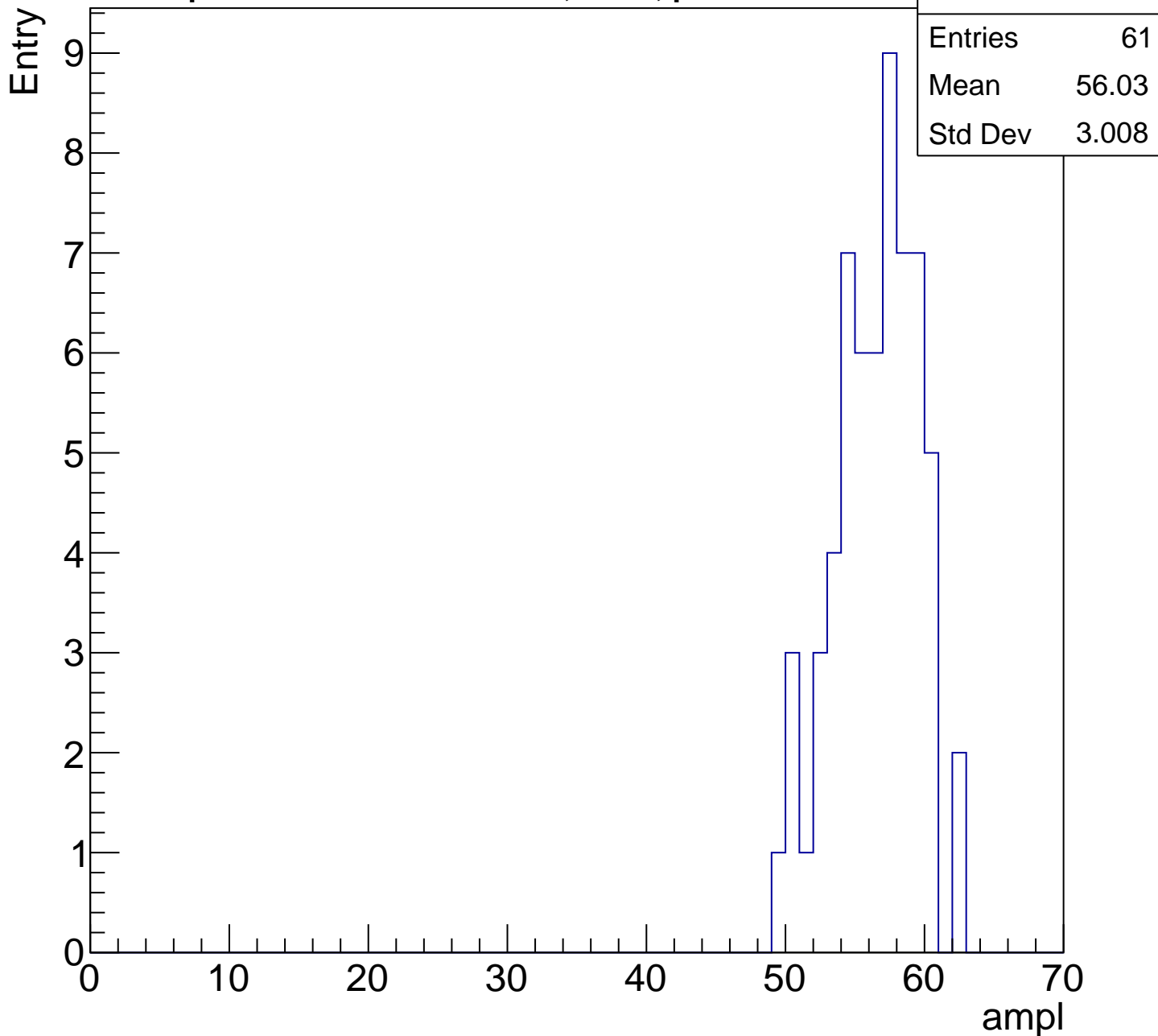
Entry

Entries	62
Mean	49.73
Std Dev	3.446



B1L100S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

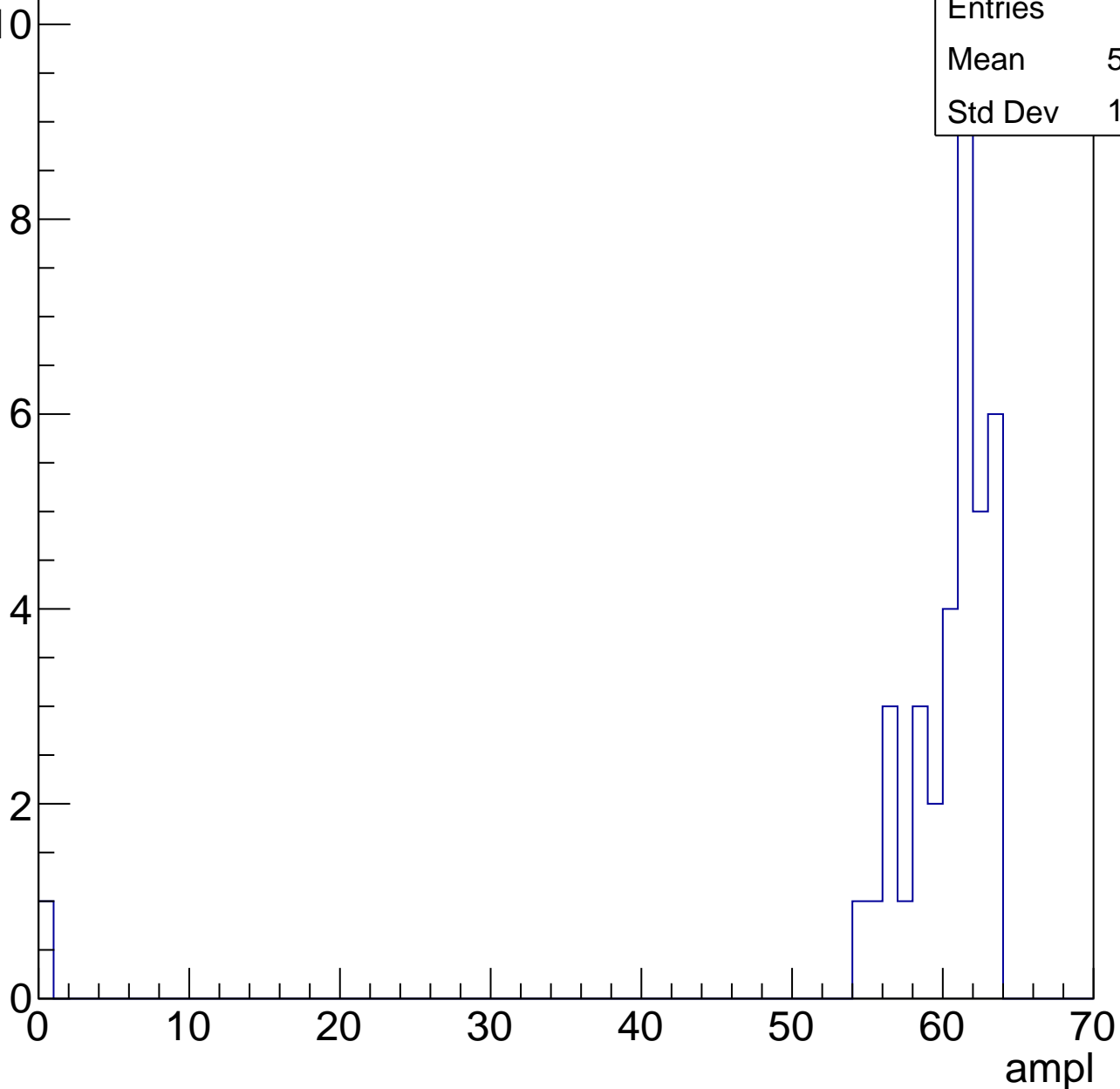


B1L100S, U14-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.49
Std Dev	10.04



B1L100S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch4, adc0

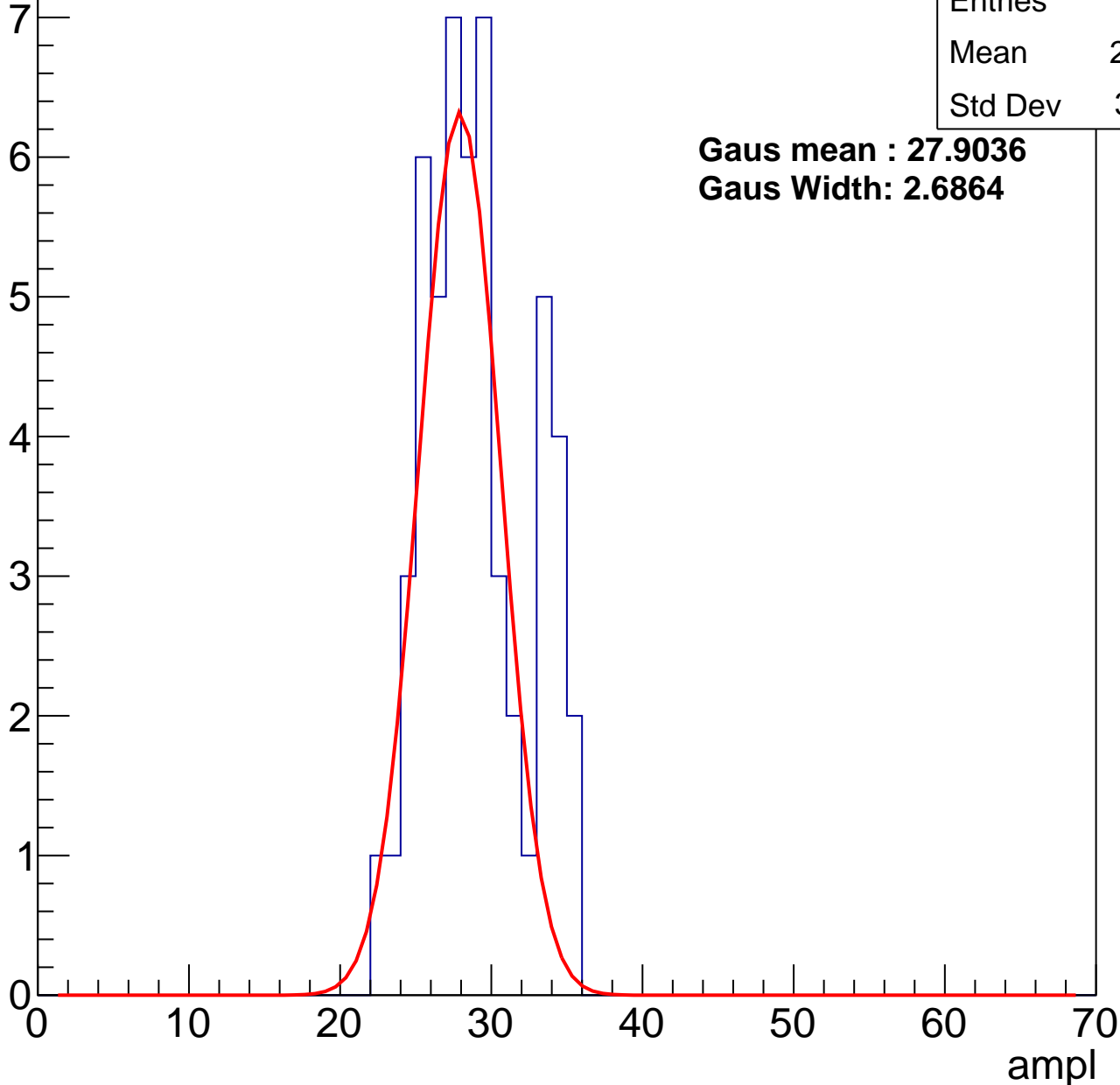
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	28.53
Std Dev	3.351

Gaus mean : 27.9036

Gaus Width: 2.6864



B1L100S, U14-ch4, adc1

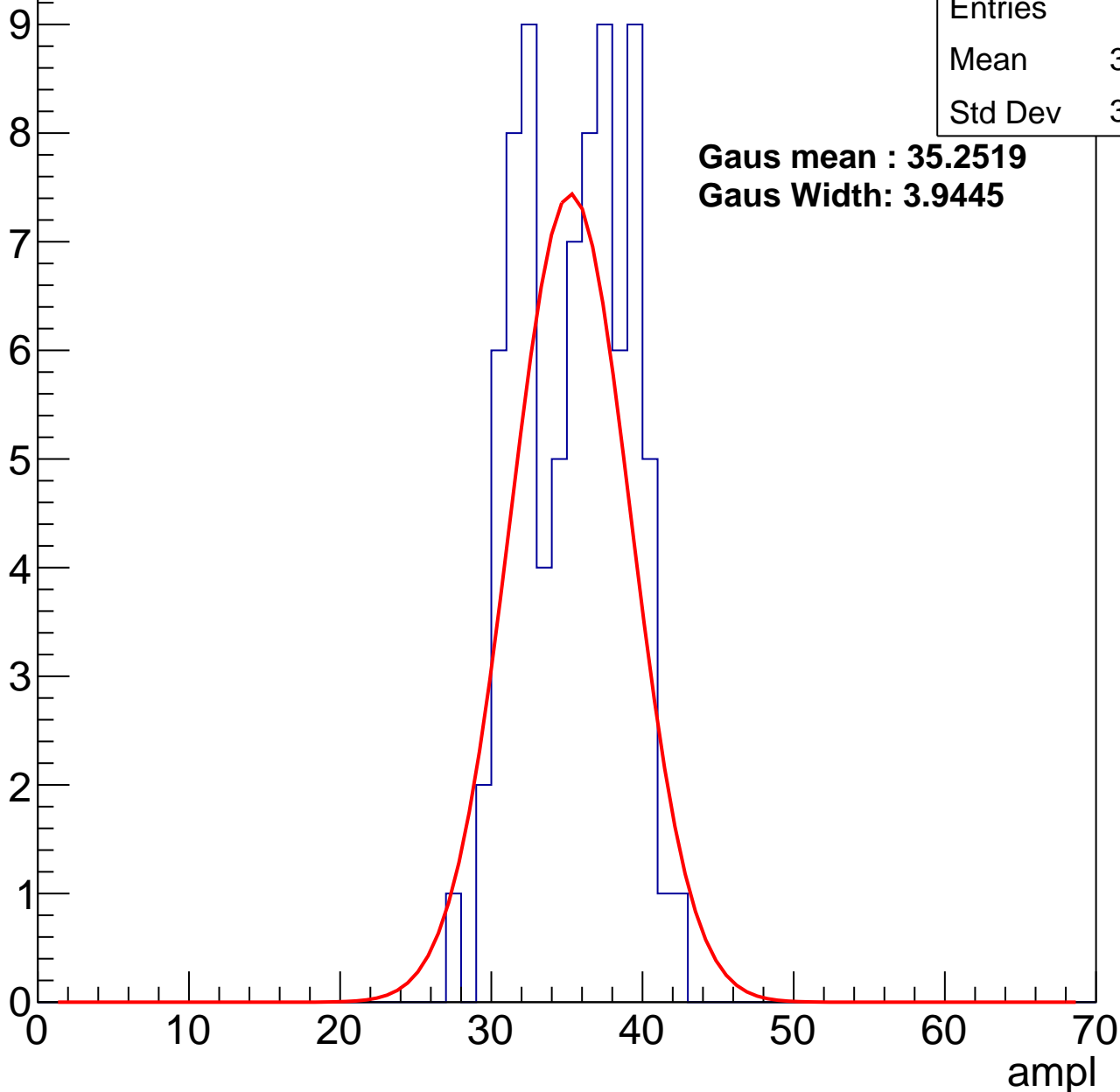
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	34.95
Std Dev	3.457

Gaus mean : 35.2519

Gaus Width: 3.9445



B1L100S, U14-ch4, adc2

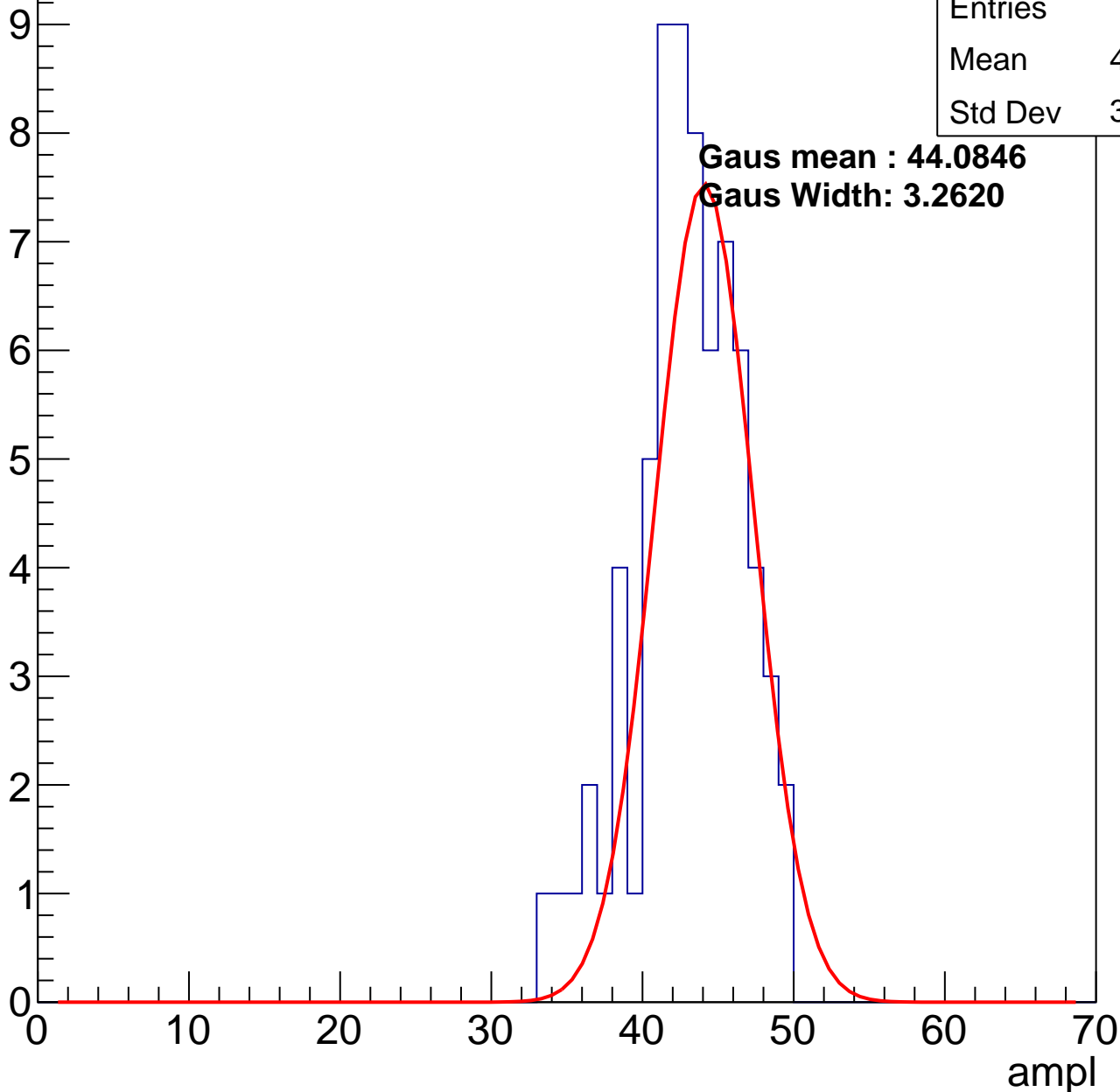
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.54
Std Dev	3.524

Gaus mean : 44.0846

Gaus Width: 3.2620

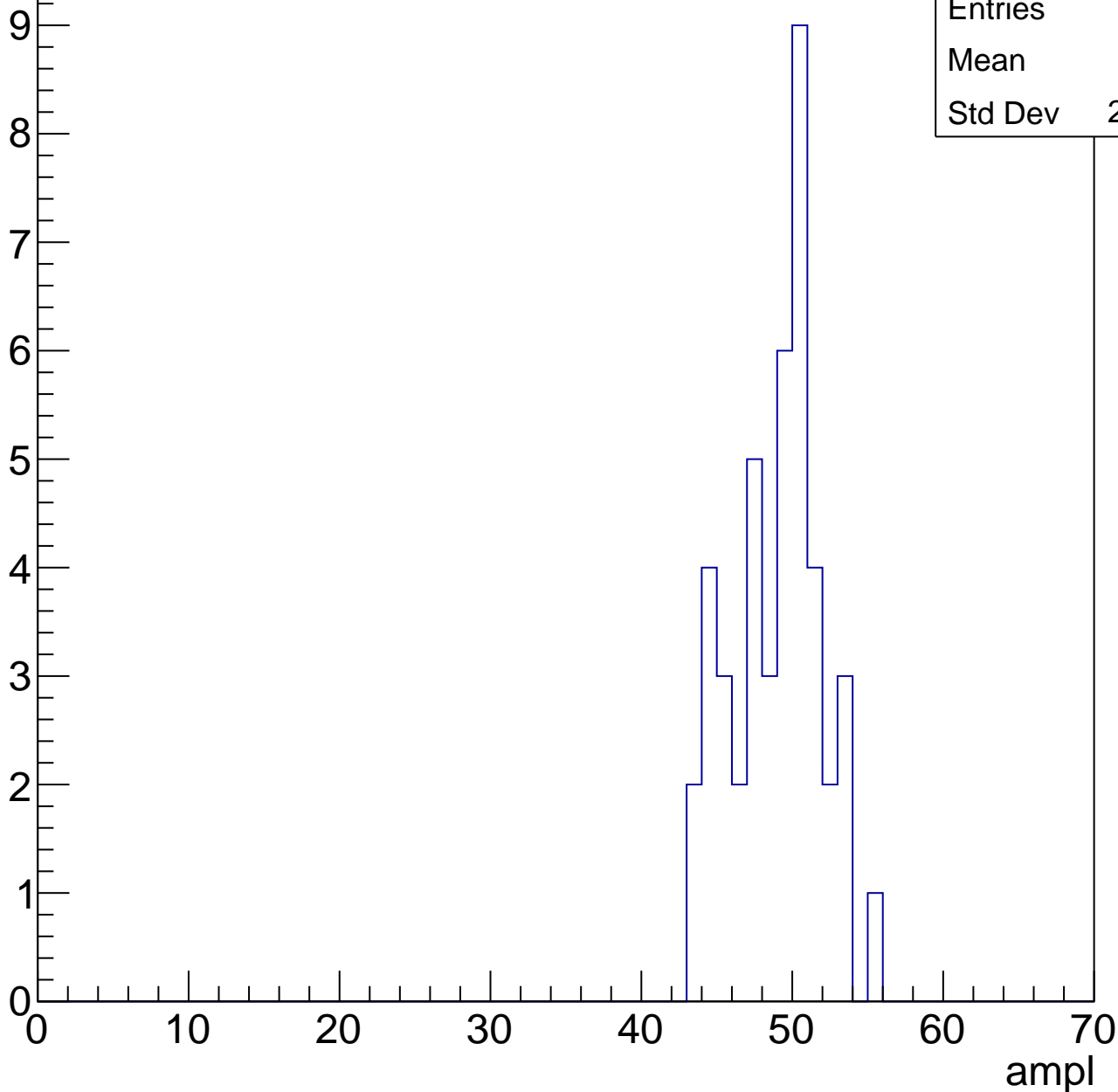


B1L100S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	48.5
Std Dev	2.927

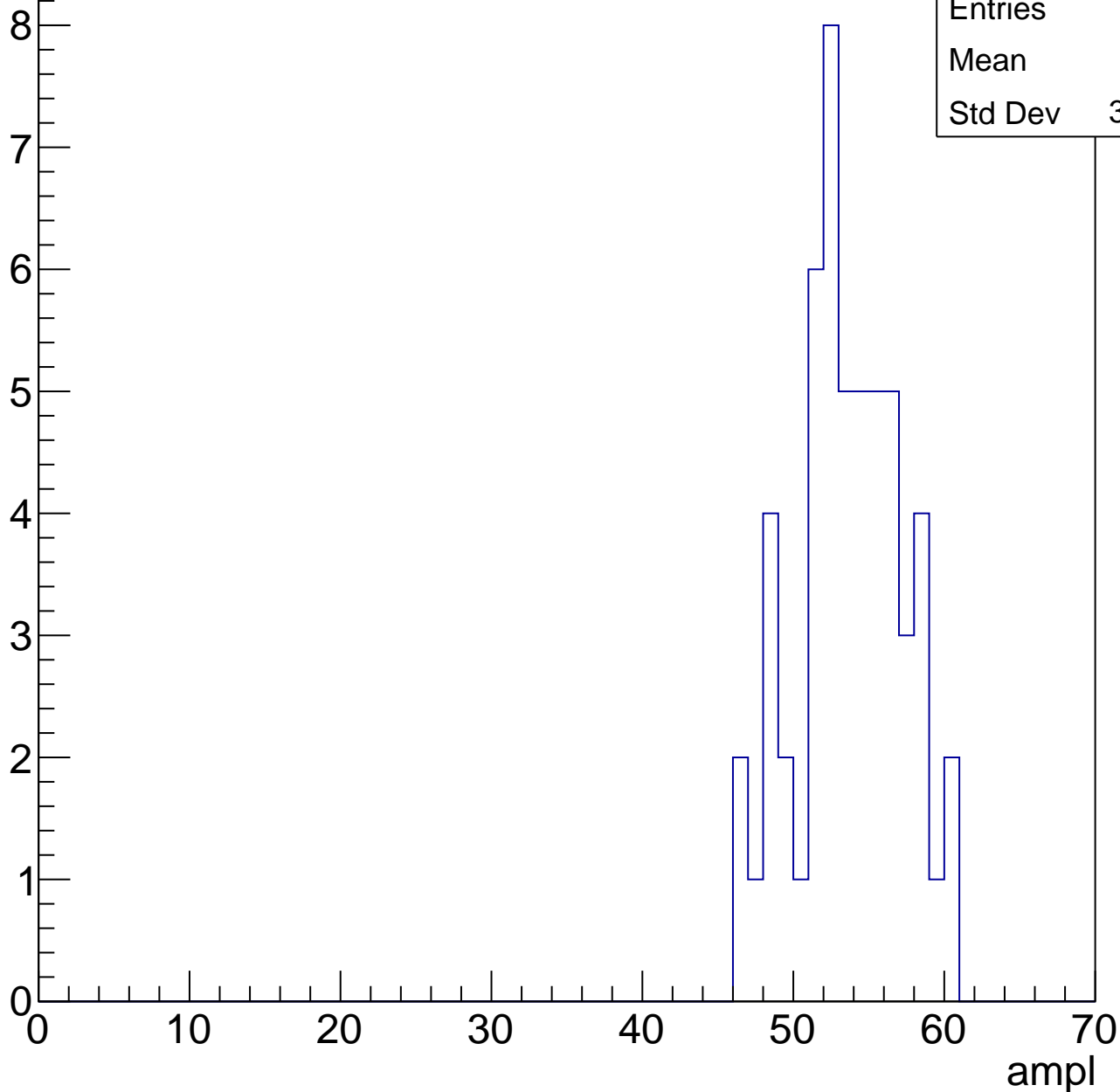


B1L100S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

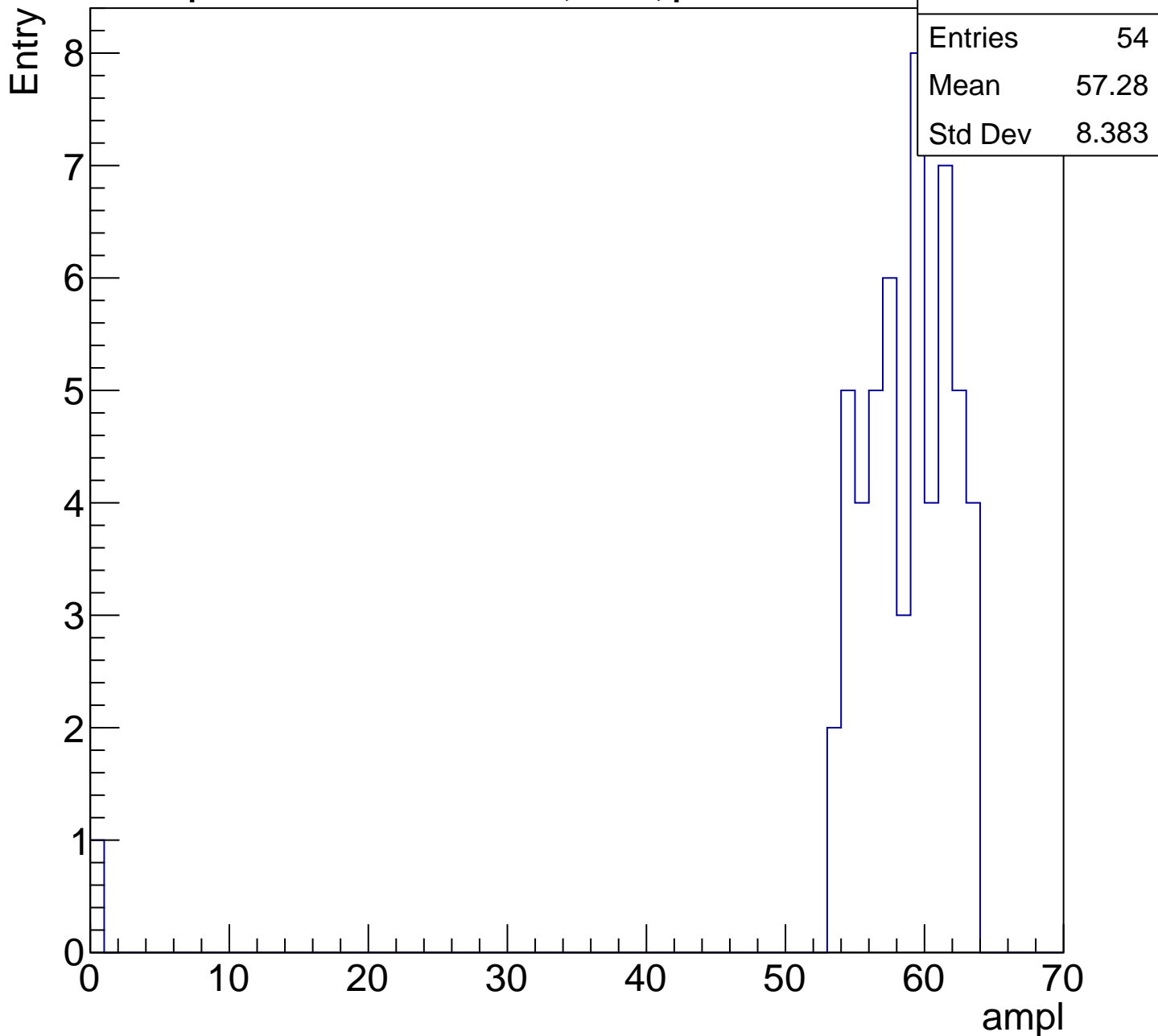
Entry

Entries	54
Mean	53.2
Std Dev	3.487



B1L100S, U14-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

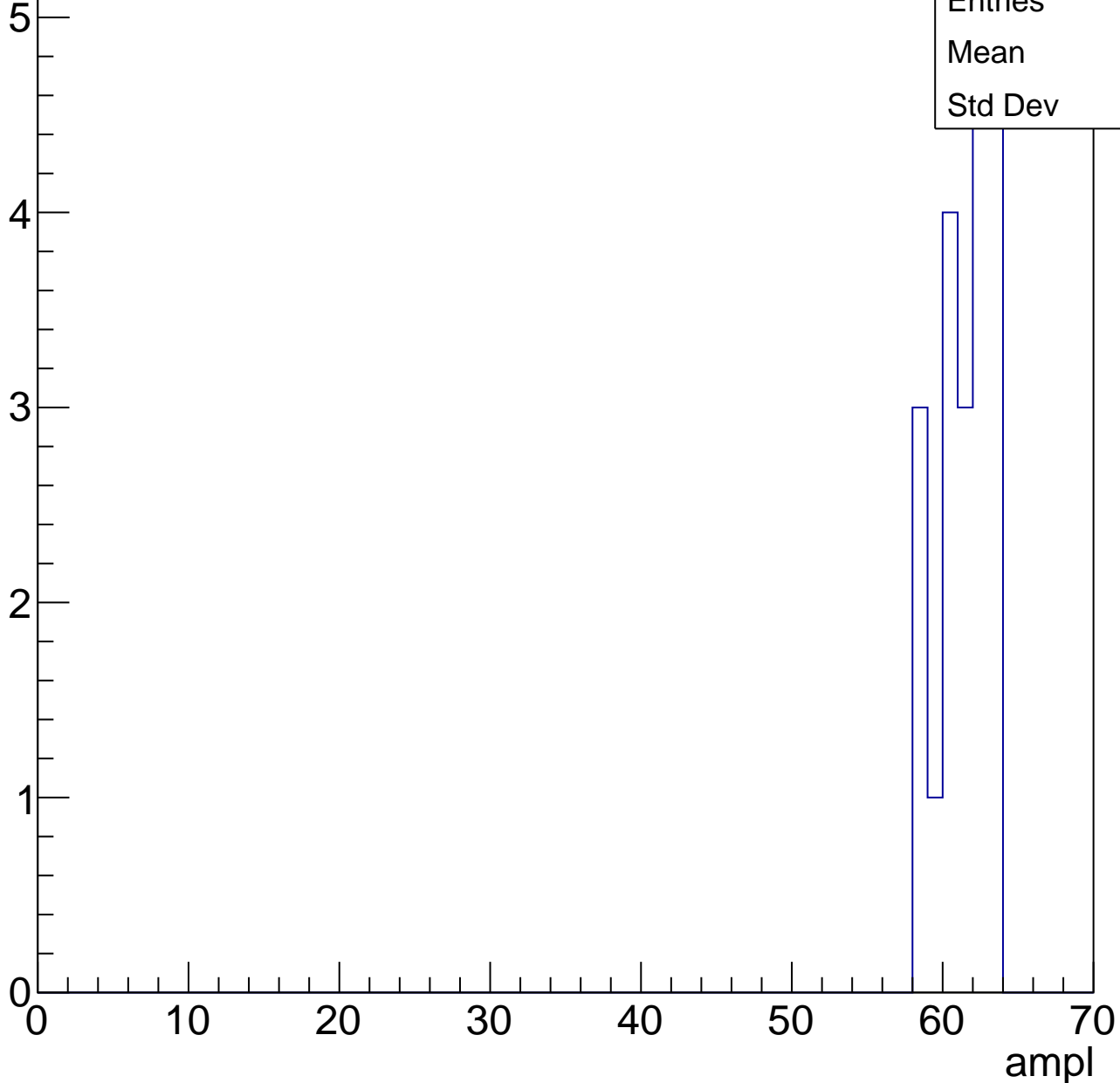


B1L100S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

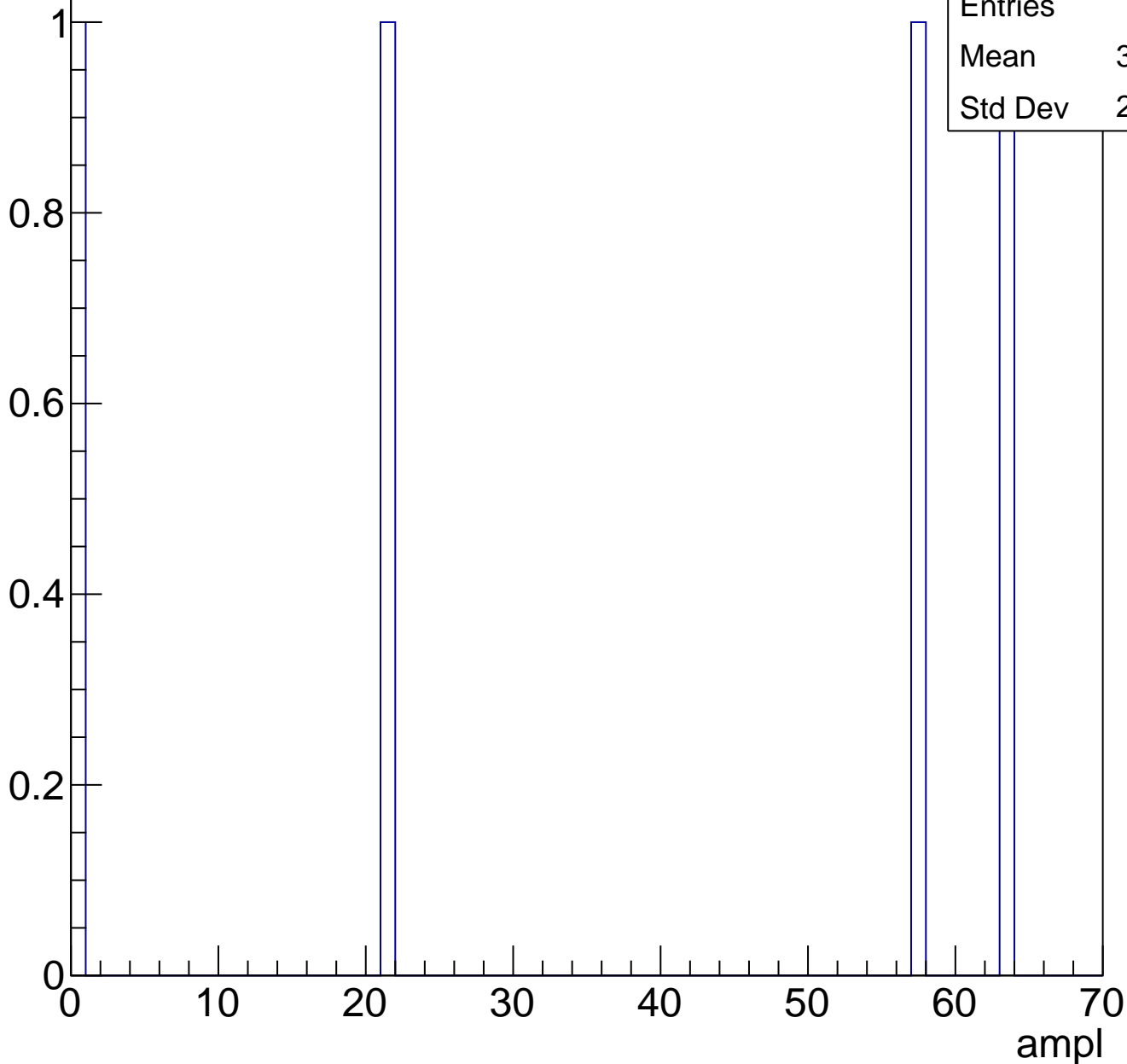
Entries	21
Mean	61
Std Dev	1.69



B1L100S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch5, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.17
Std Dev	3.664

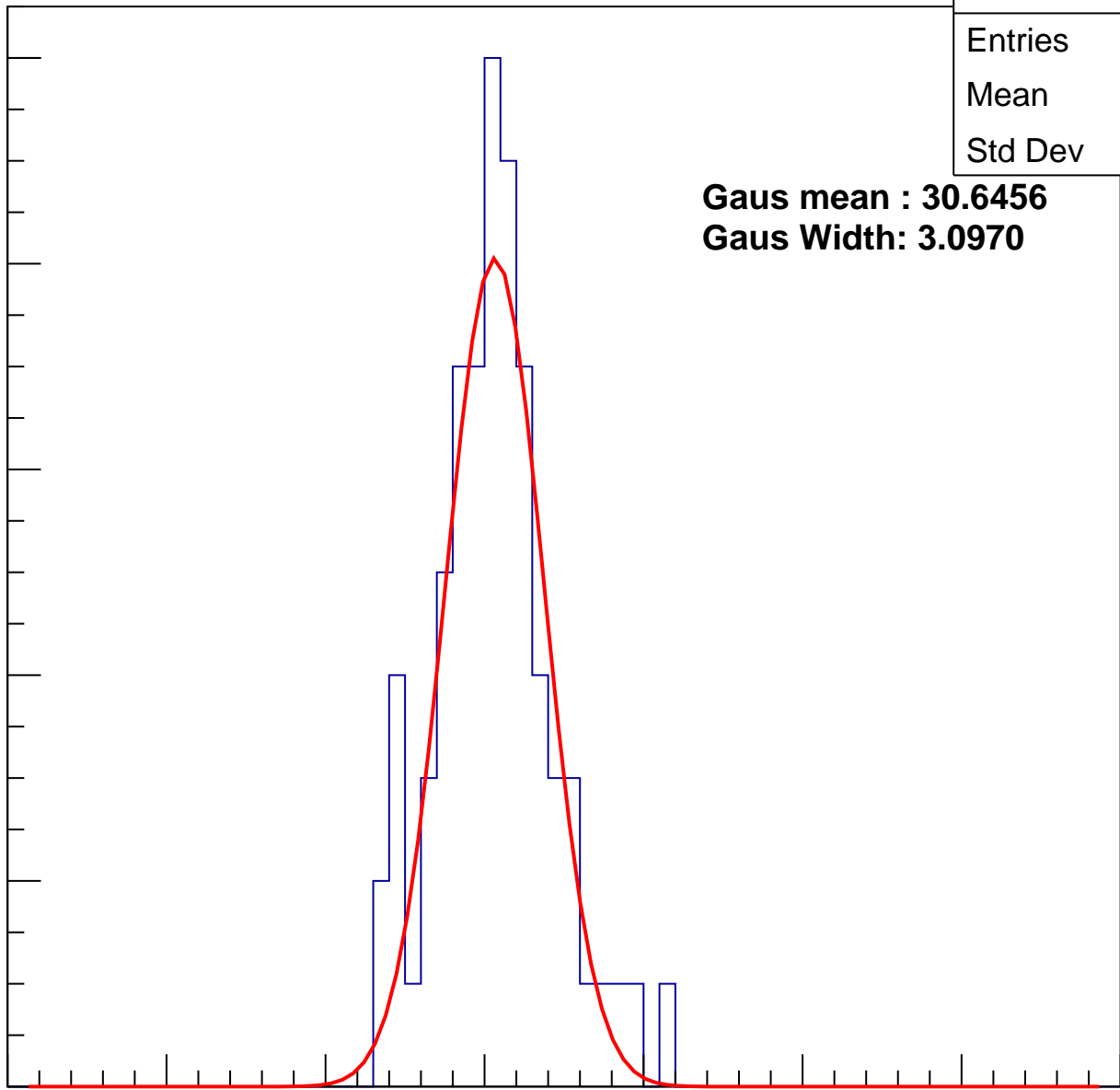
Gaus mean : 30.6456

Gaus Width: 3.0970

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch5, adc1

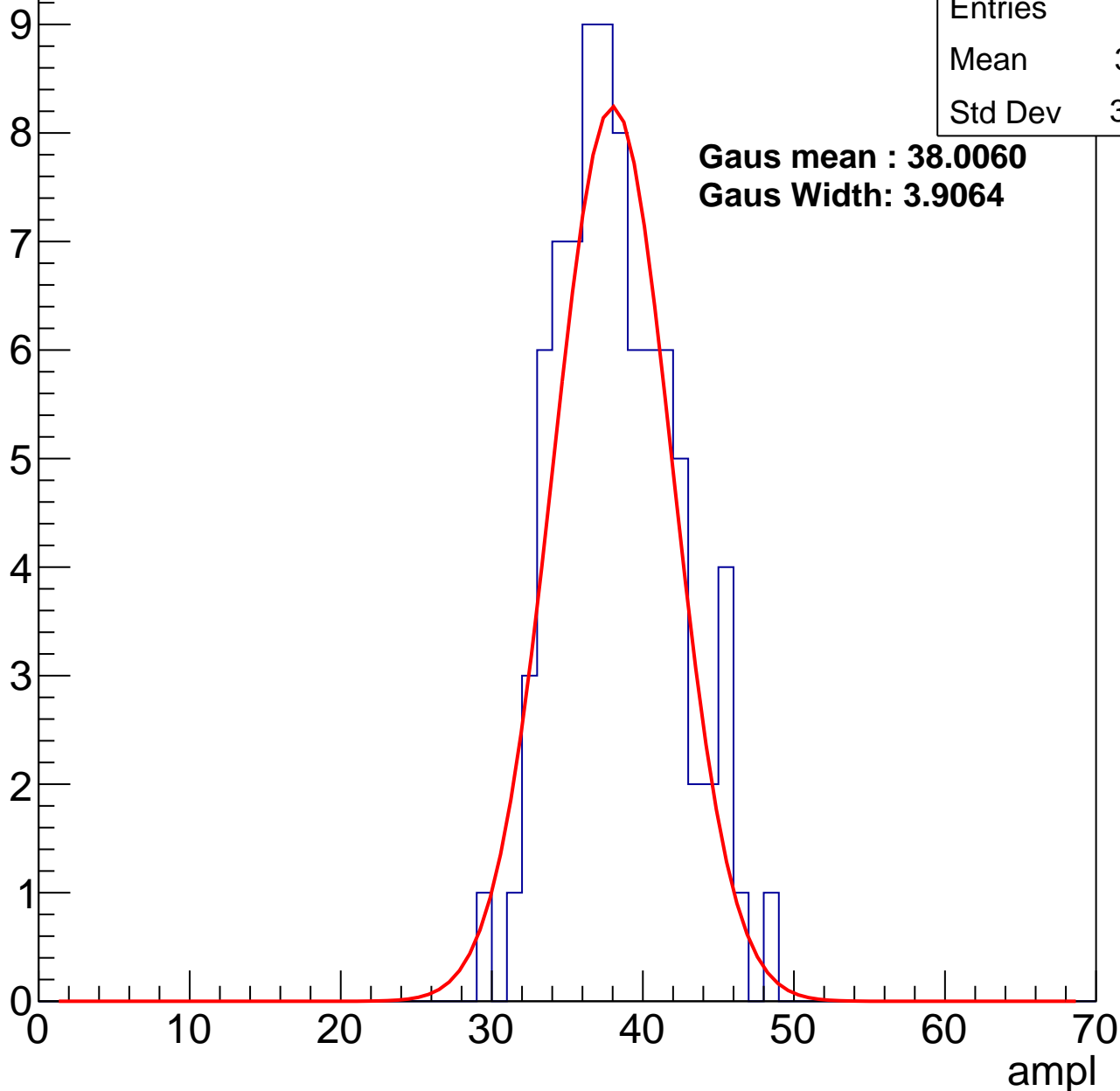
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	37.81
Std Dev	3.859

Gaus mean : 38.0060

Gaus Width: 3.9064



B1L100S, U14-ch5, adc2

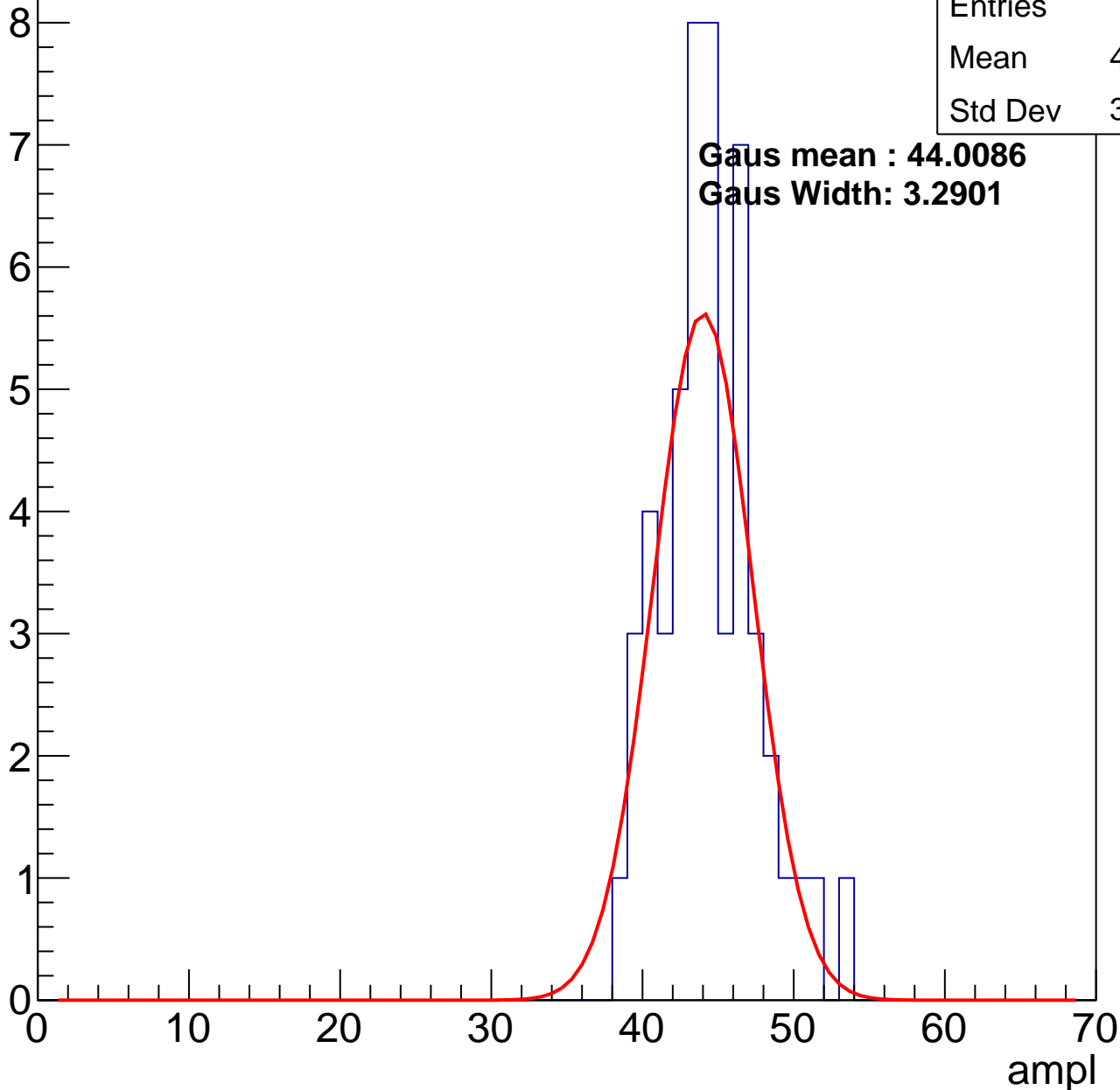
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	43.94
Std Dev	3.165

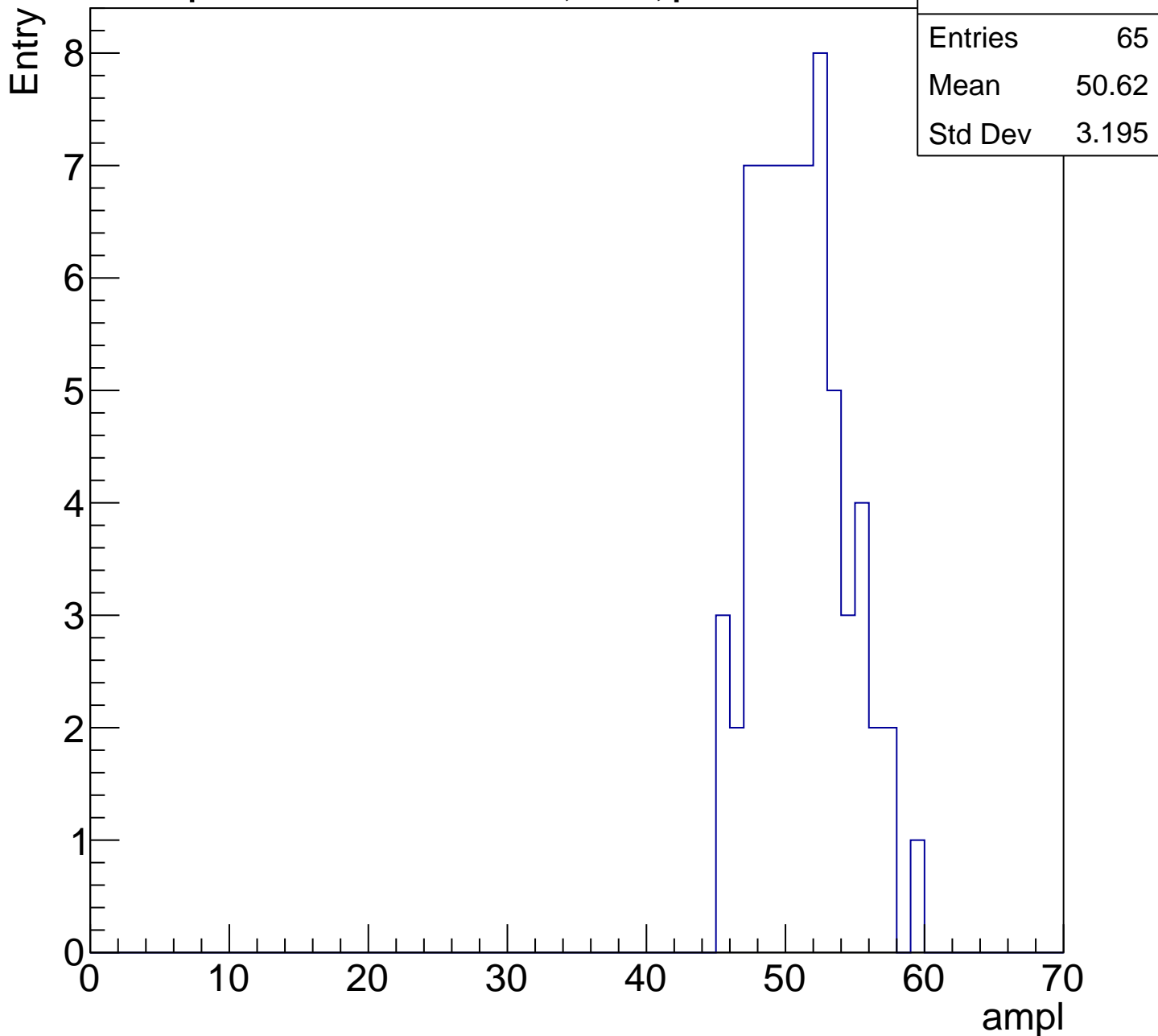
Gaus mean : 44.0086

Gaus Width: 3.2901



B1L100S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 57

Mean 55.91

Std Dev 3.125

0

0

10

20

30

40

50

60

70

ampl

0

1

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322

B1L100S, U14-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.15
Std Dev	9.578

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L100S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch6, adc0

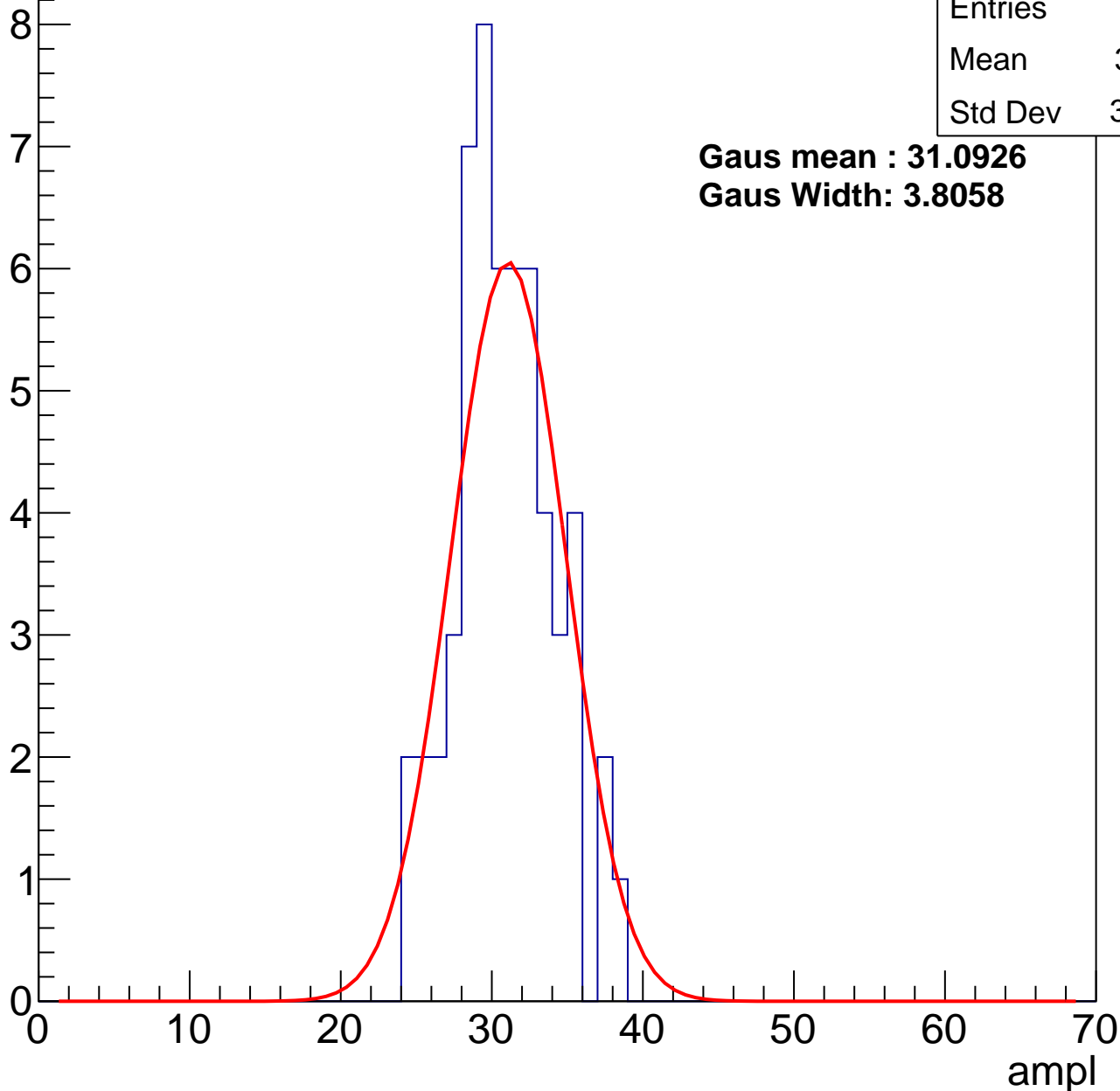
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.41
Std Dev	3.228

Gaus mean : 31.0926

Gaus Width: 3.8058



B1L100S, U14-ch6, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	36.55
Std Dev	3.406

Gaus mean : 36.8439

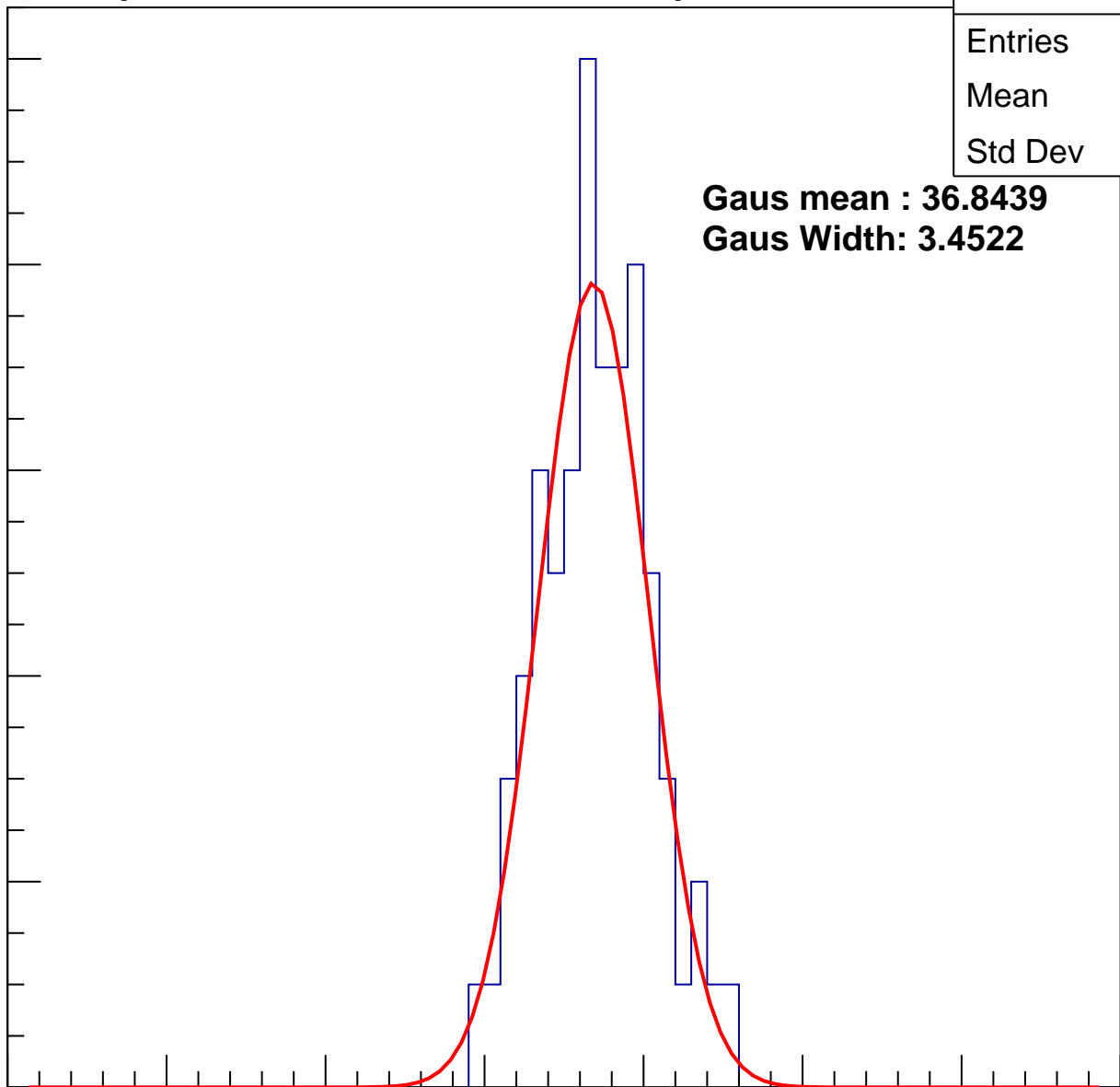
Gaus Width: 3.4522

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch6, adc2

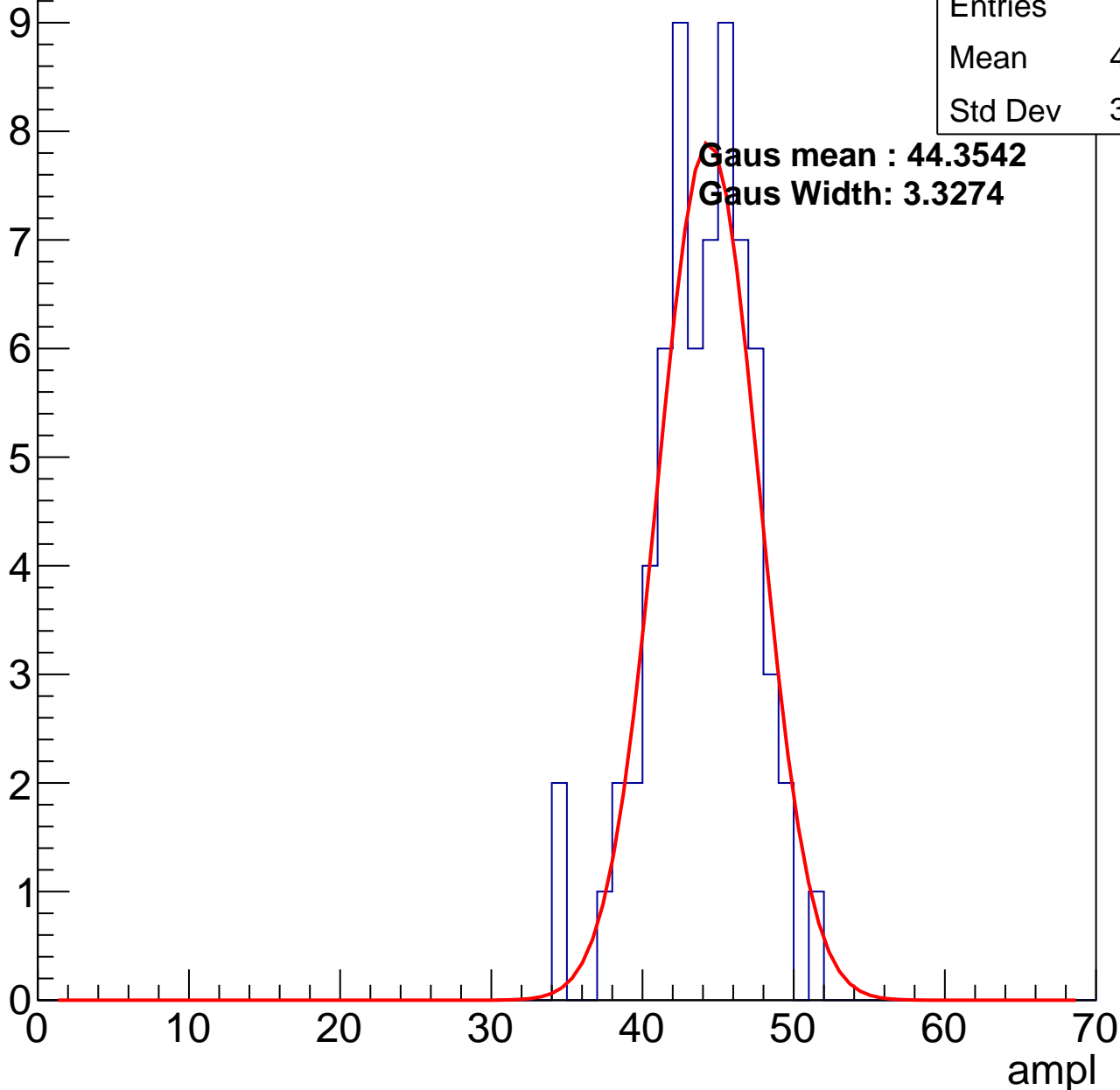
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.45
Std Dev	3.343

Gaus mean : 44.3542

Gaus Width: 3.3274

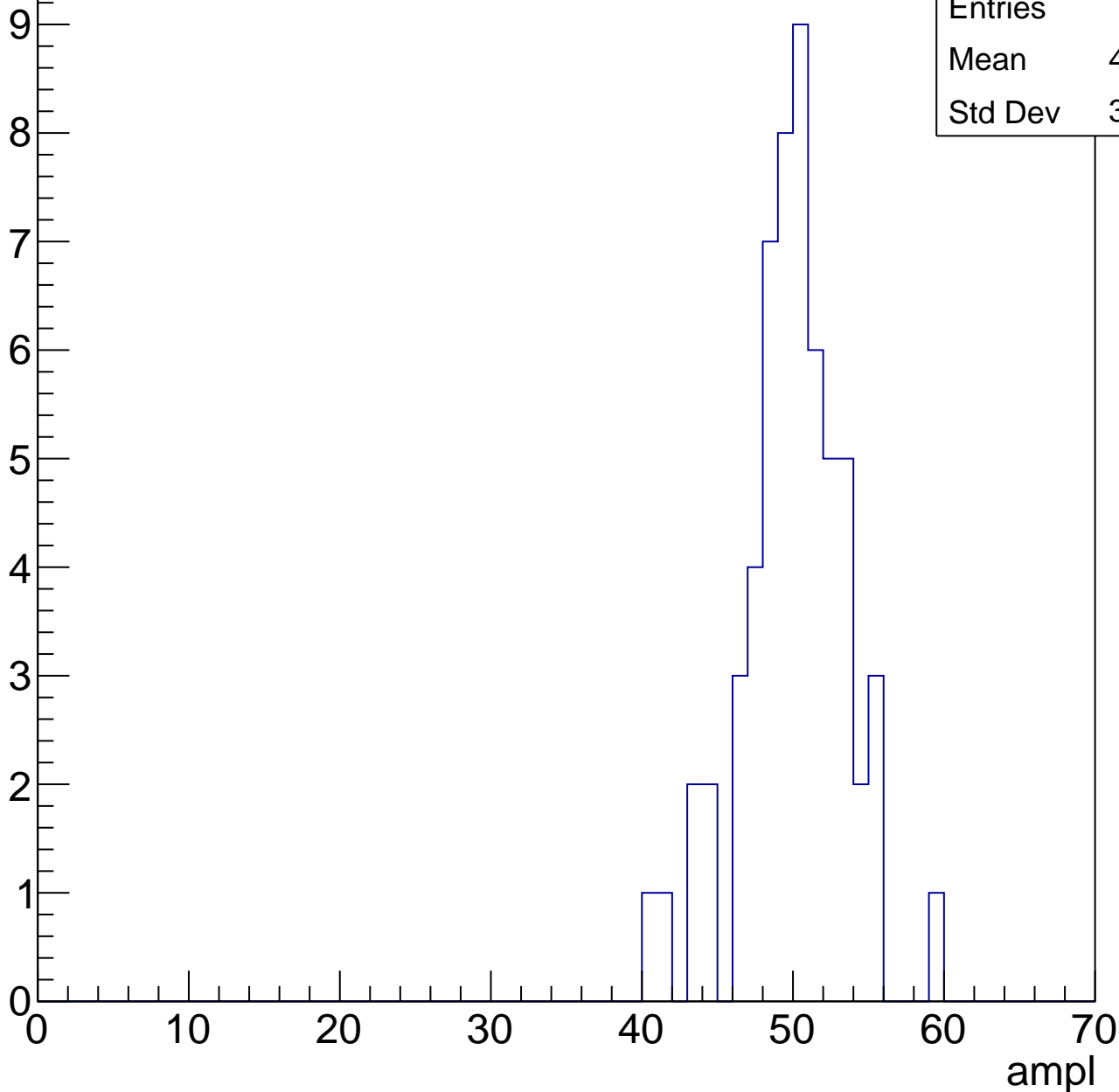


B1L100S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.53
Std Dev	3.485



B1L100S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 51

Mean 55.51

Std Dev 2.768

ampl

0

10

20

30

40

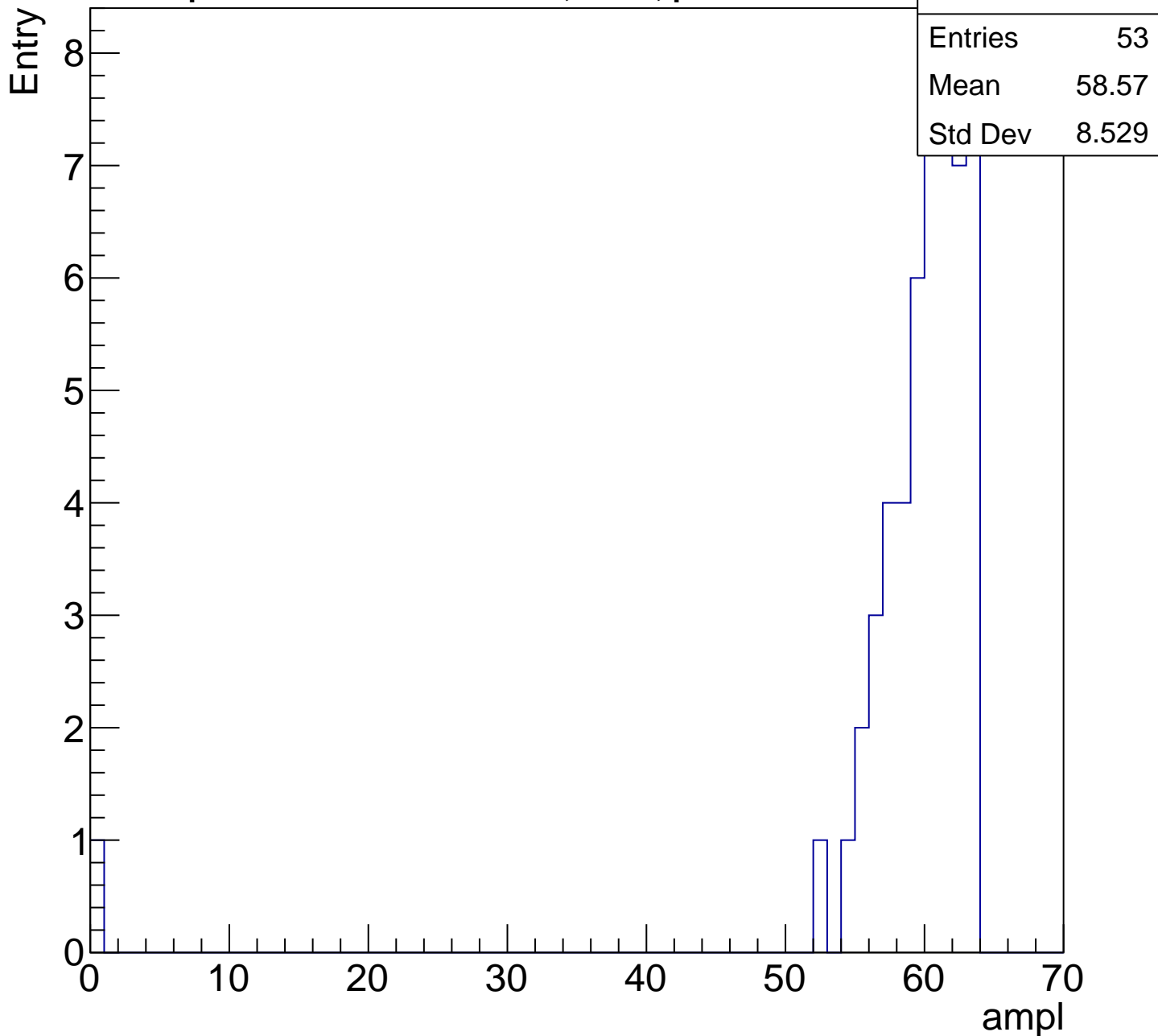
50

60

70

B1L100S, U14-ch6, adc5

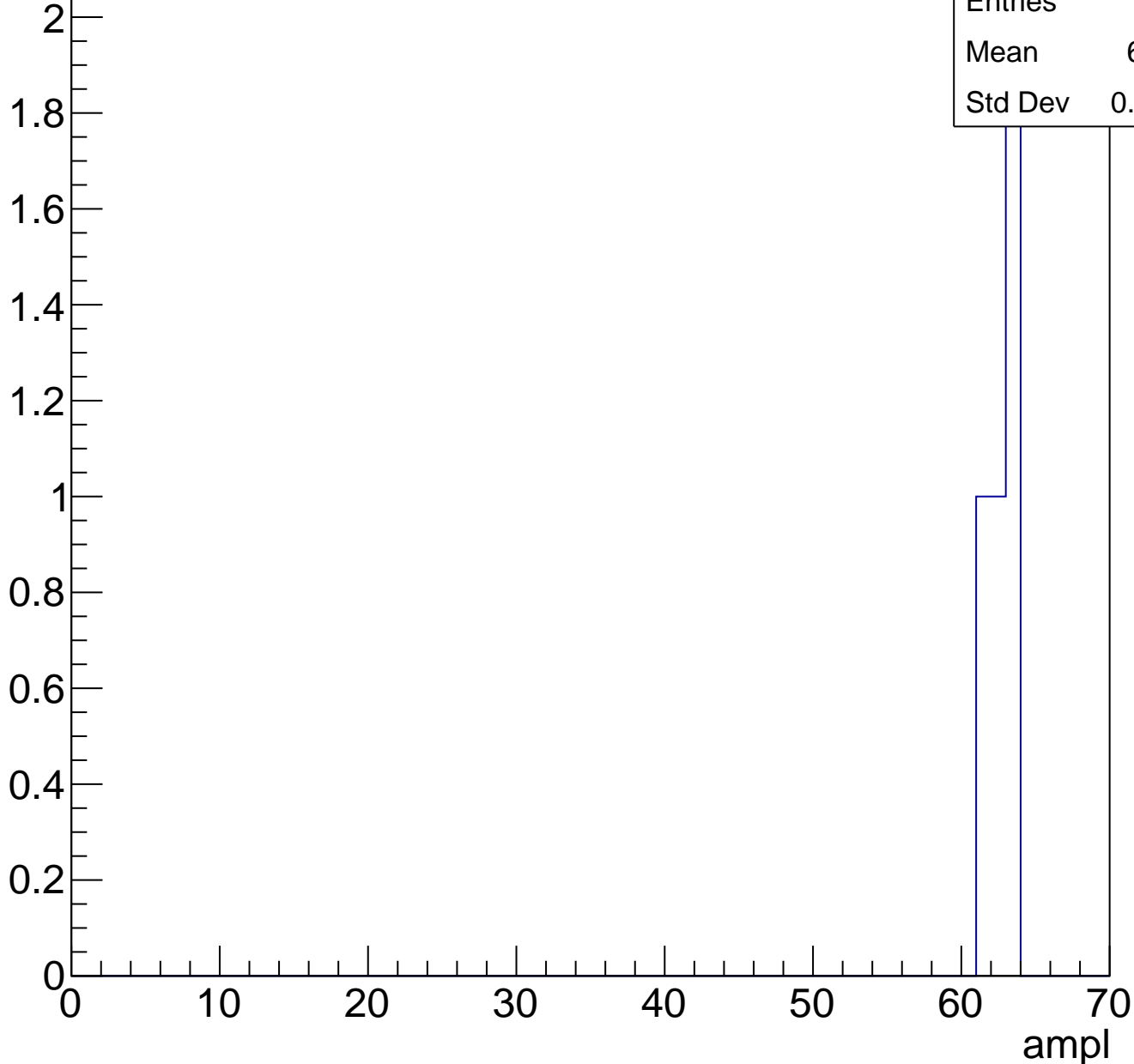
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

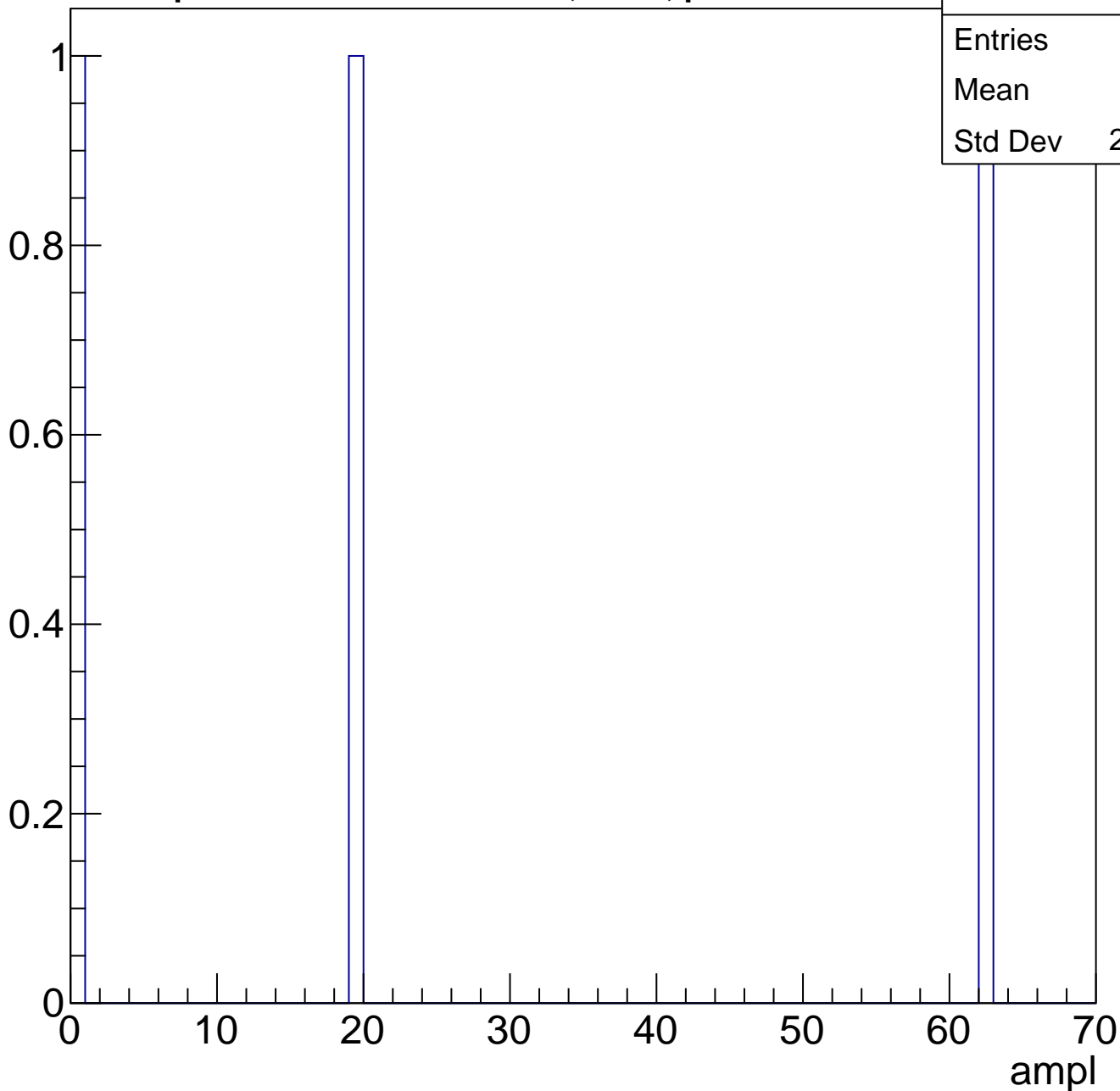
Entry



B1L100S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	27
Std Dev	25.94

B1L100S, U14-ch7, adc0

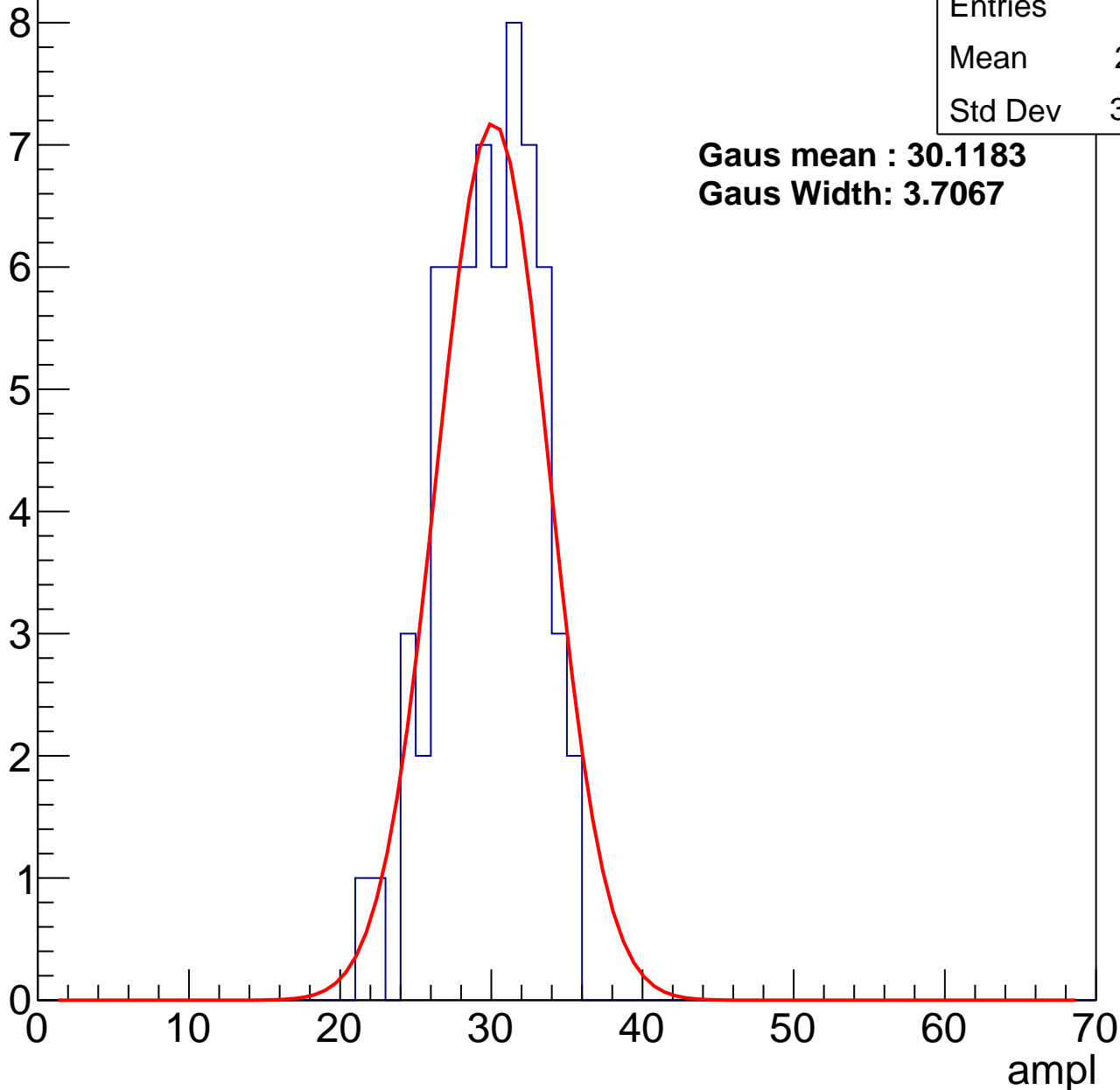
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.31
Std Dev	3.167

Gaus mean : 30.1183

Gaus Width: 3.7067



B1L100S, U14-ch7, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	36.3
Std Dev	3.744

Gaus mean : 37.0647

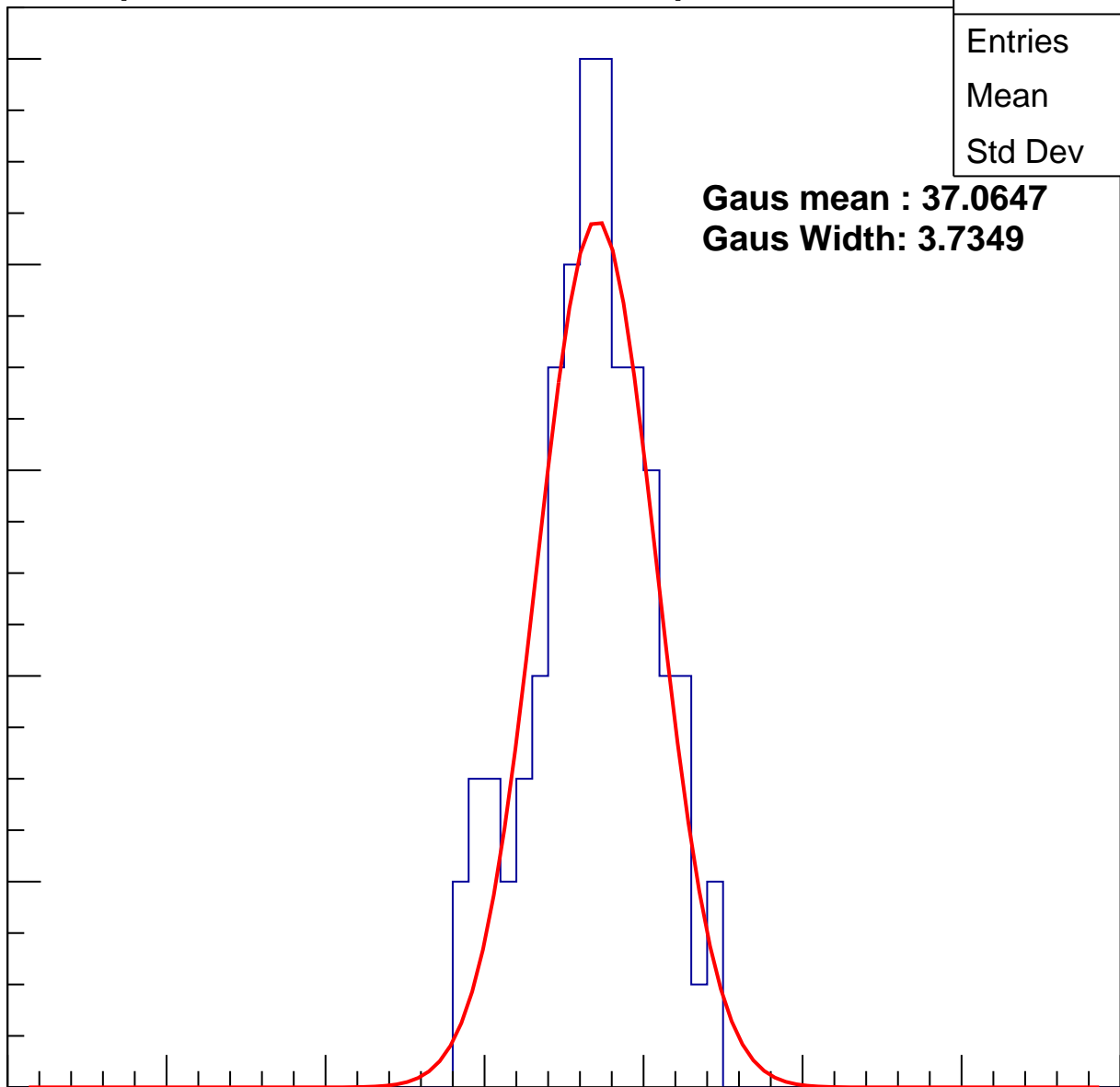
Gaus Width: 3.7349

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch7, adc2

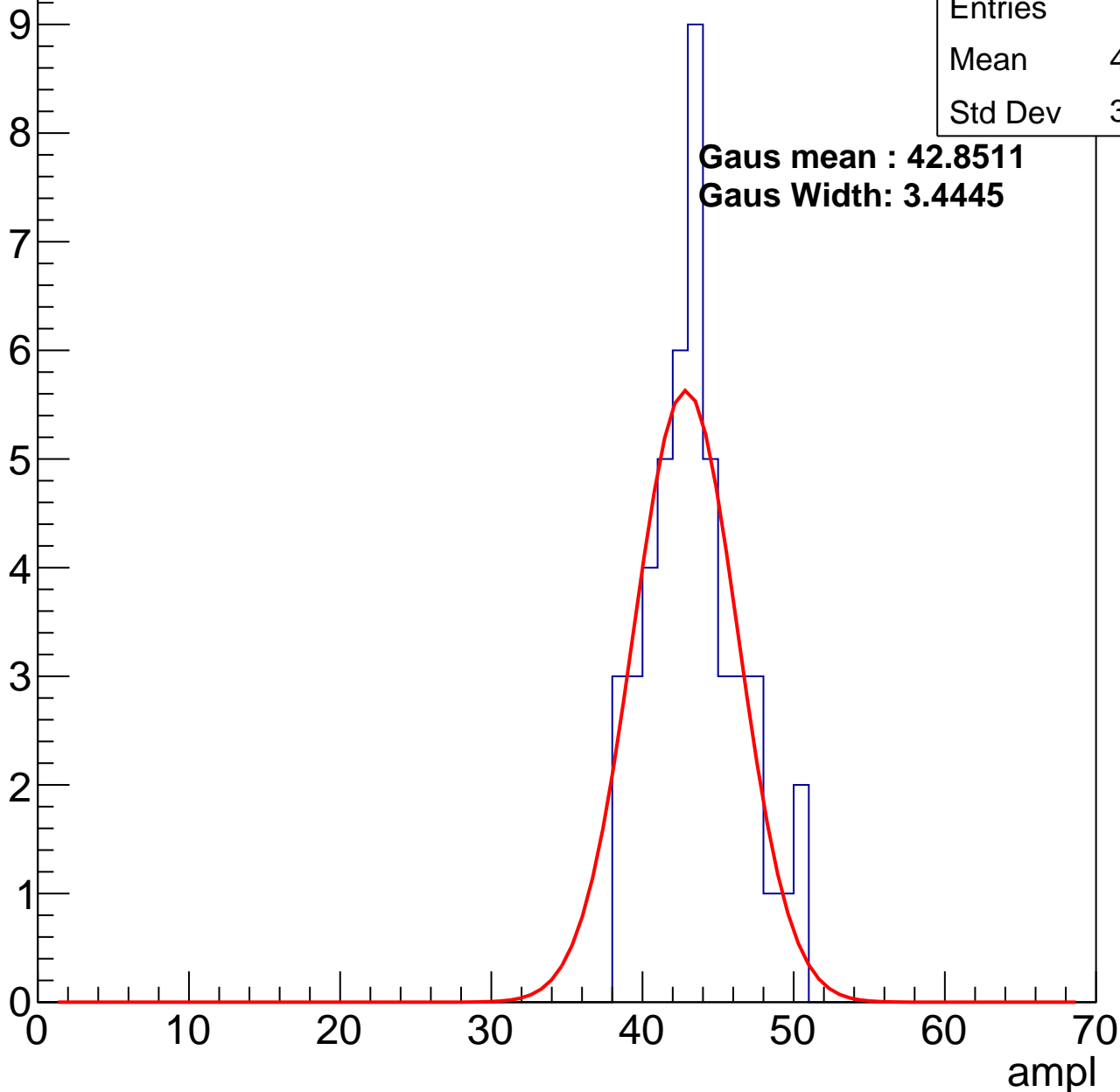
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	43.04
Std Dev	3.014

Gaus mean : 42.8511

Gaus Width: 3.4445

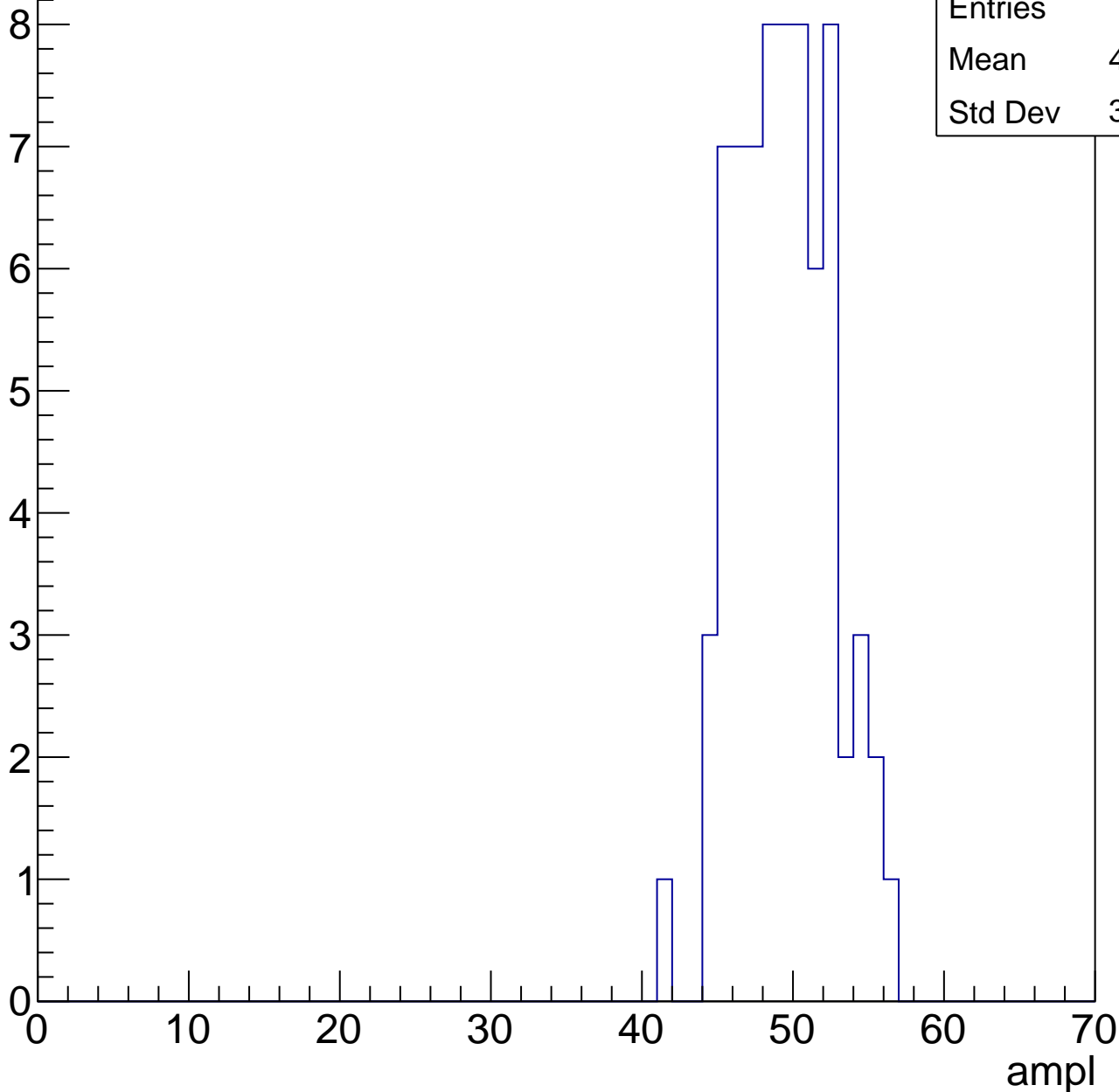


B1L100S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	48.89
Std Dev	3.097

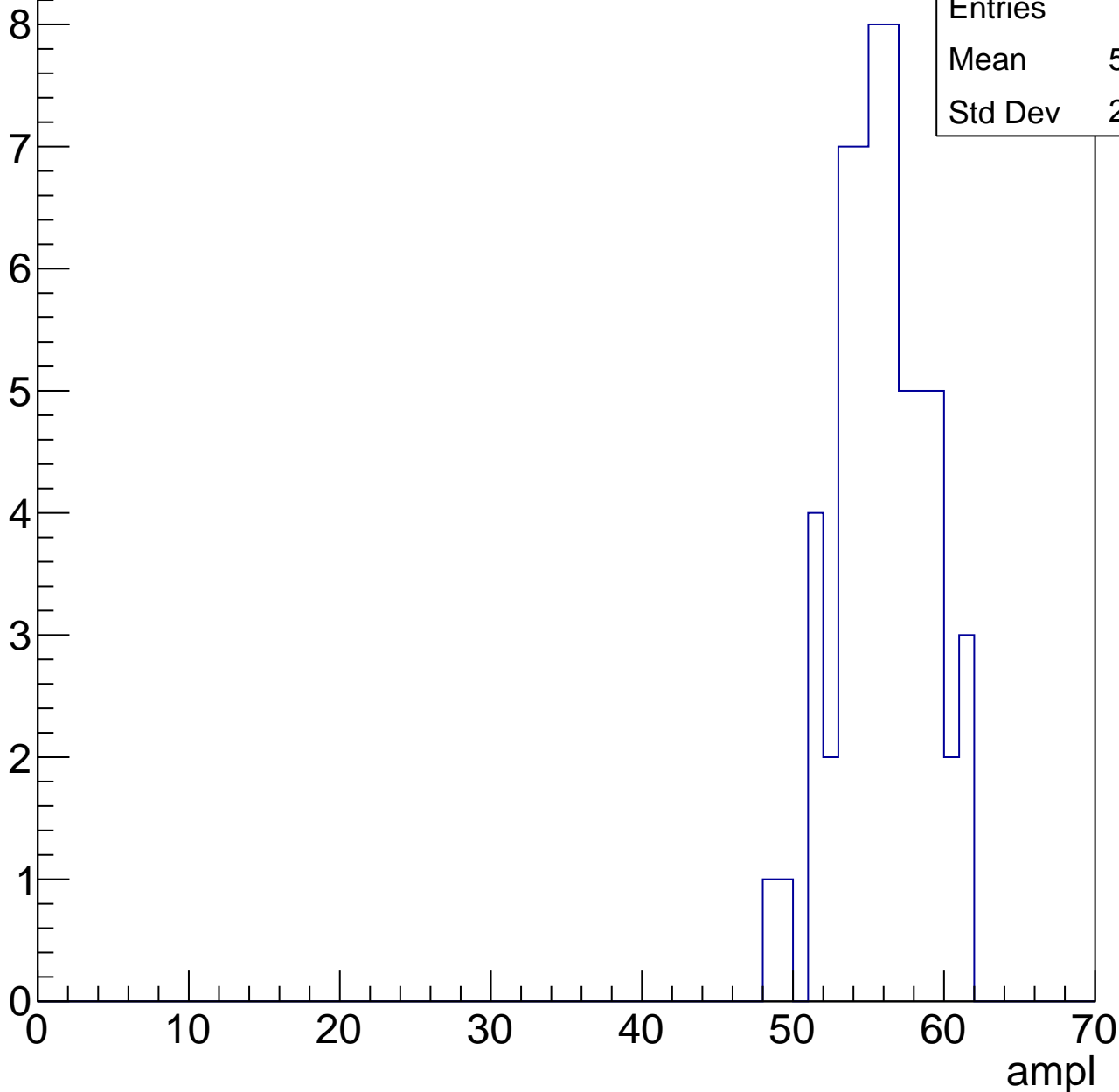


B1L100S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.43
Std Dev	2.943

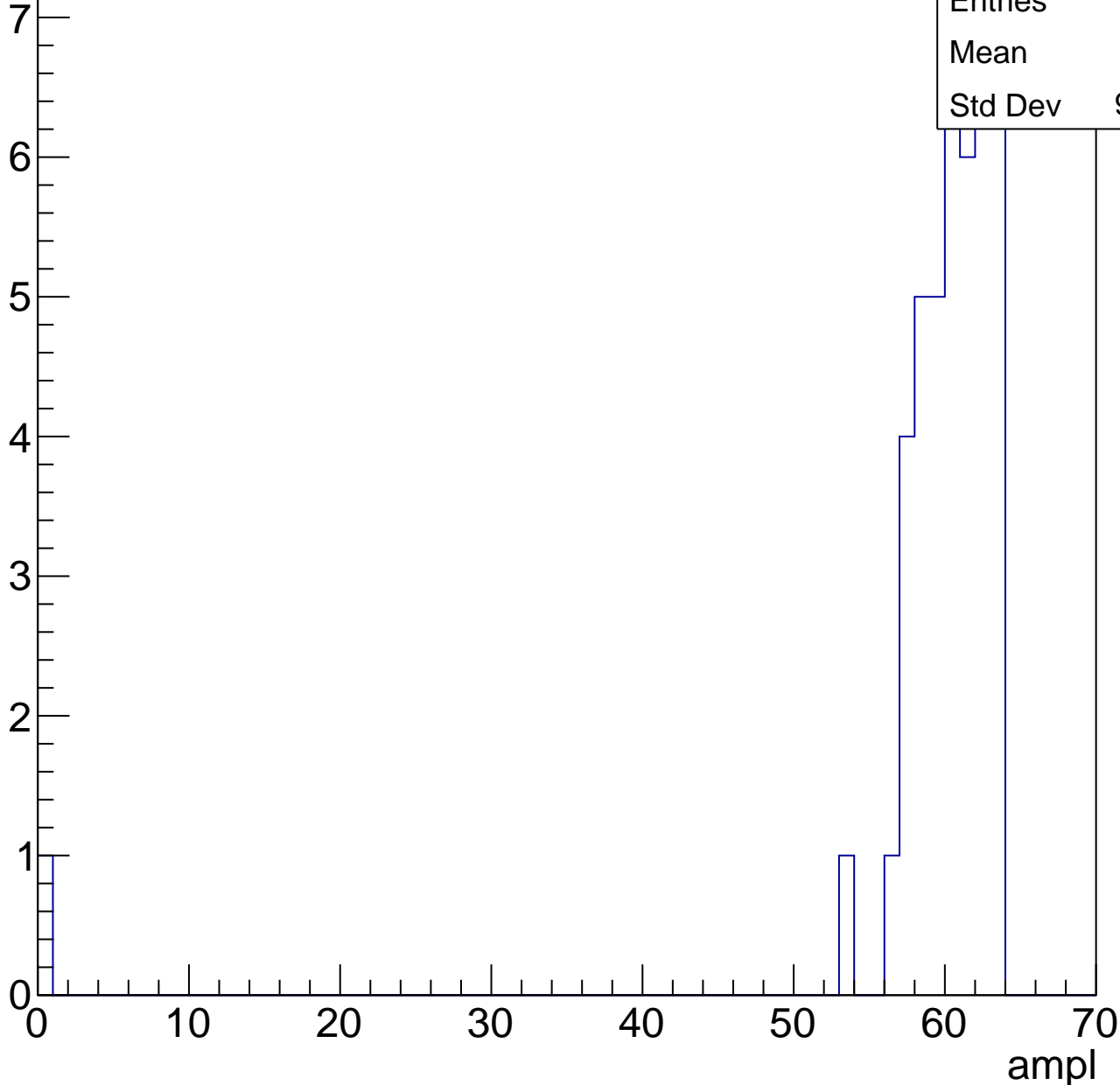


B1L100S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

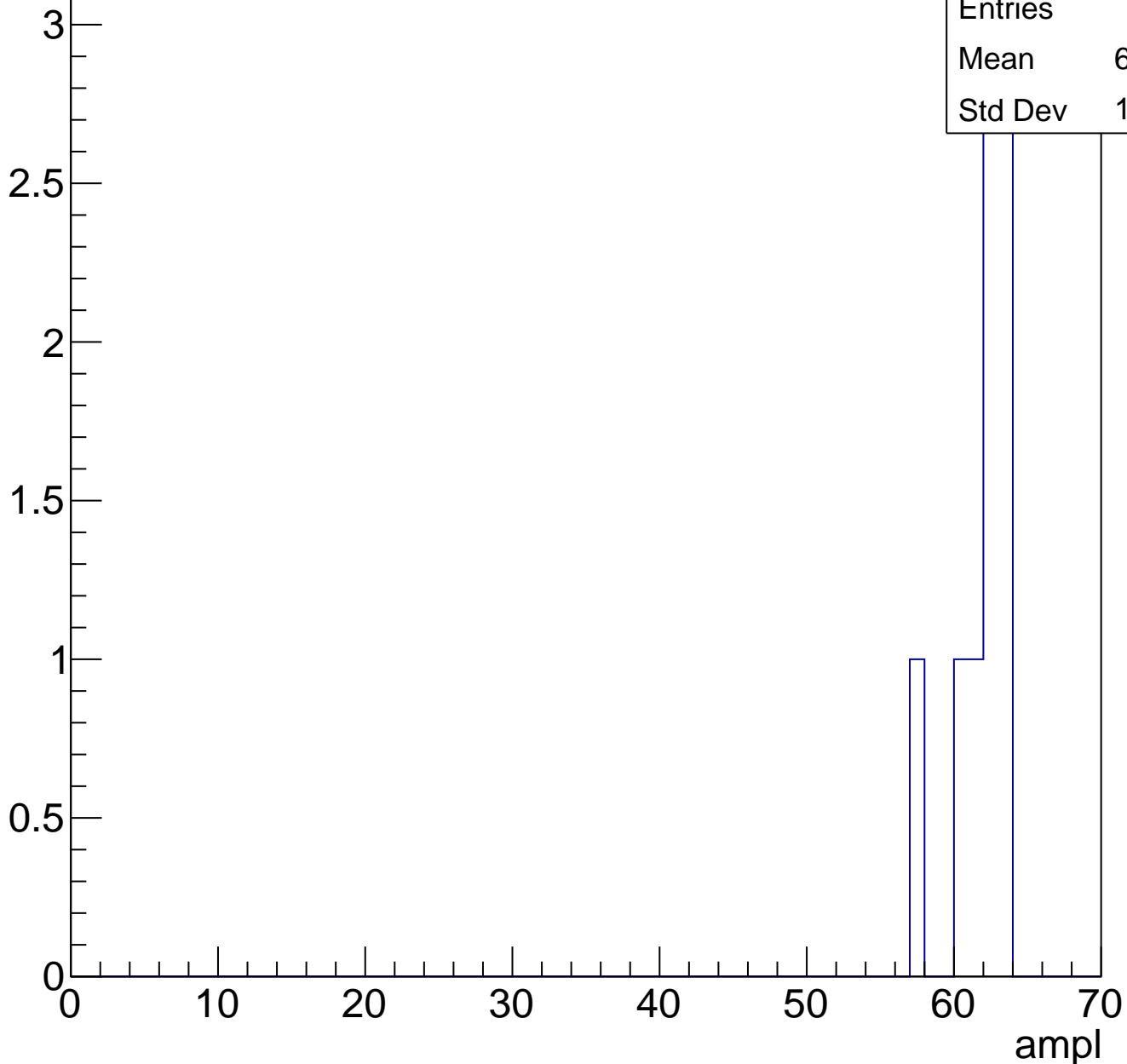
Entries	44
Mean	58.7
Std Dev	9.231



B1L100S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch8, adc0

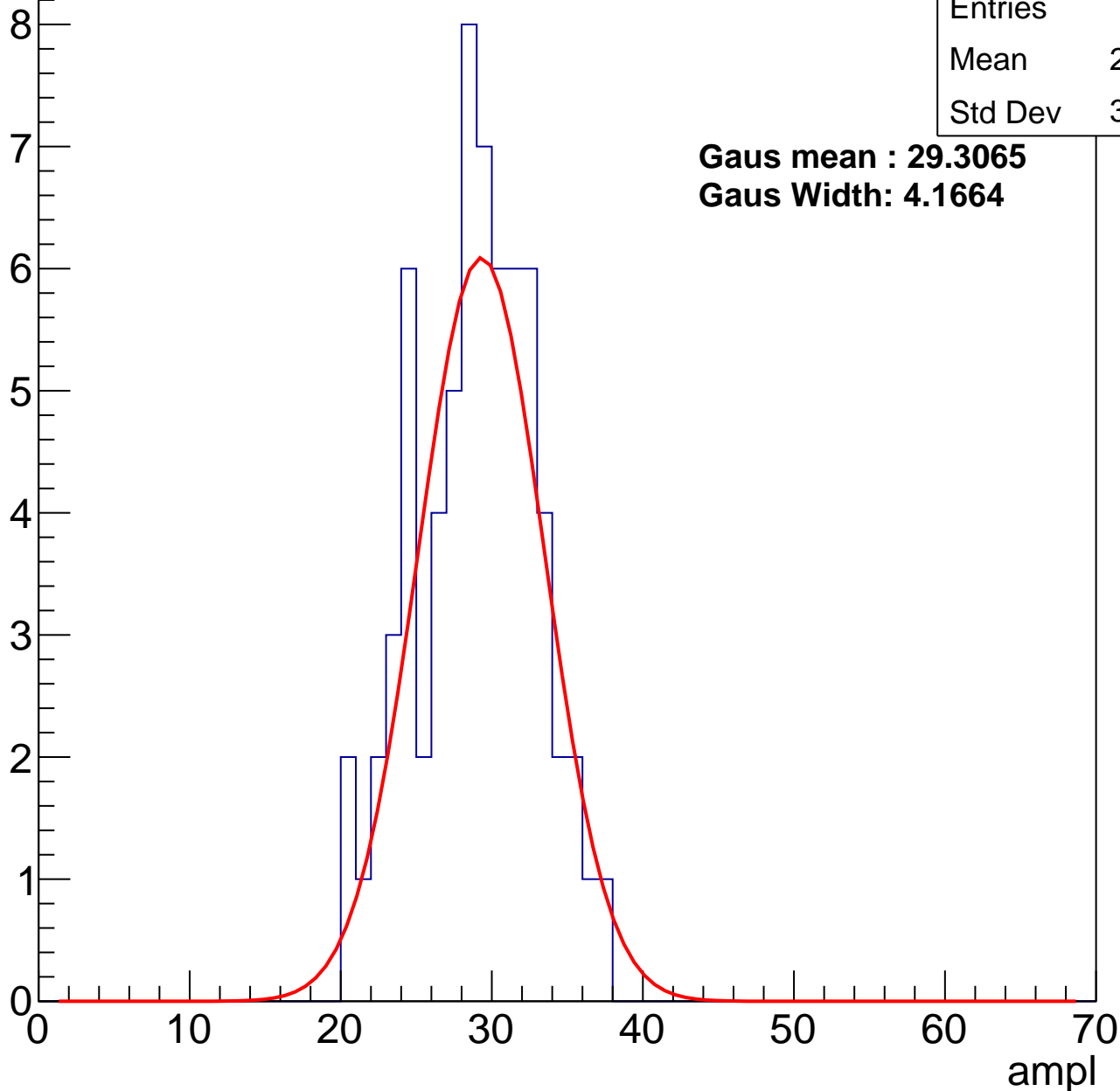
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.46
Std Dev	3.912

Gaus mean : 29.3065

Gaus Width: 4.1664



B1L100S, U14-ch8, adc1

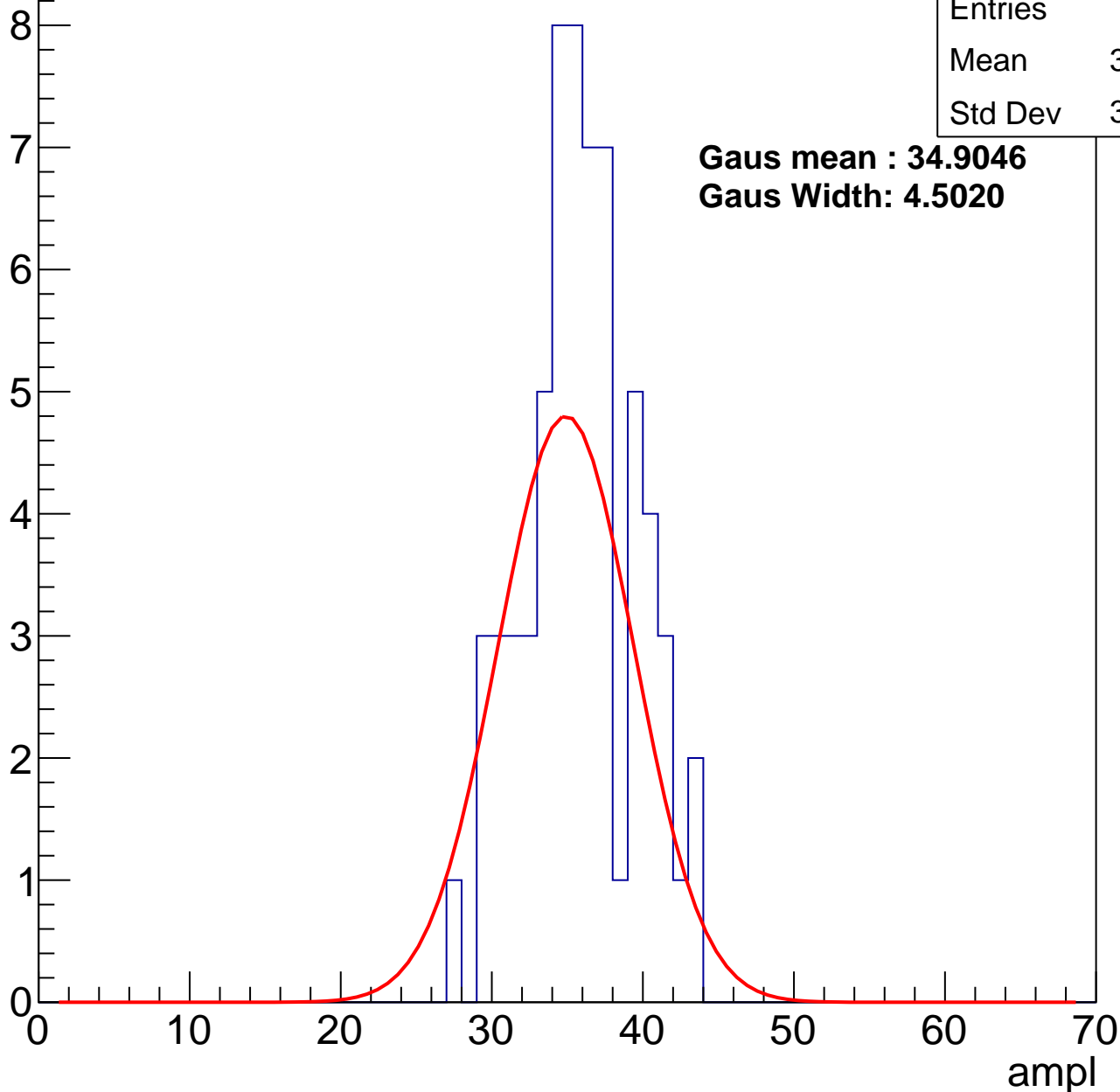
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	35.39
Std Dev	3.643

Gaus mean : 34.9046

Gaus Width: 4.5020



B1L100S, U14-ch8, adc2

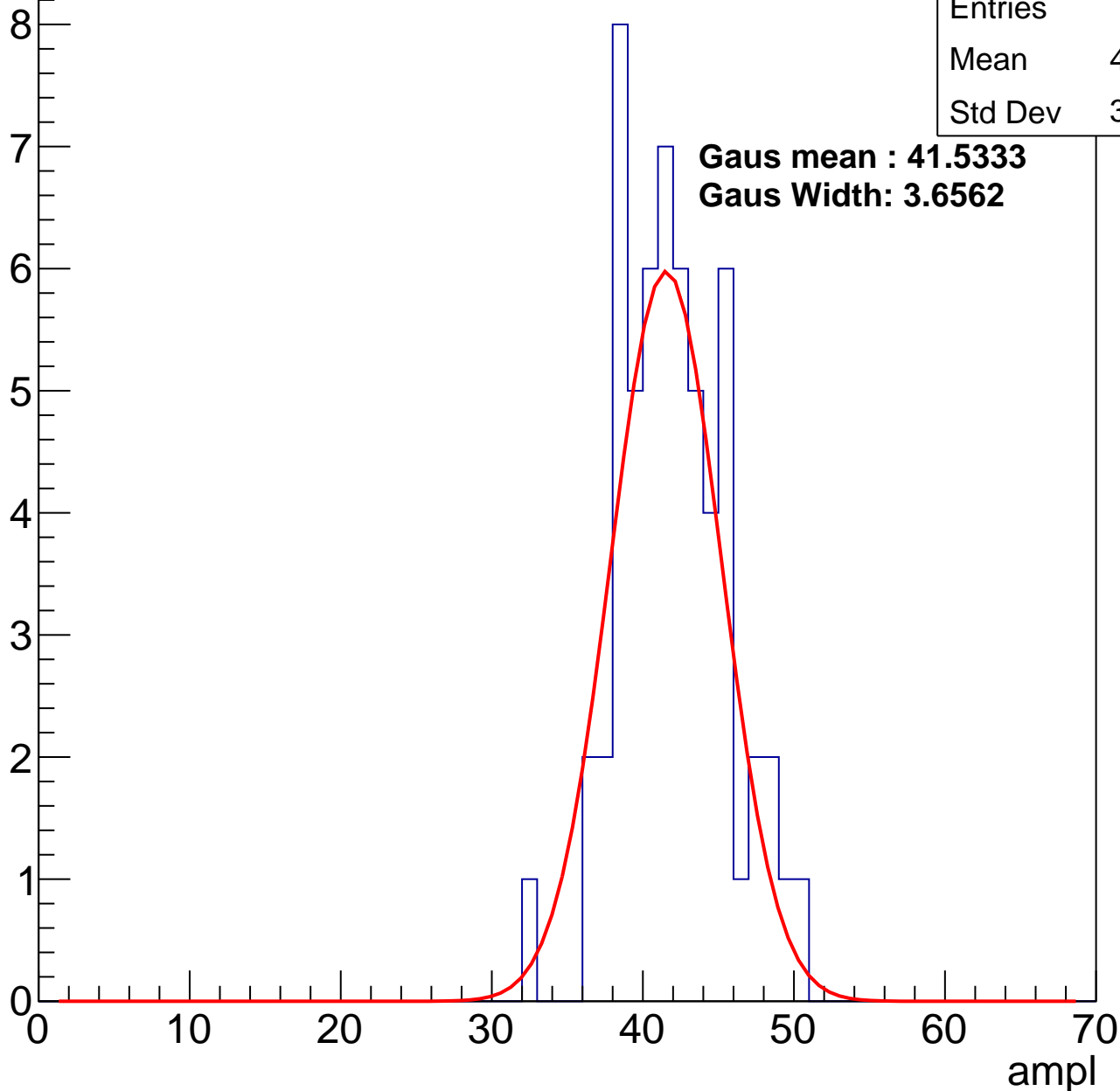
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	41.56
Std Dev	3.543

Gaus mean : 41.5333

Gaus Width: 3.6562

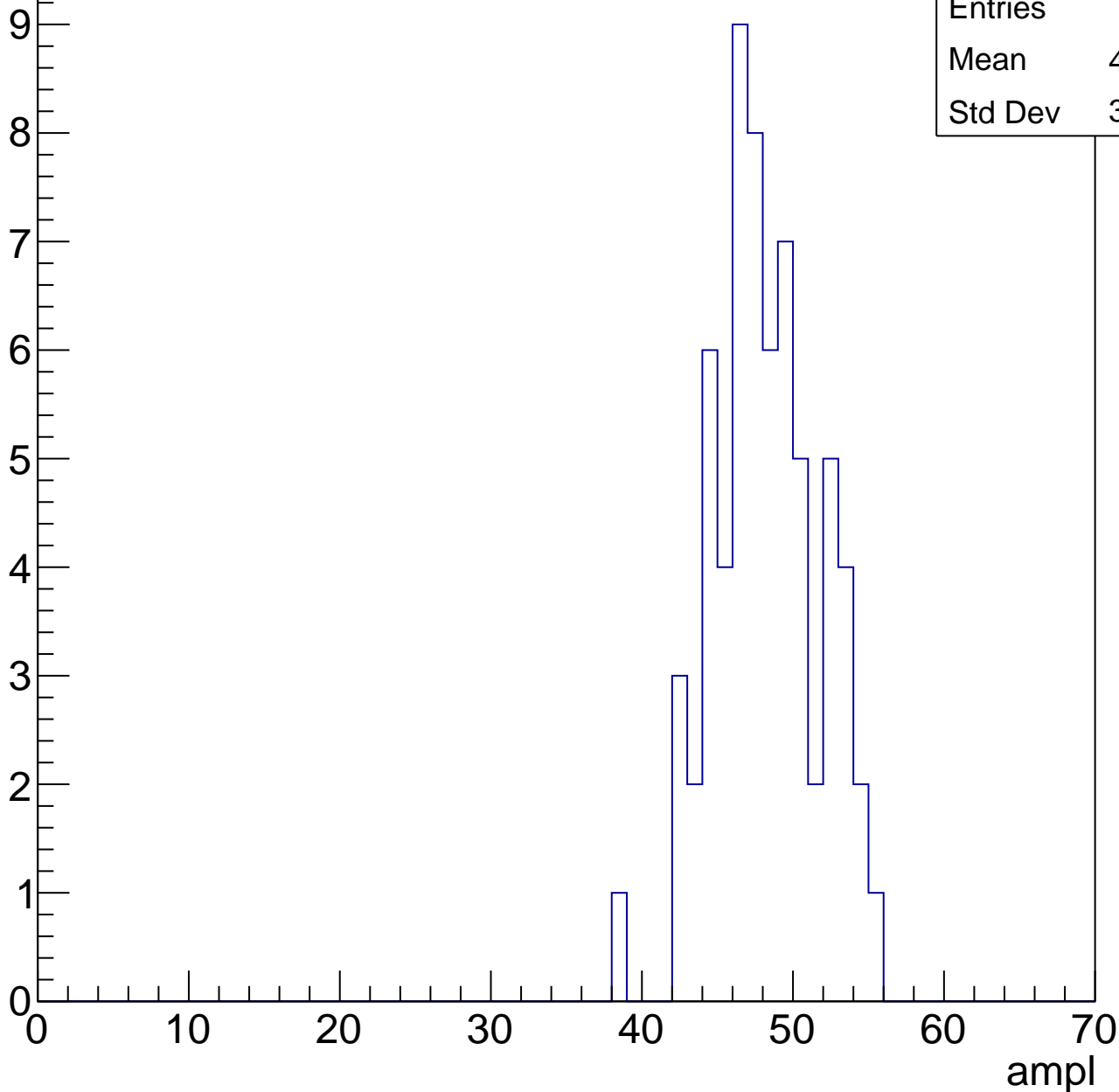


B1L100S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	47.72
Std Dev	3.453

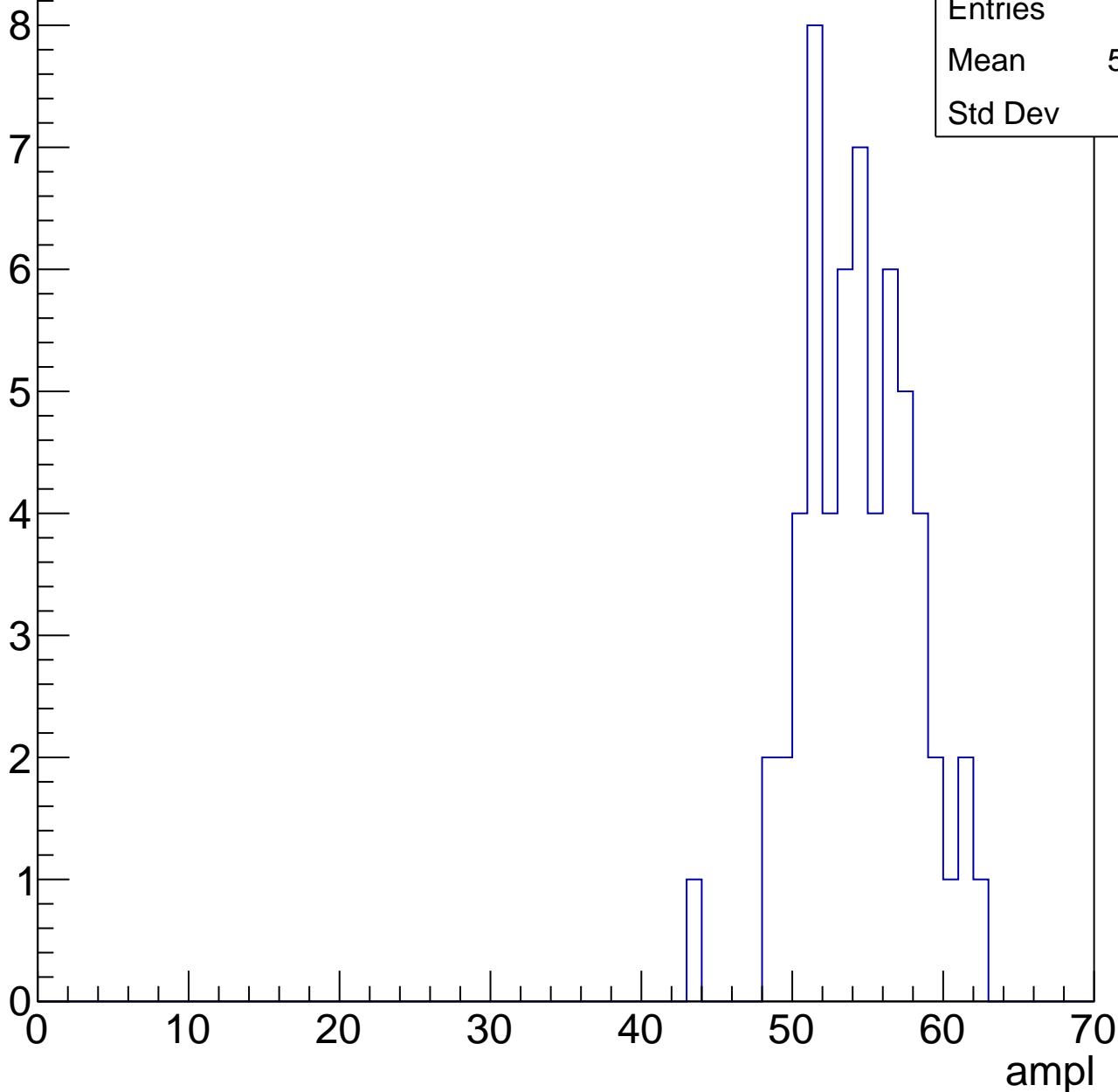


B1L100S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	53.97
Std Dev	3.65

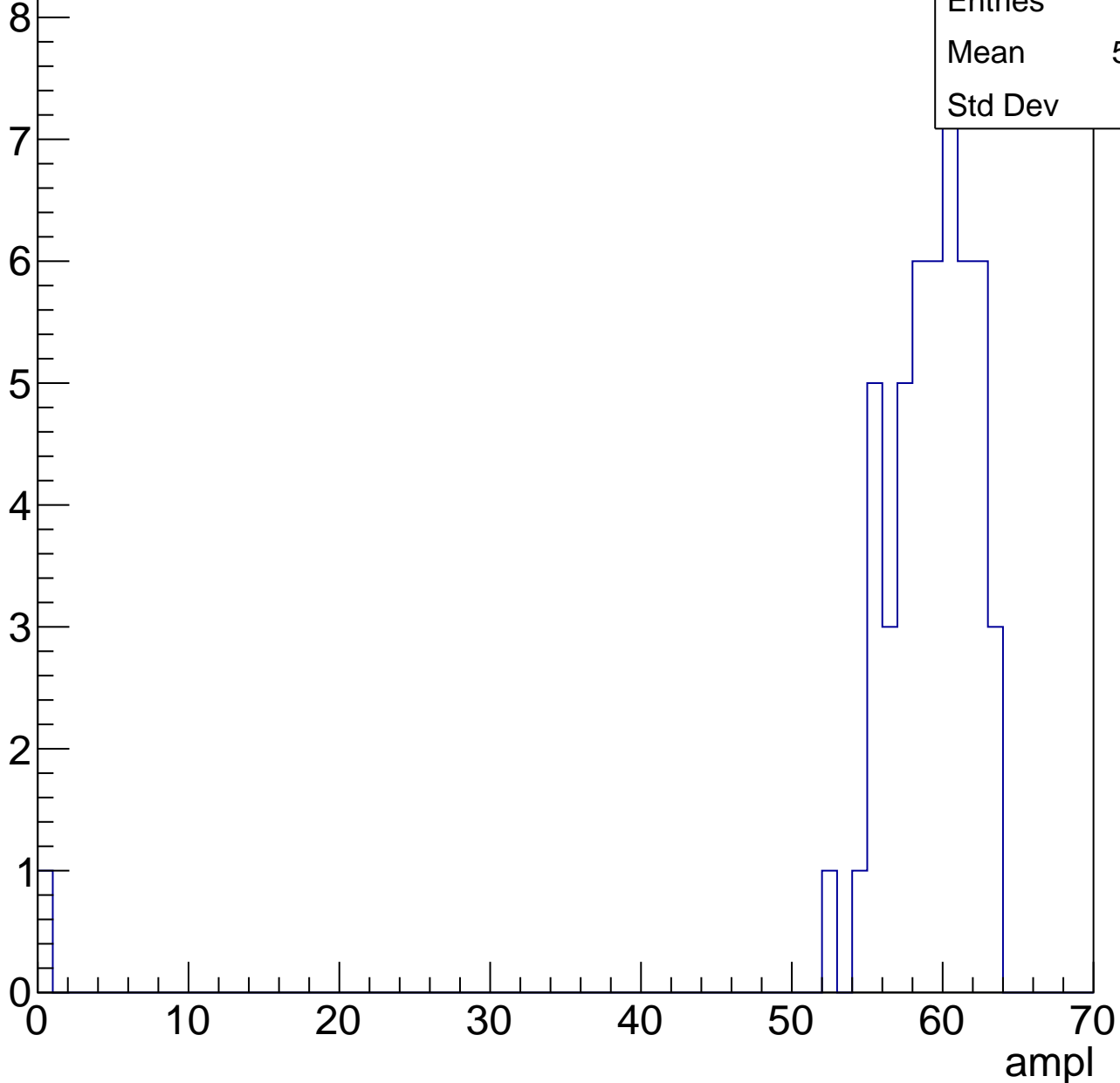


B1L100S, U14-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	57.71
Std Dev	8.56

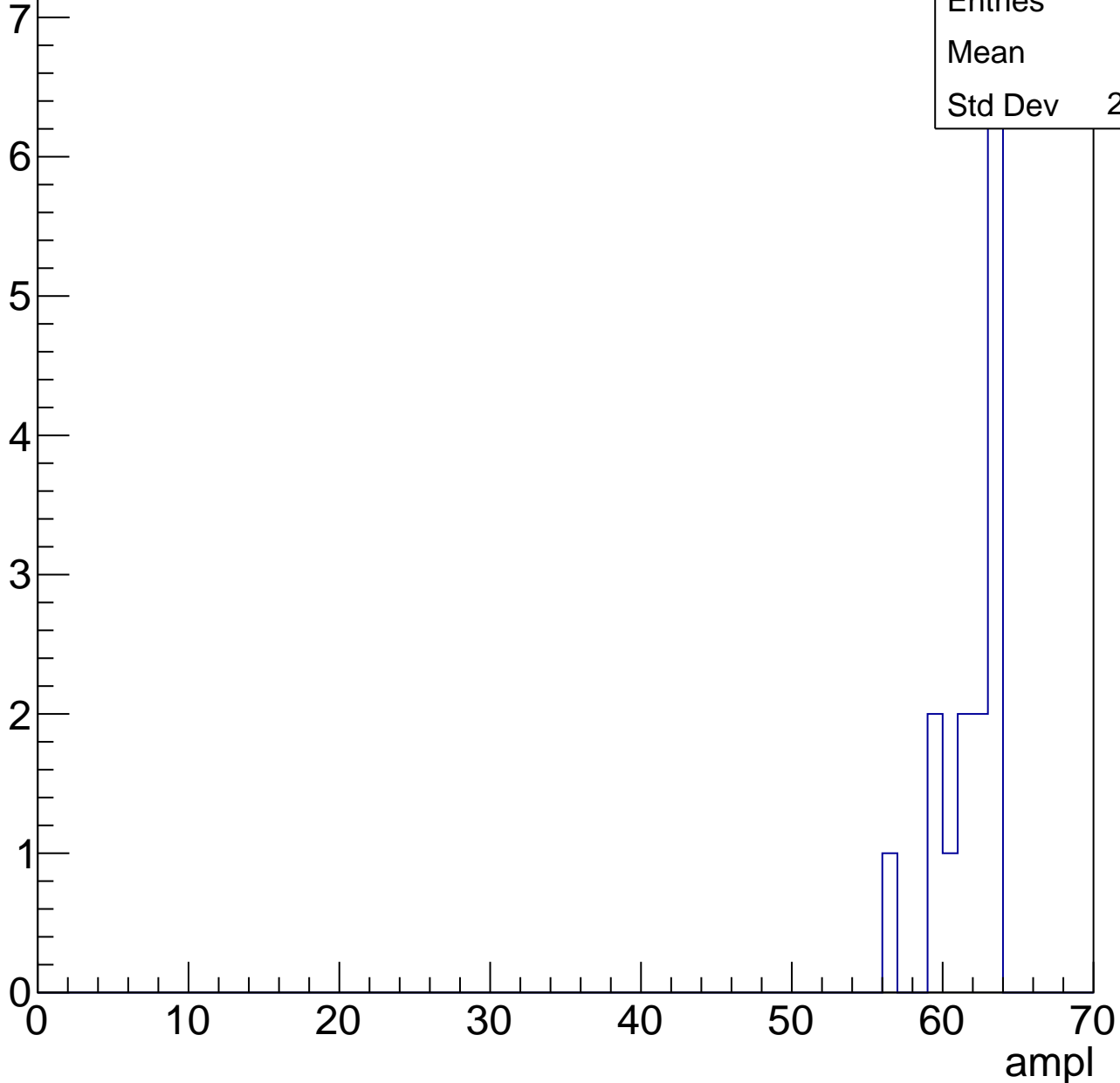


B1L100S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.4
Std Dev	2.026



B1L100S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U14-ch9, adc0

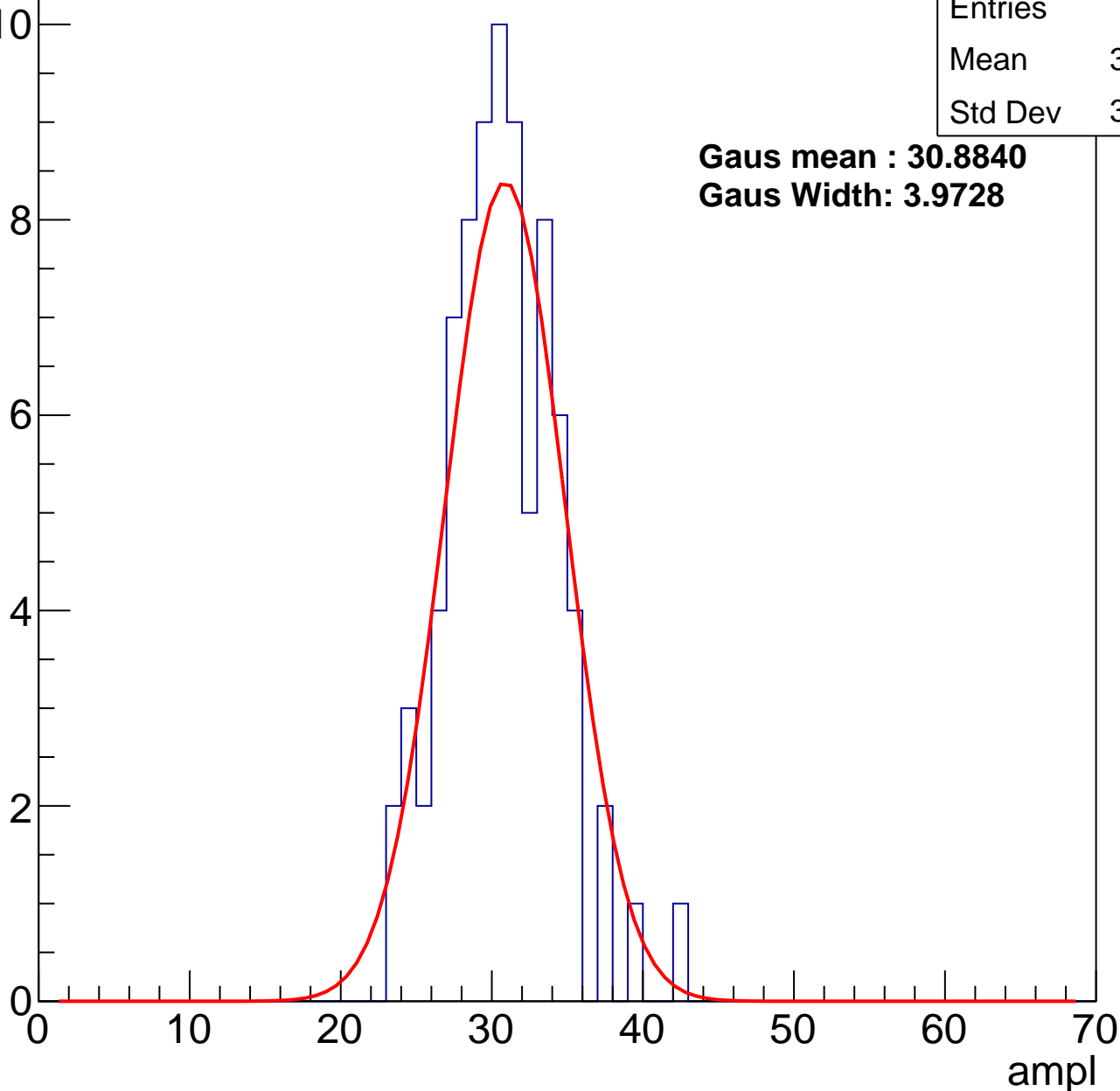
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	30.22
Std Dev	3.583

Gaus mean : 30.8840

Gaus Width: 3.9728



B1L100S, U14-ch9, adc1

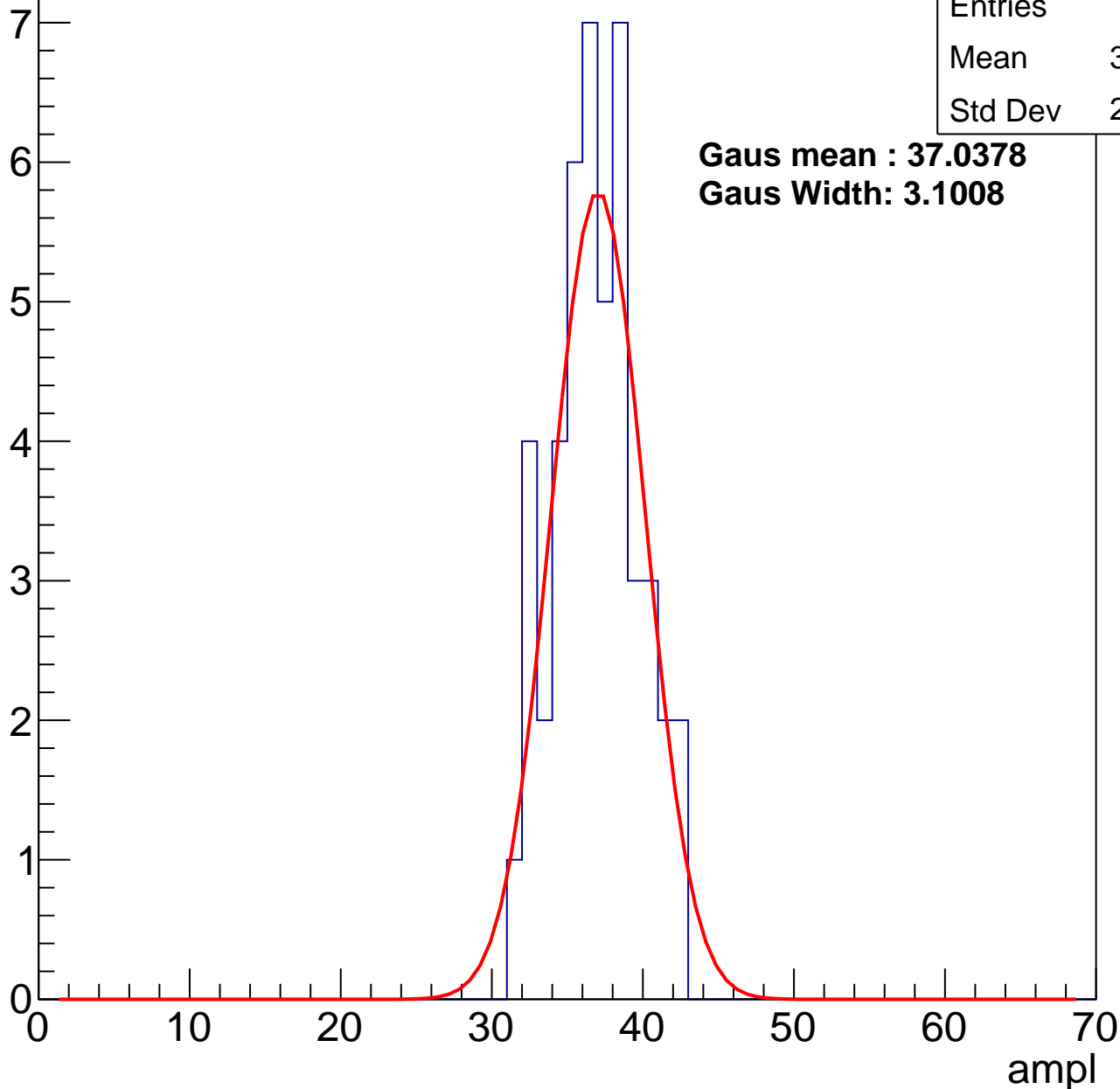
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	36.46
Std Dev	2.756

Gaus mean : 37.0378

Gaus Width: 3.1008



B1L100S, U14-ch9, adc2

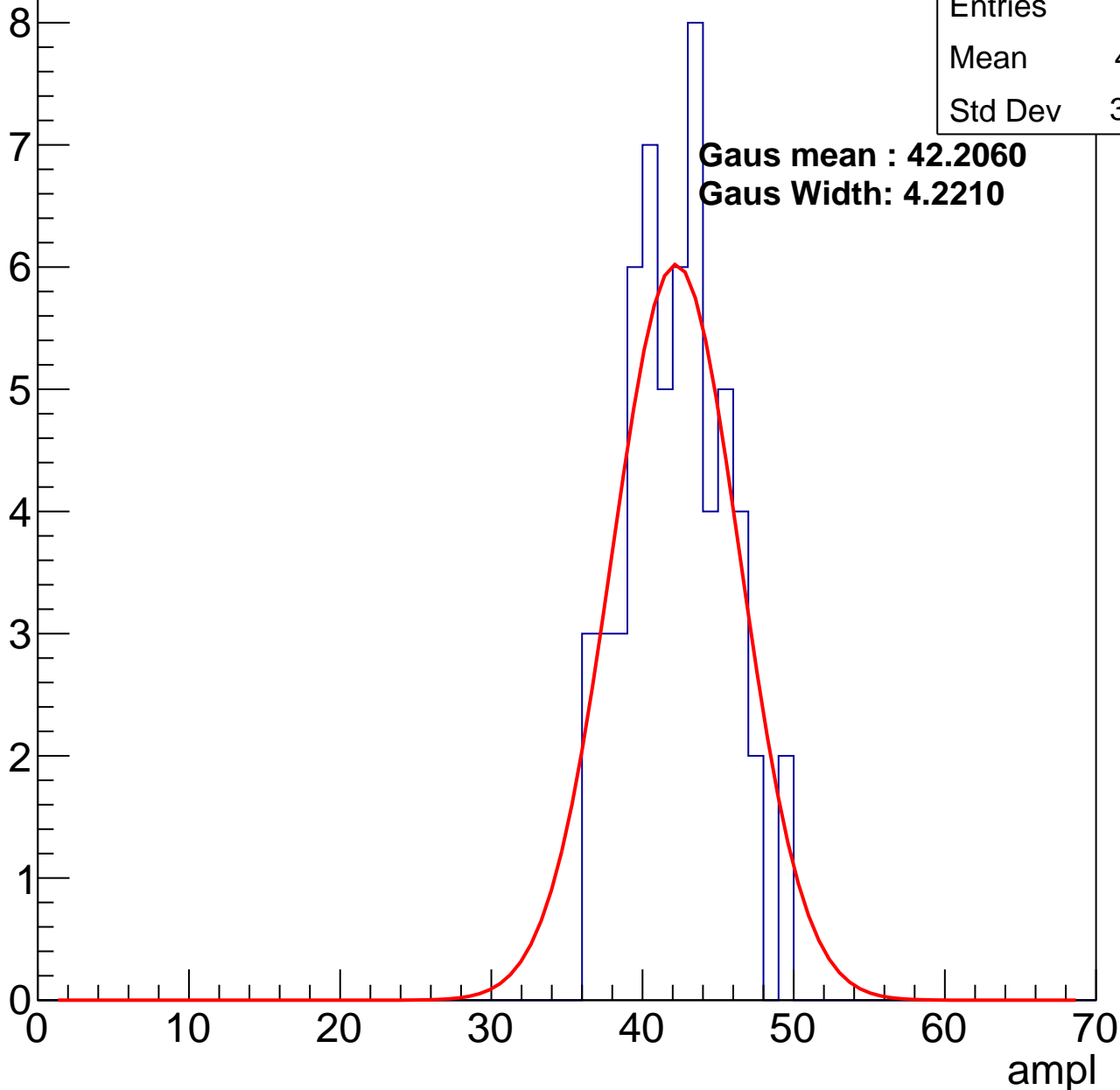
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	41.81
Std Dev	3.208

Gaus mean : 42.2060

Gaus Width: 4.2210

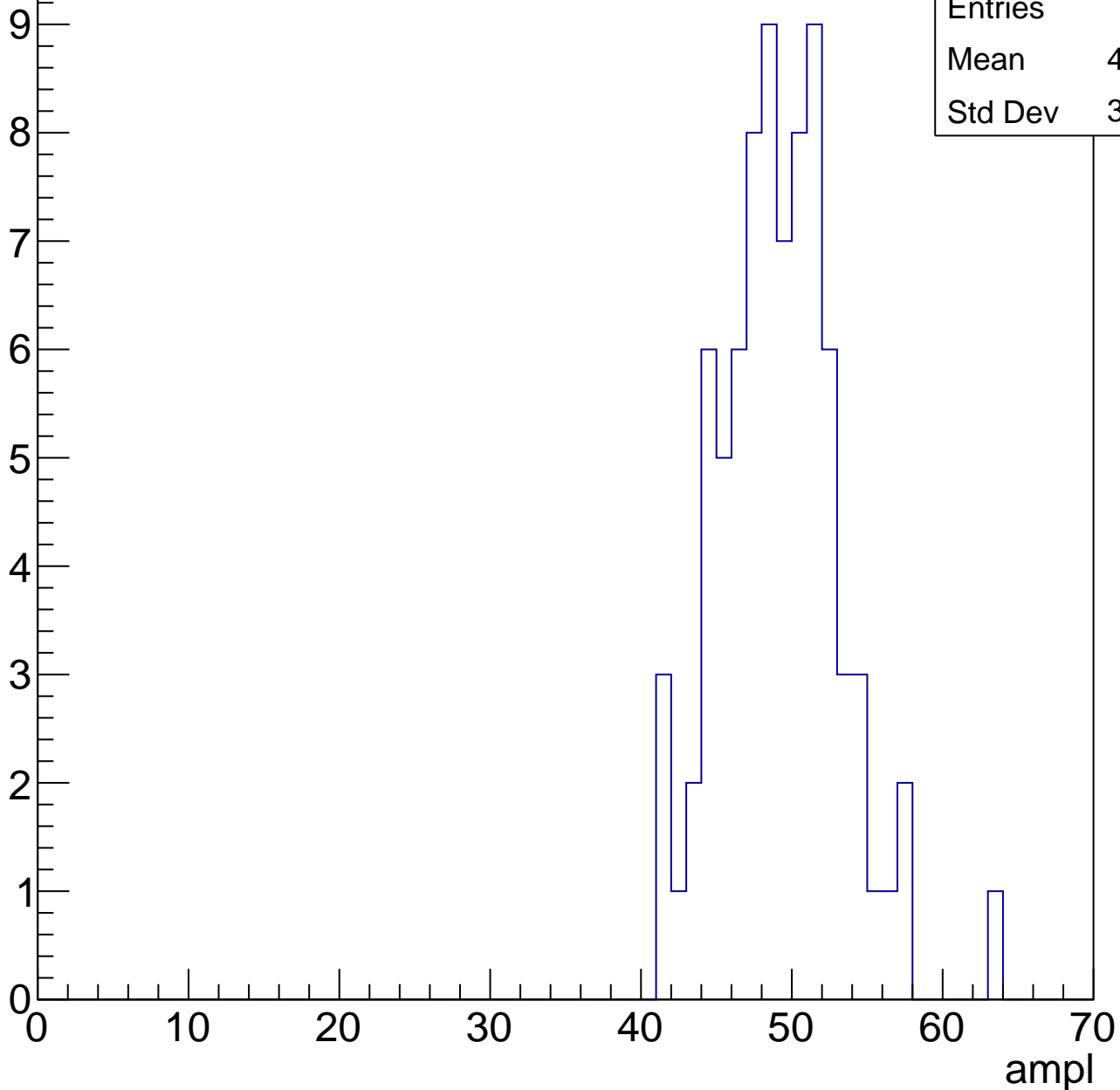


B1L100S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

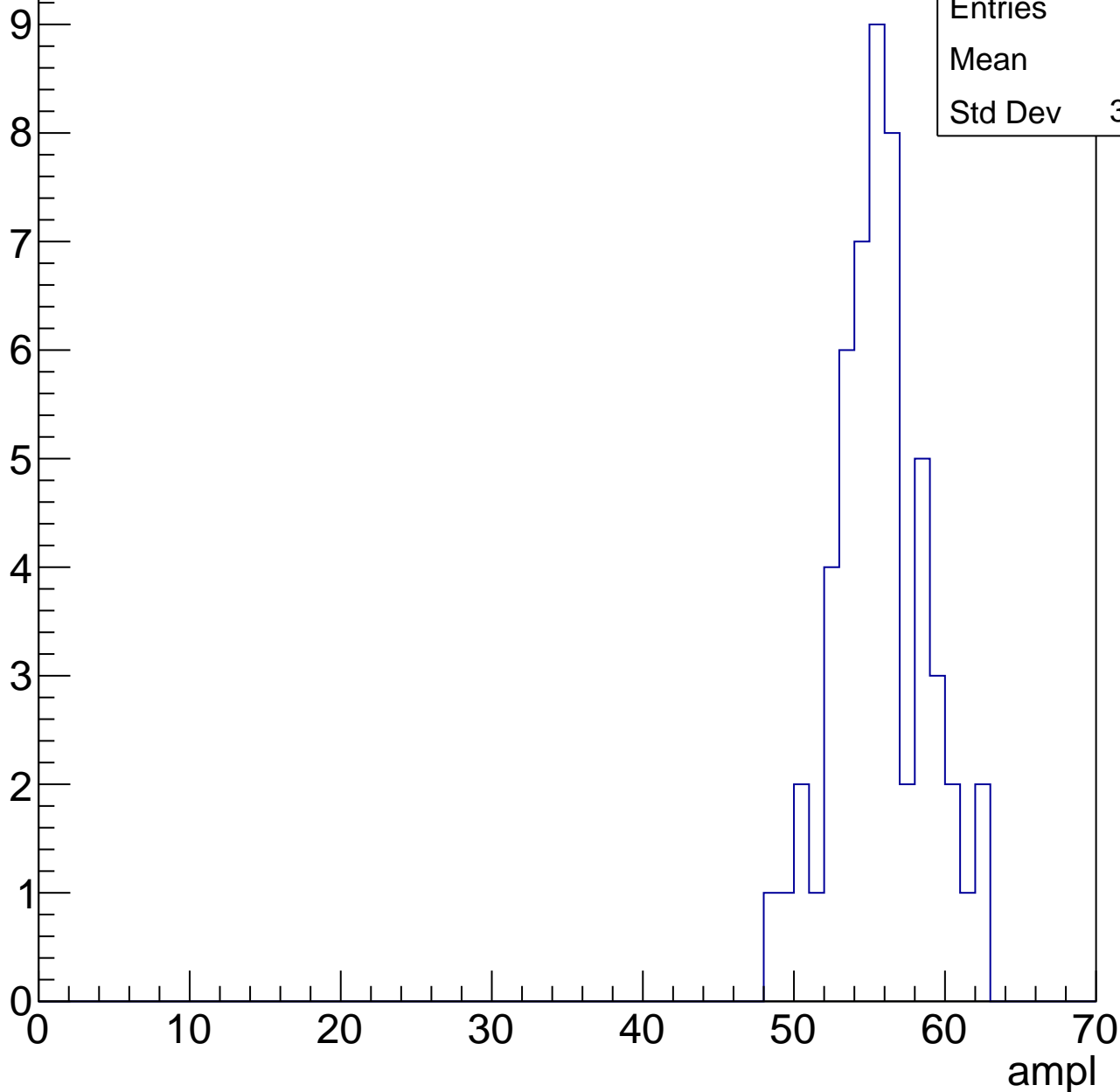
Entries	81
Mean	48.73
Std Dev	3.947



B1L100S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



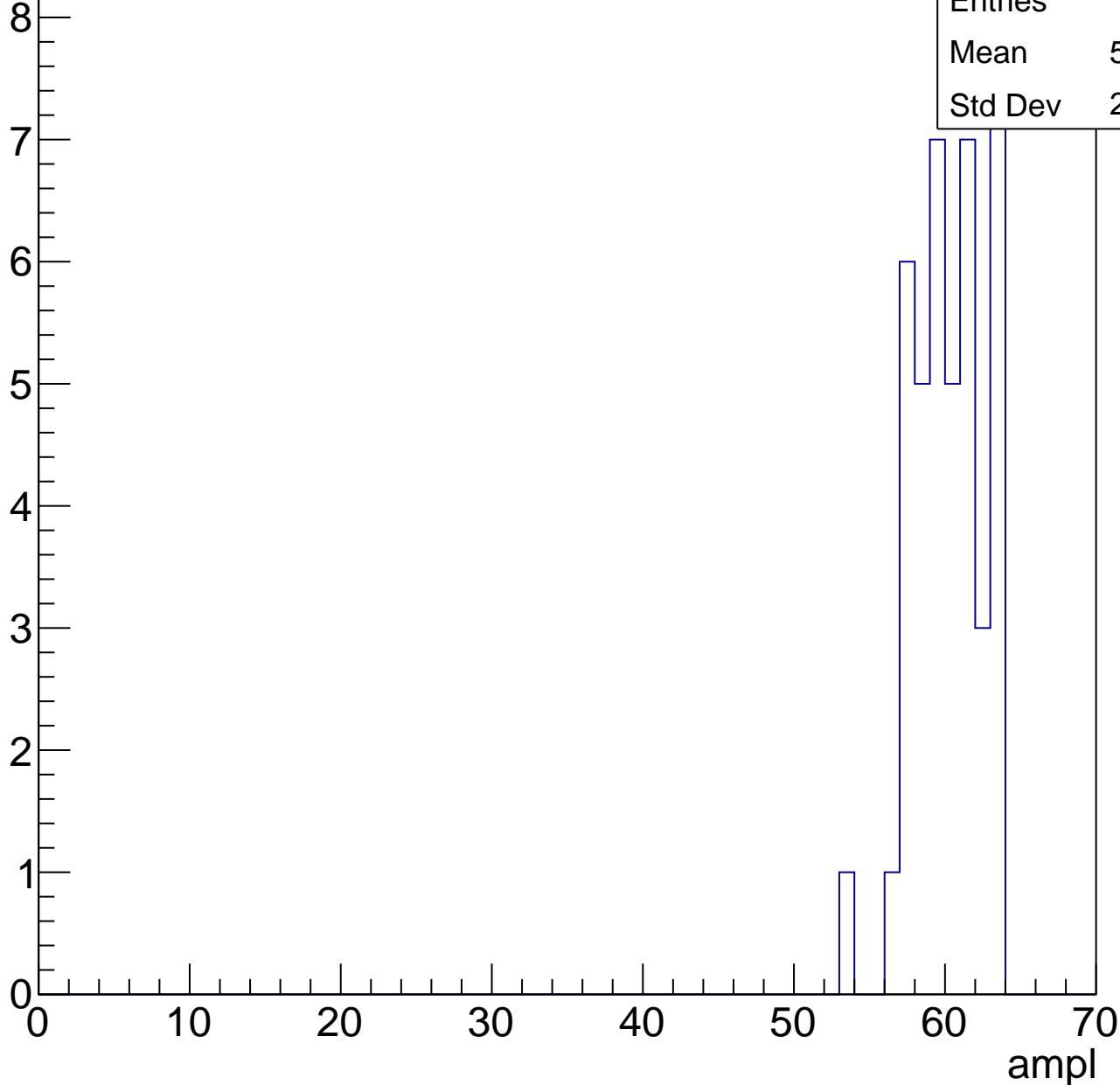
Entries	54
Mean	55.2
Std Dev	3.069

B1L100S, U14-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	59.79
Std Dev	2.338

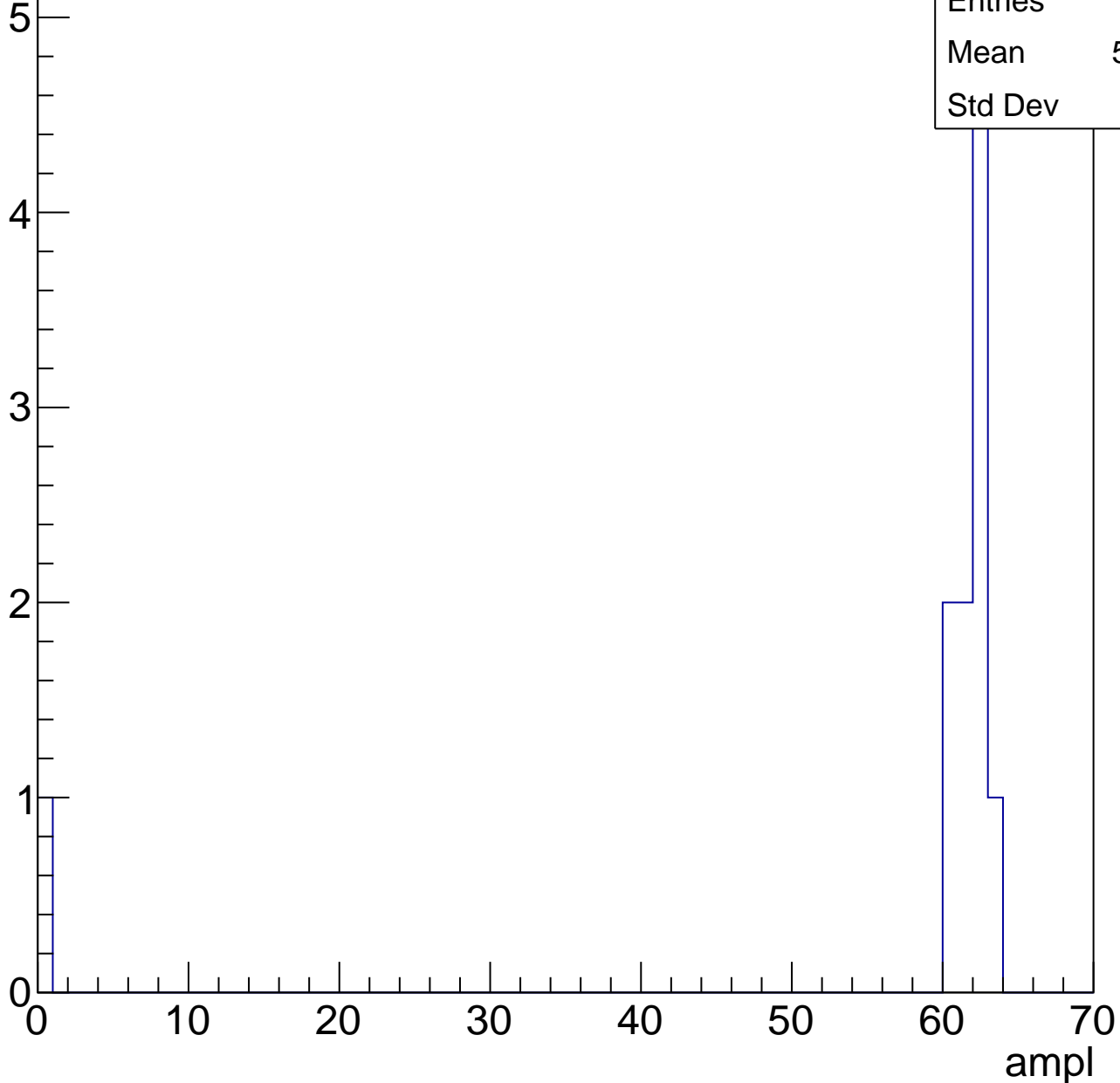


B1L100S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

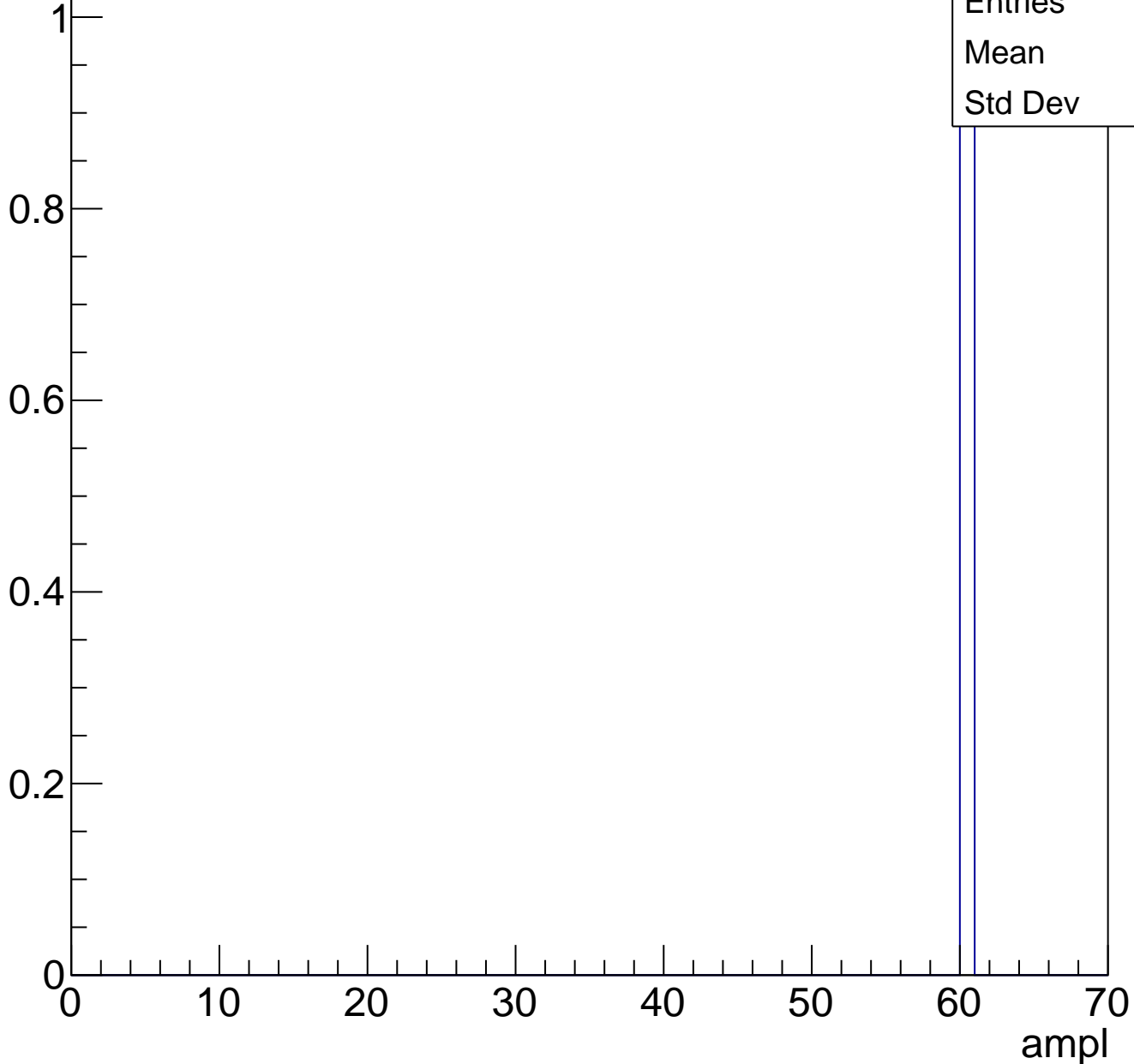
Entries	11
Mean	55.91
Std Dev	17.7



B1L100S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch10, adc0

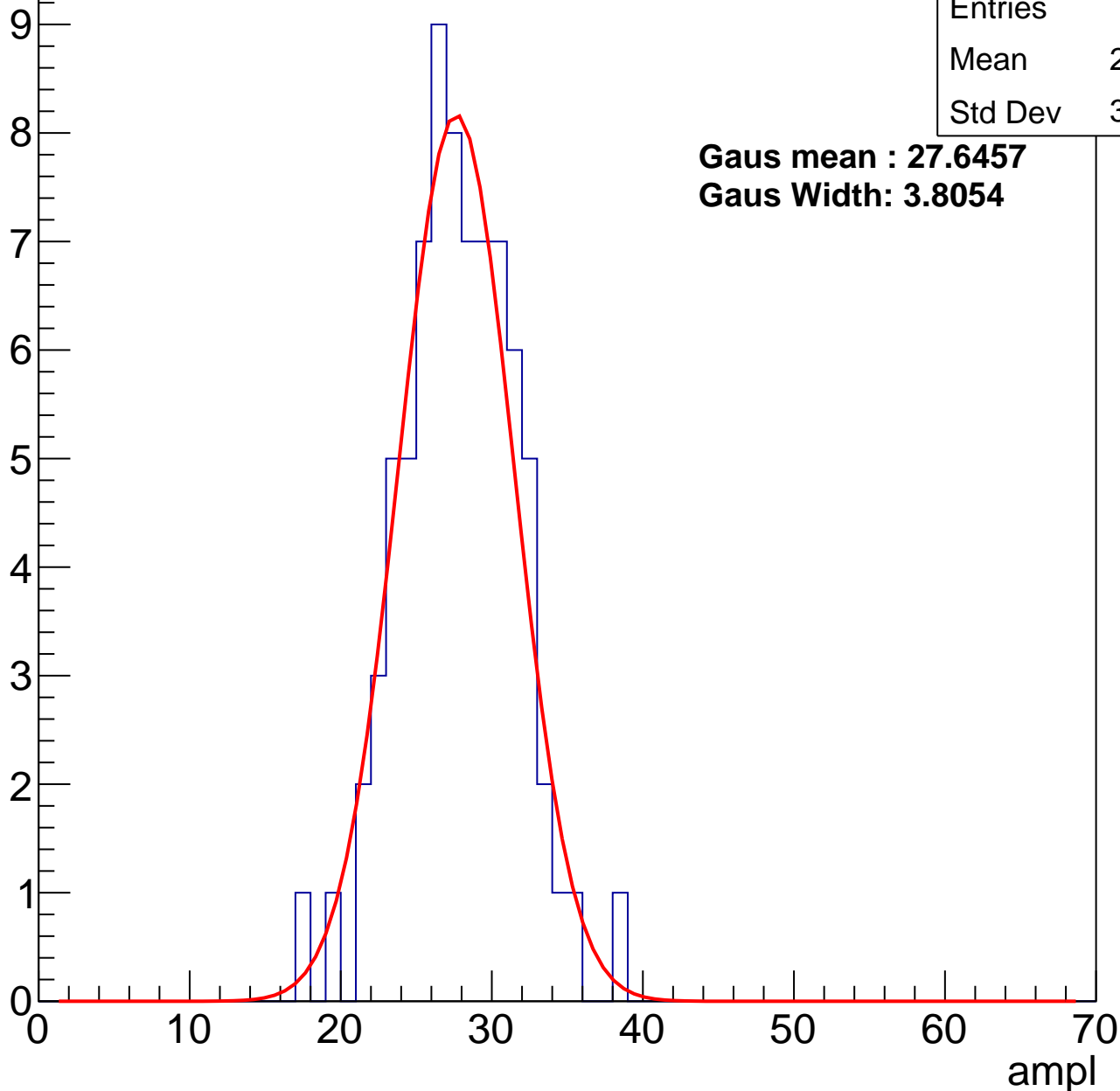
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	27.33
Std Dev	3.737

Gaus mean : 27.6457

Gaus Width: 3.8054



B1L100S, U14-ch10, adc1

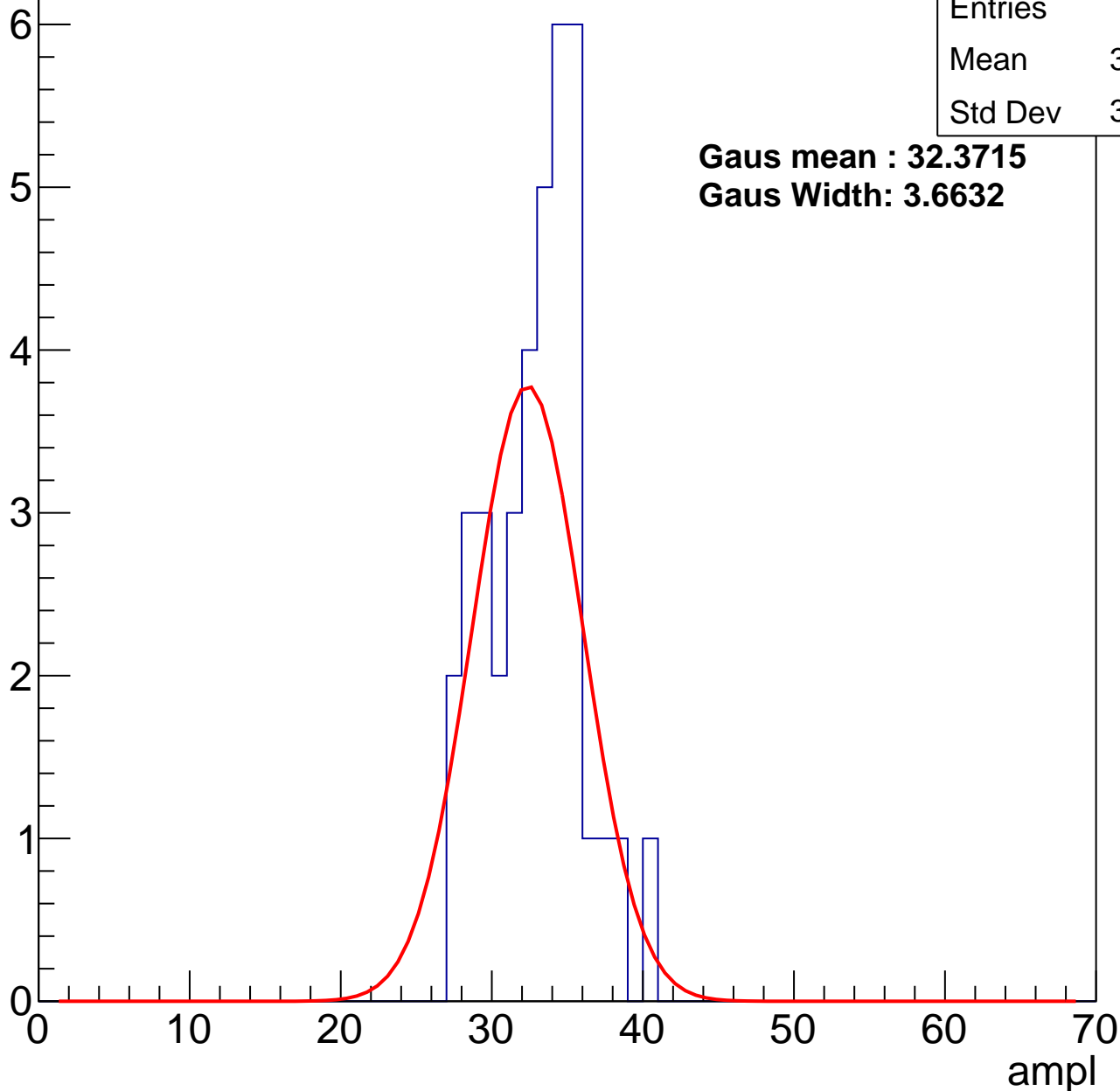
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	32.53
Std Dev	3.033

Gaus mean : 32.3715

Gaus Width: 3.6632



B1L100S, U14-ch10, adc2

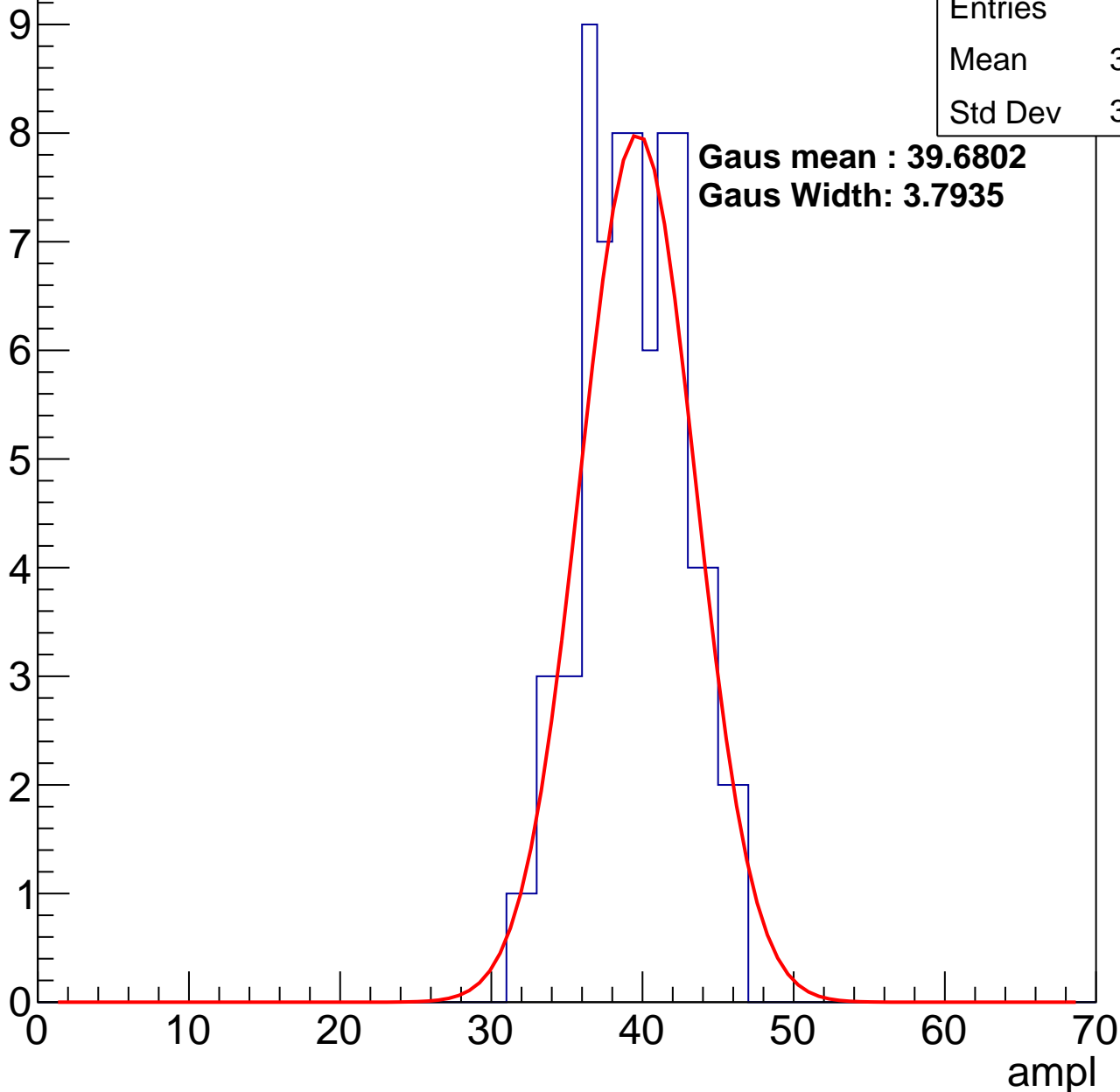
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.99
Std Dev	3.428

Gaus mean : 39.6802

Gaus Width: 3.7935

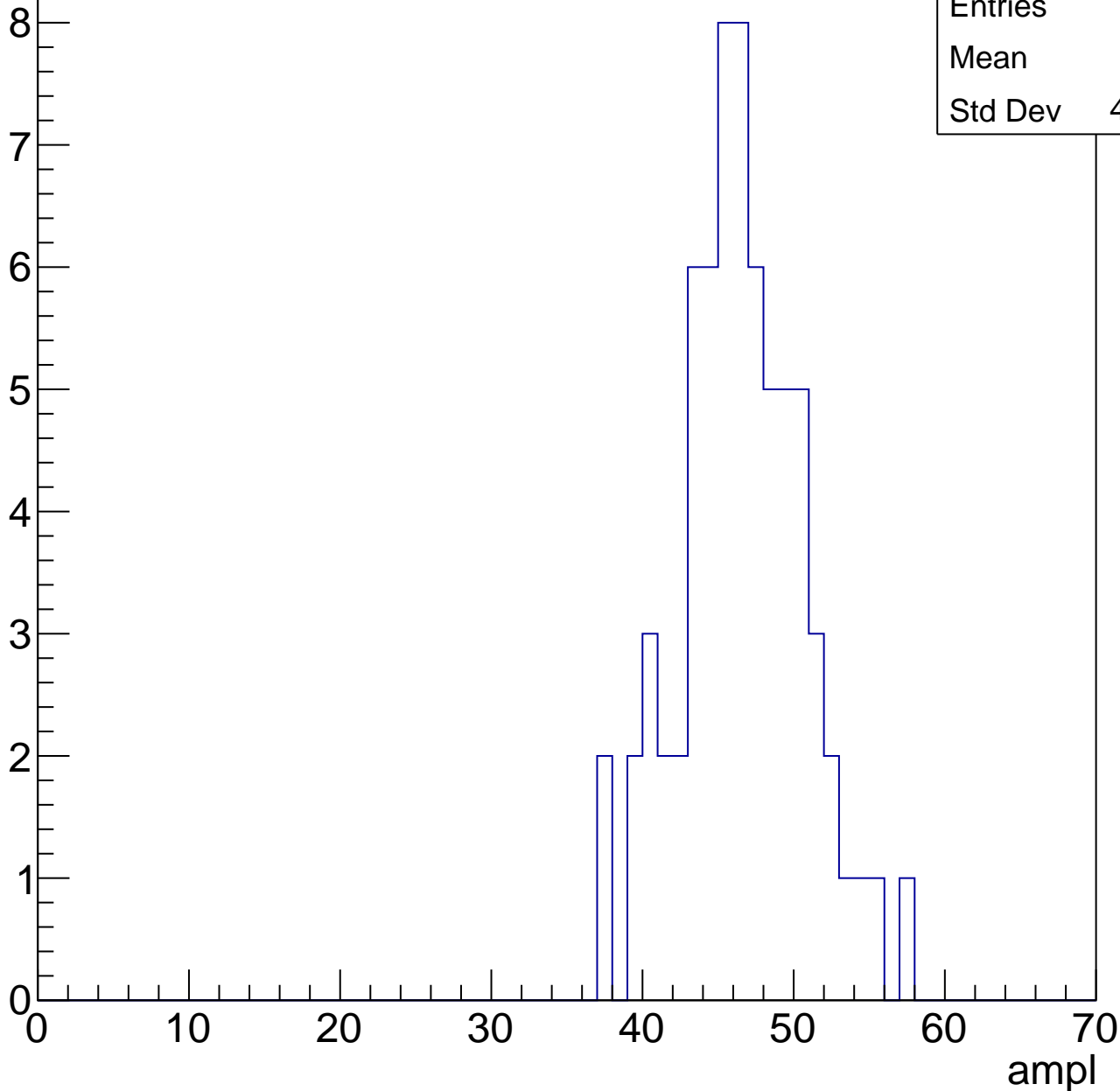


B1L100S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	46.1
Std Dev	4.079

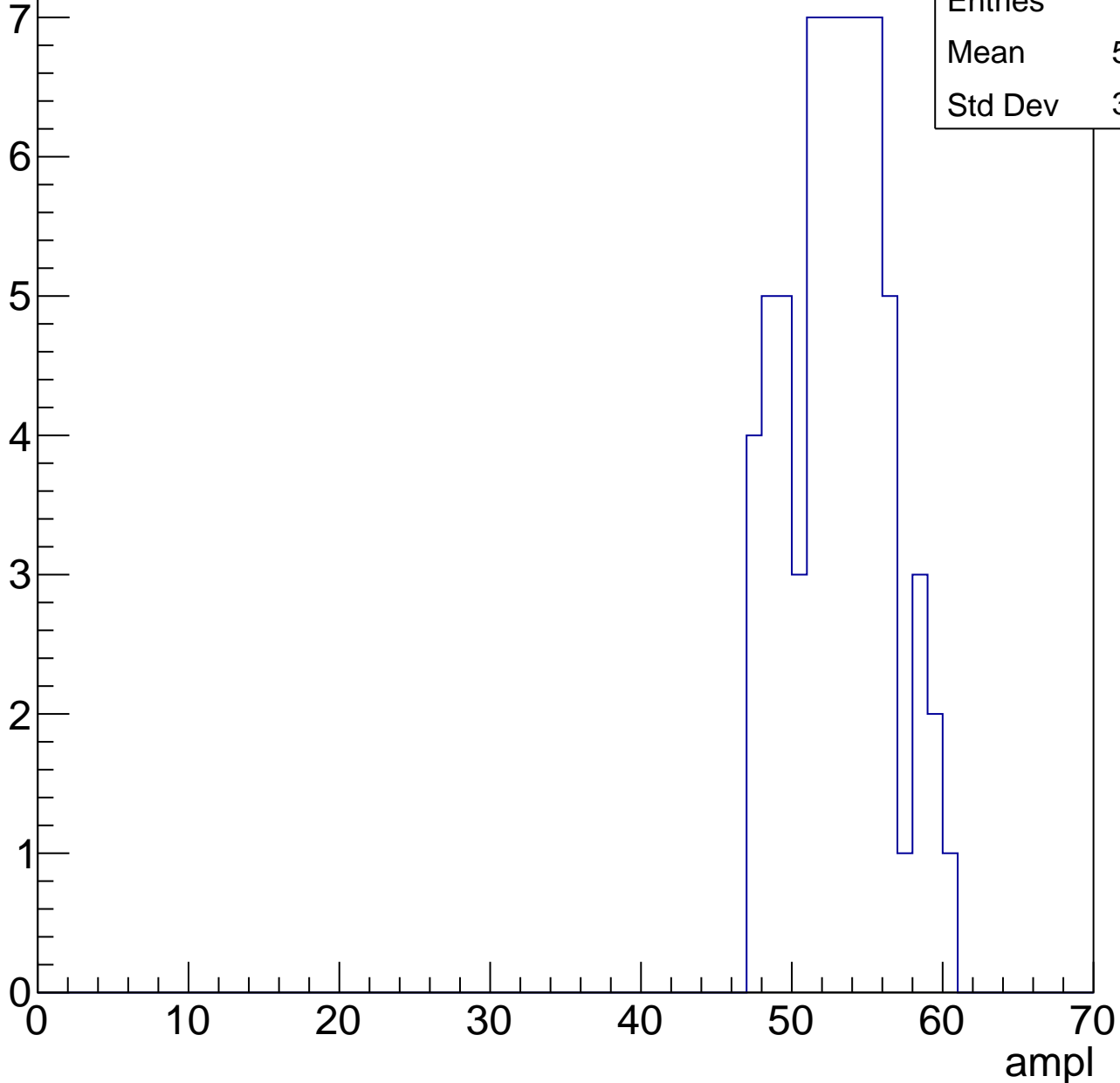


B1L100S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	52.61
Std Dev	3.291



B1L100S, U14-ch10, adc5

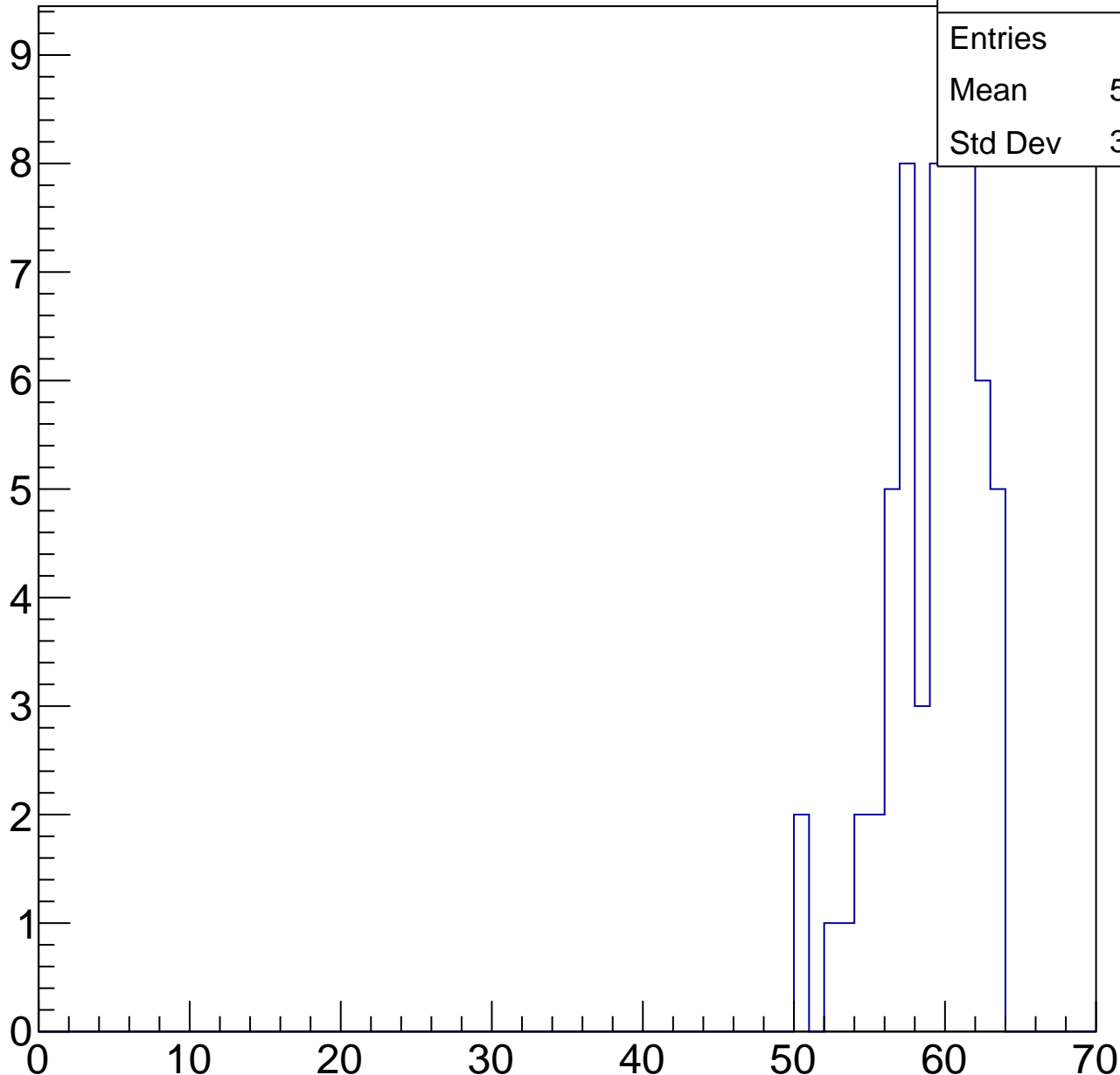
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.67
Std Dev	3.102

ampl

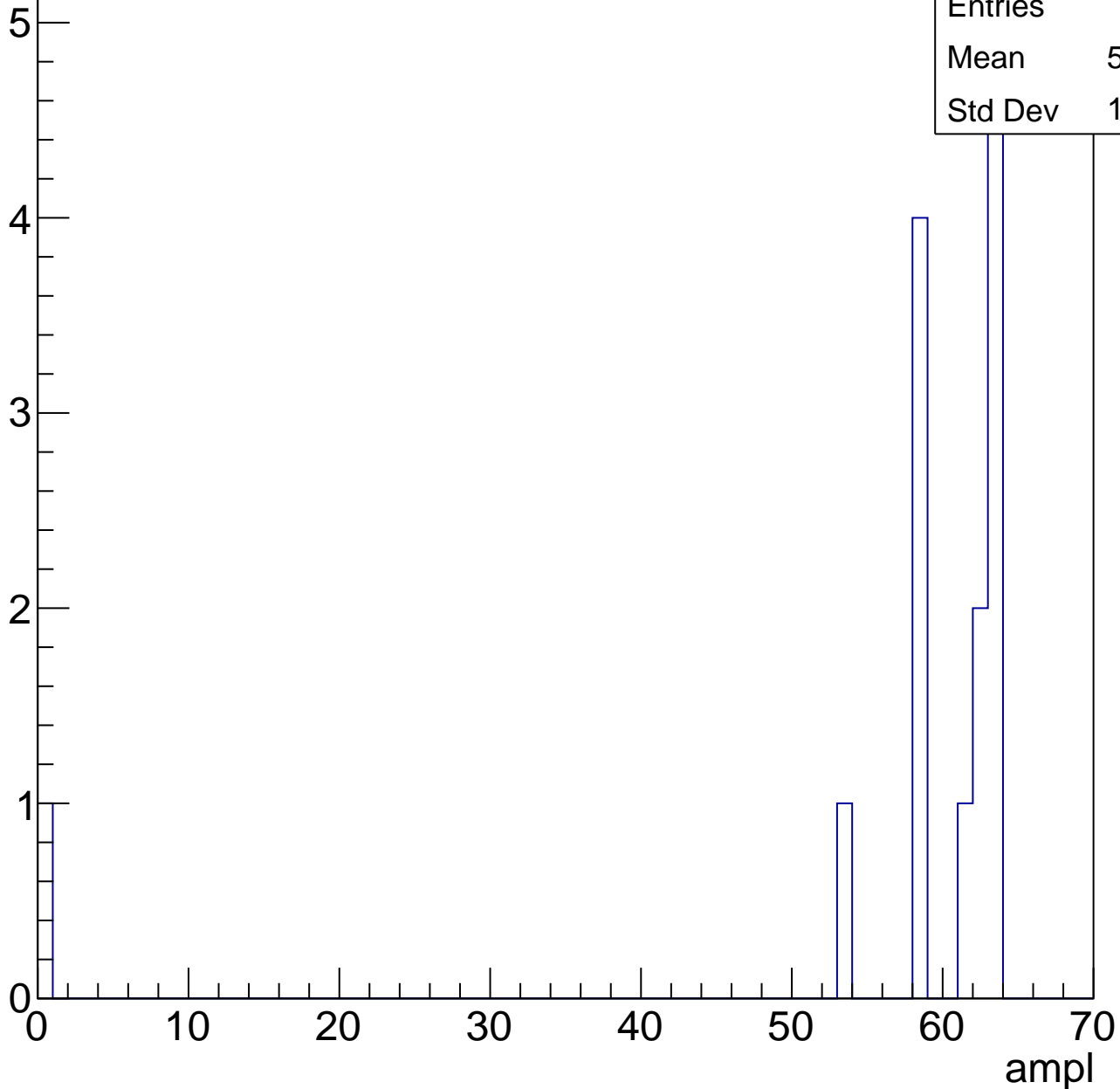


B1L100S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

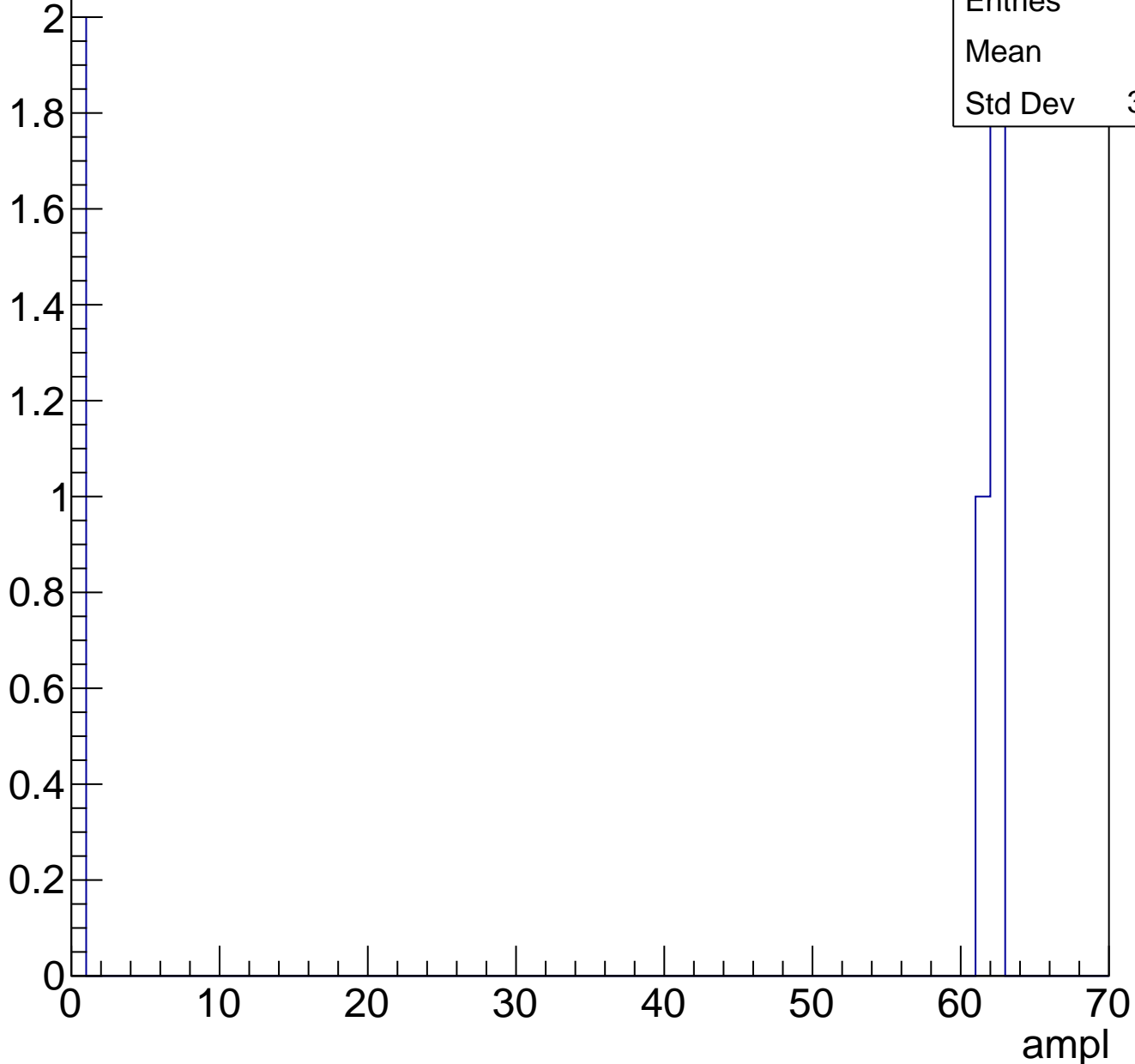
Entries	14
Mean	56.07
Std Dev	15.82



B1L100S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	37
Std Dev	30.21

B1L100S, U14-ch11, adc0

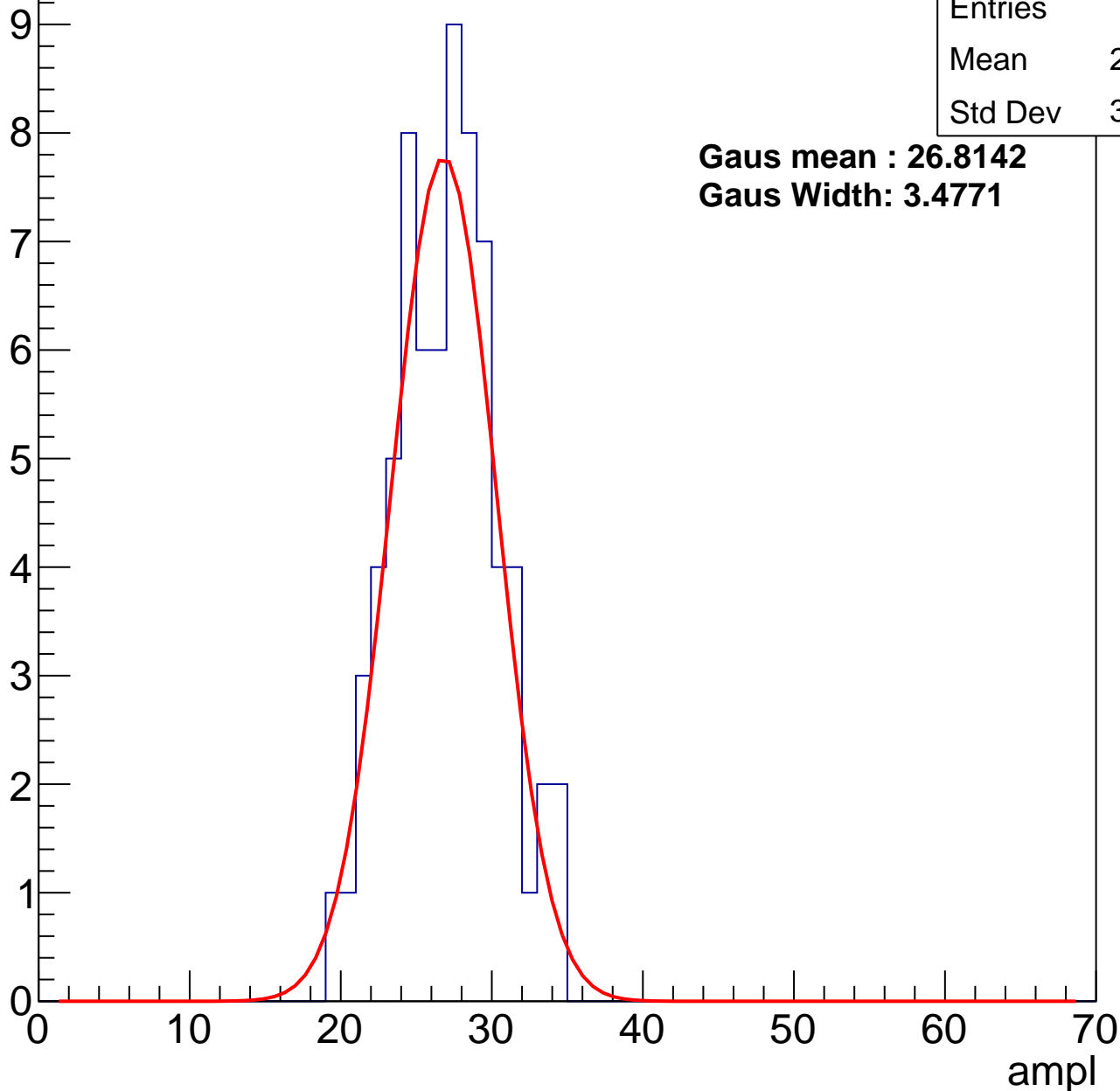
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	26.52
Std Dev	3.394

Gaus mean : 26.8142

Gaus Width: 3.4771



B1L100S, U14-ch11, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	33.84
Std Dev	3.415

Gaus mean : 34.2534

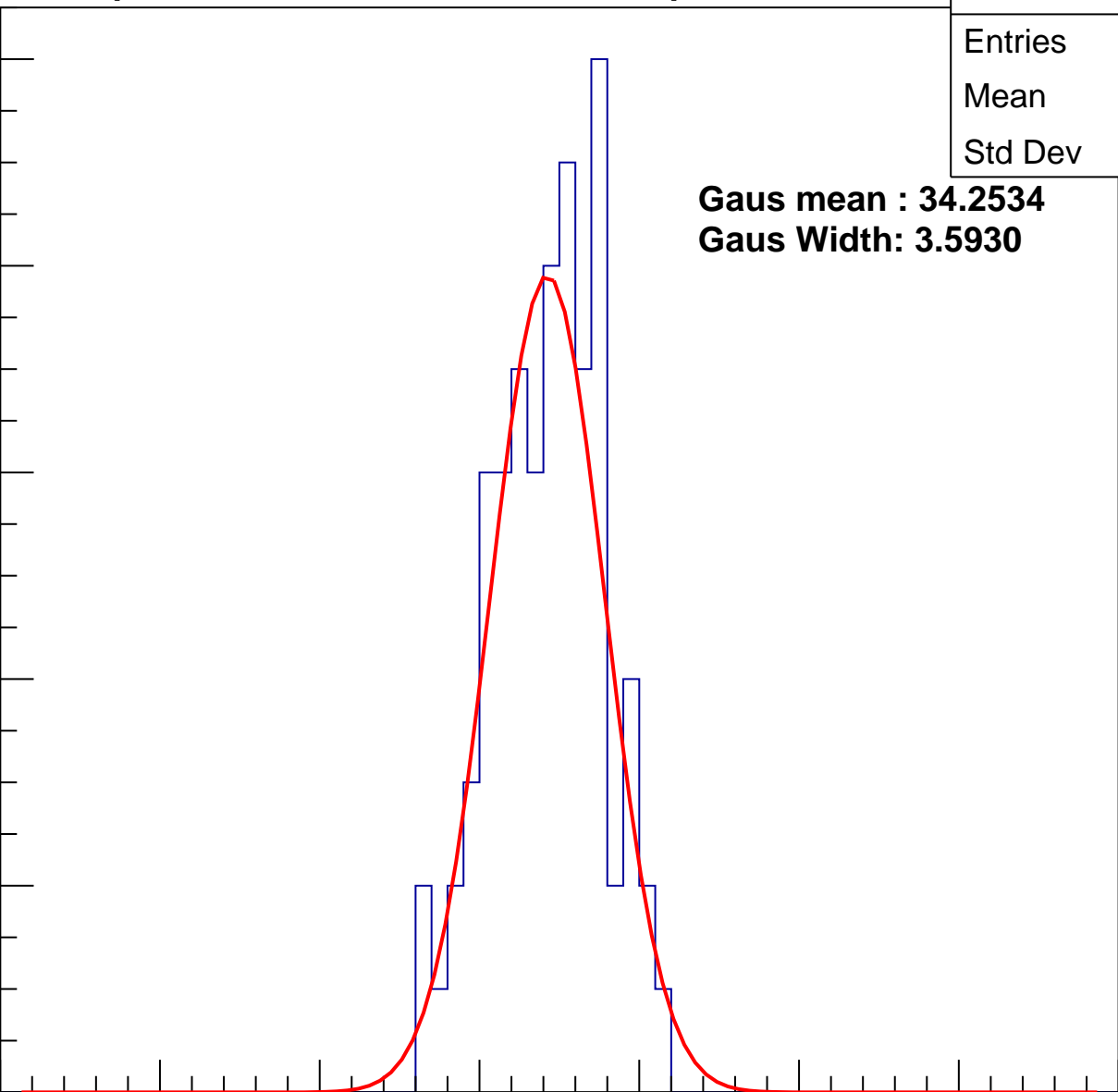
Gaus Width: 3.5930

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

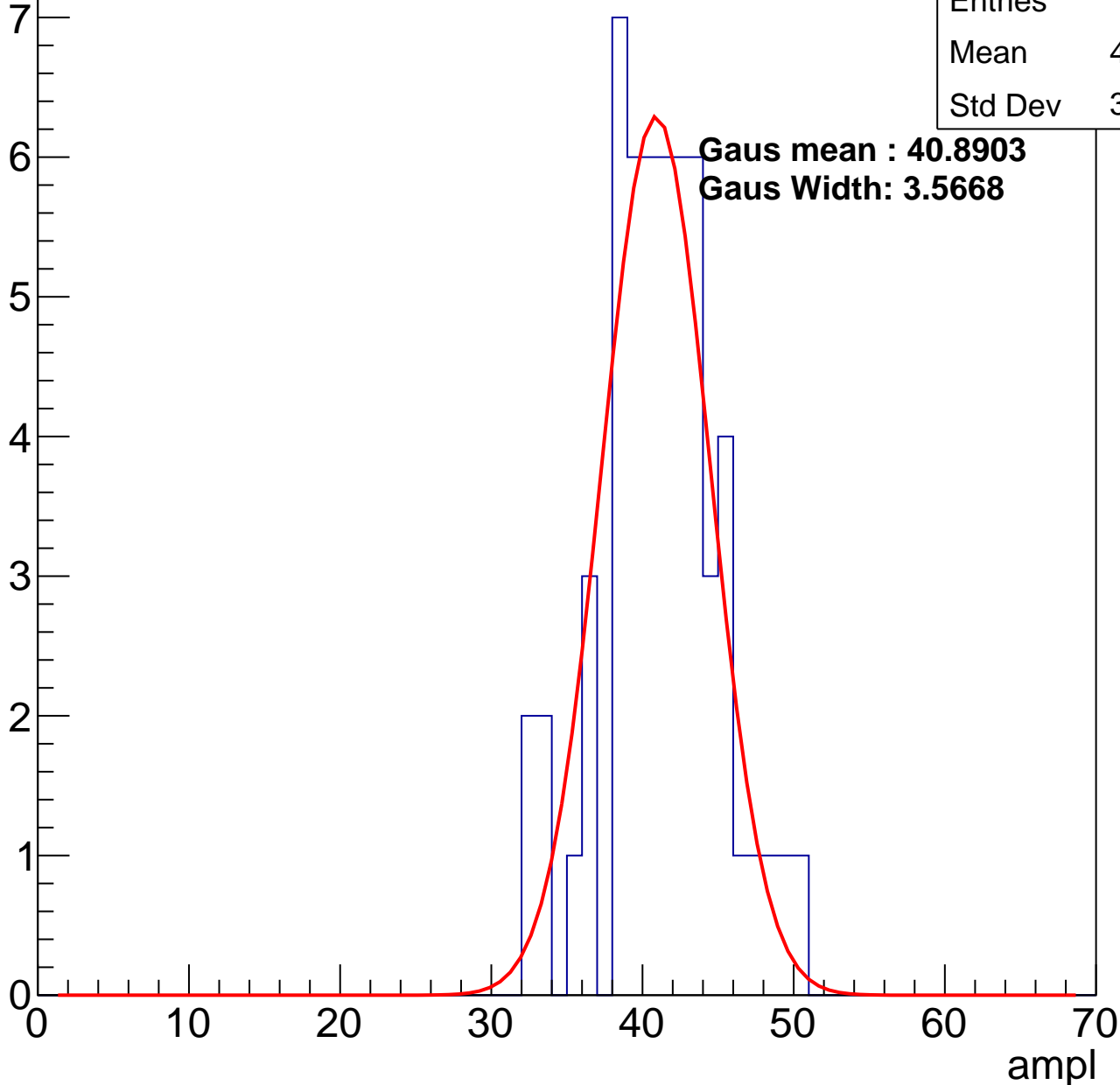


B1L100S, U14-ch11, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	40.72
Std Dev	3.897

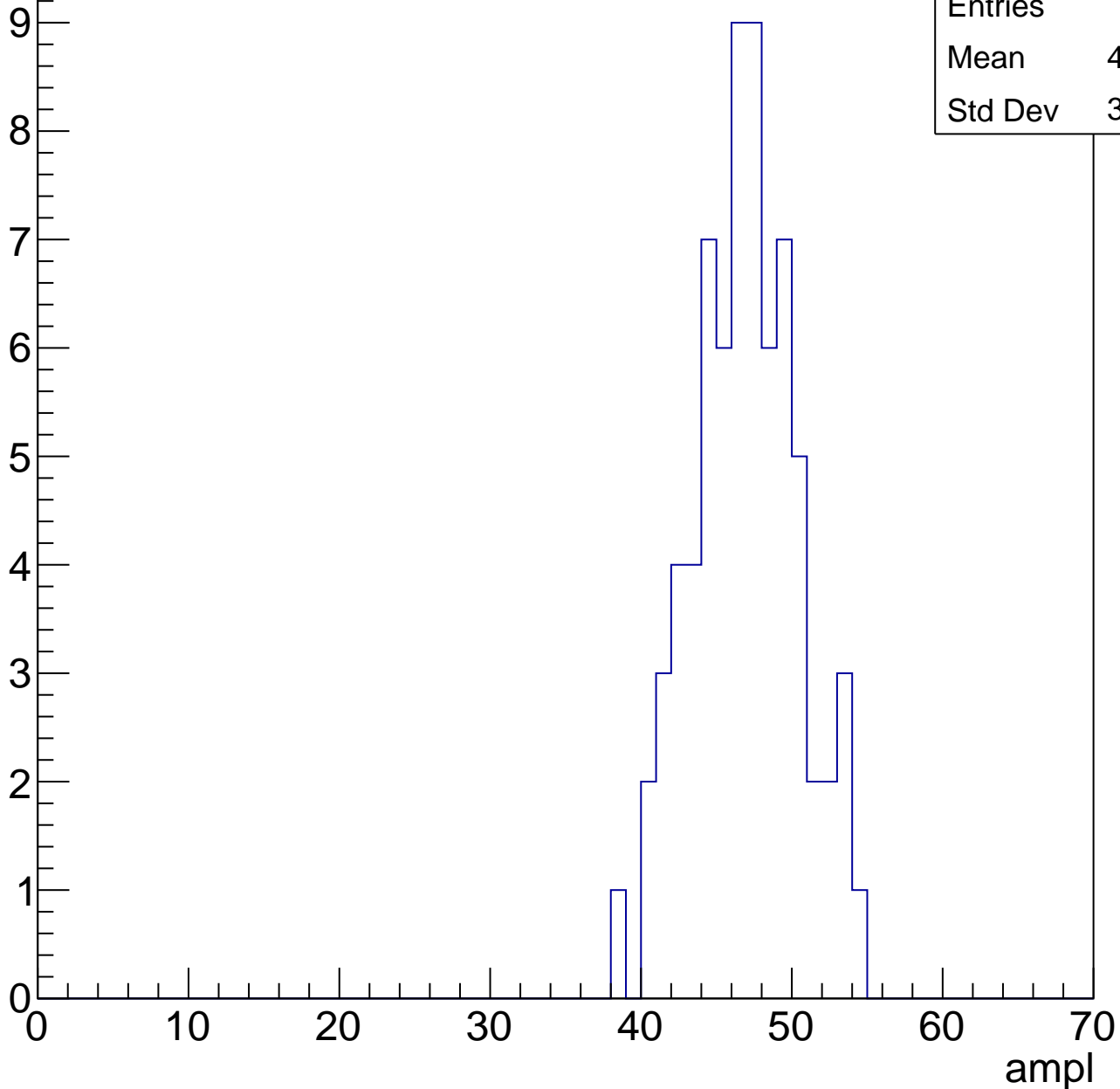


B1L100S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

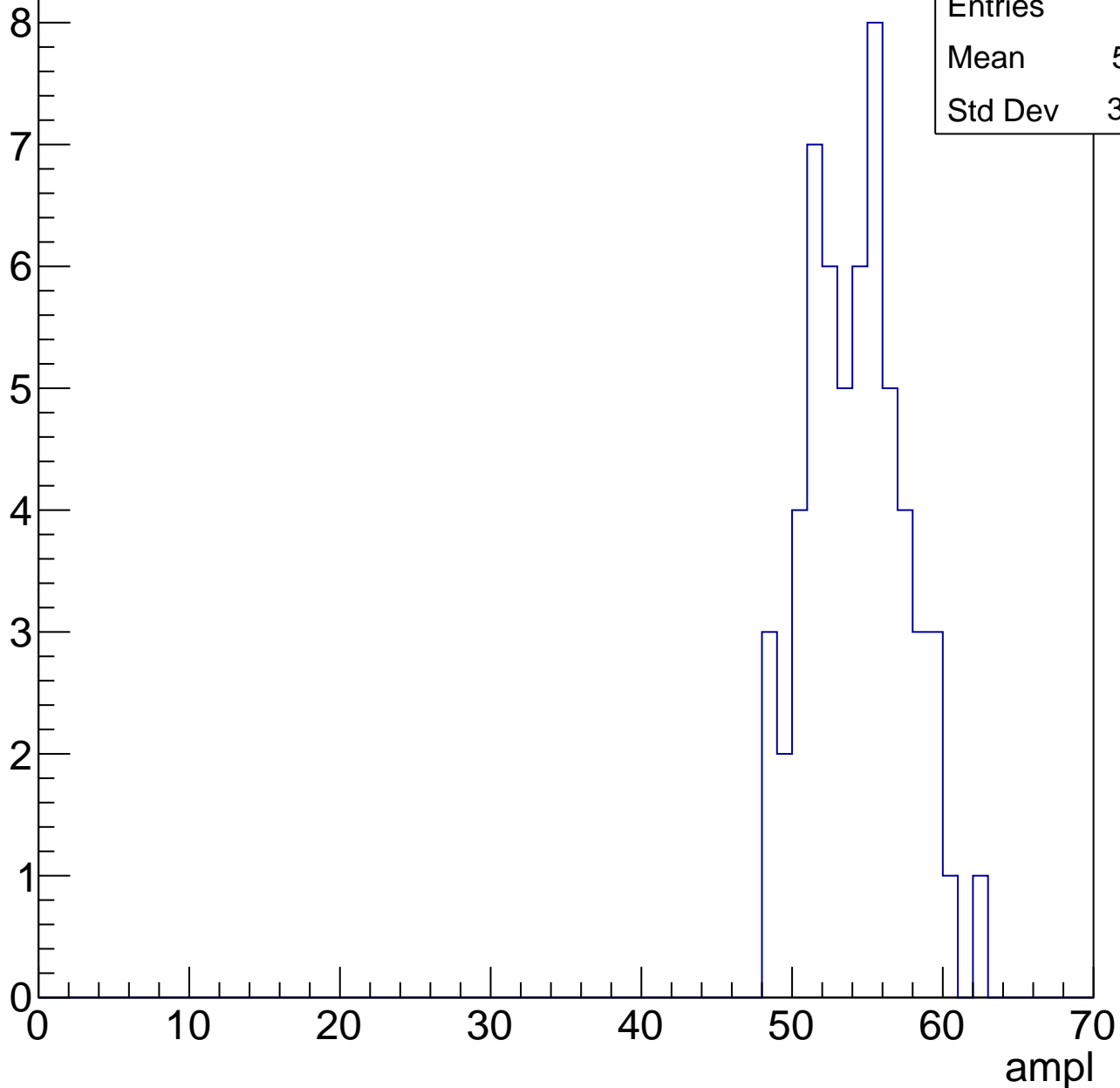
Entries	71
Mean	46.42
Std Dev	3.442



B1L100S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



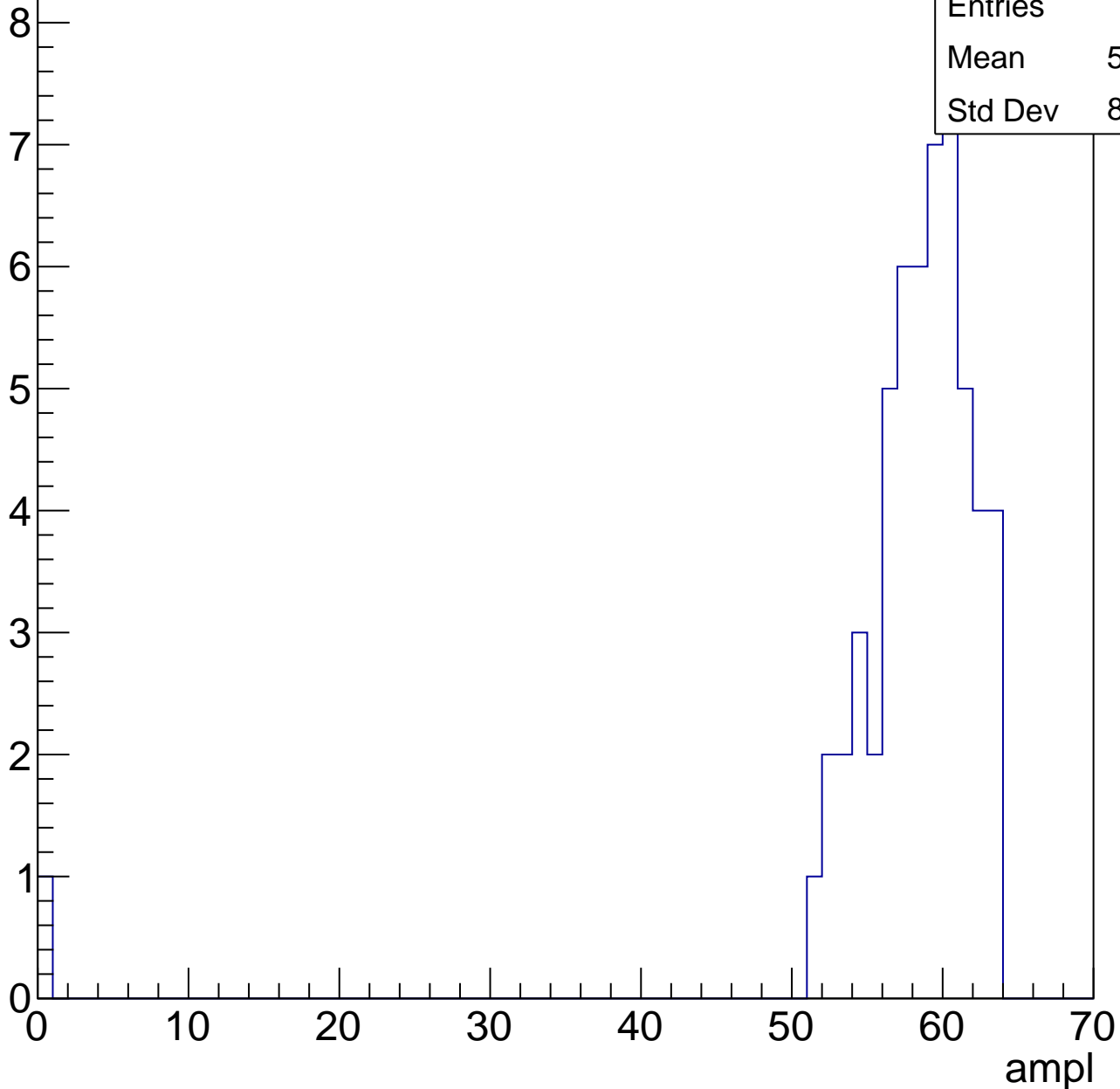
Entries	58
Mean	53.81
Std Dev	3.213

B1L100S, U14-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.16
Std Dev	8.276

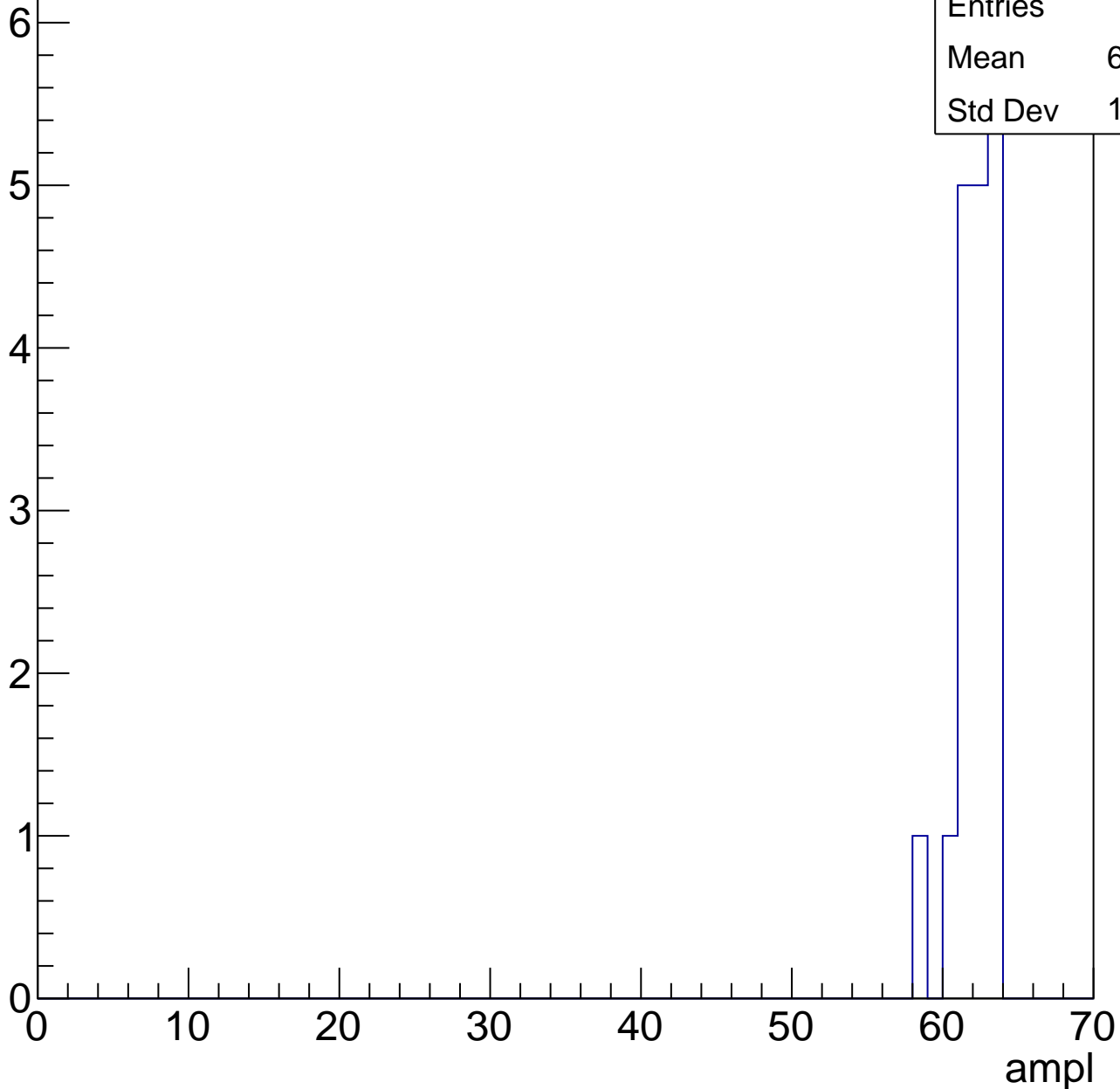


B1L100S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	61.72
Std Dev	1.283



B1L100S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L100S, U14-ch12, adc0

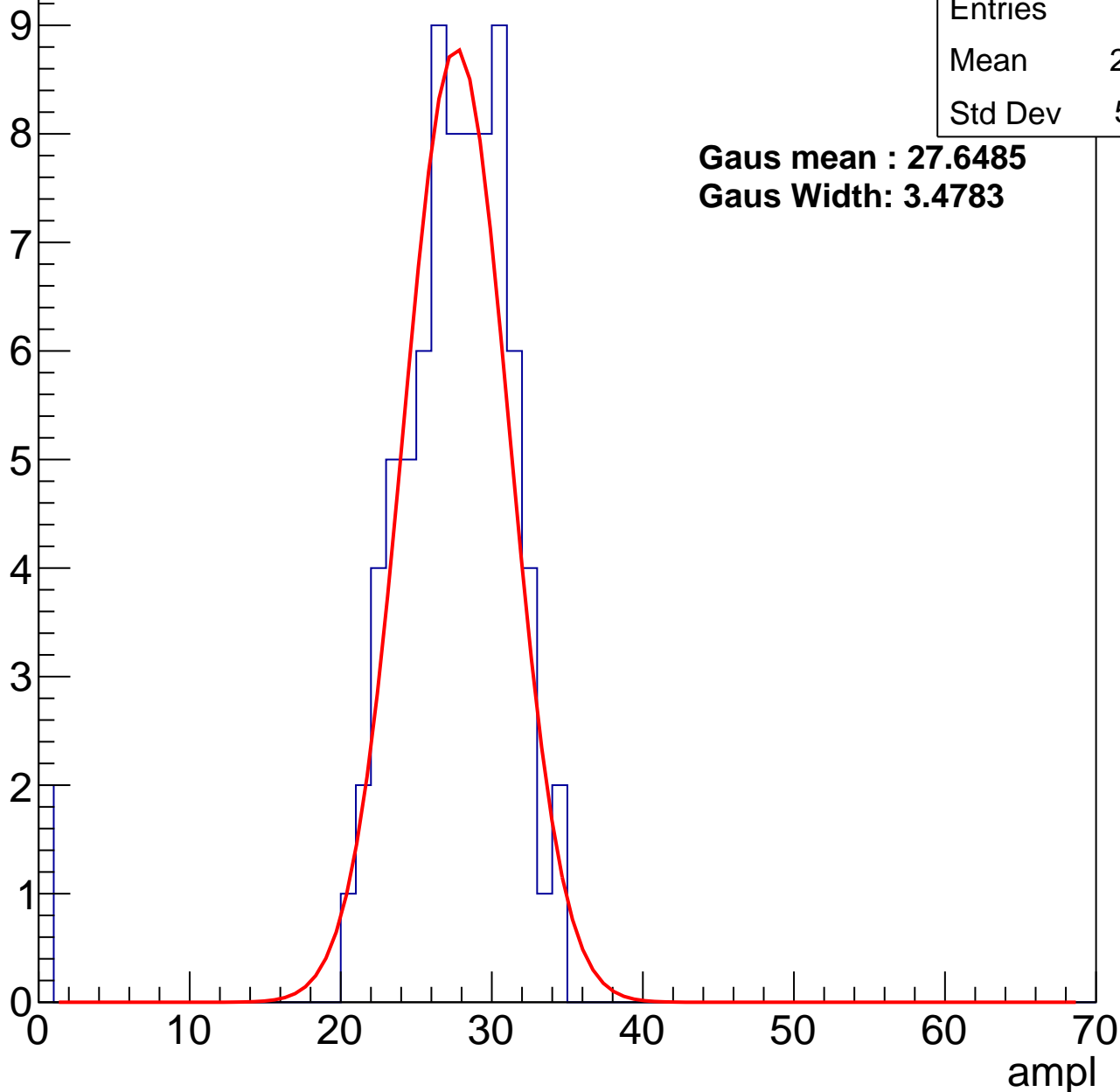
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	26.57
Std Dev	5.331

Gaus mean : 27.6485

Gaus Width: 3.4783



B1L100S, U14-ch12, adc1

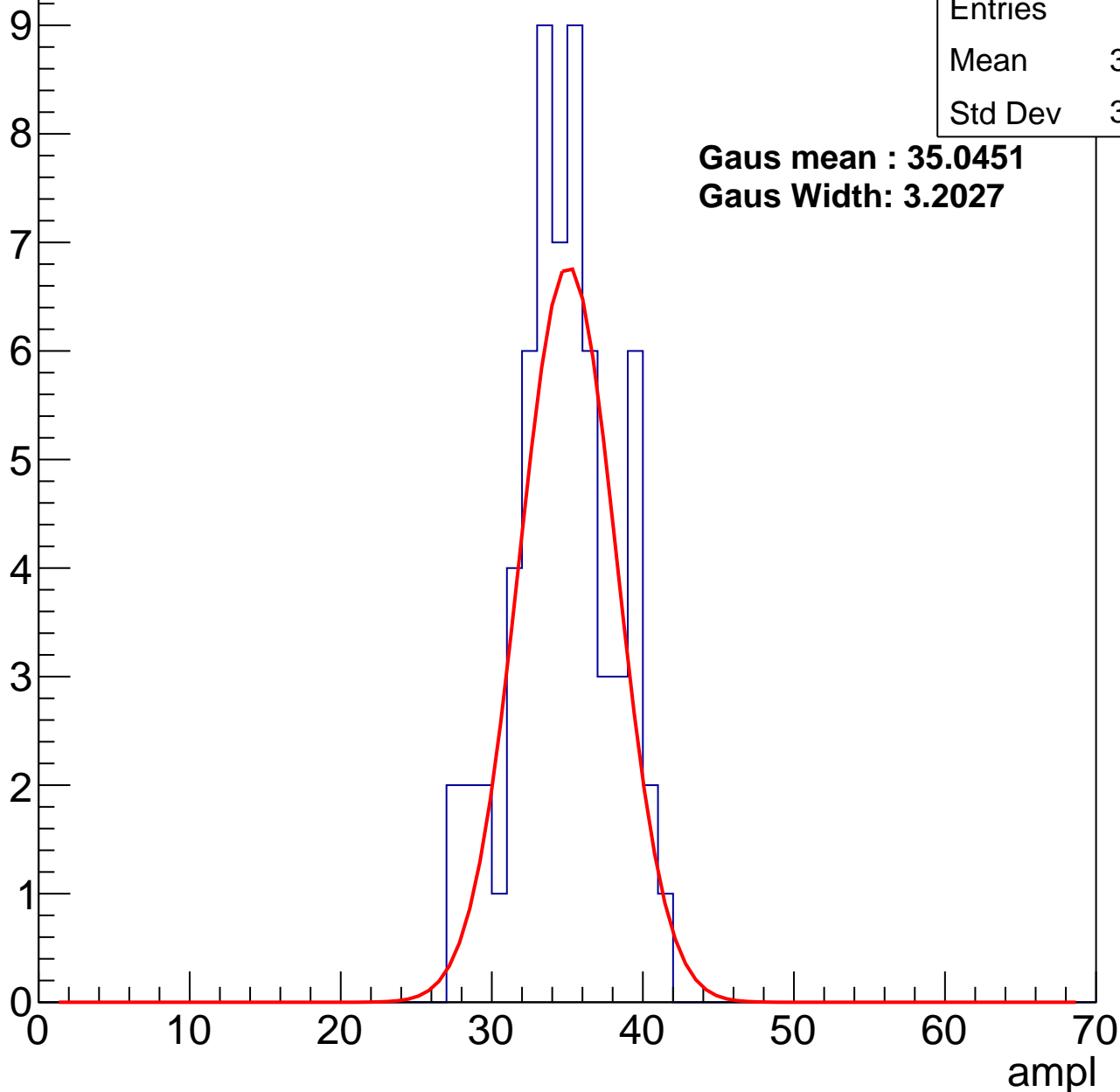
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	34.29
Std Dev	3.283

Gaus mean : 35.0451

Gaus Width: 3.2027



B1L100S, U14-ch12, adc2

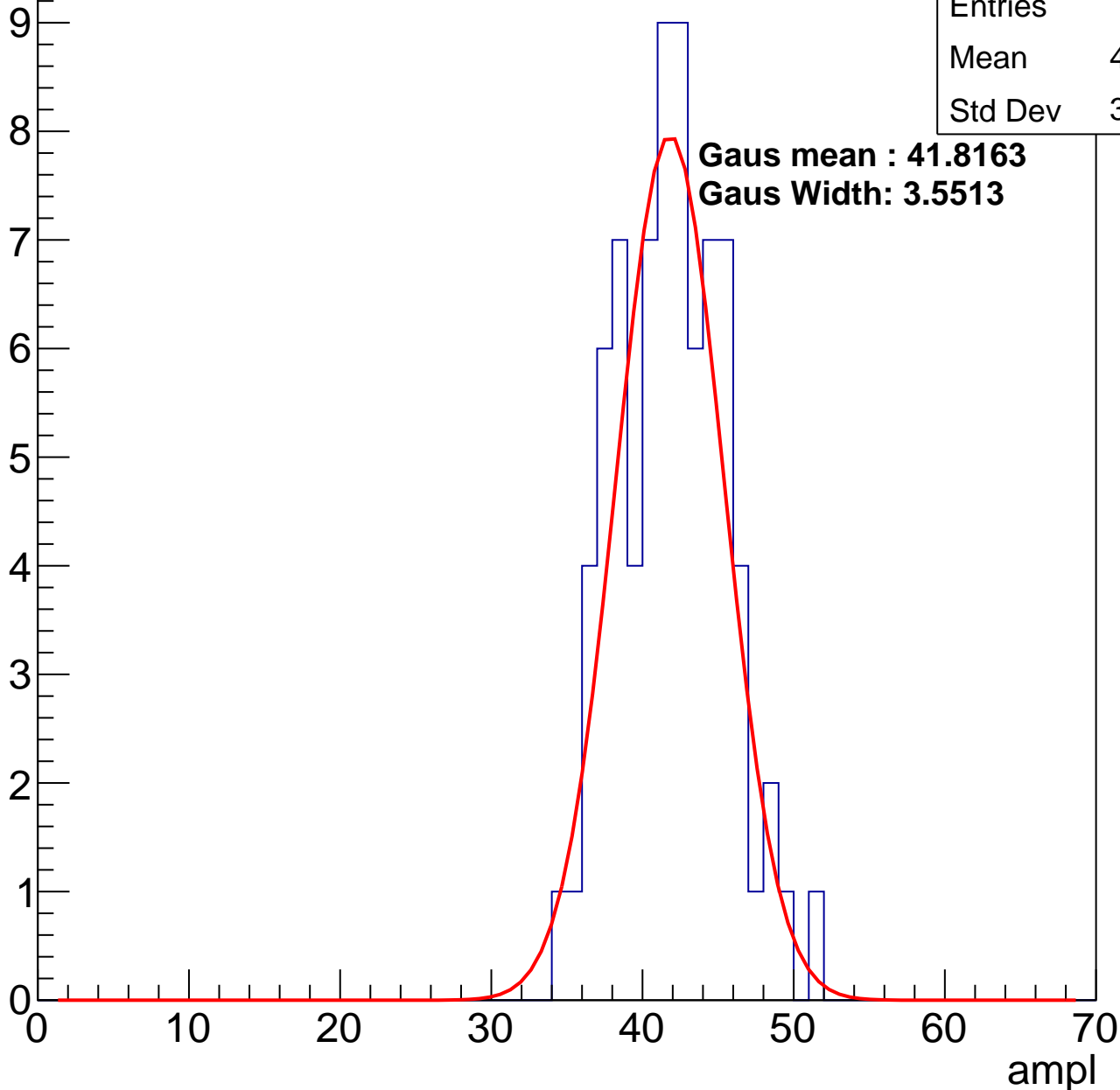
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	41.45
Std Dev	3.526

Gaus mean : 41.8163

Gaus Width: 3.5513

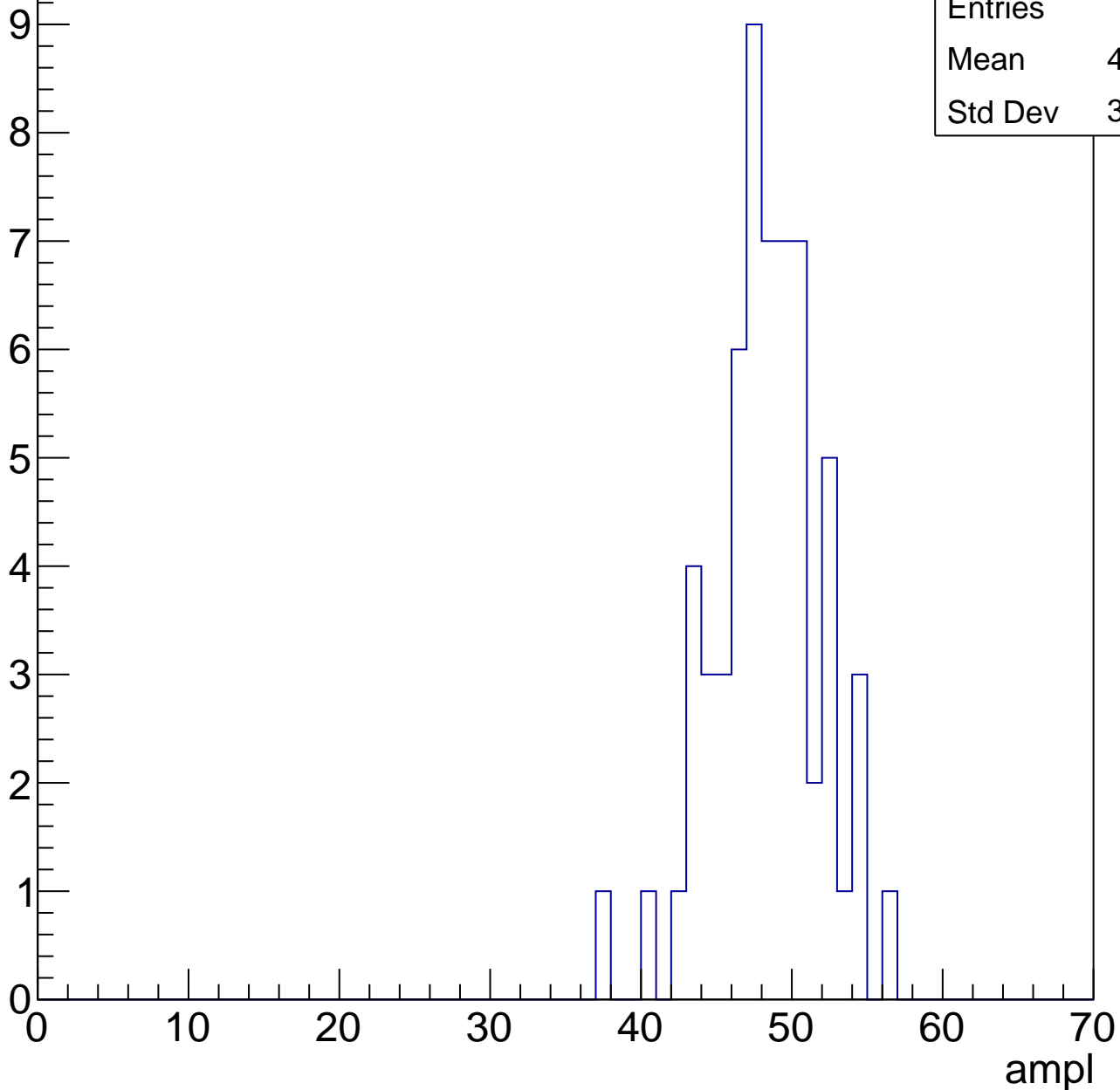


B1L100S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	47.85
Std Dev	3.524

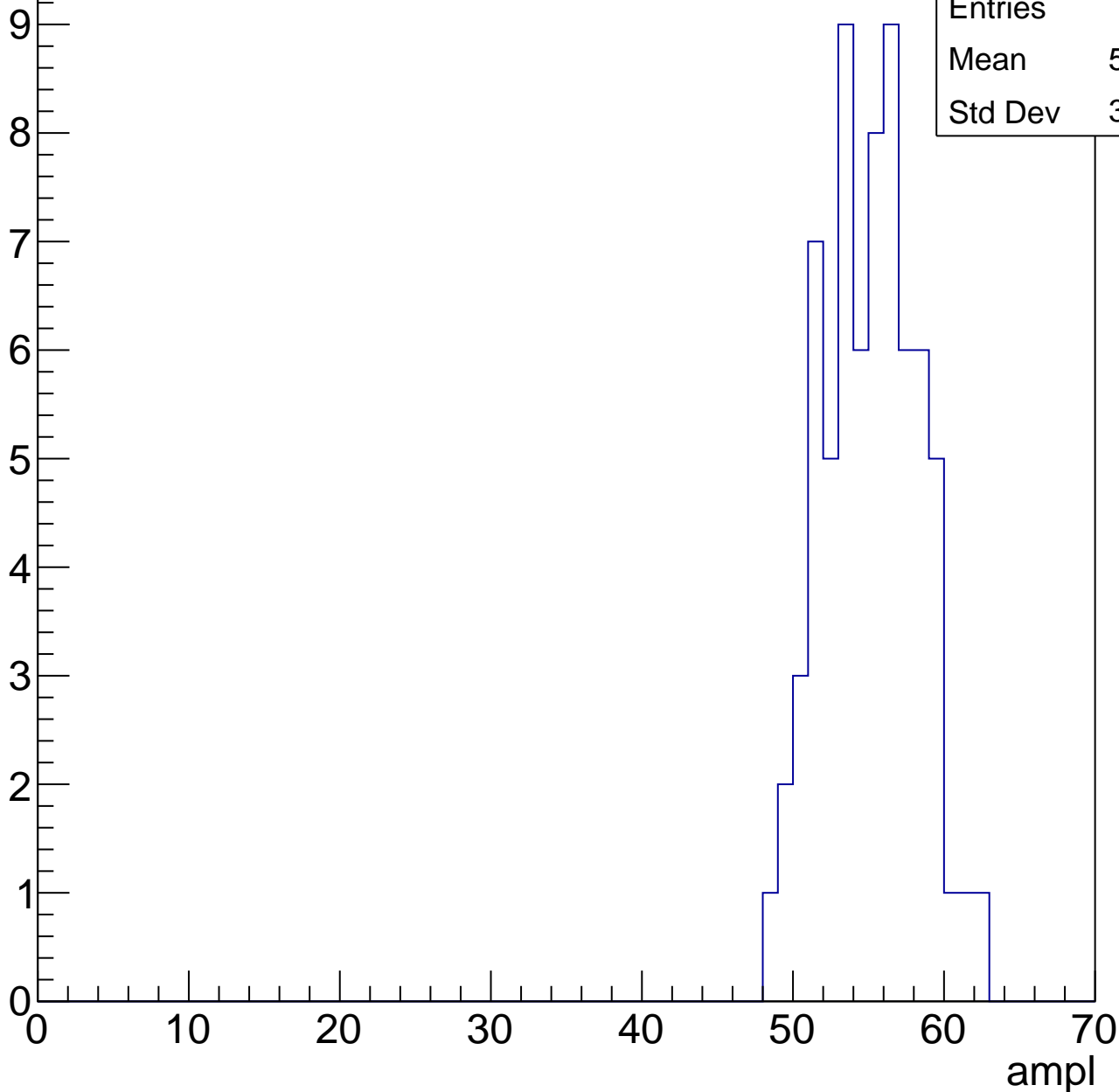


B1L100S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	54.66
Std Dev	3.079



B1L100S, U14-ch12, adc5

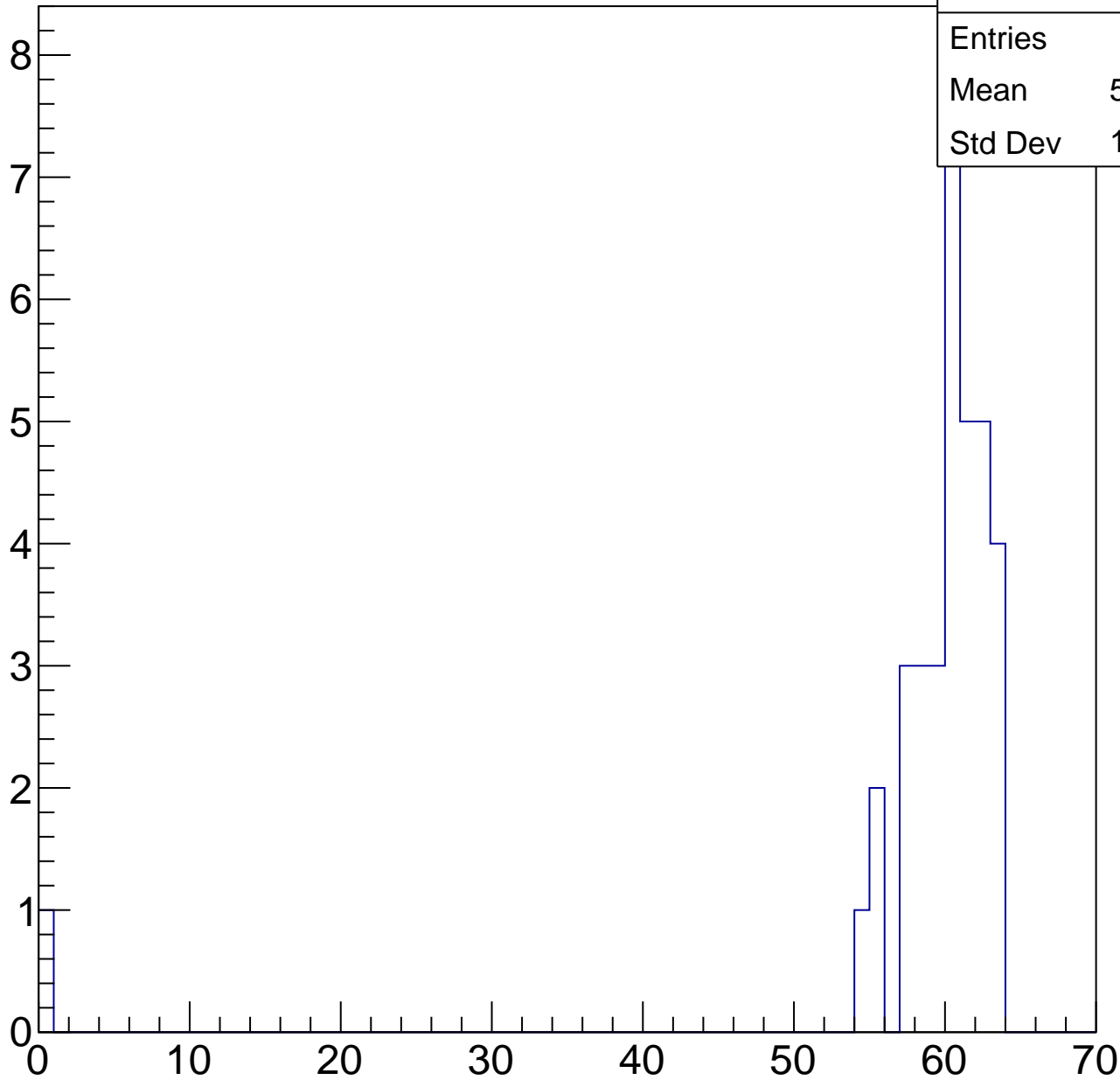
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	58.09
Std Dev	10.23

ampl

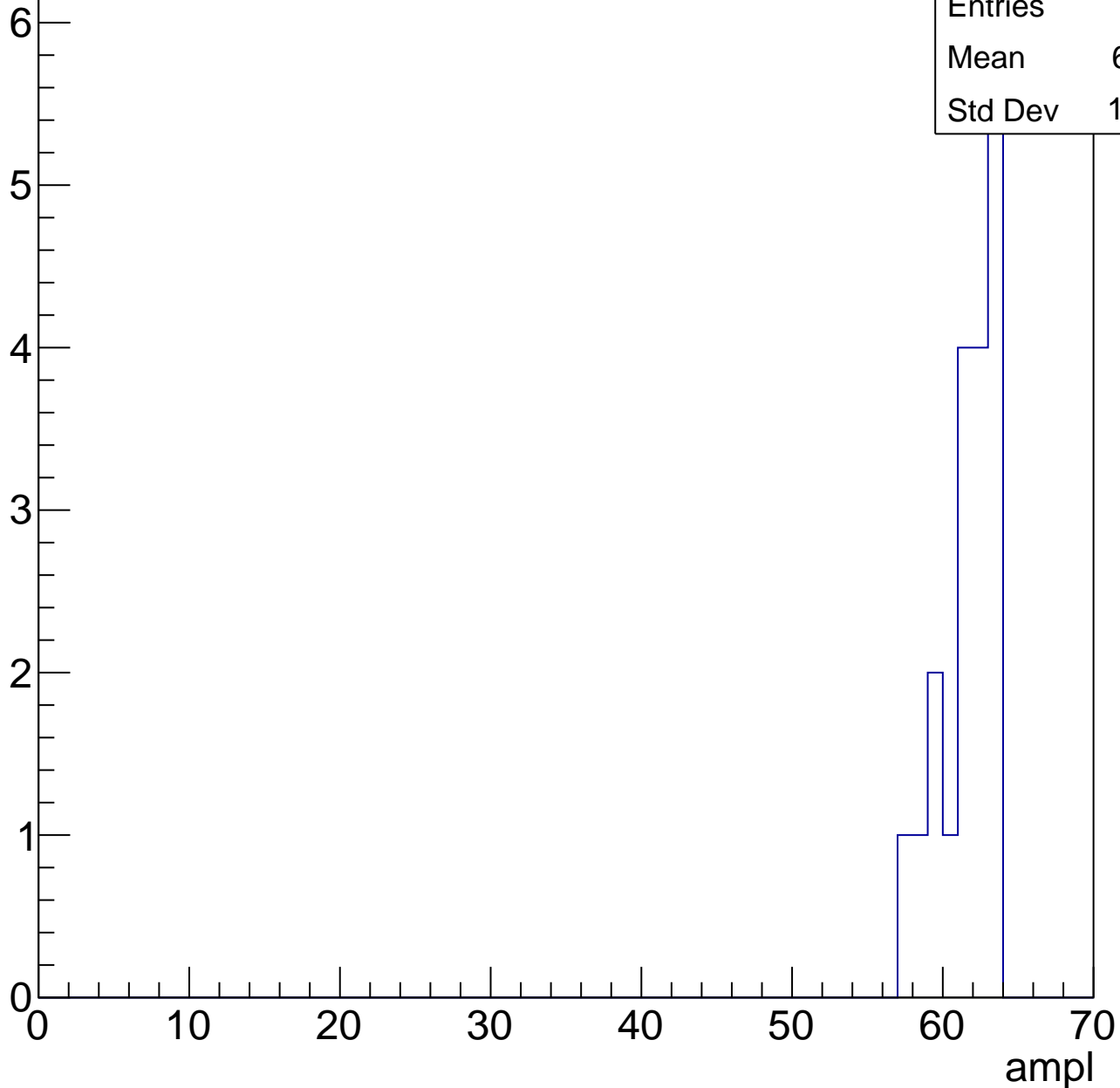


B1L100S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	61.21
Std Dev	1.794



B1L100S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch13, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	28.74
Std Dev	3.446

Gaus mean : 28.5610

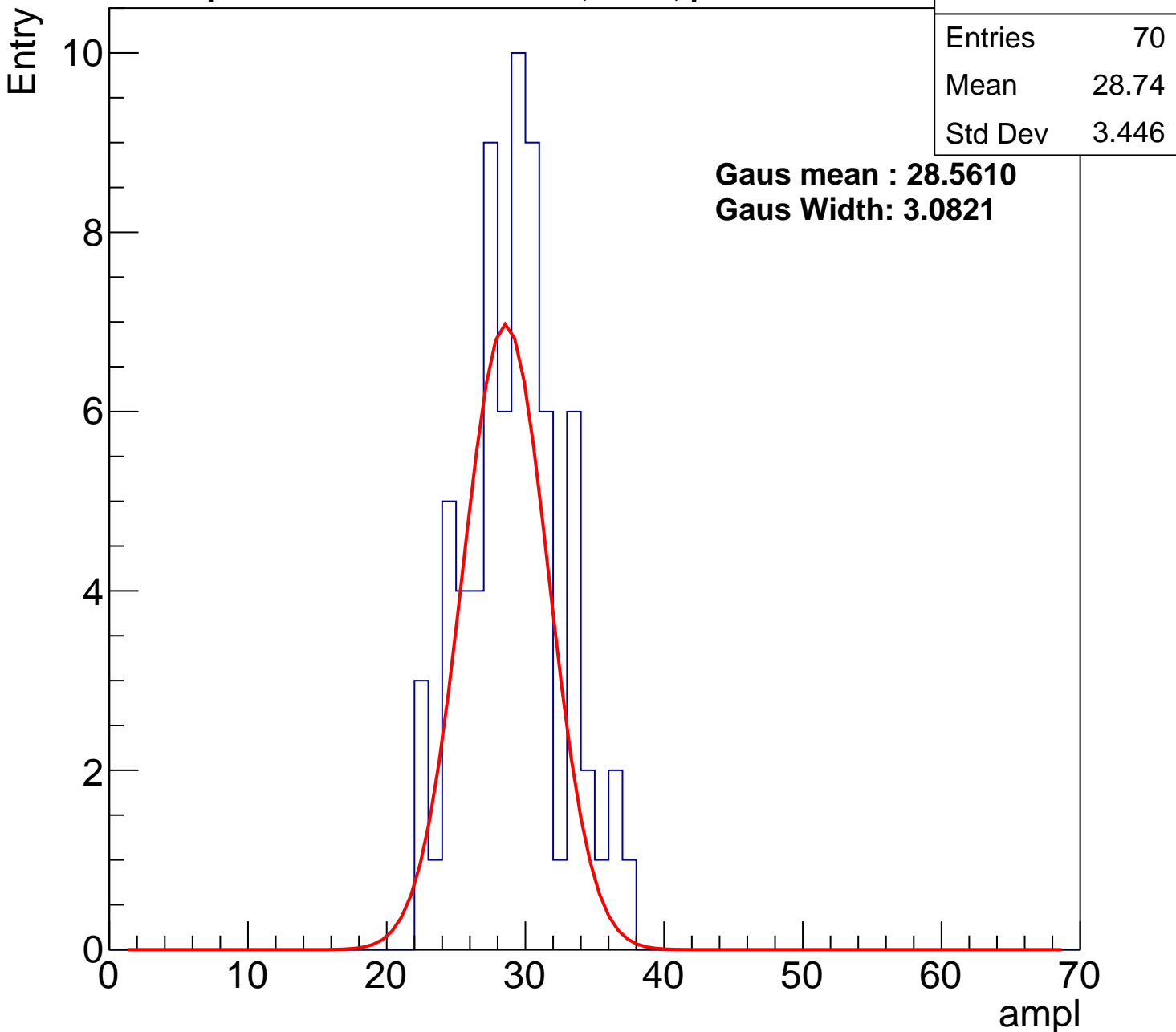
Gaus Width: 3.0821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch13, adc1

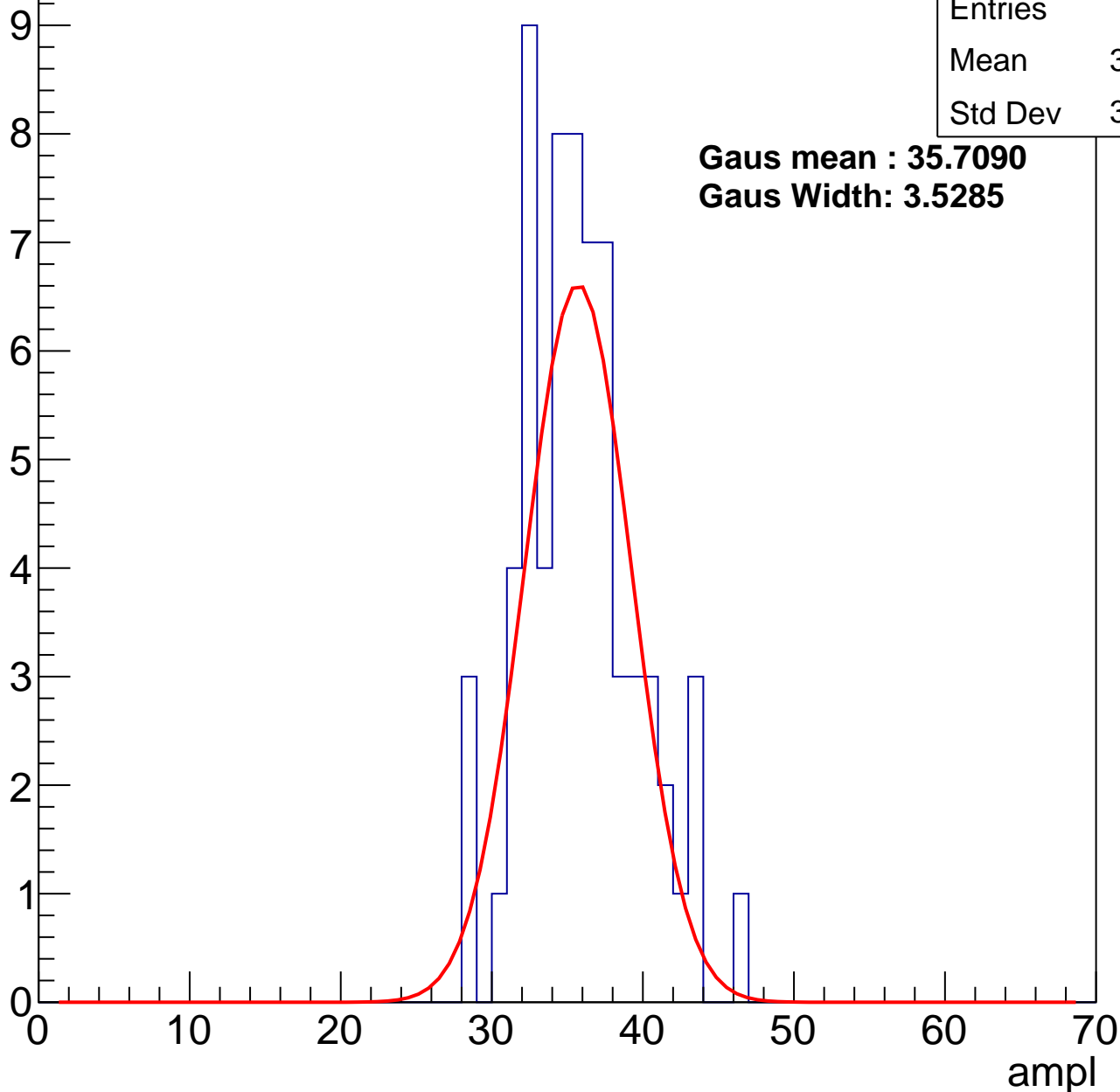
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.39
Std Dev	3.765

Gaus mean : 35.7090

Gaus Width: 3.5285

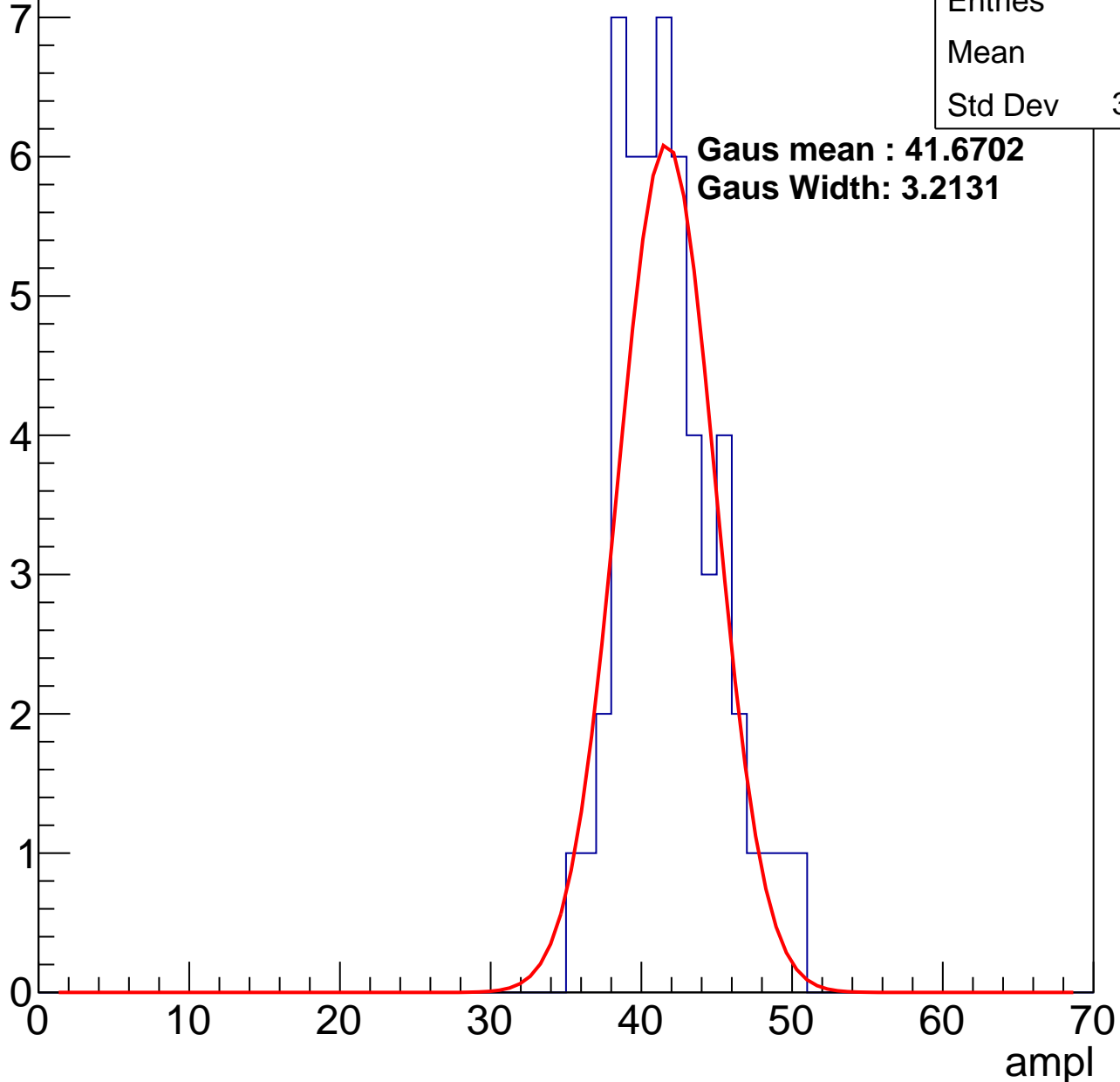


B1L100S, U14-ch13, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	41.4
Std Dev	3.281

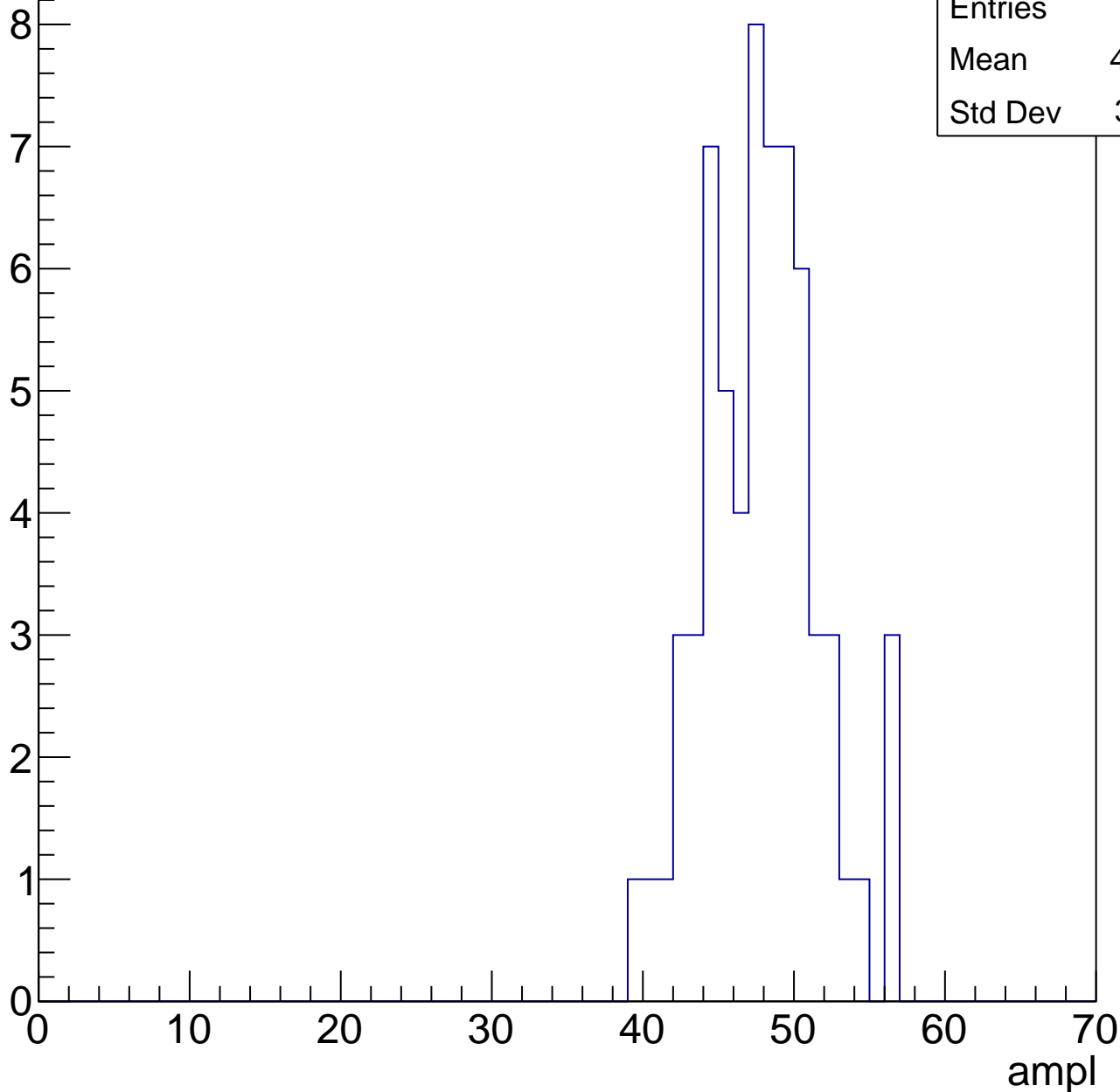


B1L100S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	47.36
Std Dev	3.731

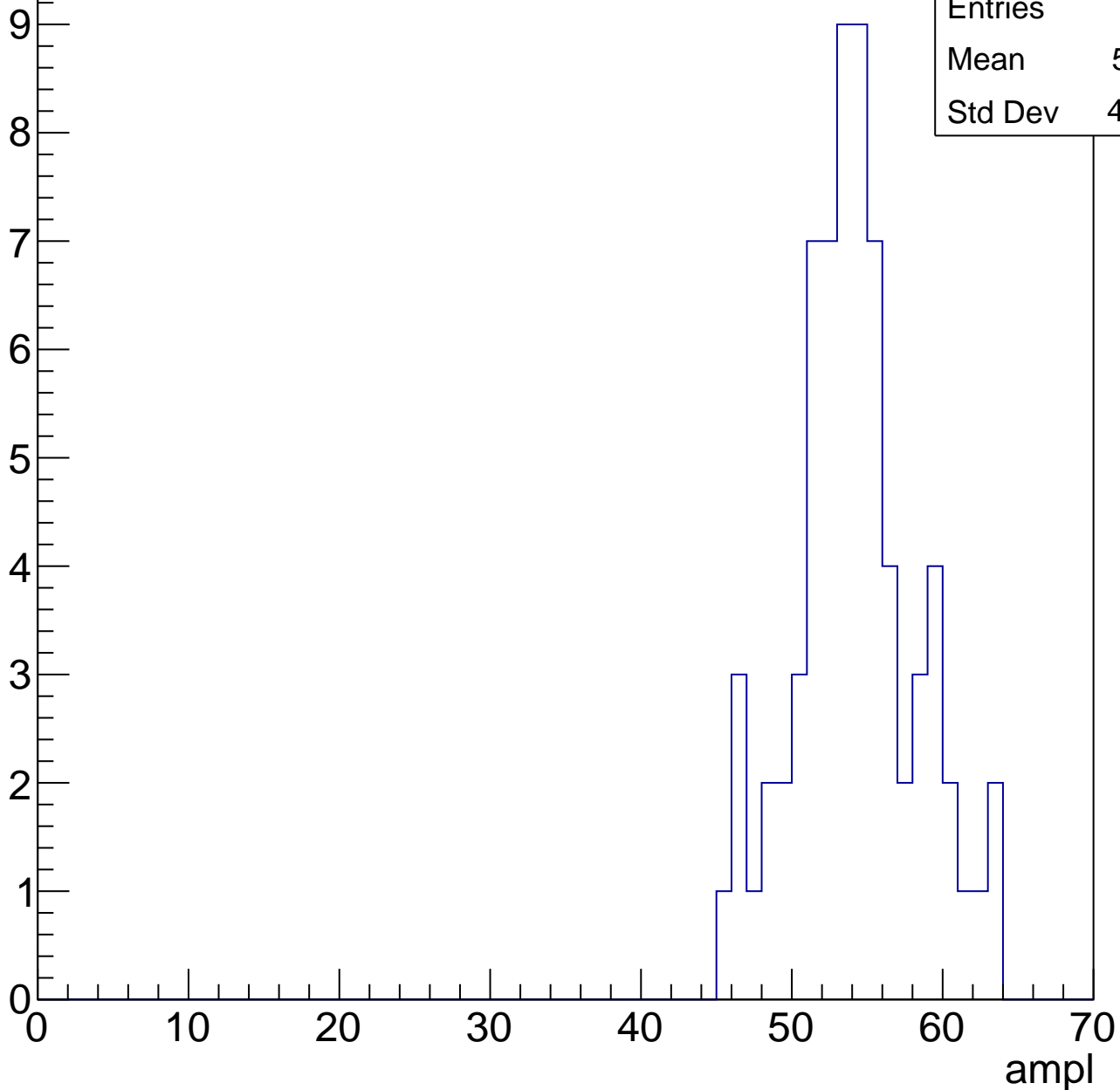


B1L100S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

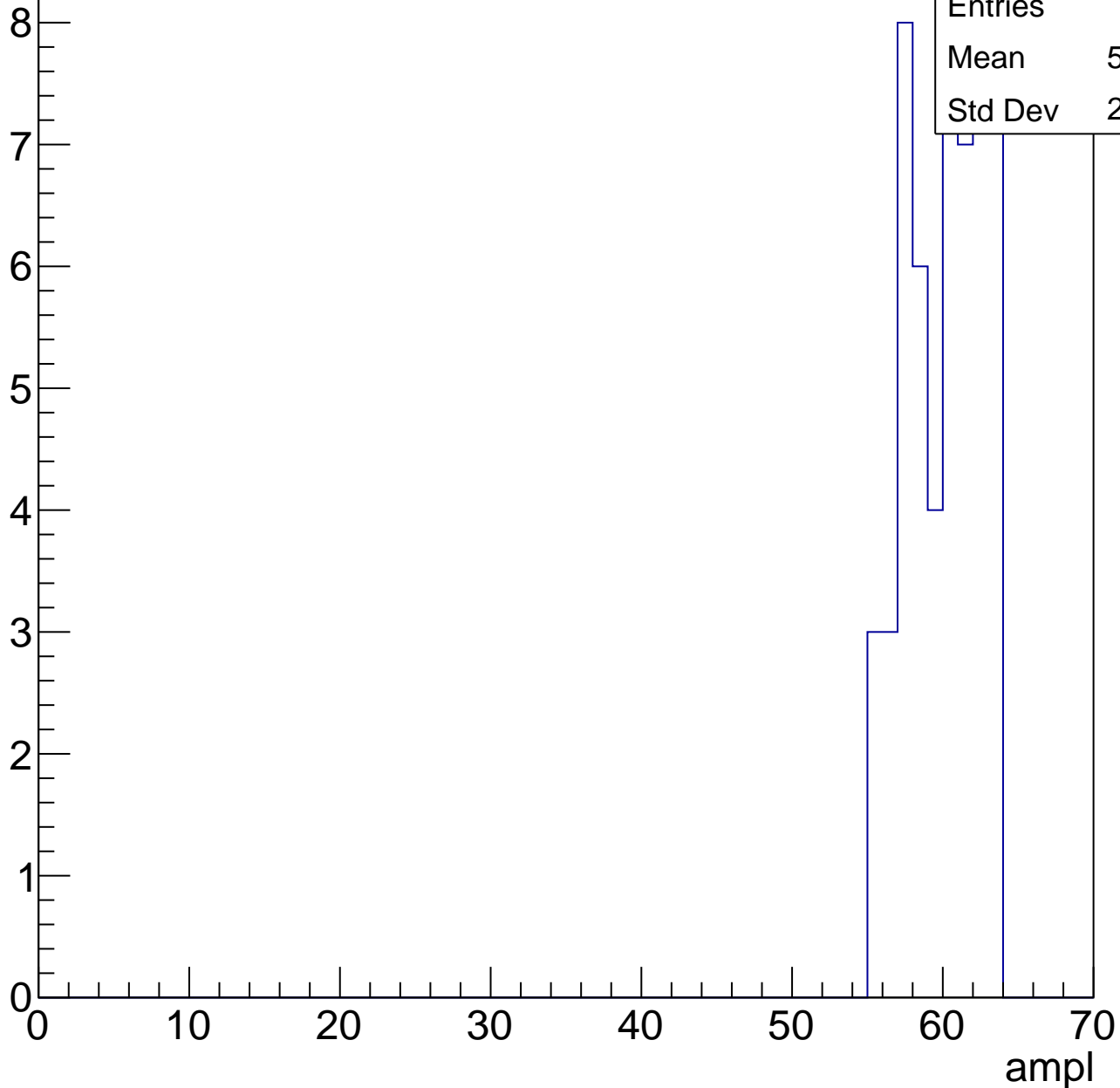
Entries	70
Mean	53.71
Std Dev	4.004



B1L100S, U14-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	55
Mean	59.64
Std Dev	2.437

B1L100S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

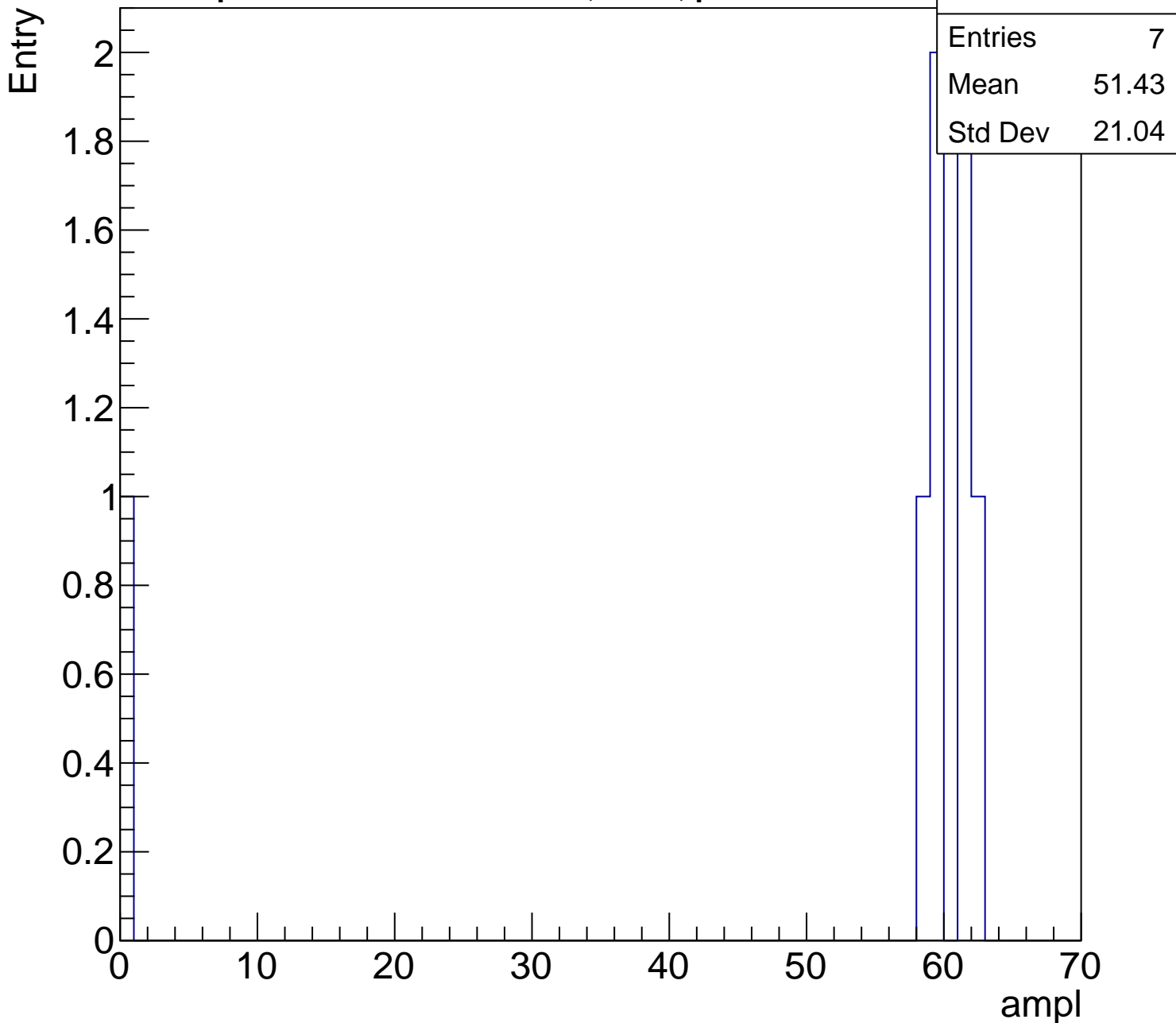
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	51.43
Std Dev	21.04

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch14, adc0

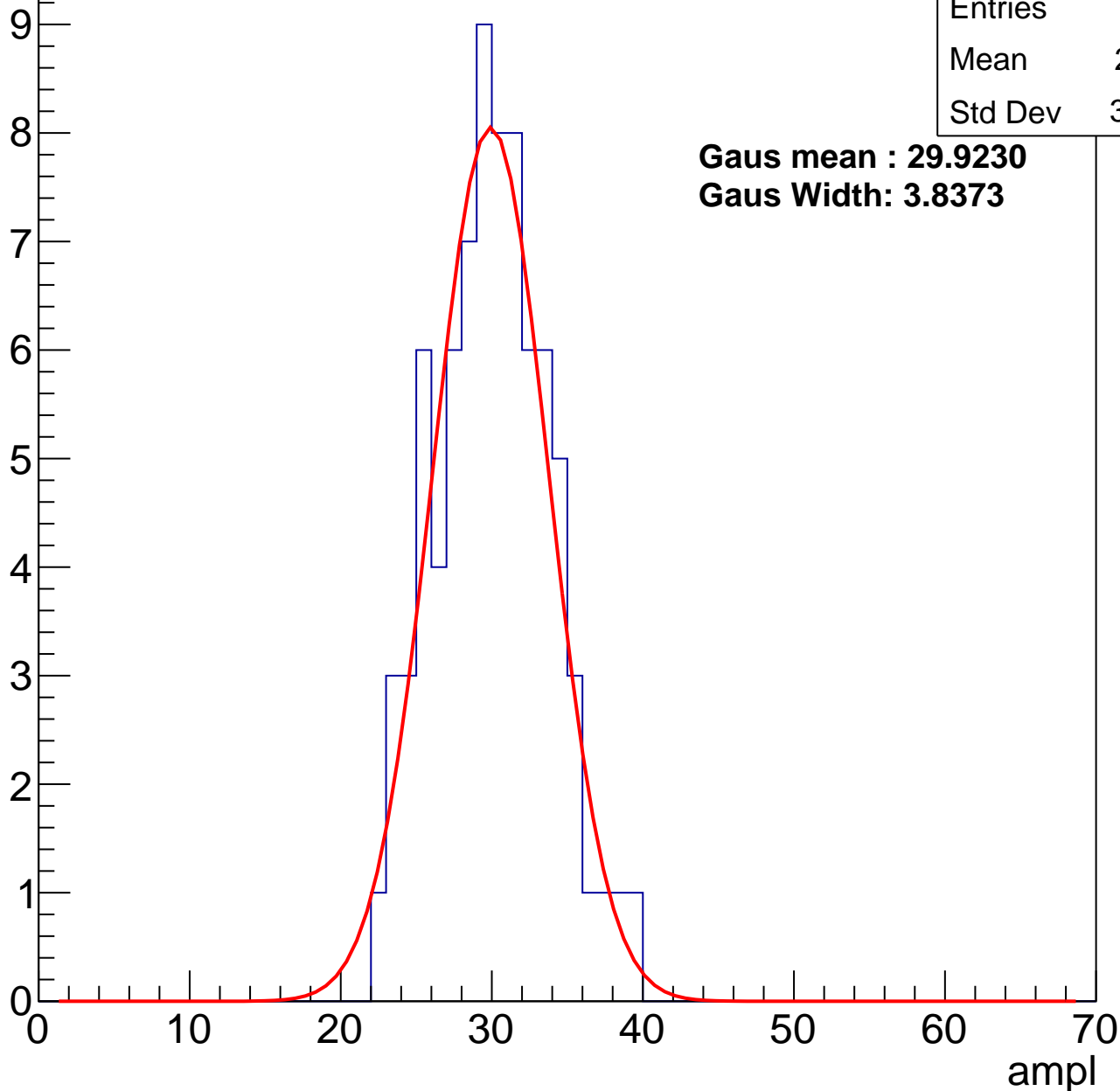
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	29.61
Std Dev	3.702

Gaus mean : 29.9230

Gaus Width: 3.8373



B1L100S, U14-ch14, adc1

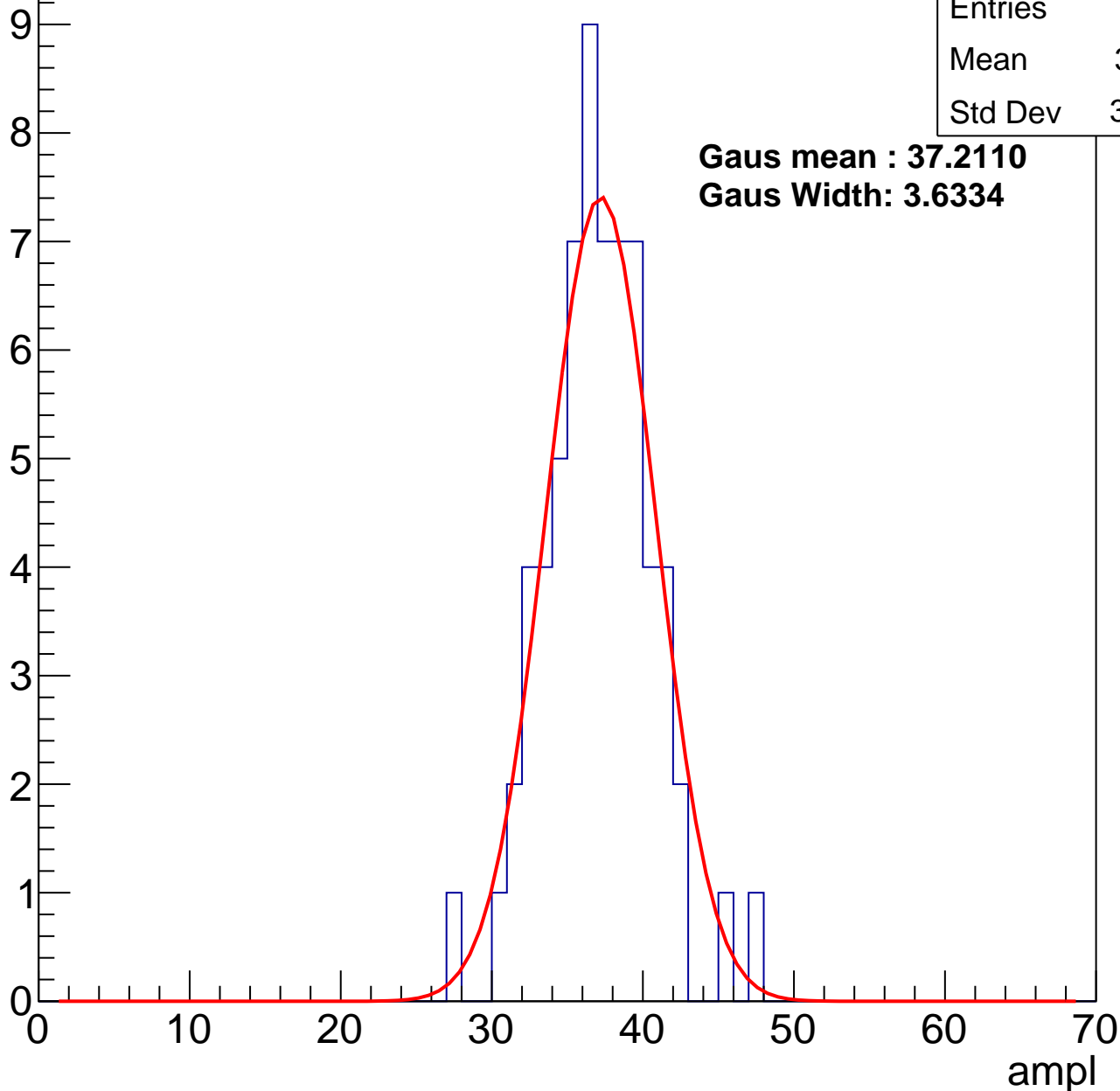
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.61
Std Dev	3.494

Gaus mean : 37.2110

Gaus Width: 3.6334



B1L100S, U14-ch14, adc2

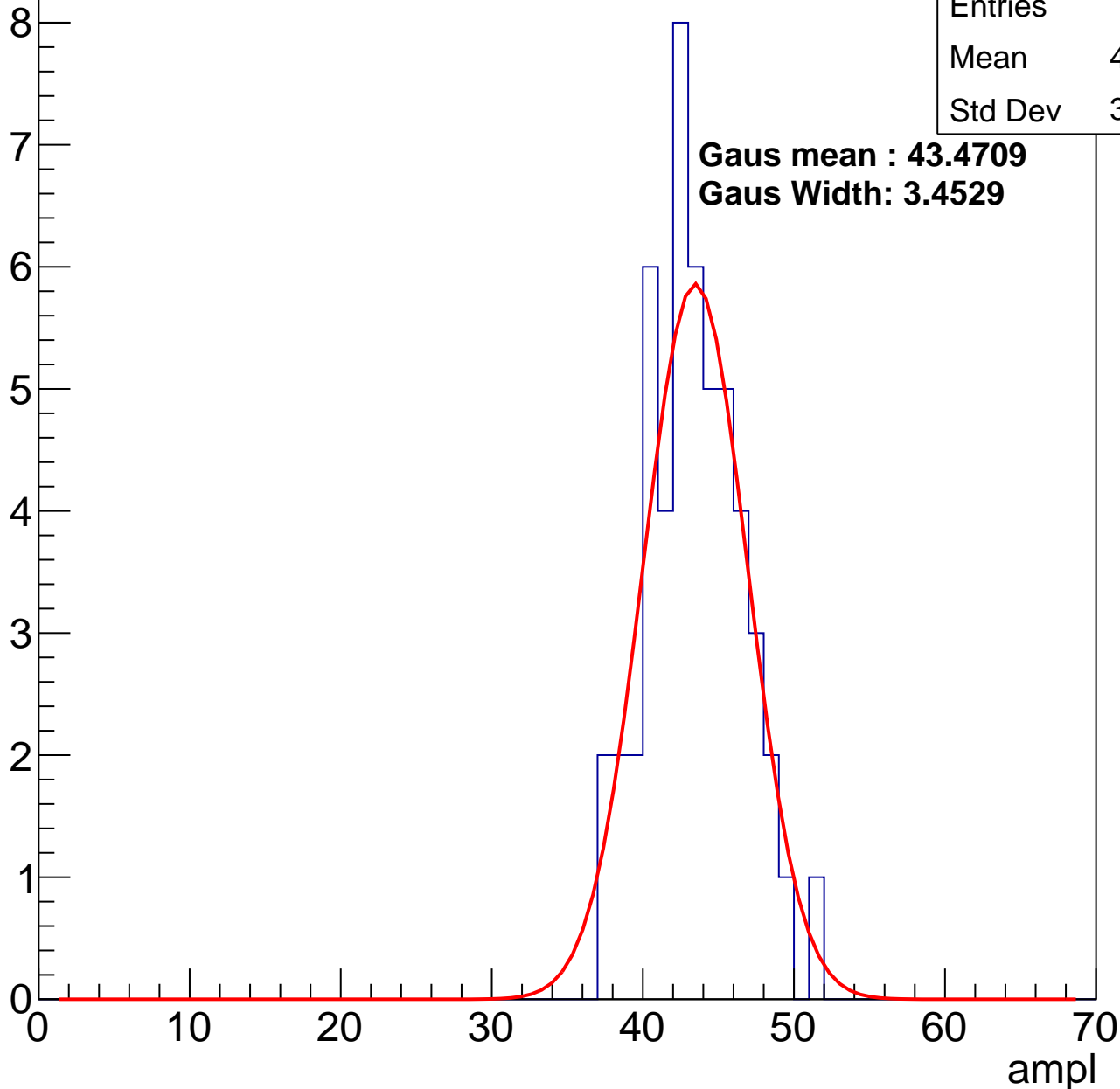
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.98
Std Dev	3.103

Gaus mean : 43.4709

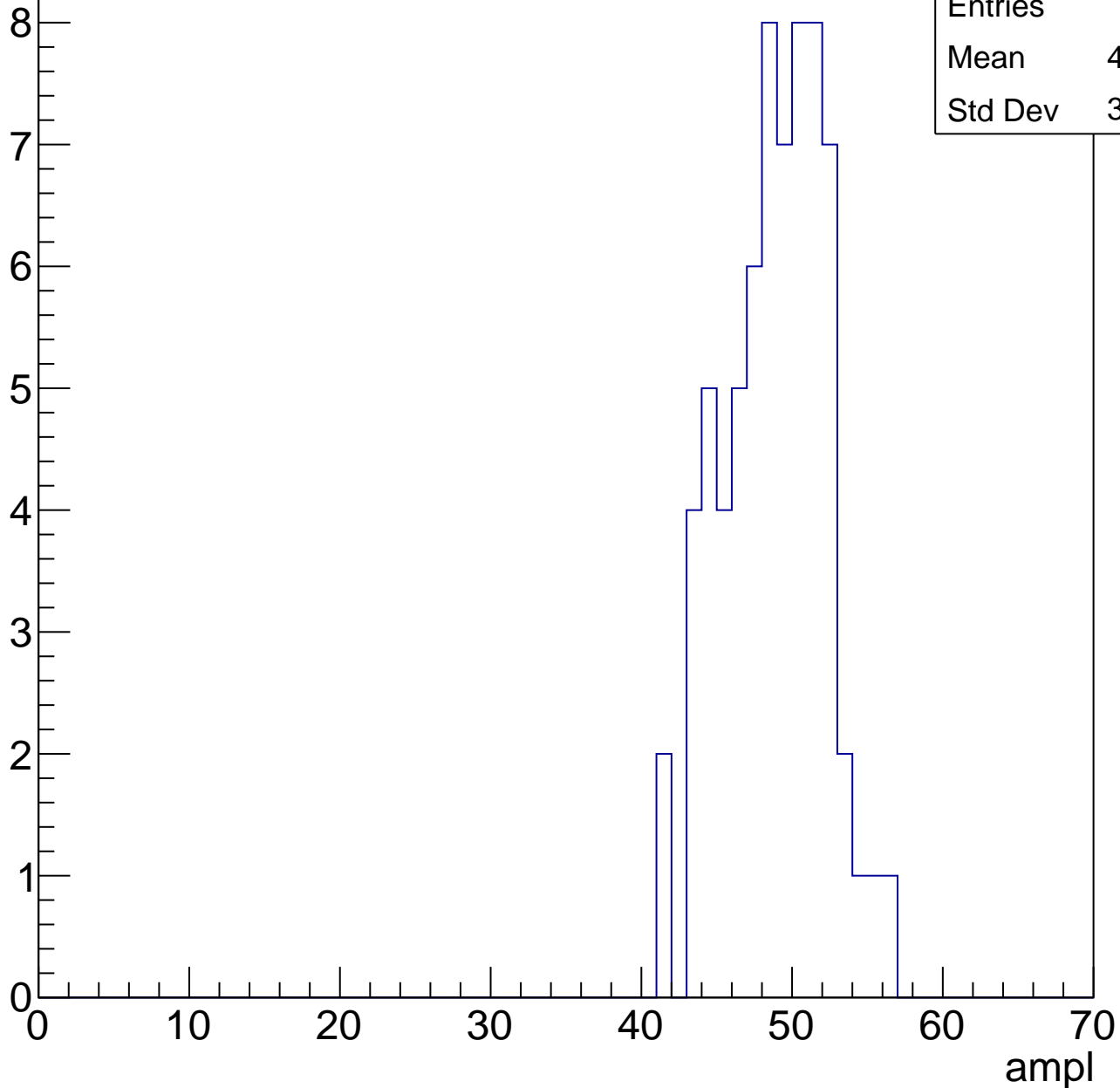
Gaus Width: 3.4529



B1L100S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

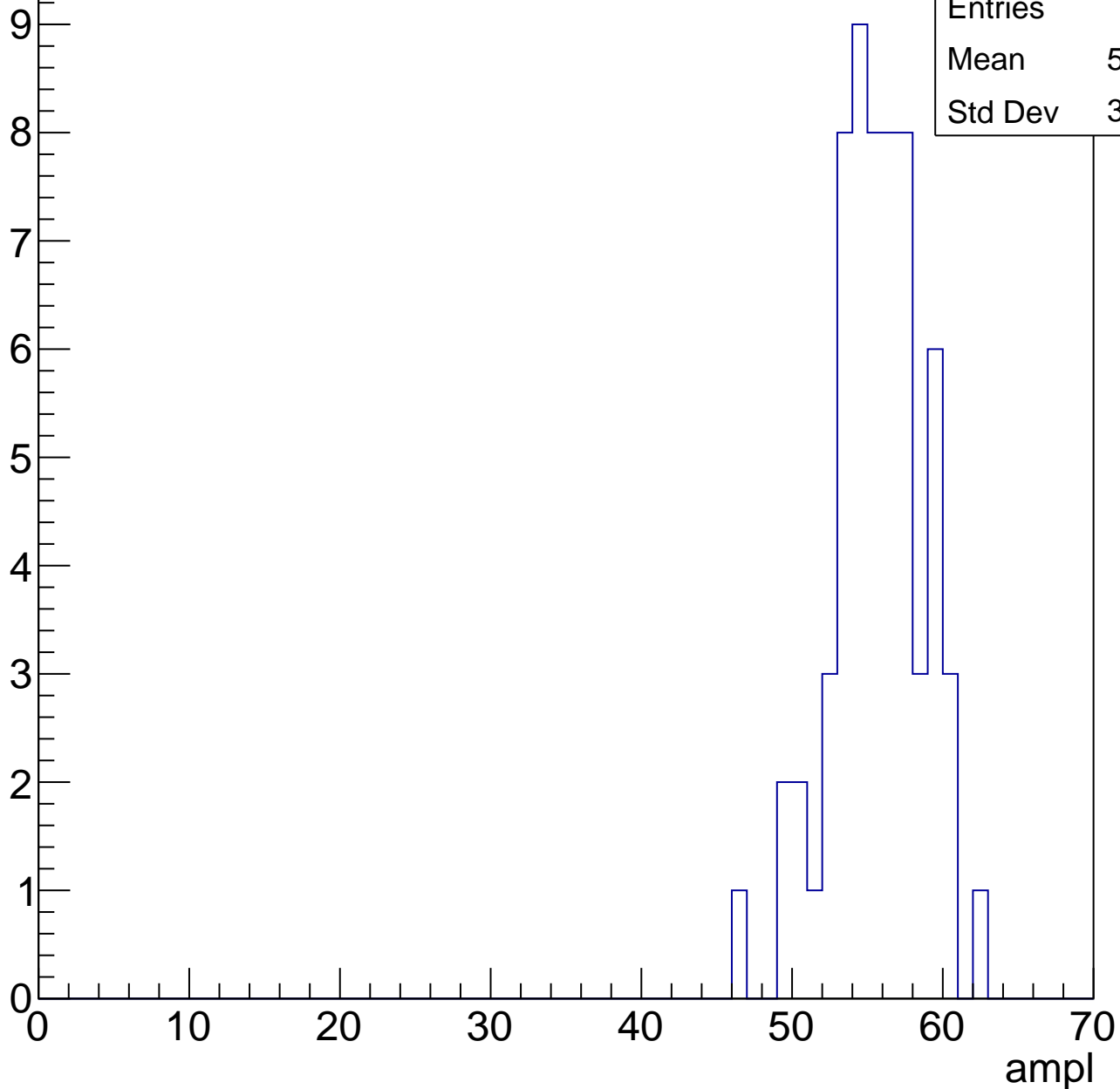
Entry



B1L100S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

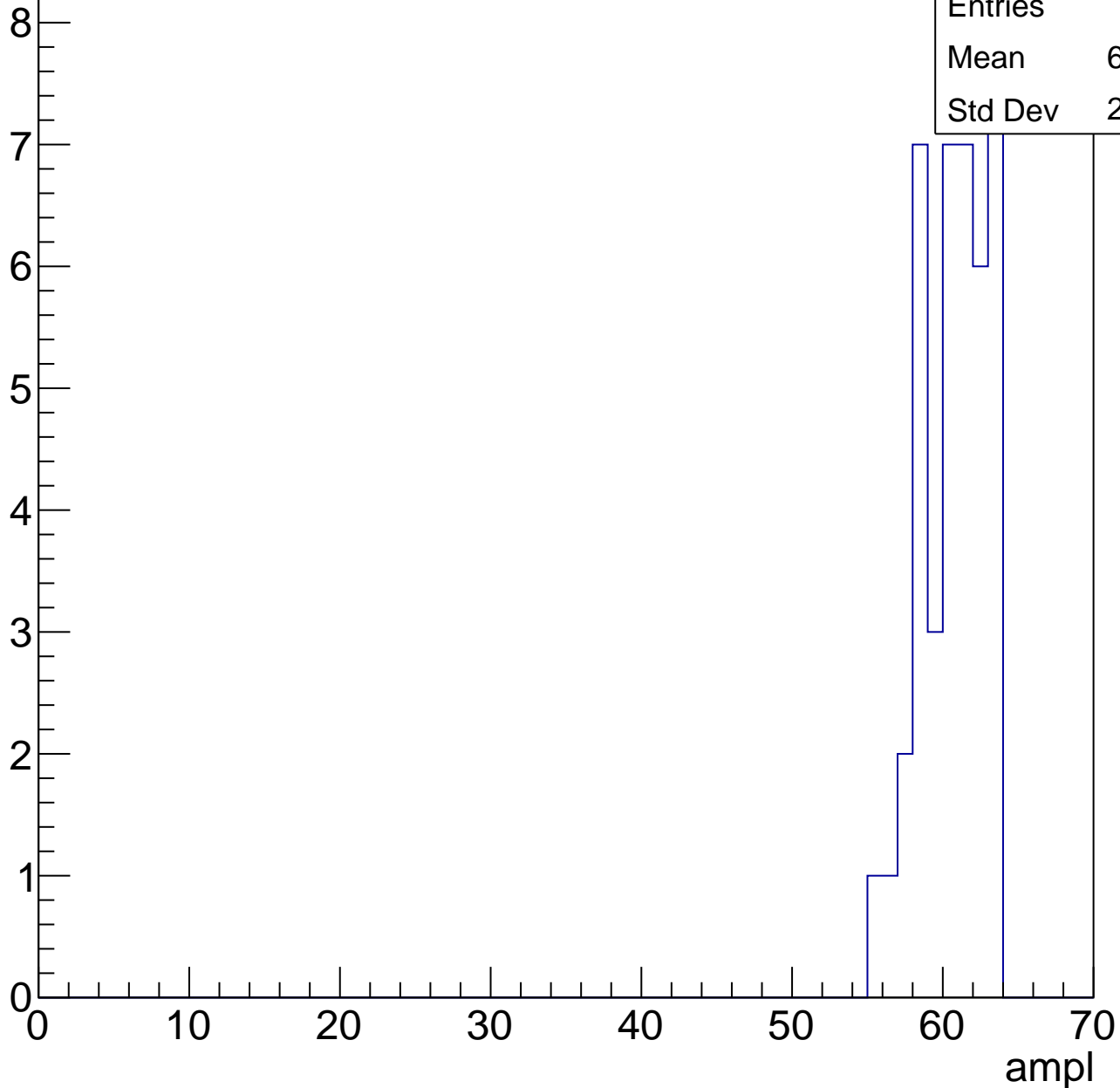
Entry



B1L100S, U14-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

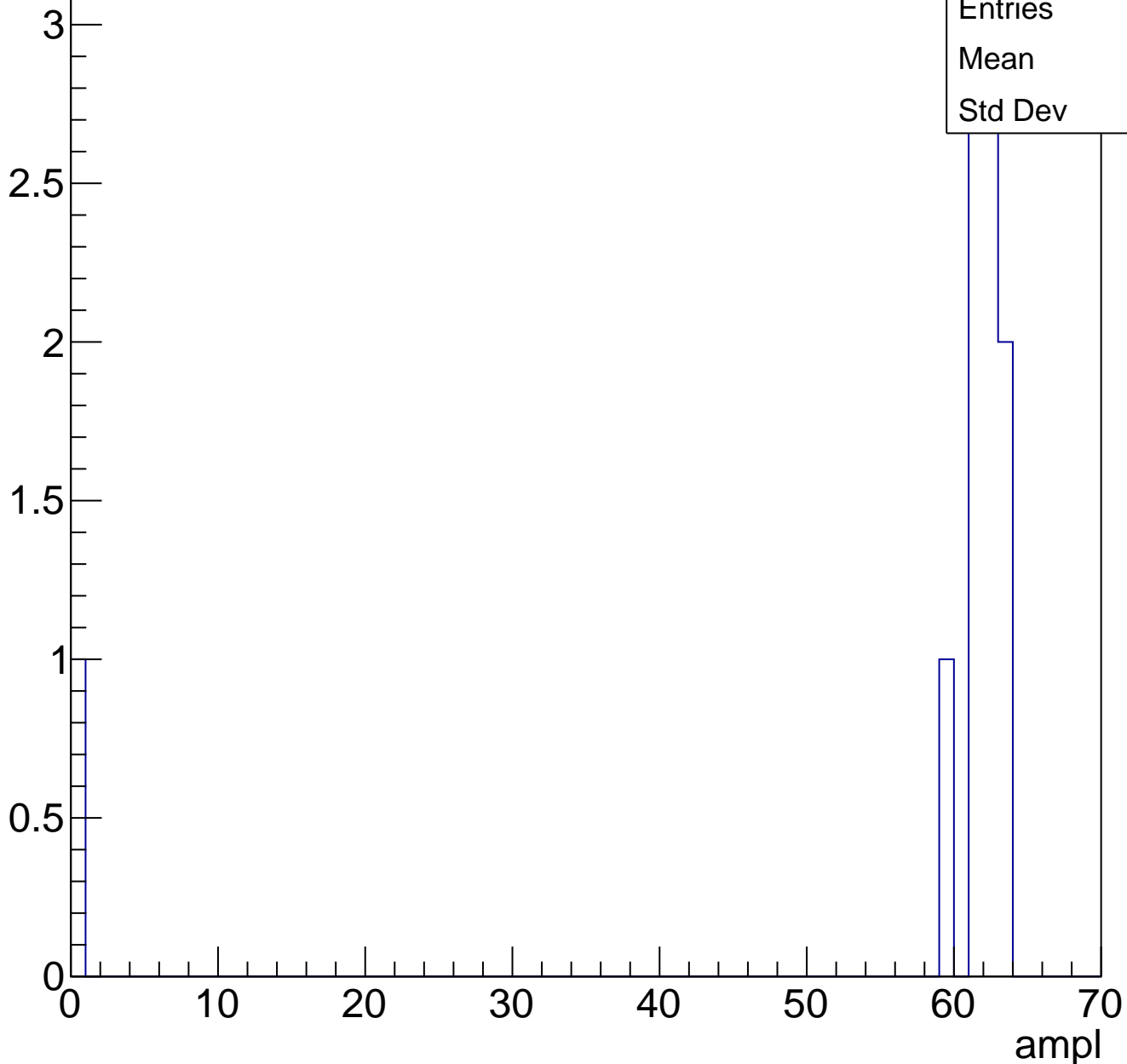


Entries	42
Mean	60.26
Std Dev	2.128

B1L100S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch15, adc0

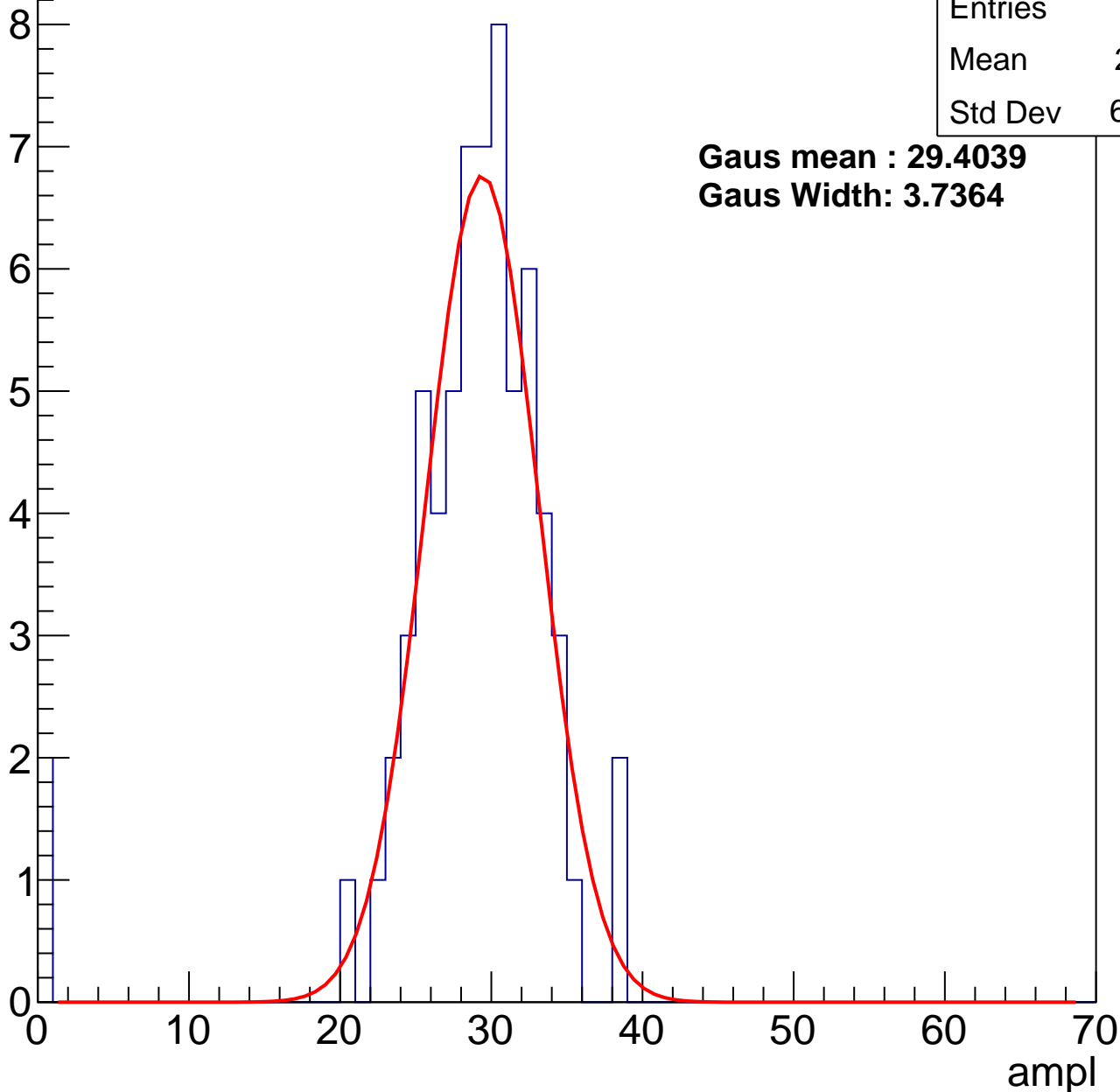
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.11
Std Dev	6.115

Gaus mean : 29.4039

Gaus Width: 3.7364



B1L100S, U14-ch15, adc1

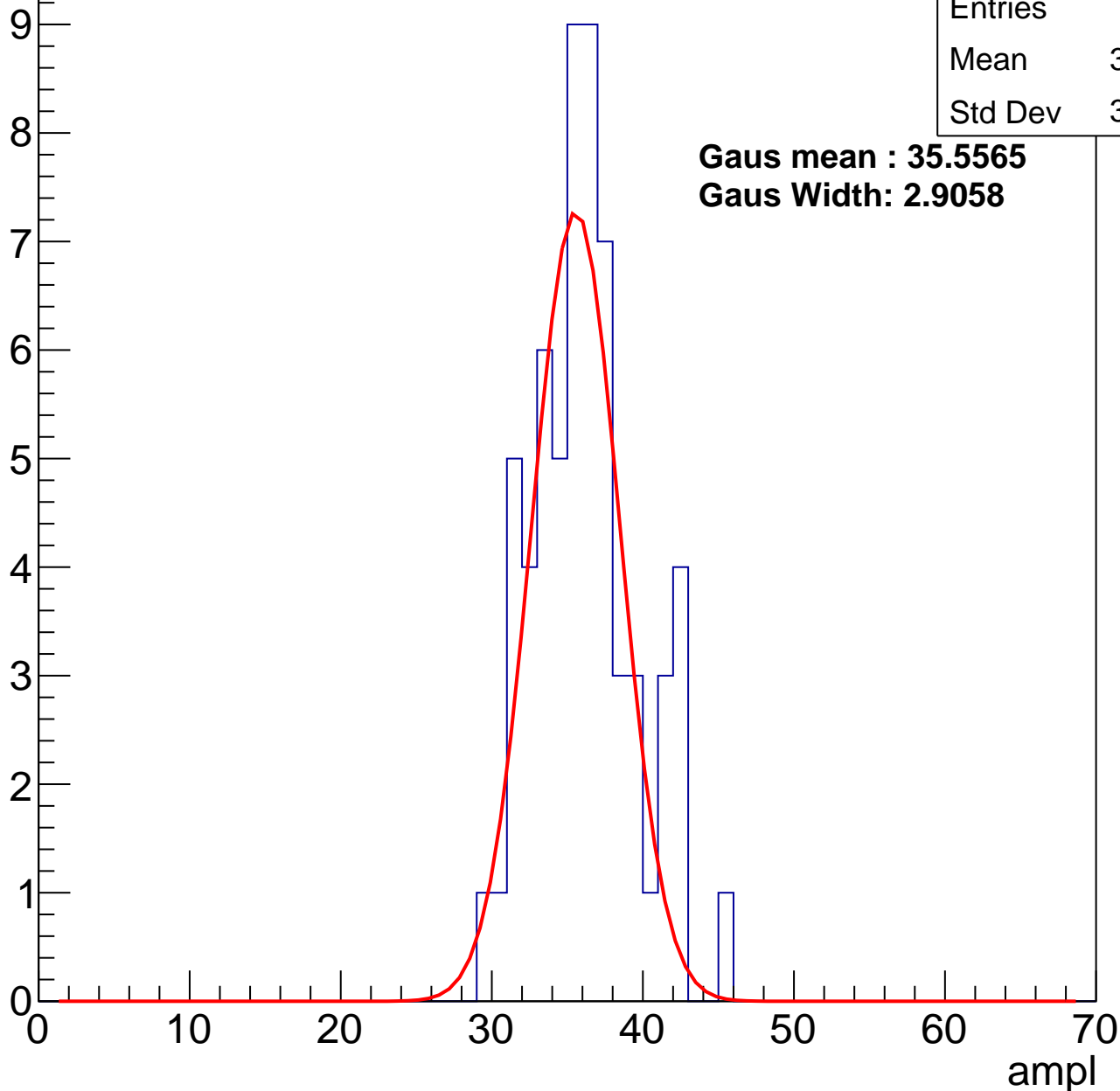
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.73
Std Dev	3.394

Gaus mean : 35.5565

Gaus Width: 2.9058



B1L100S, U14-ch15, adc2

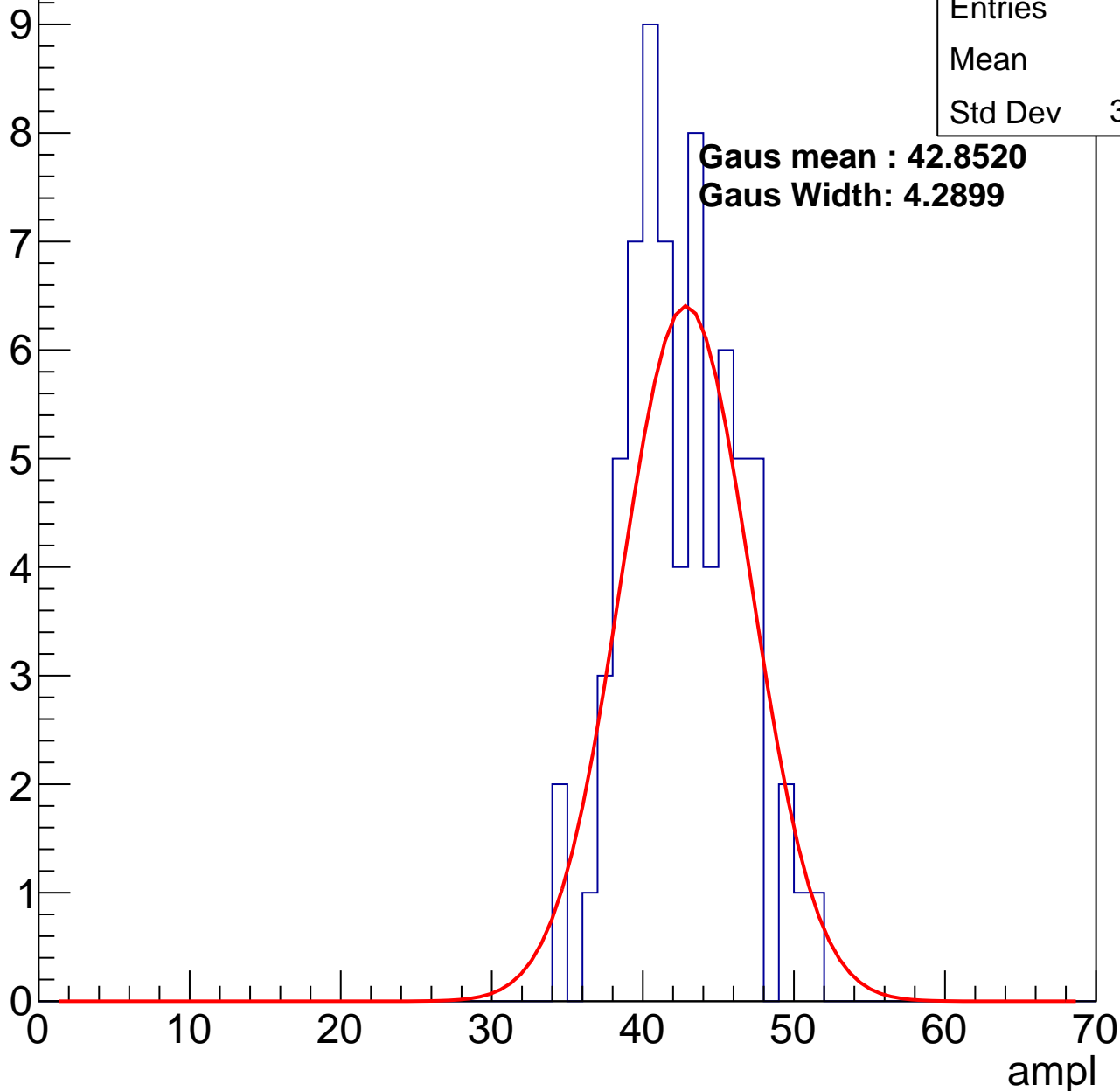
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.1
Std Dev	3.685

Gaus mean : 42.8520

Gaus Width: 4.2899



B1L100S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	48.96
Std Dev	3.732

Entry

10

8

6

4

2

0

0

10

20

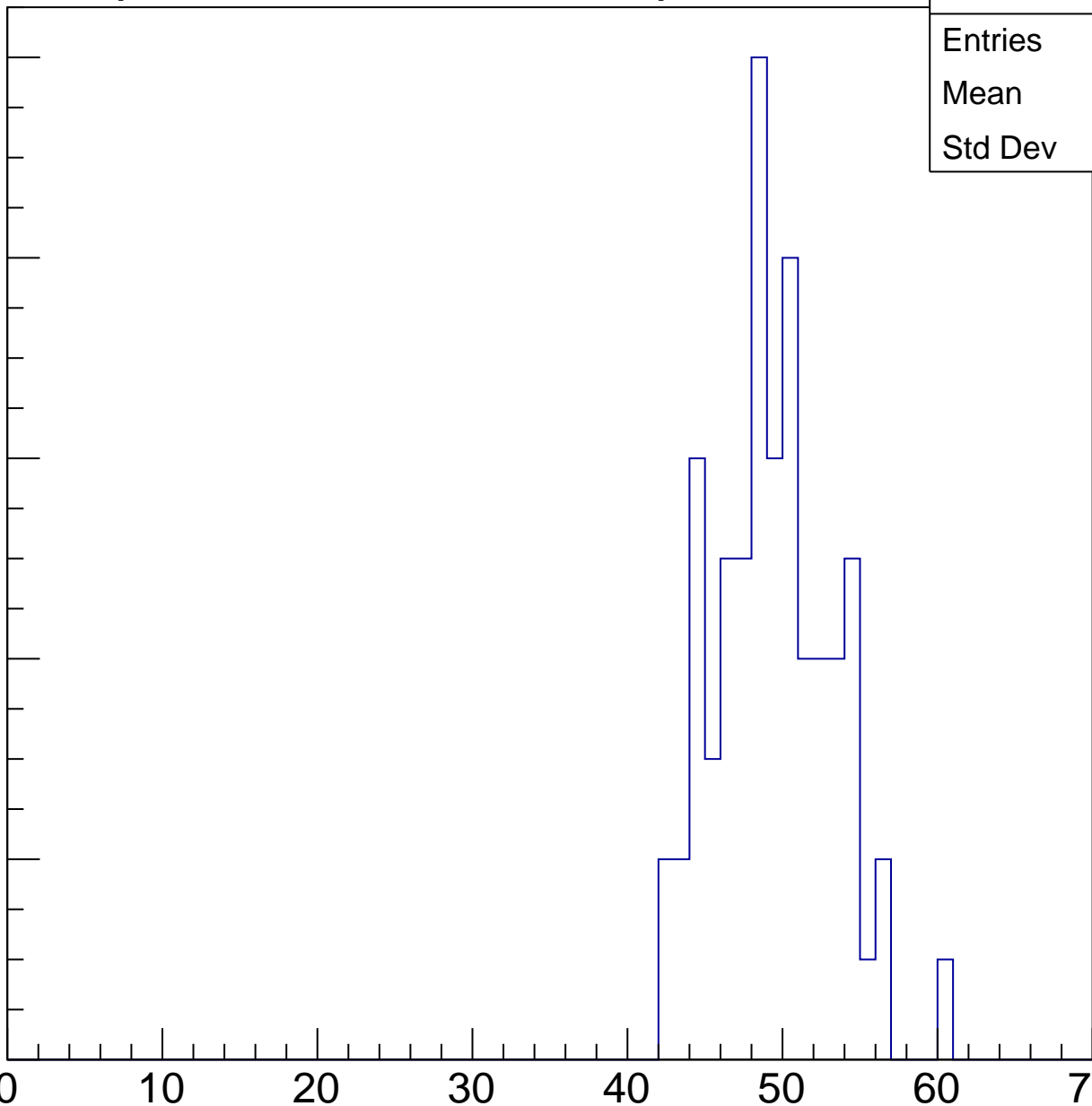
30

40

50

60

ampl

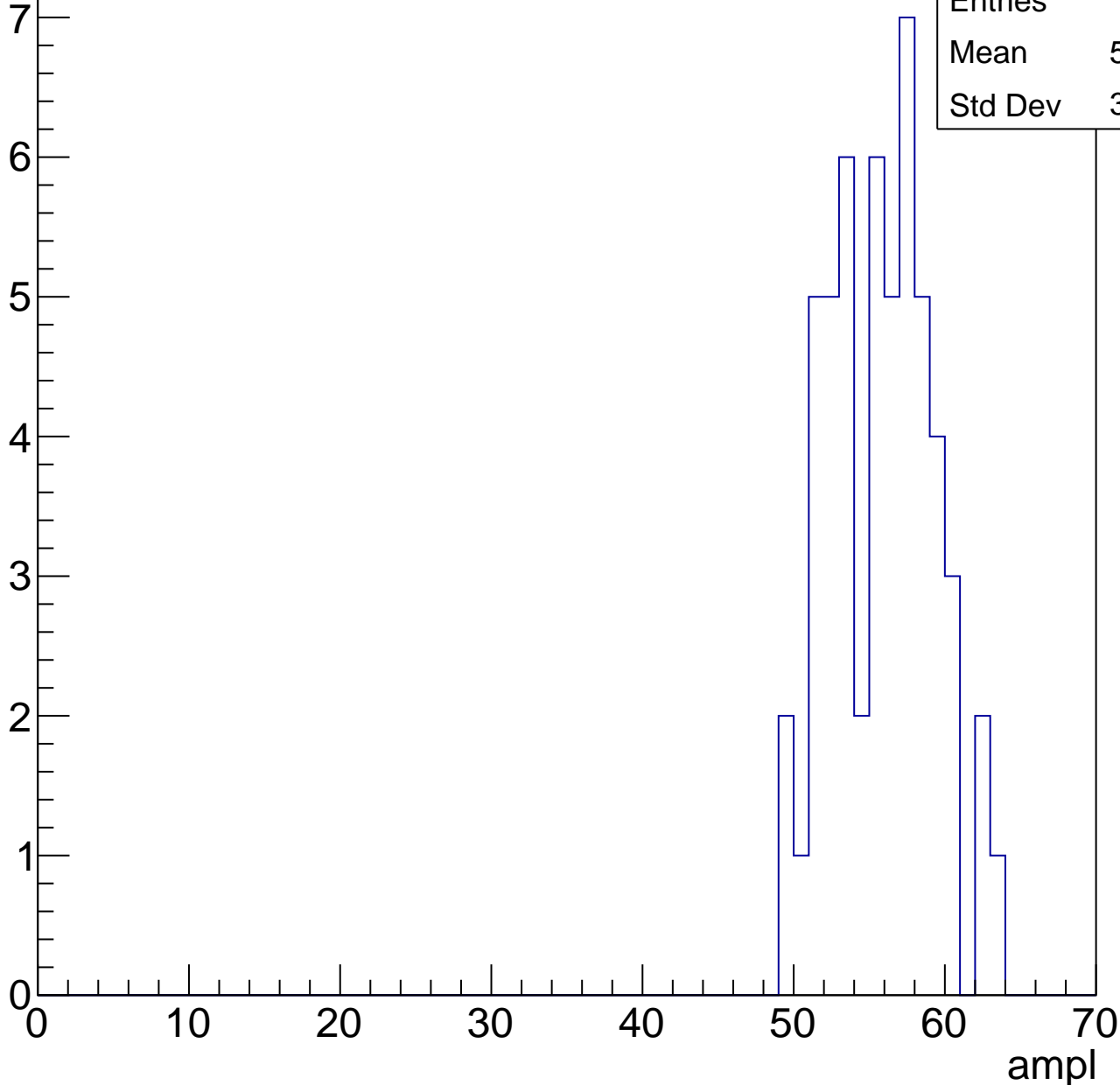


B1L100S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	55.39
Std Dev	3.385

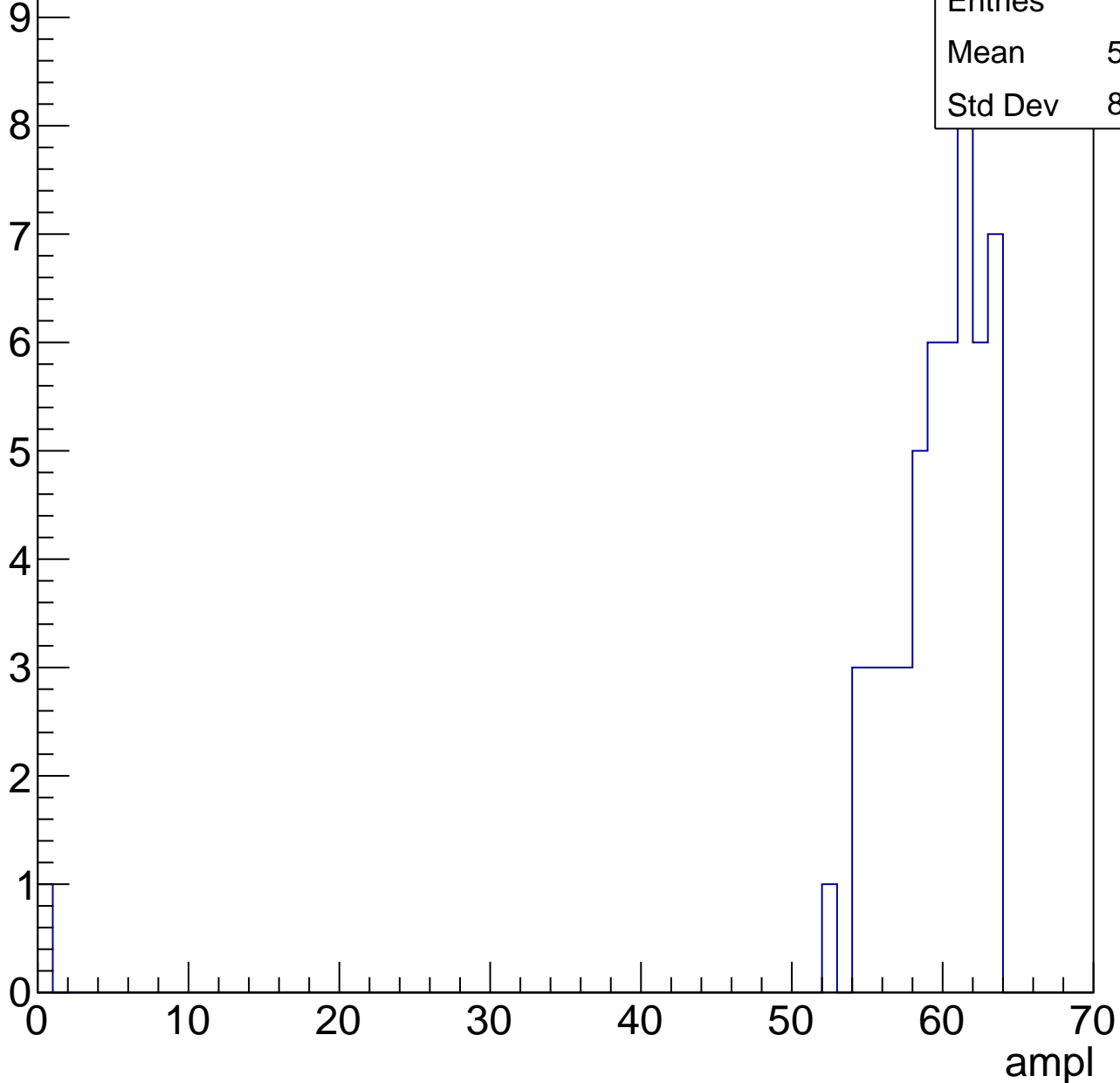


B1L100S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.19
Std Dev	8.545



B1L100S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B1L100S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch16, adc0

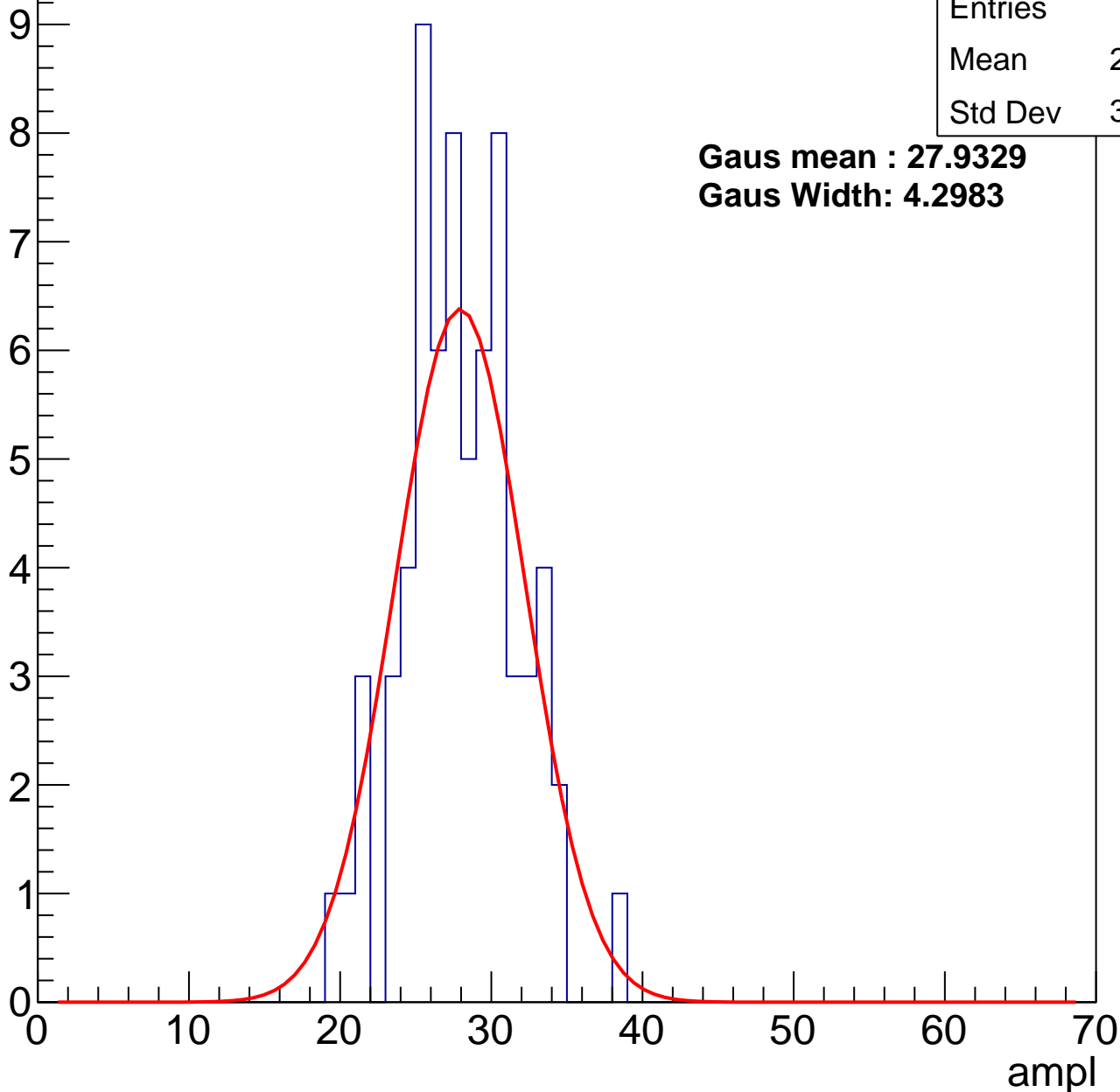
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	27.54
Std Dev	3.695

Gaus mean : 27.9329

Gaus Width: 4.2983

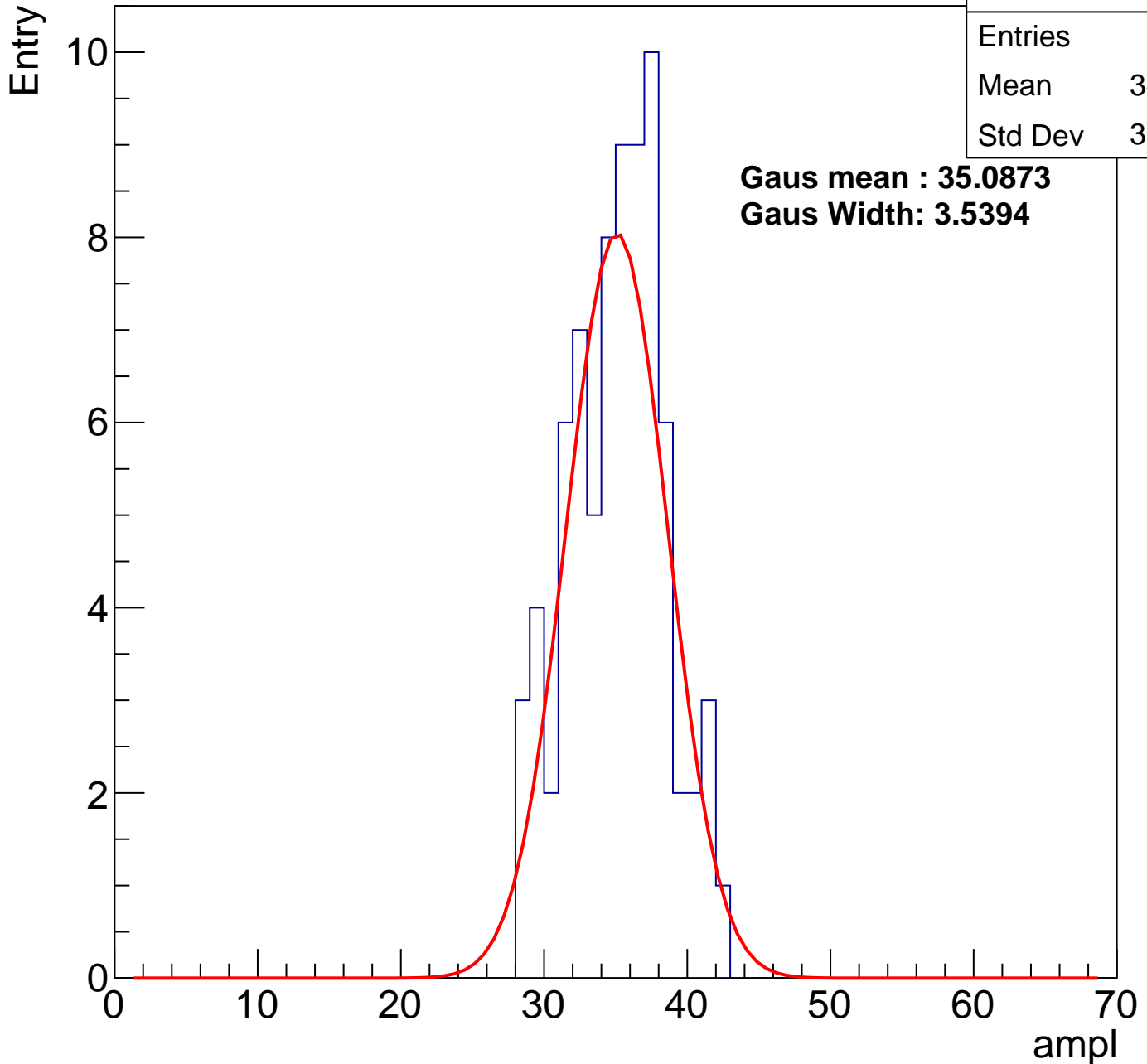


B1L100S, U14-ch16, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	34.64
Std Dev	3.342

Gaus mean : 35.0873
Gaus Width: 3.5394



B1L100S, U14-ch16, adc2

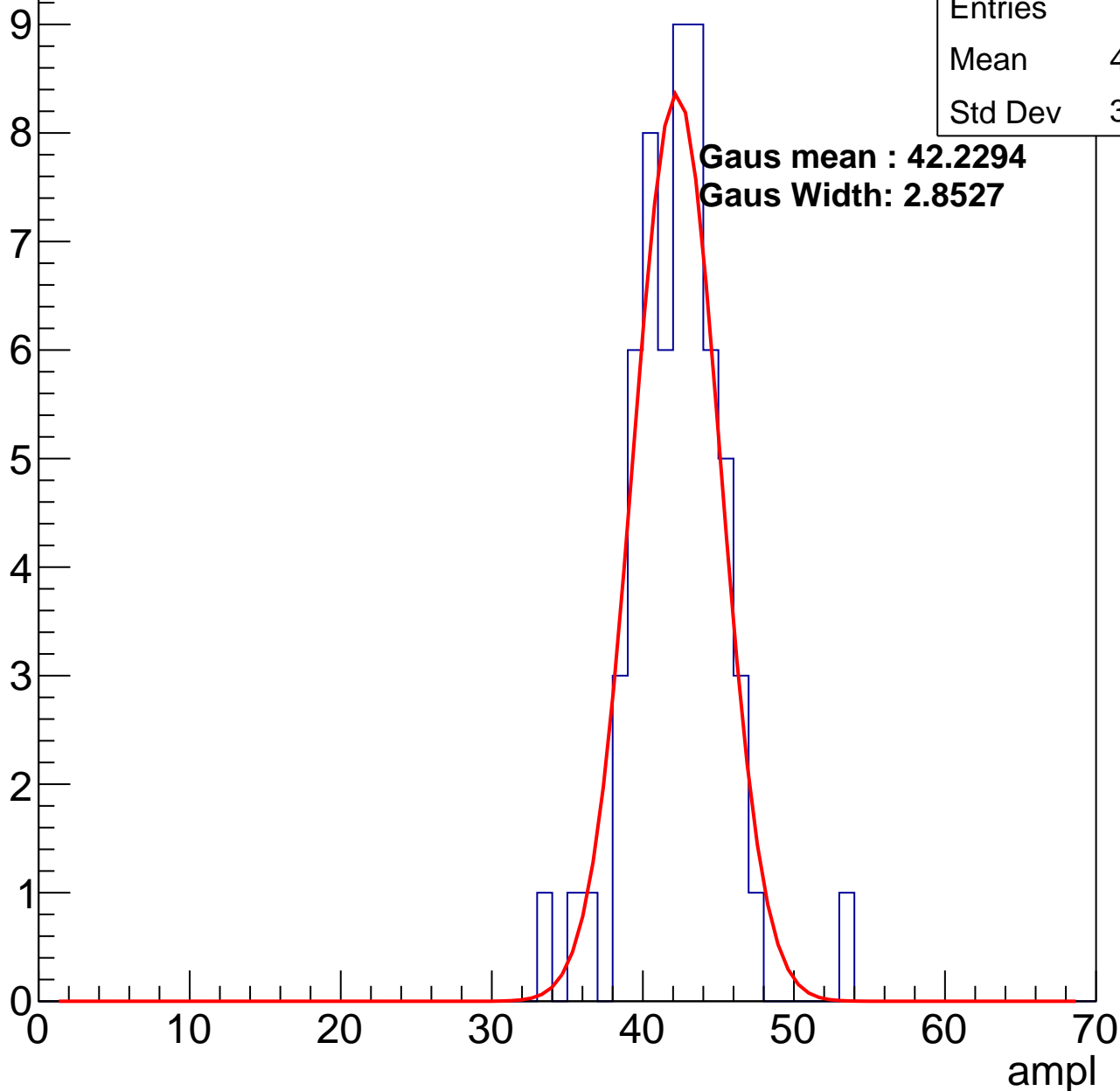
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.83
Std Dev	3.099

Gaus mean : 42.2294

Gaus Width: 2.8527



B1L100S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

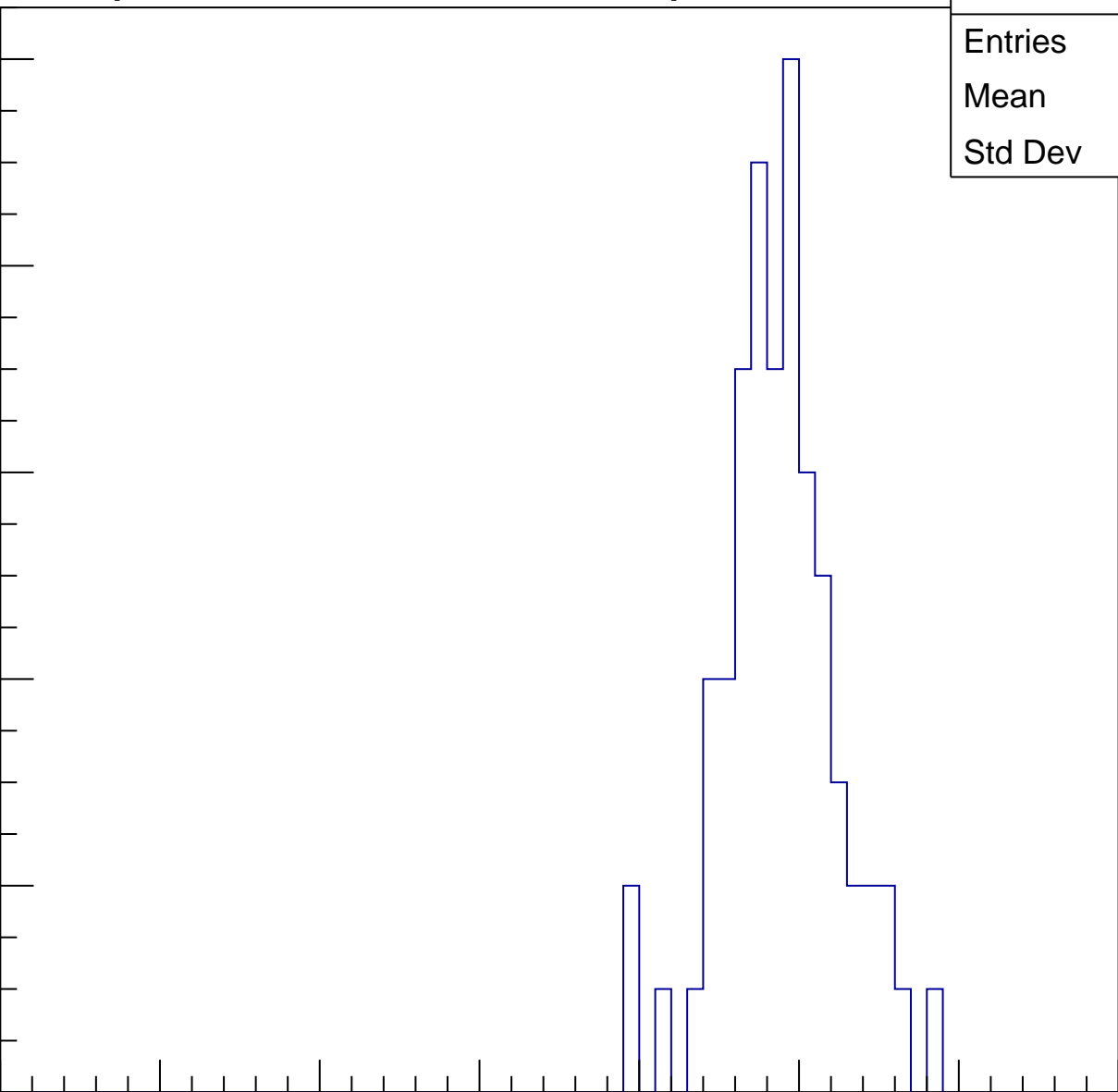
Entries	67
Mean	48.33
Std Dev	3.617

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

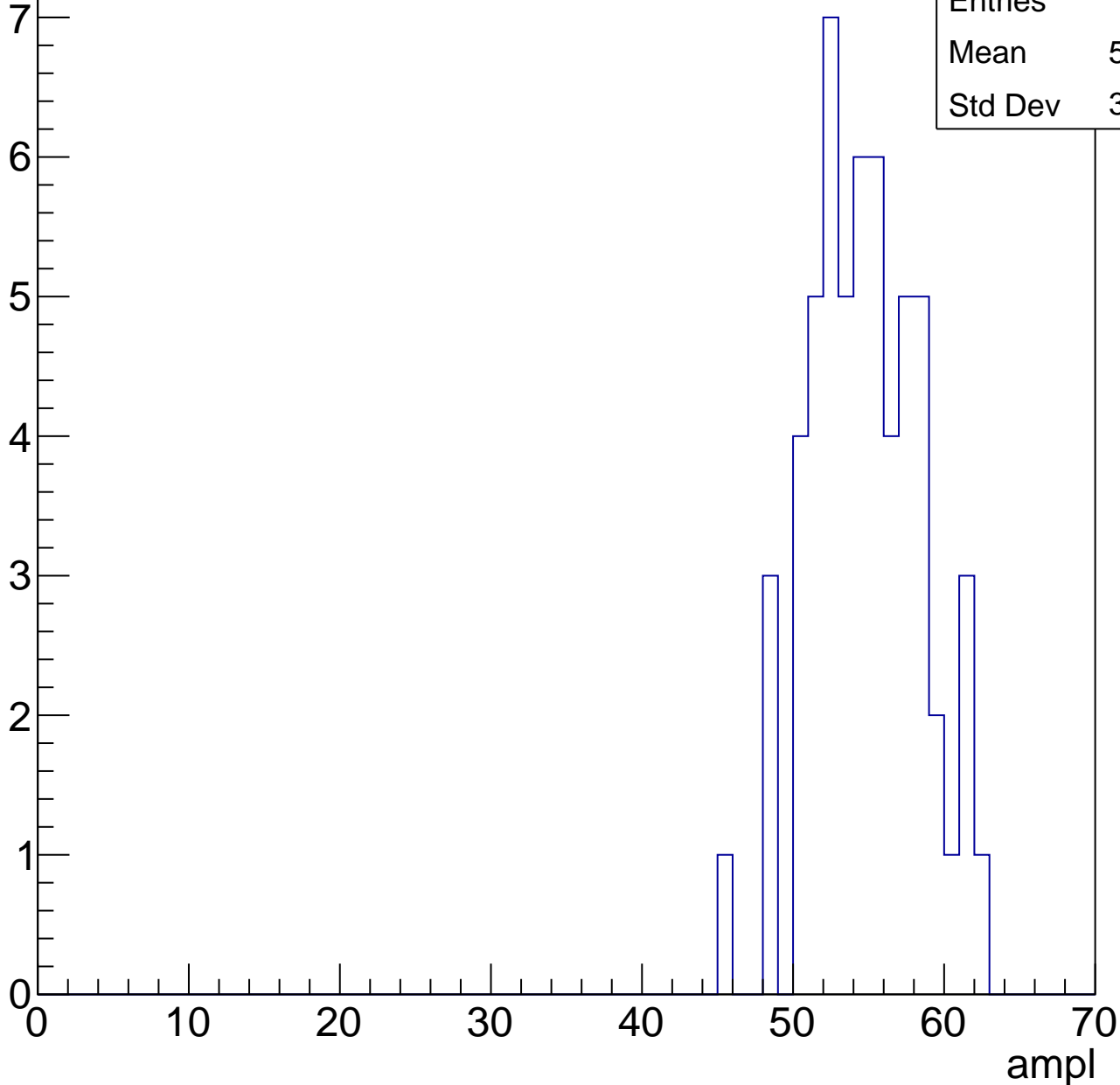


B1L100S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.29
Std Dev	3.644



B1L100S, U14-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 58

Std Dev 8.512

ampl

0

10

20

30

40

50

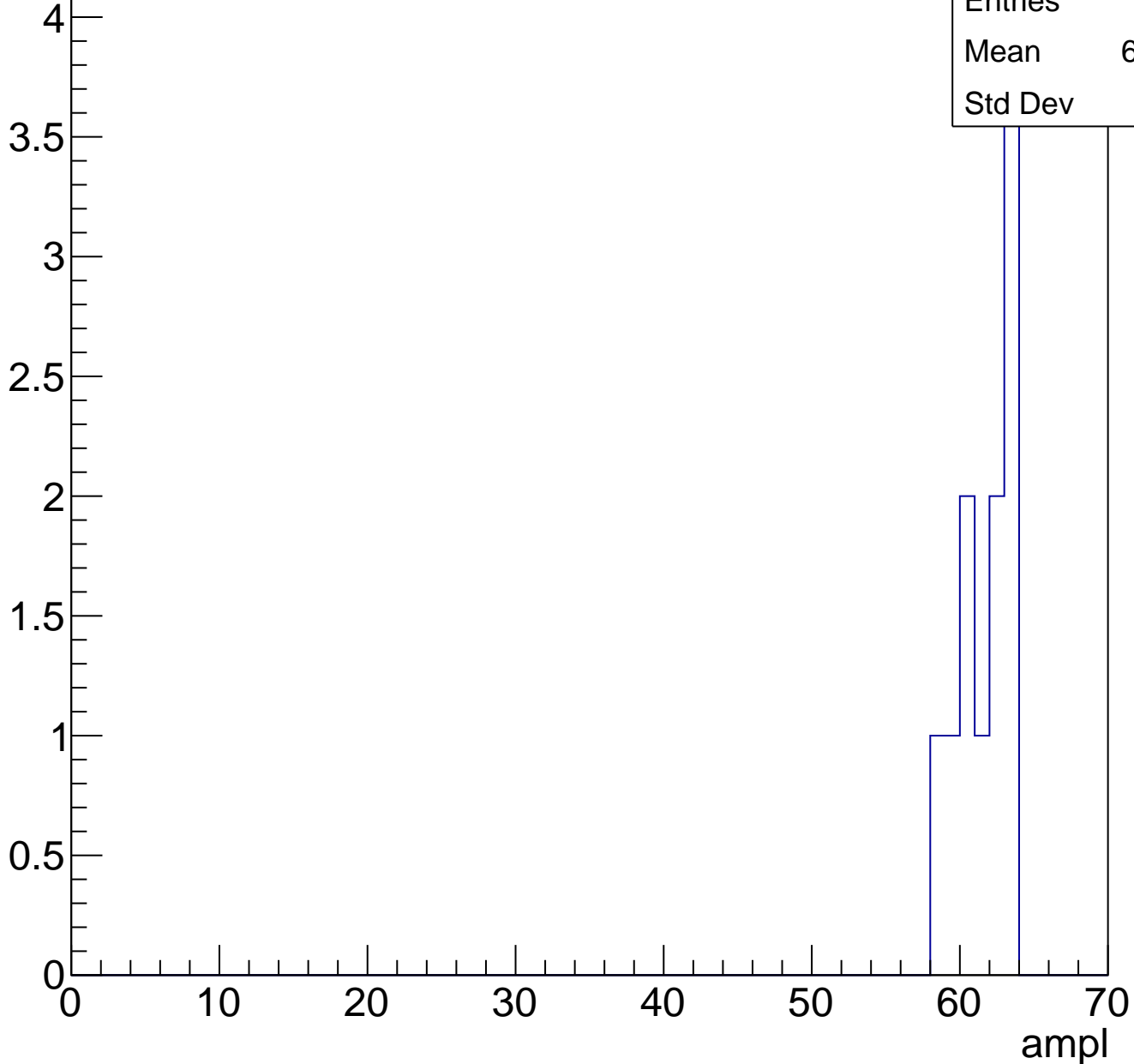
60

70

B1L100S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch17, adc0

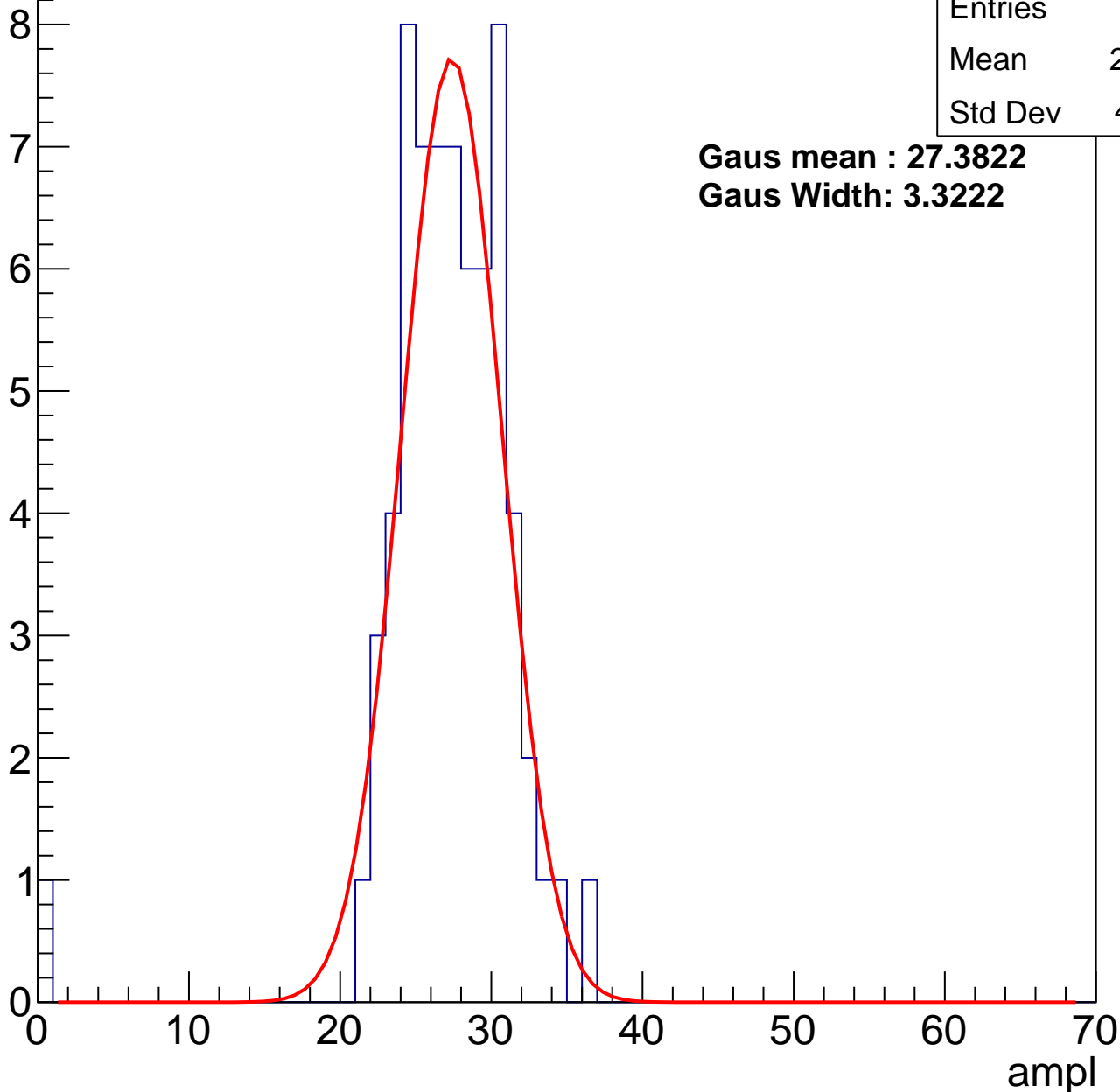
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	26.72
Std Dev	4.551

Gaus mean : 27.3822

Gaus Width: 3.3222



B1L100S, U14-ch17, adc1

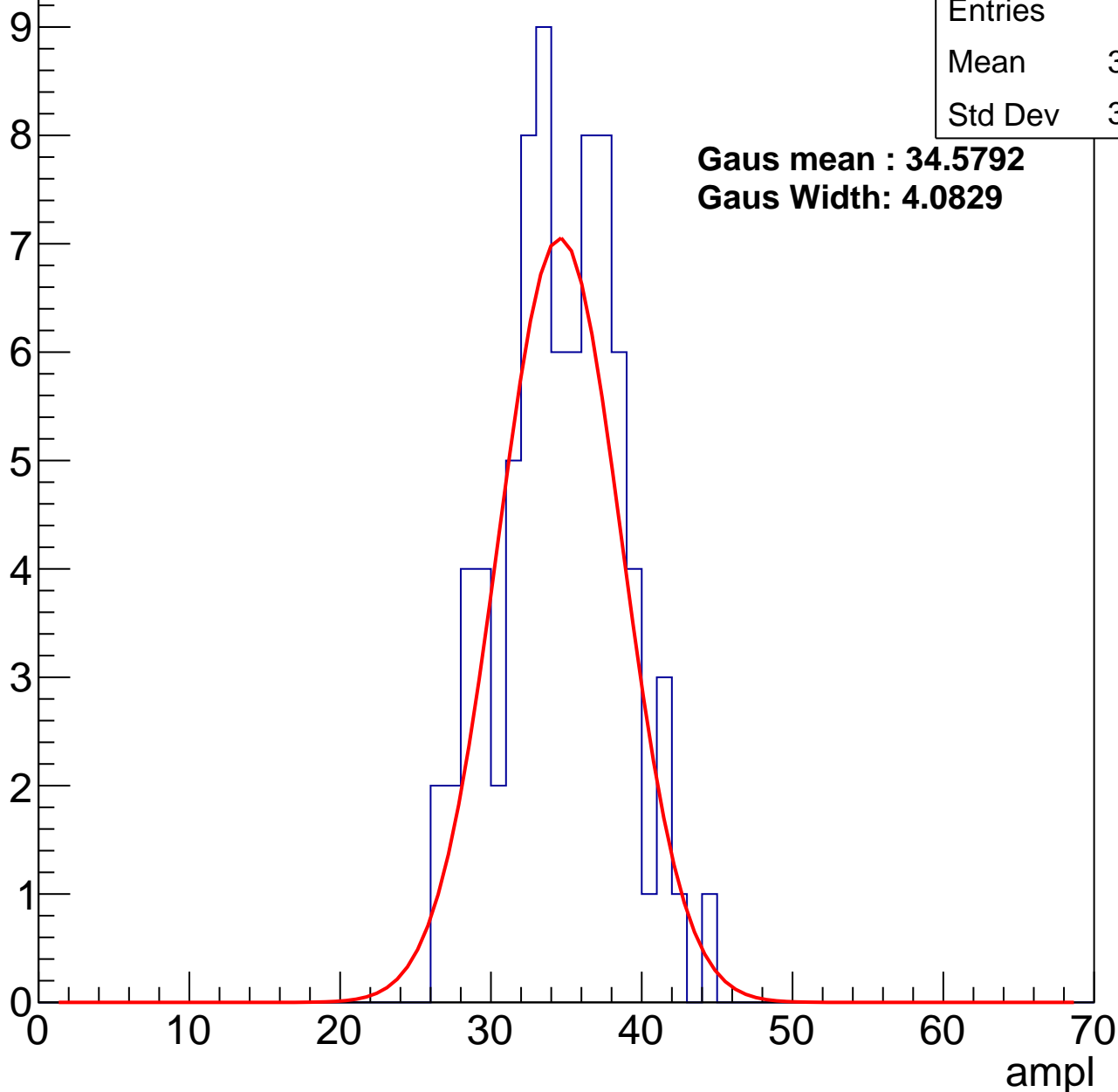
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	34.16
Std Dev	3.935

Gaus mean : 34.5792

Gaus Width: 4.0829



B1L100S, U14-ch17, adc2

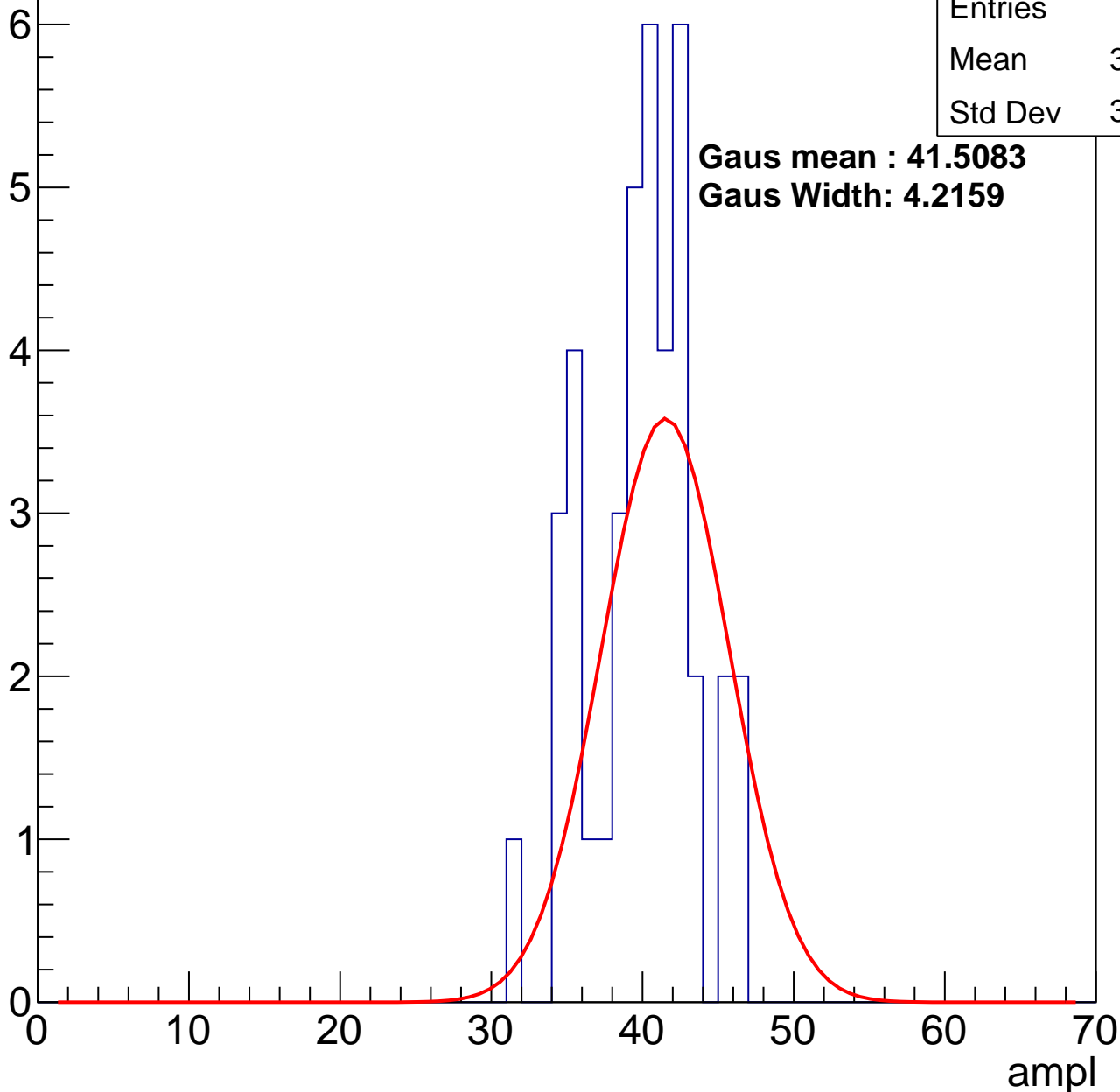
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	39.48
Std Dev	3.493

Gaus mean : 41.5083

Gaus Width: 4.2159

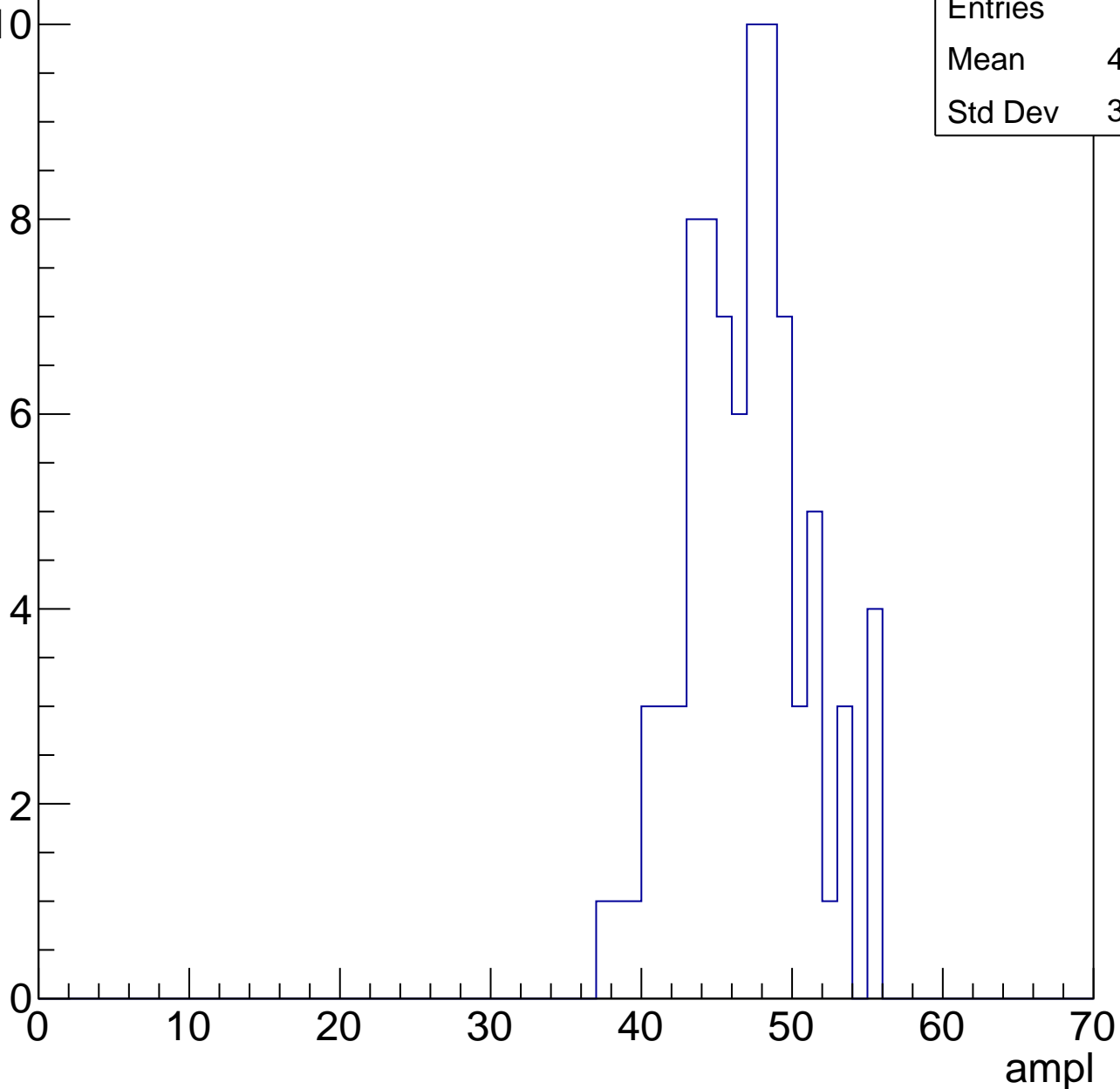


B1L100S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

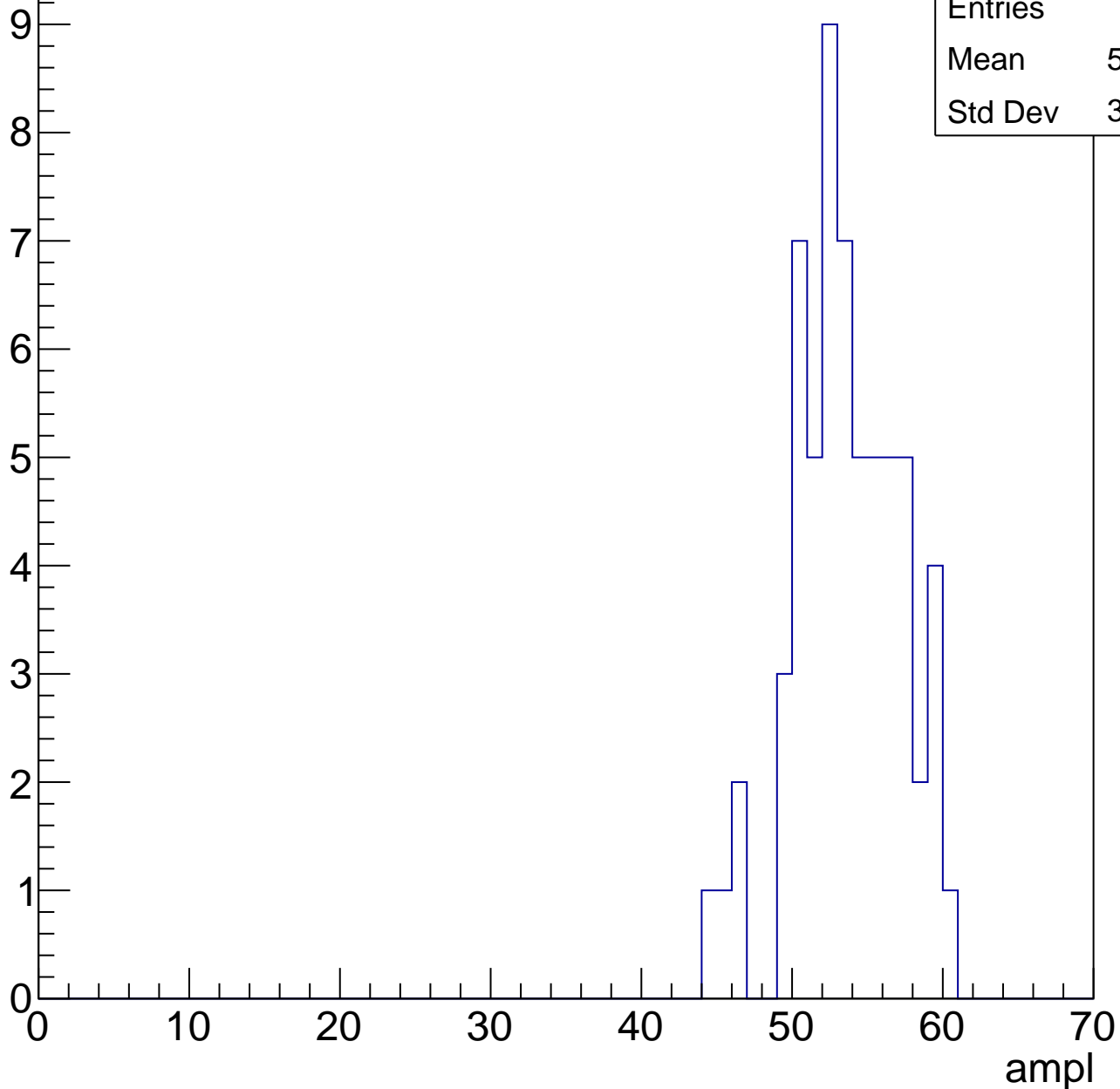
Entries	84
Mean	46.42
Std Dev	3.956



B1L100S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

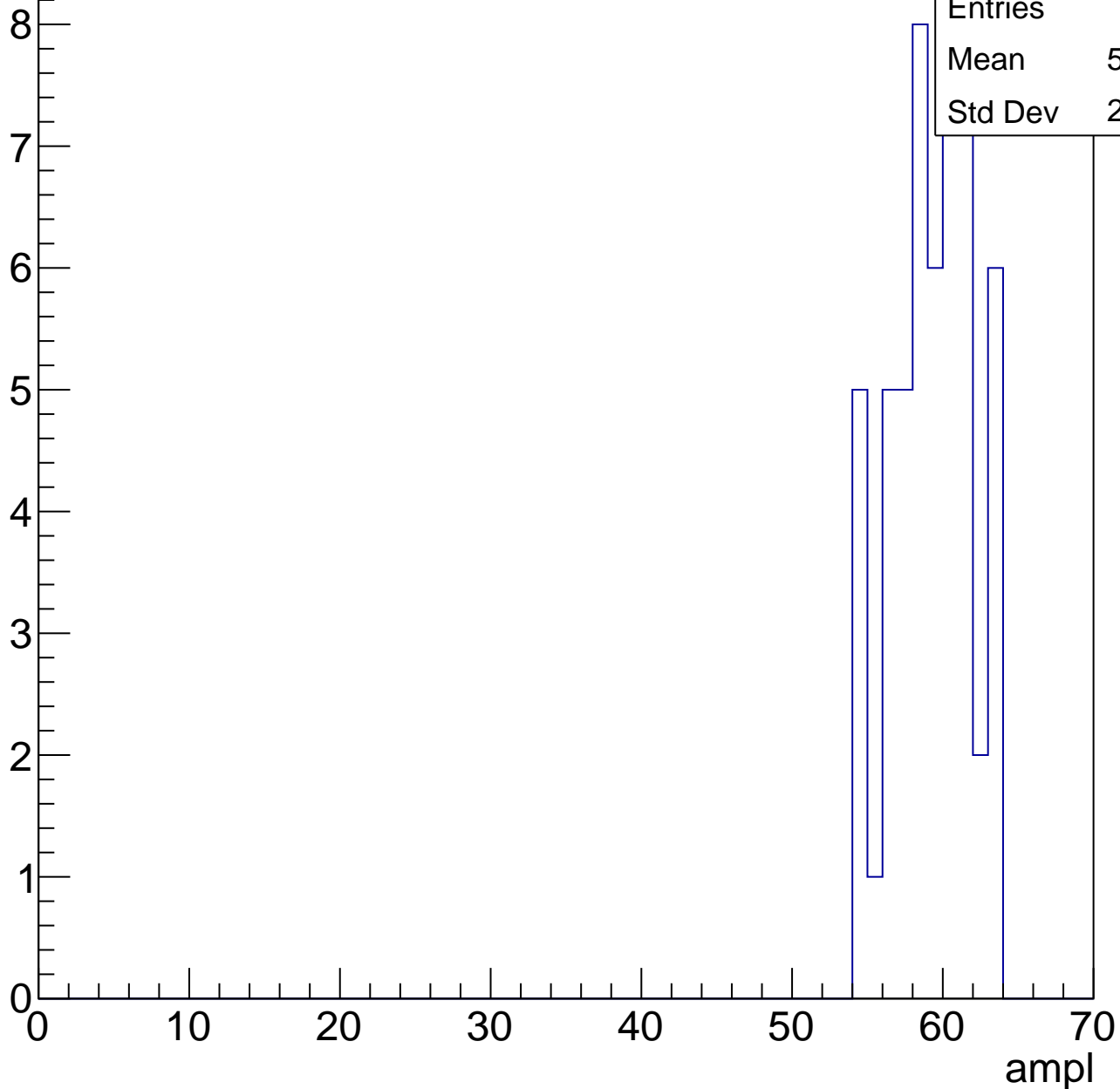


Entries	62
Mean	53.13
Std Dev	3.535

B1L100S, U14-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



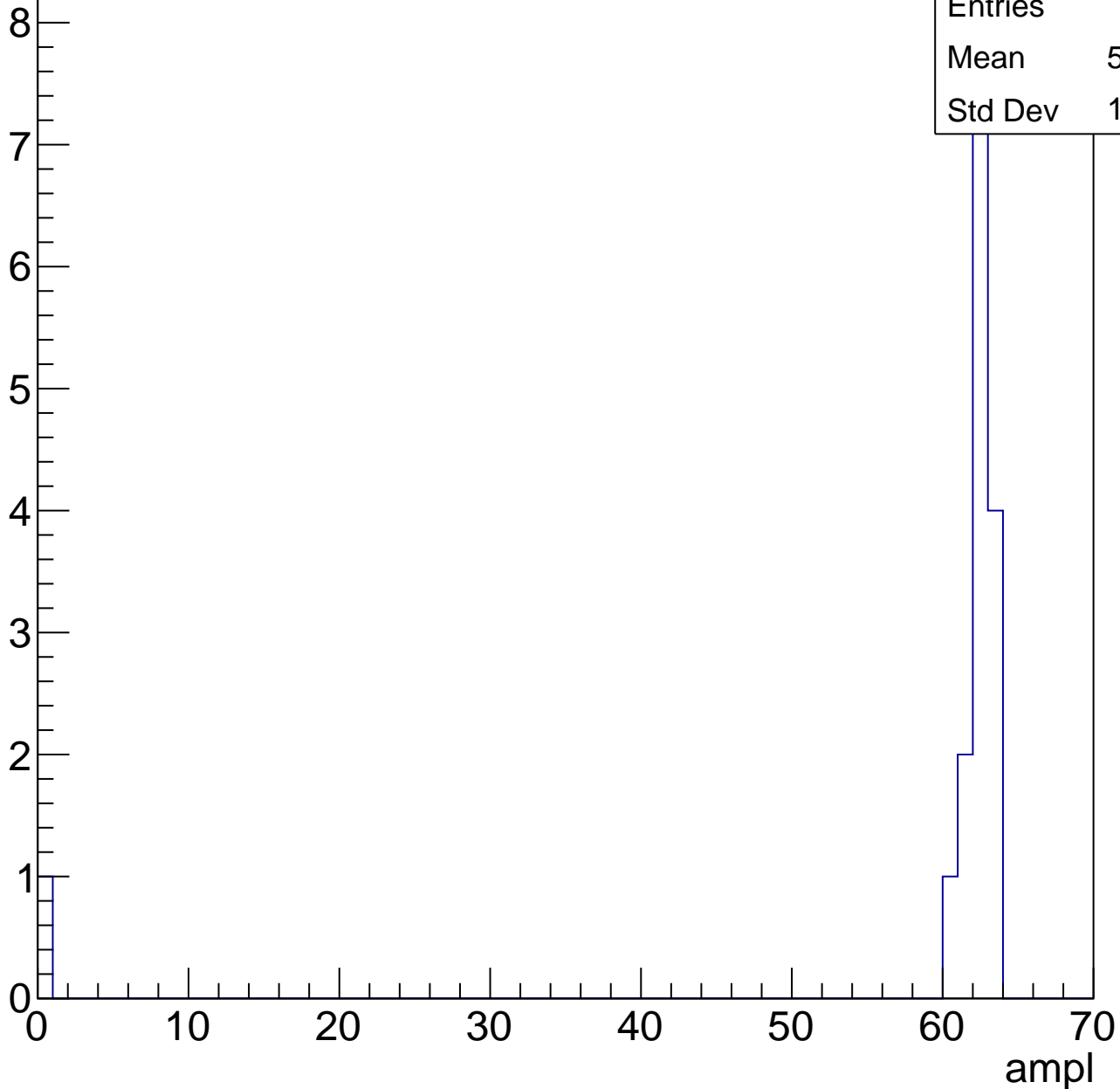
Entries	54
Mean	58.85
Std Dev	2.606

B1L100S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	58.12
Std Dev	15.03



B1L100S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch18, adc0

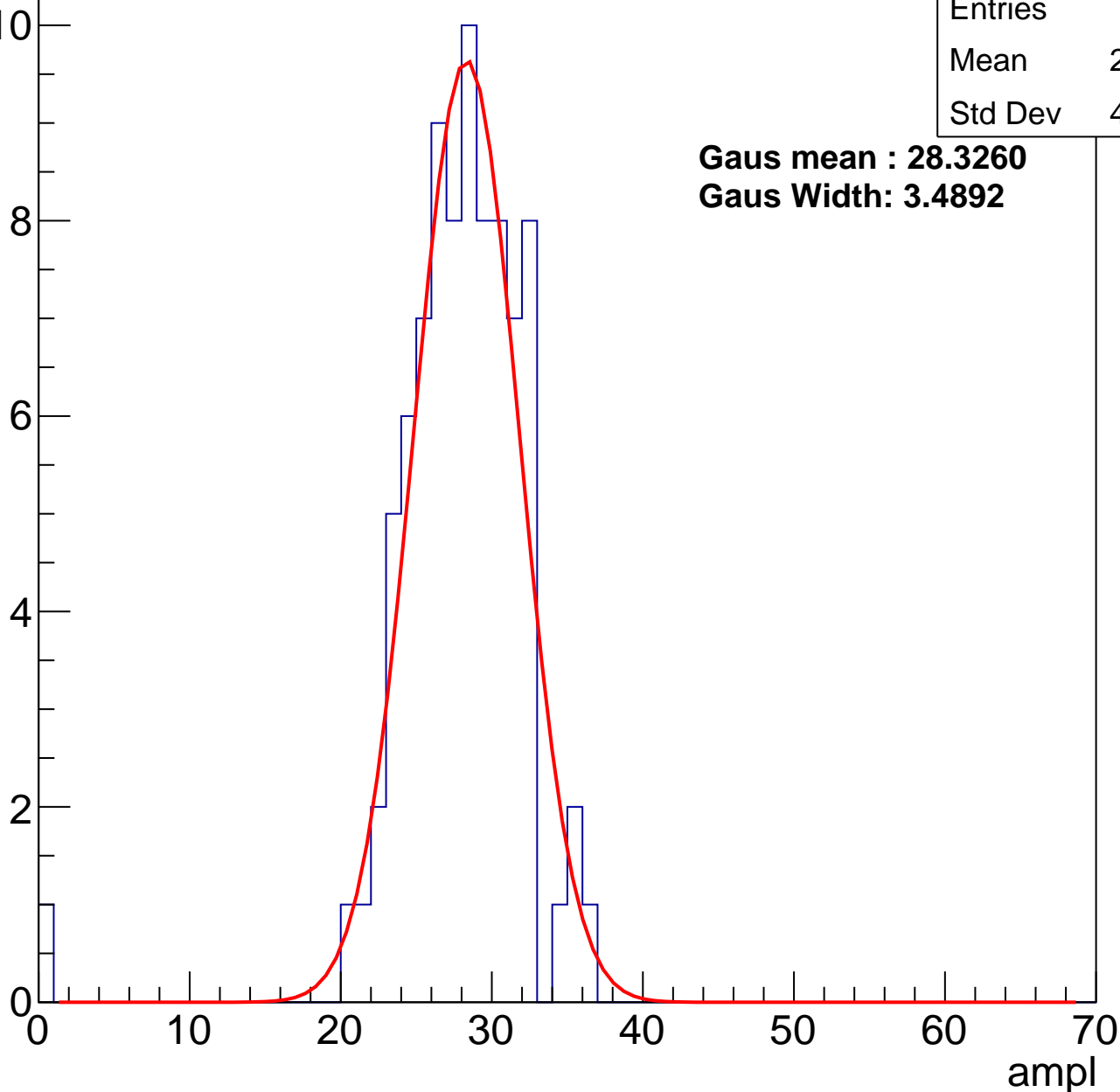
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	27.46
Std Dev	4.474

Gaus mean : 28.3260

Gaus Width: 3.4892



B1L100S, U14-ch18, adc1

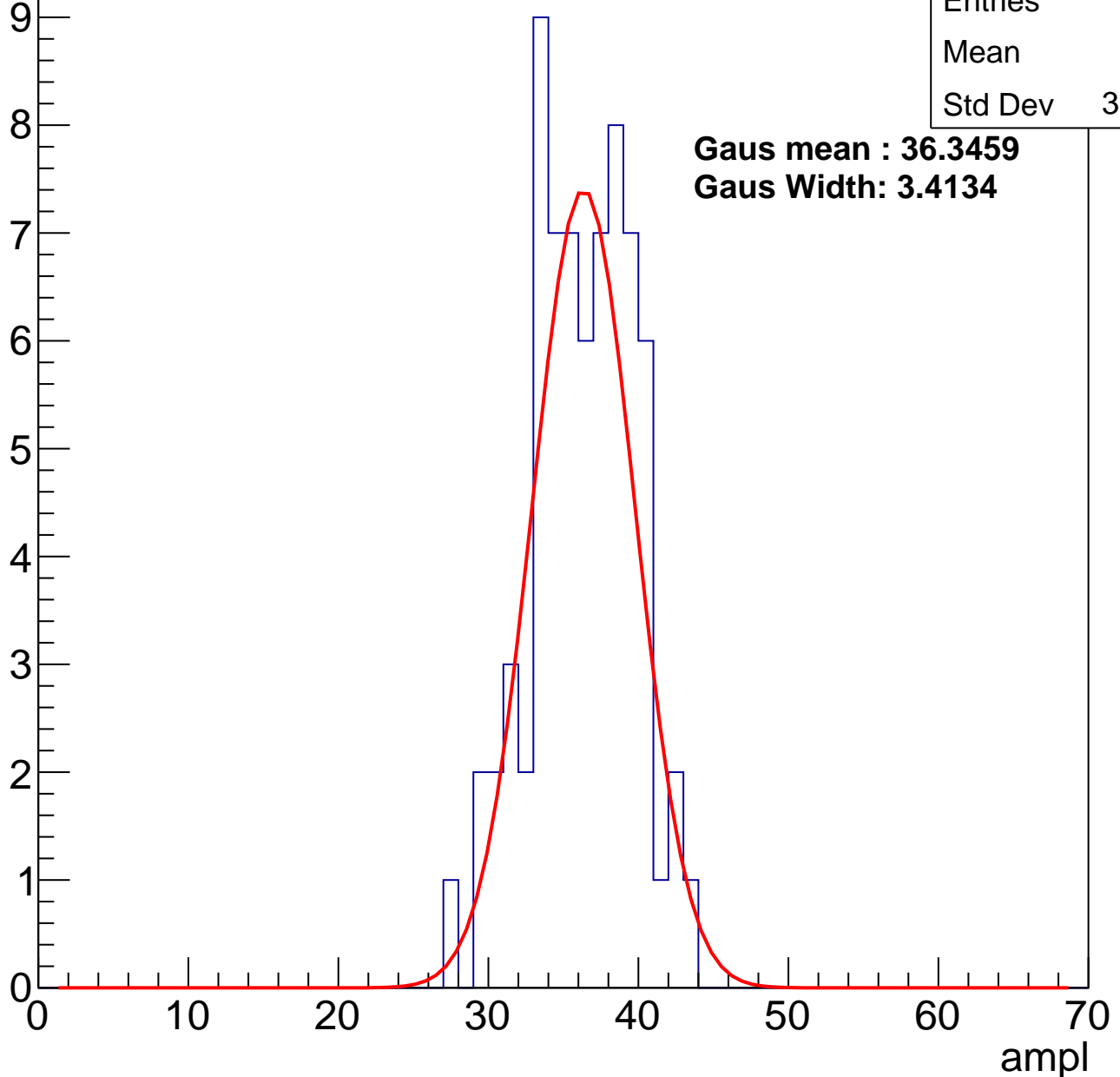
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.8
Std Dev	3.389

Gaus mean : 36.3459

Gaus Width: 3.4134



B1L100S, U14-ch18, adc2

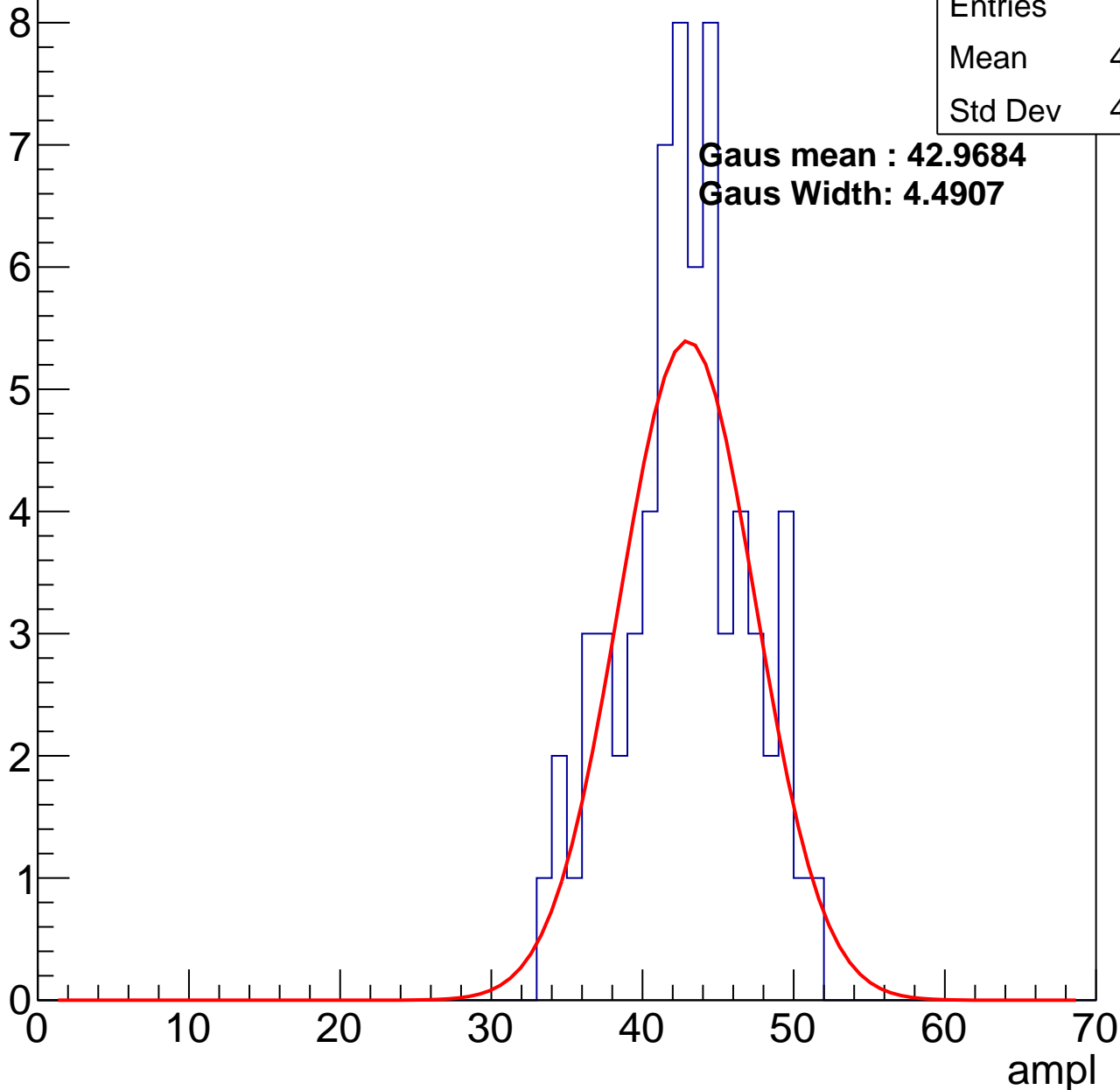
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.33
Std Dev	4.143

Gaus mean : 42.9684

Gaus Width: 4.4907

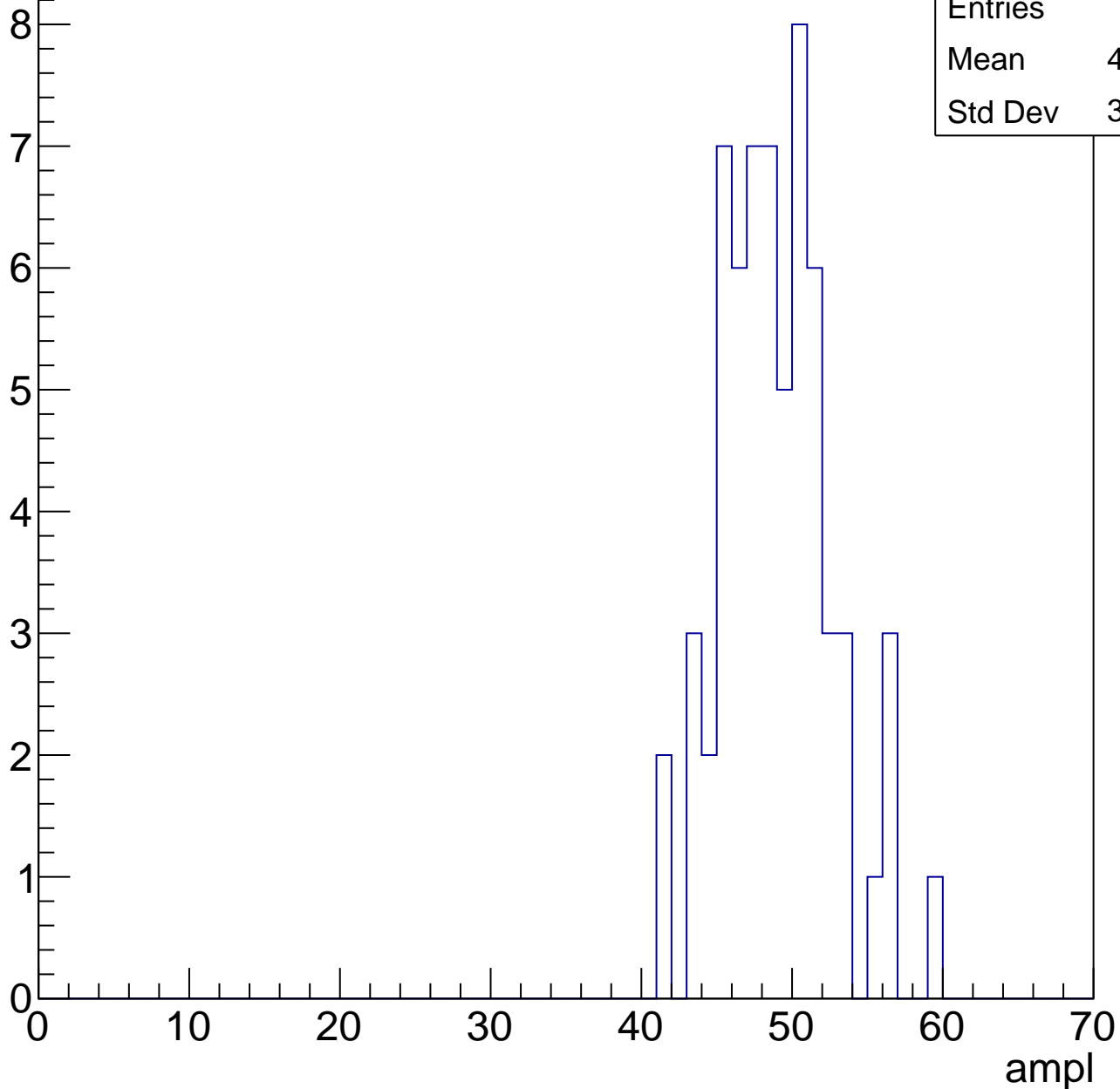


B1L100S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

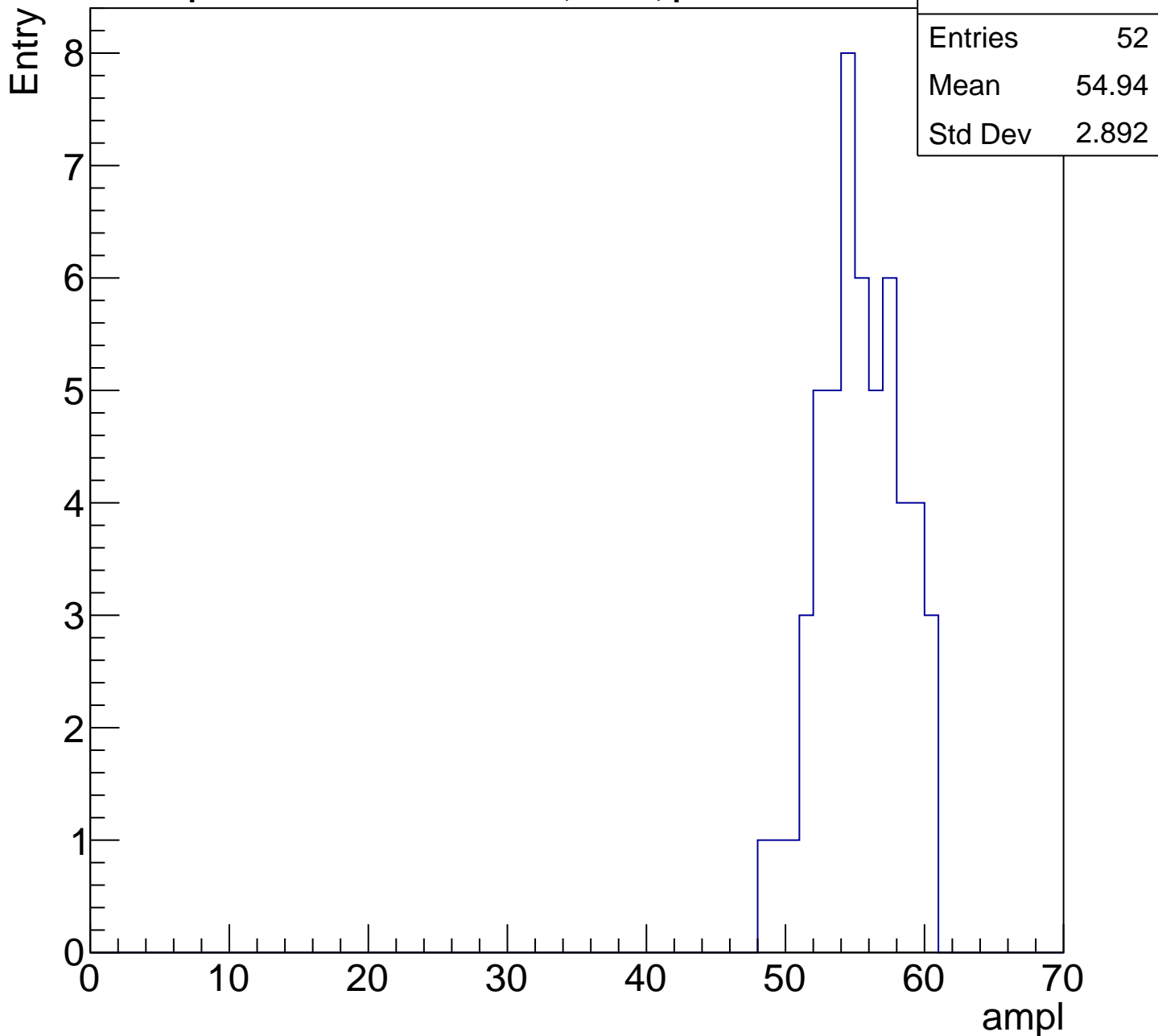
Entry

Entries	64
Mean	48.48
Std Dev	3.666



B1L100S, U14-ch18, adc4

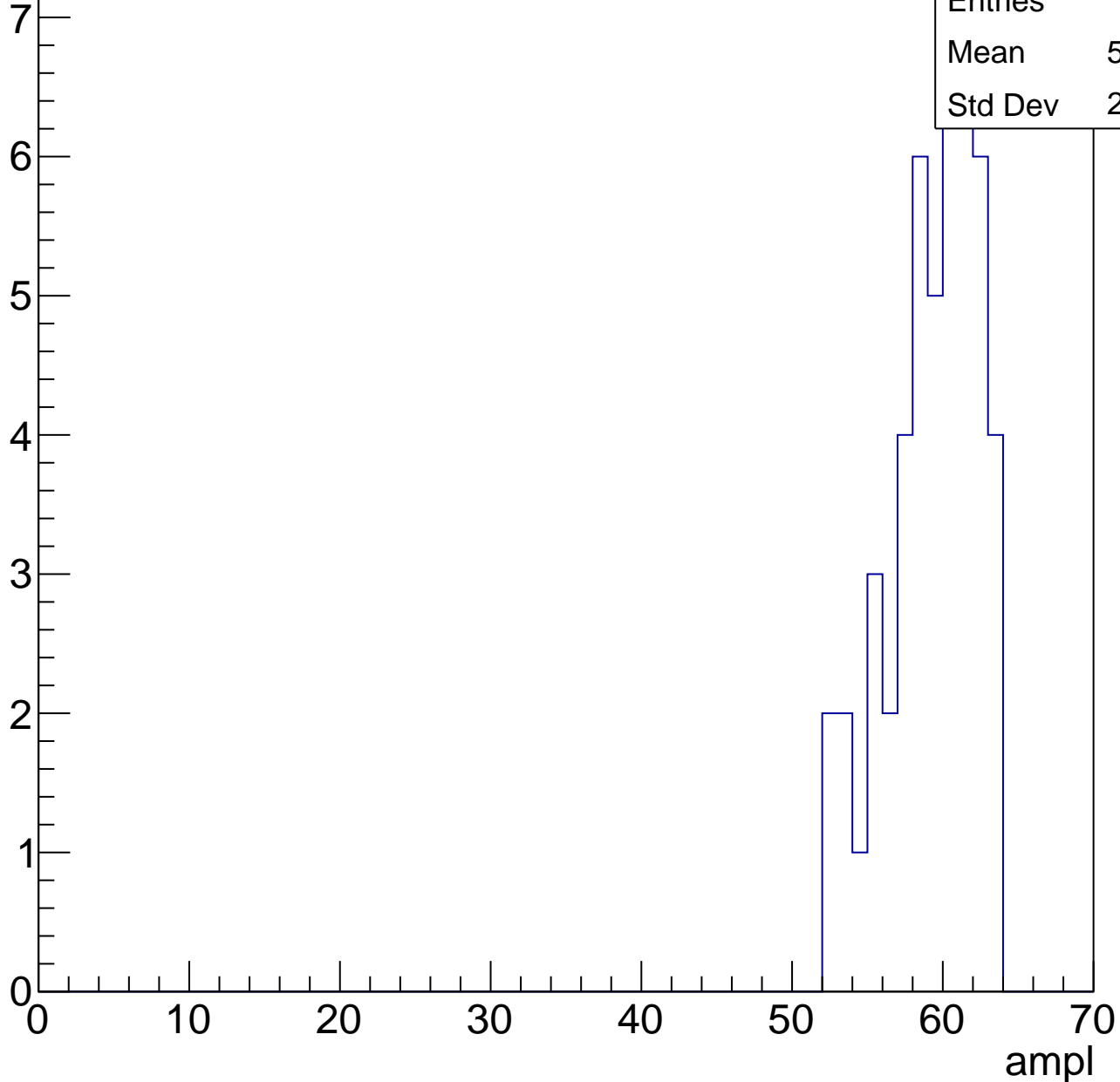
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

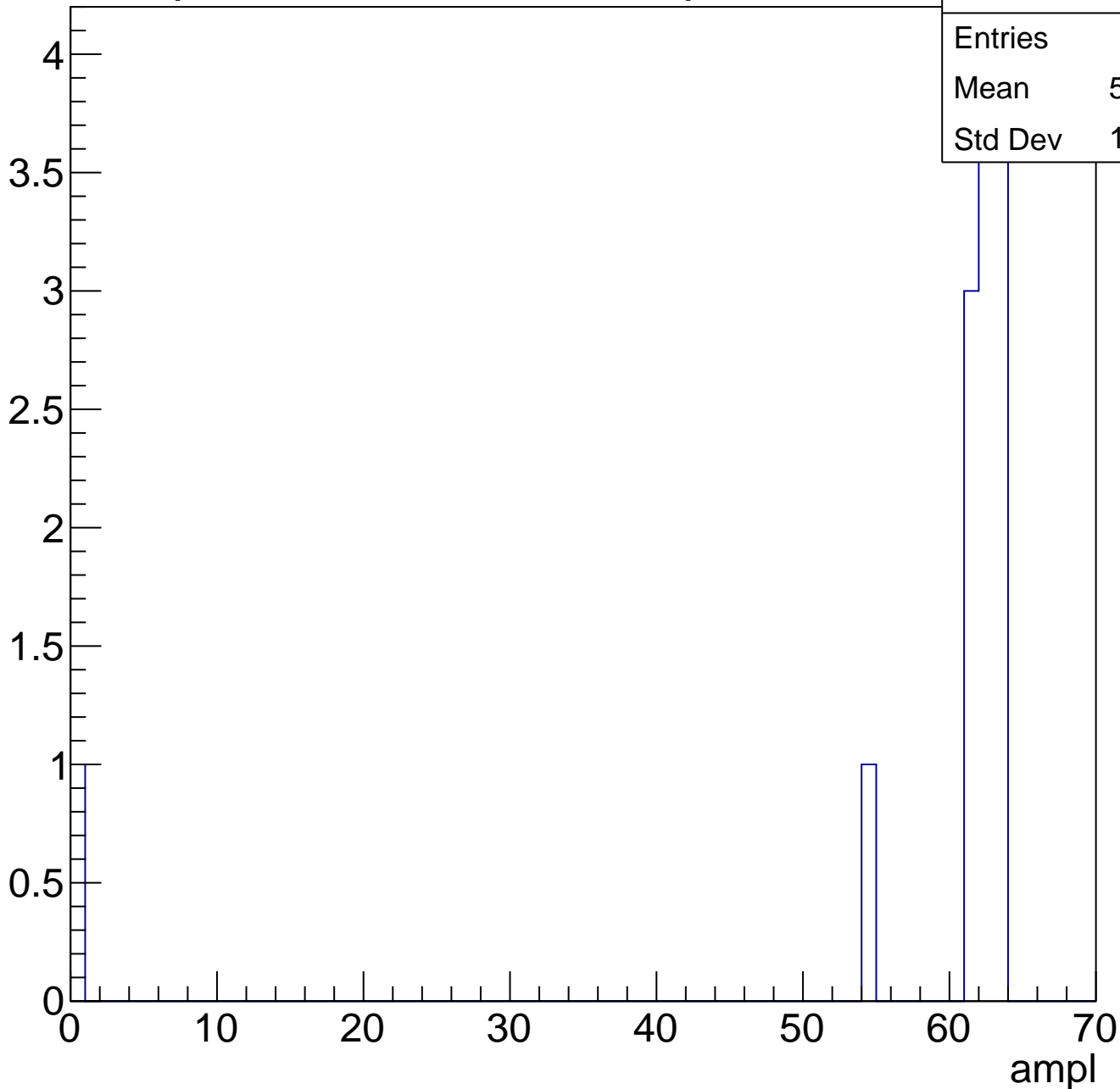
Entry



B1L100S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

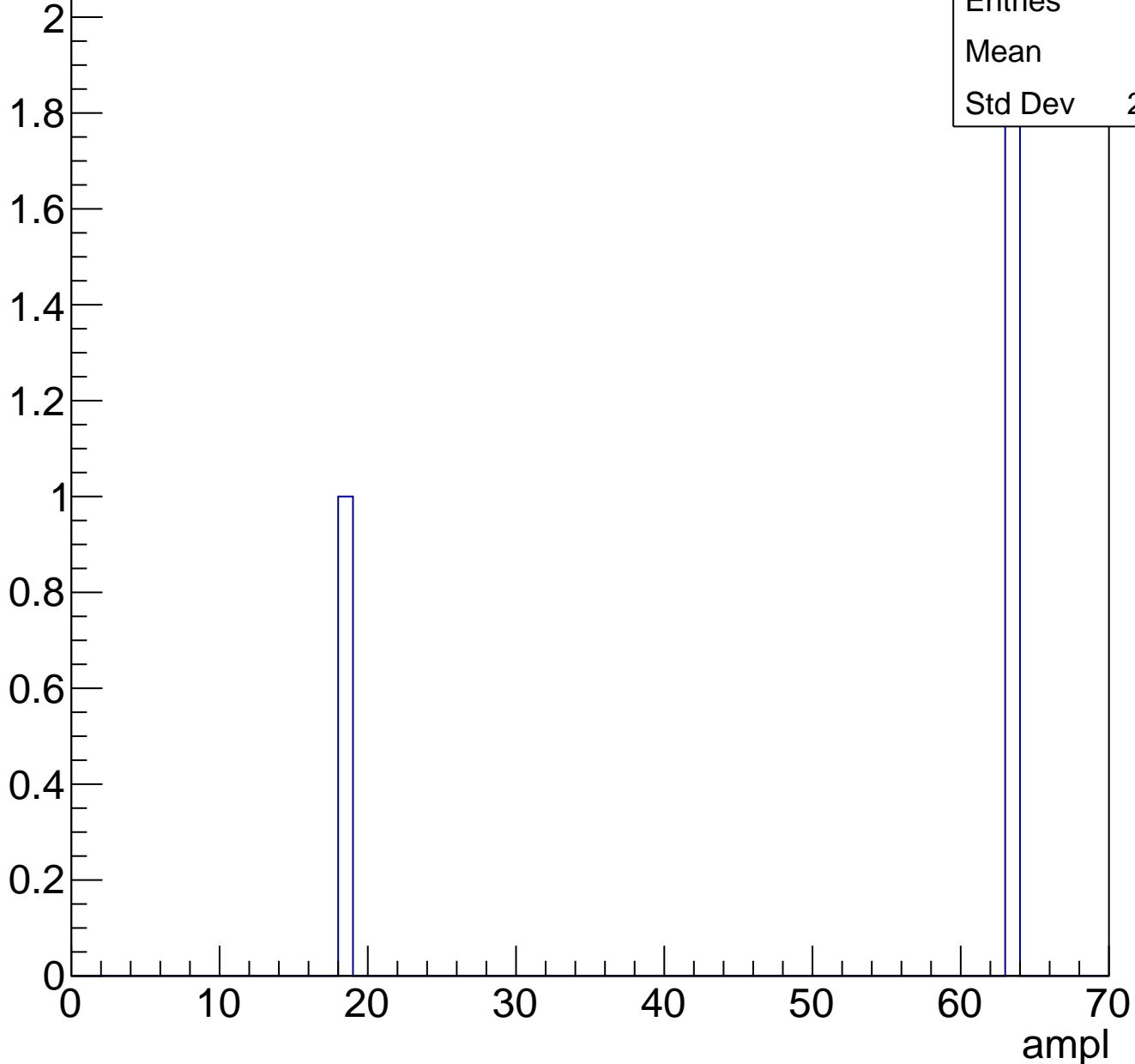
Entry



B1L100S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	48
Std Dev	21.21

B1L100S, U14-ch19, adc0

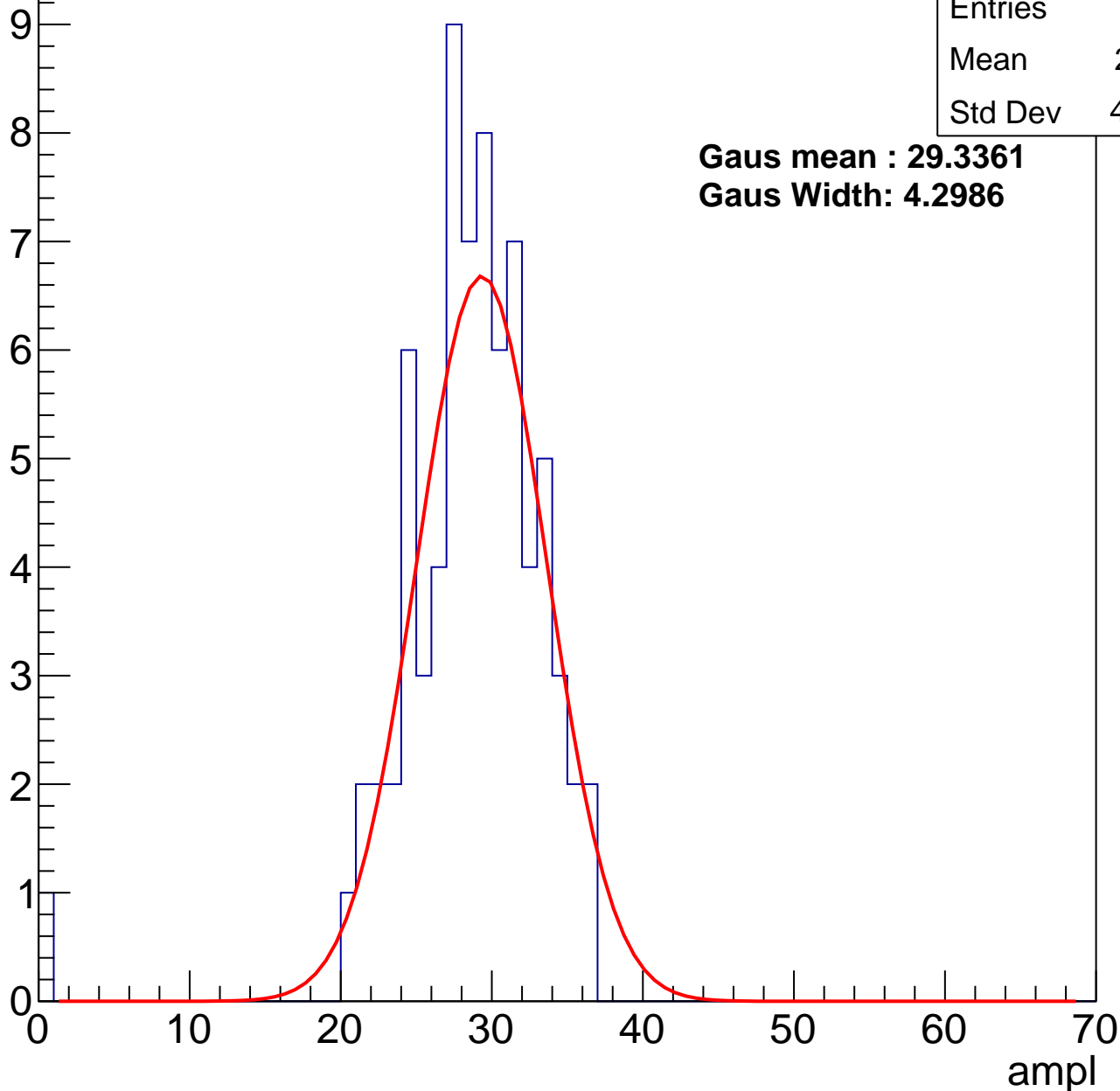
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.11
Std Dev	4.977

Gaus mean : 29.3361

Gaus Width: 4.2986



B1L100S, U14-ch19, adc1

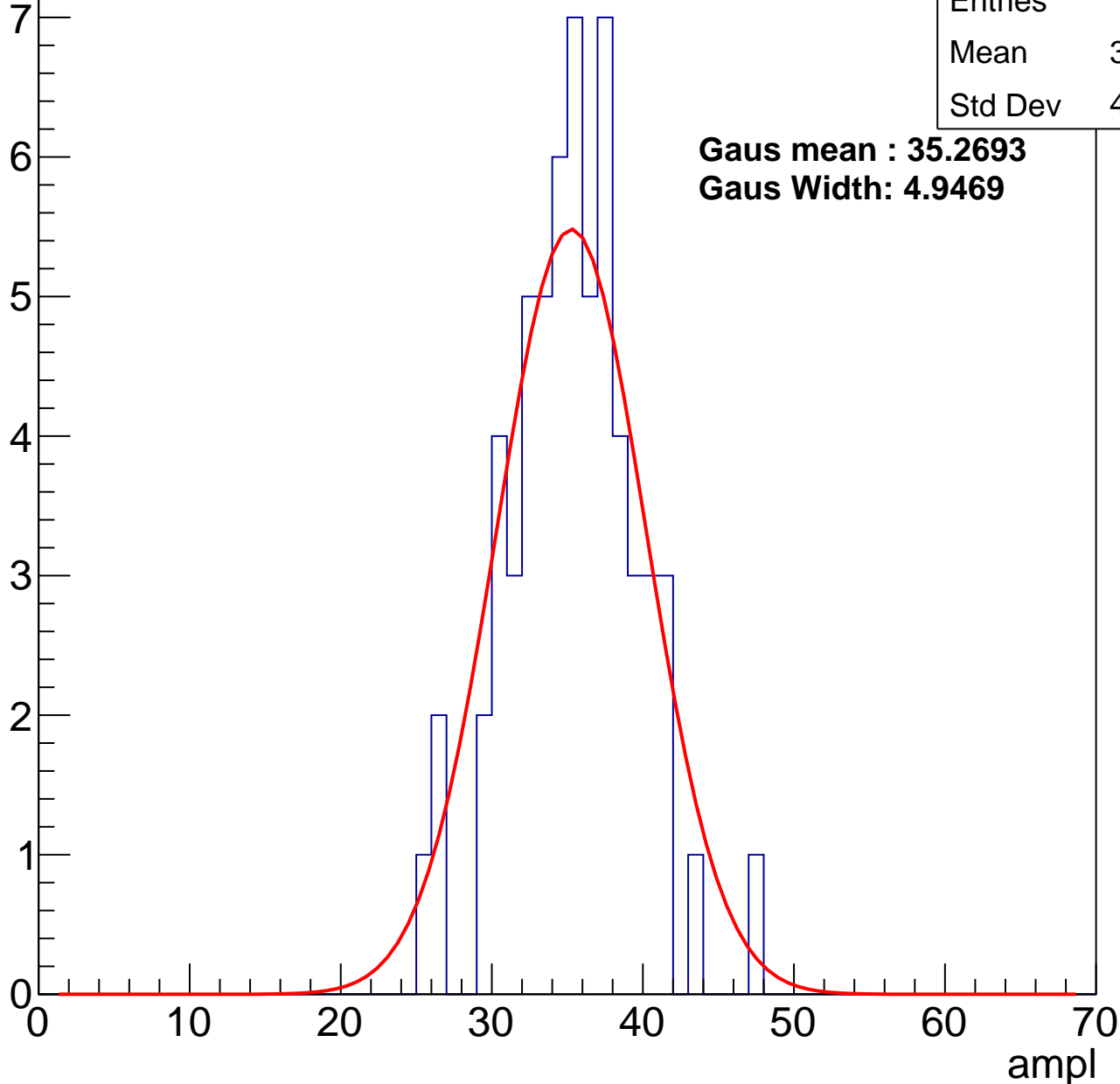
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	34.89
Std Dev	4.135

Gaus mean : 35.2693

Gaus Width: 4.9469



B1L100S, U14-ch19, adc2

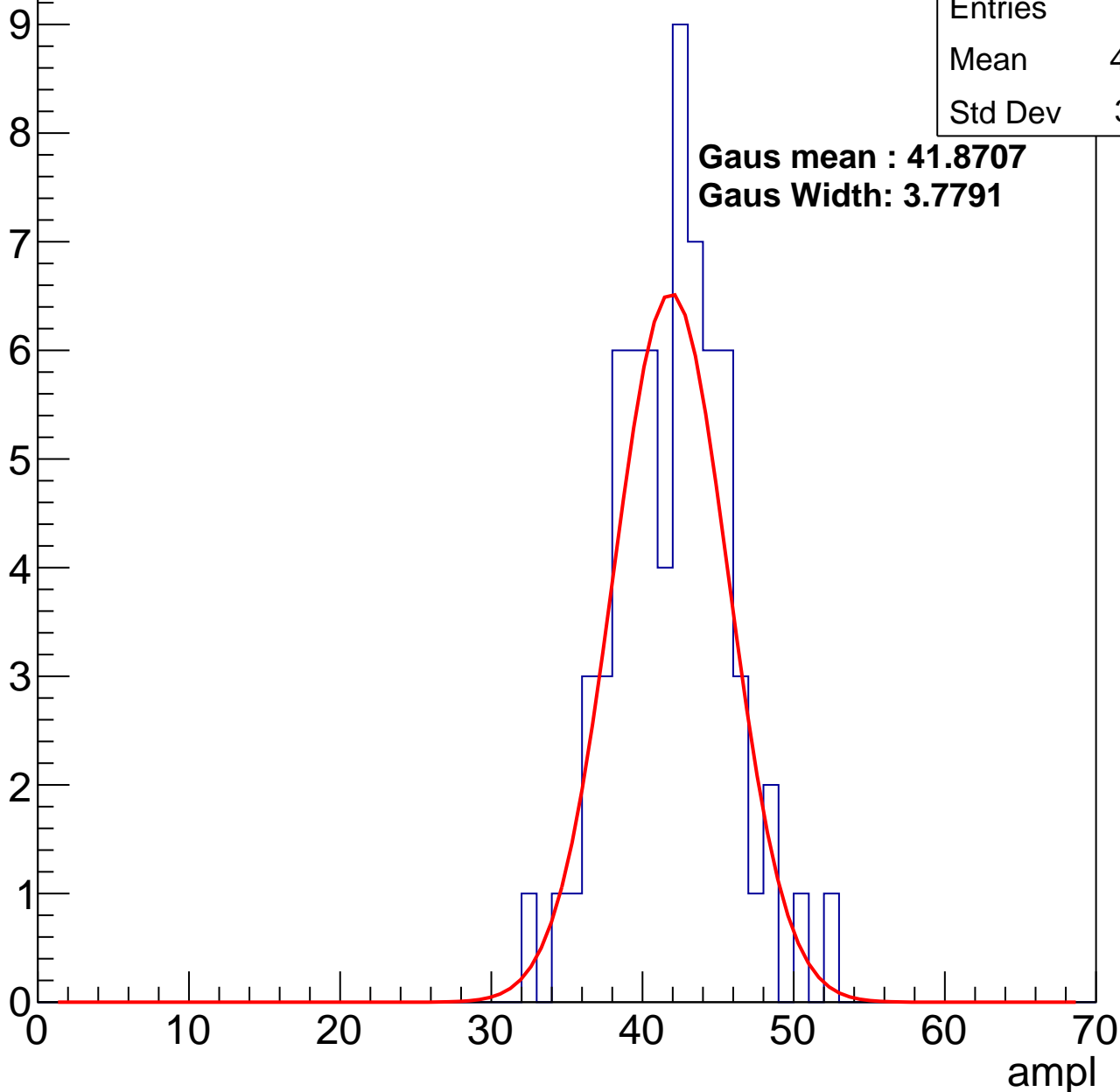
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	41.52
Std Dev	3.771

Gaus mean : 41.8707

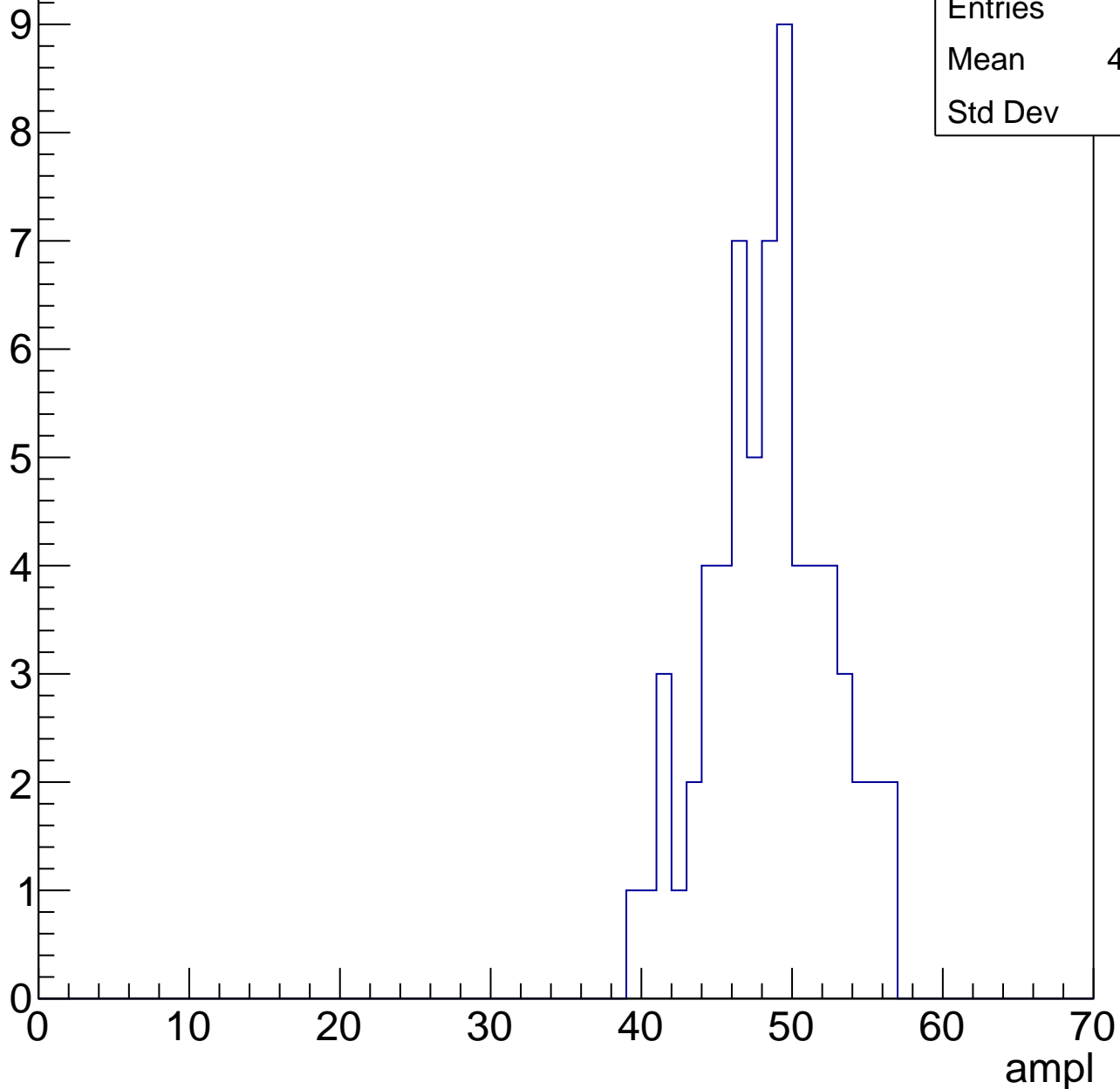
Gaus Width: 3.7791



B1L100S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

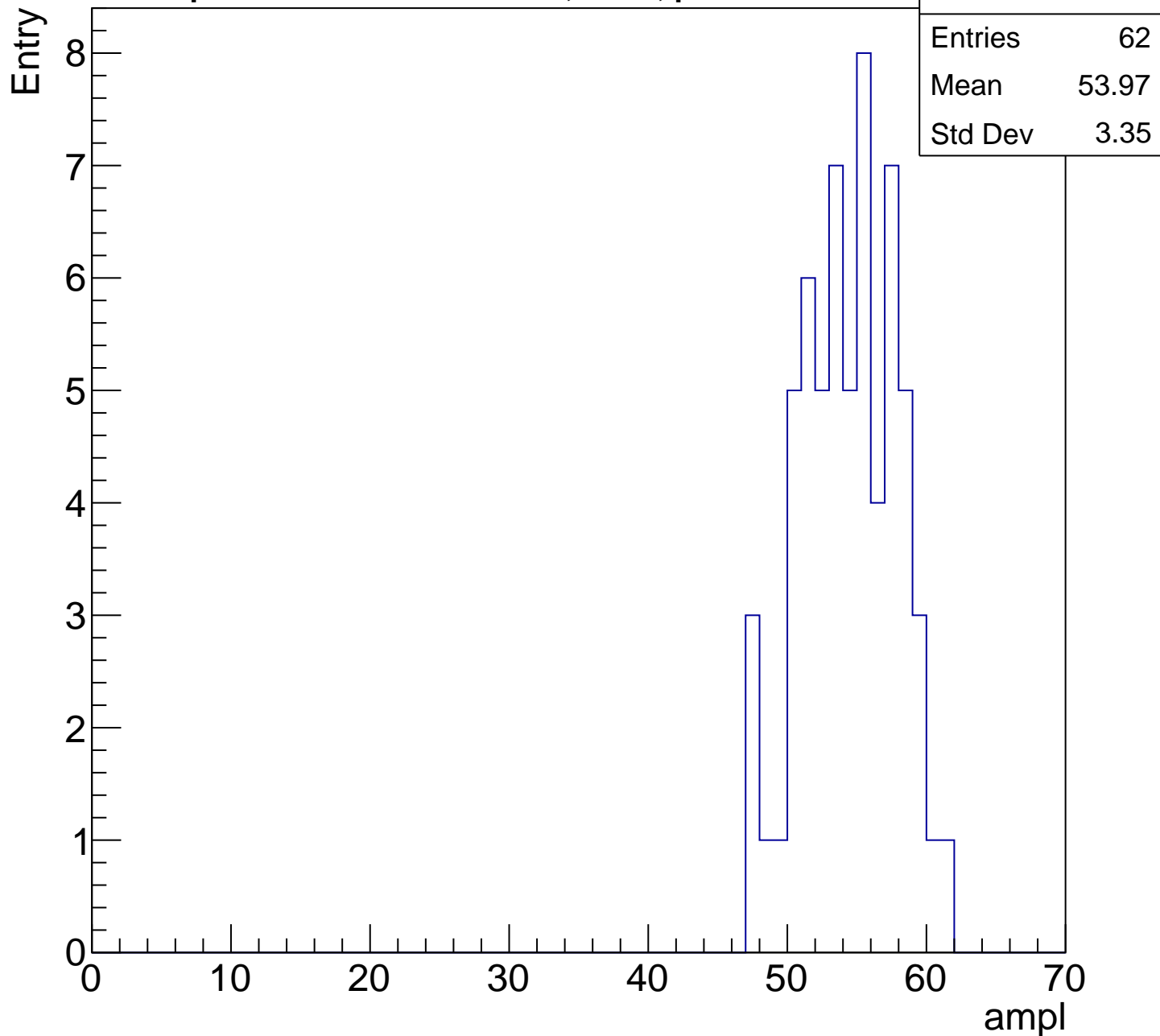
Entry



Entries	65
Mean	48.02
Std Dev	3.94

B1L100S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

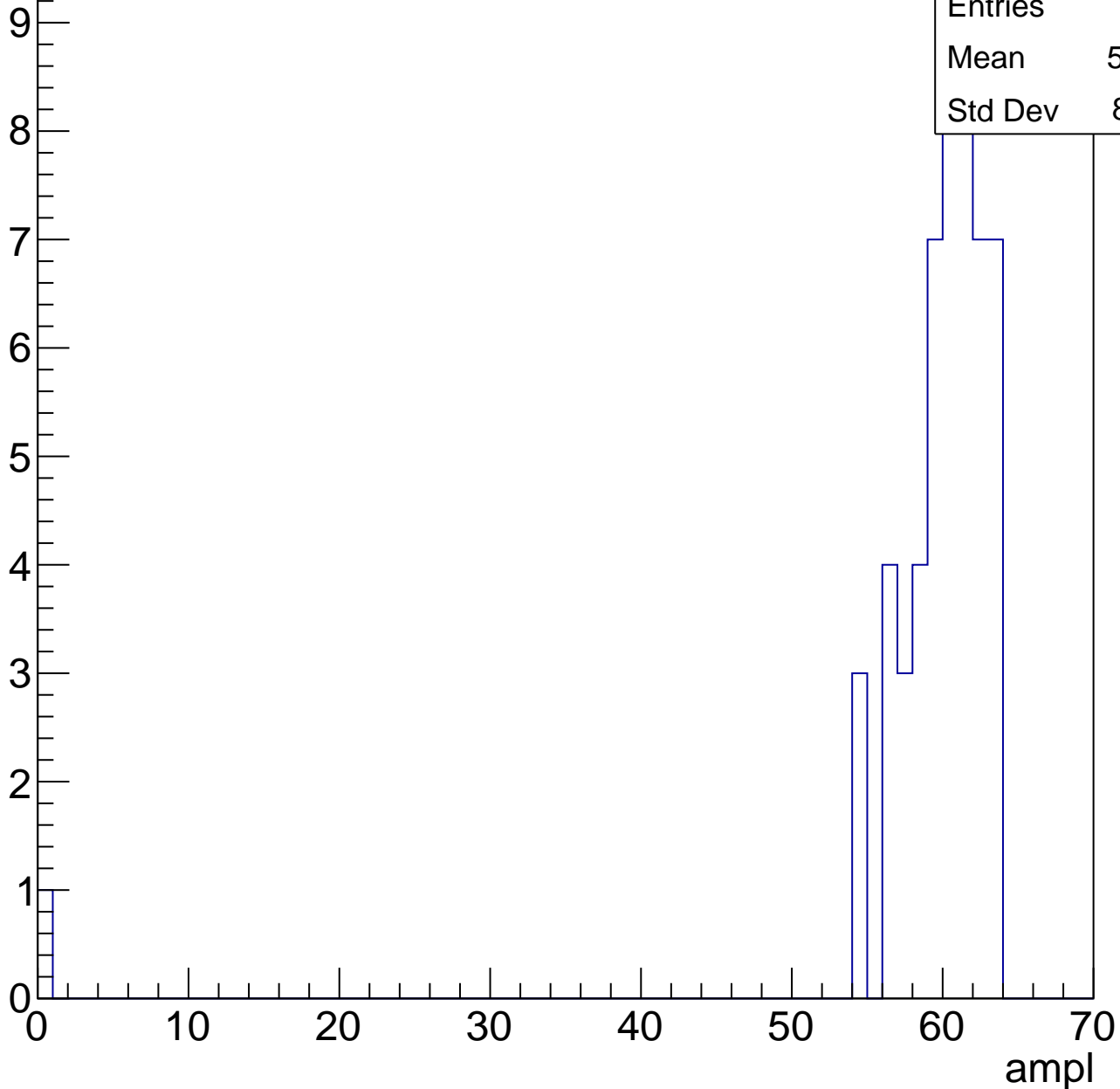


B1L100S, U14-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

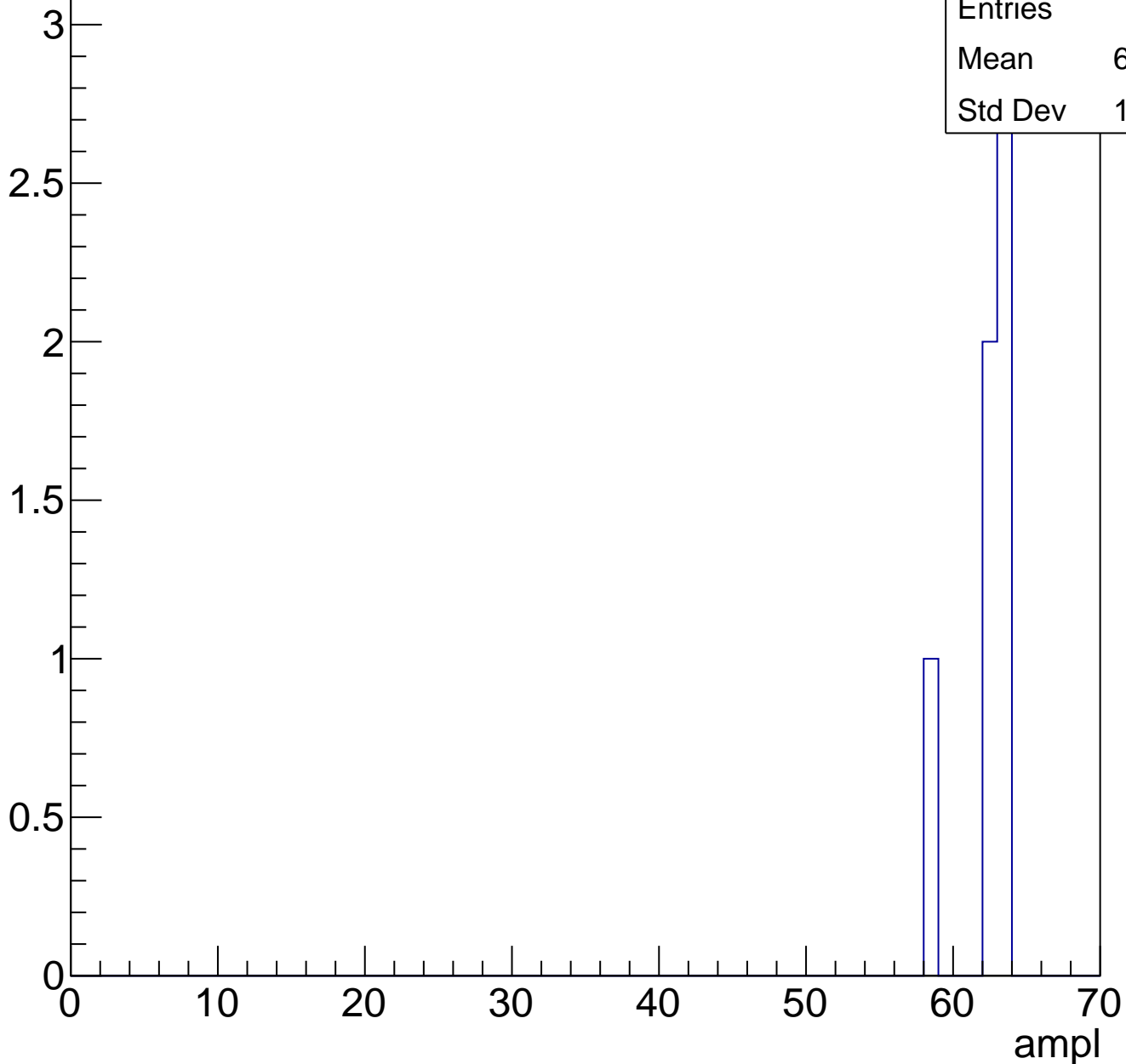
Entries	54
Mean	58.63
Std Dev	8.411



B1L100S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch20, adc0

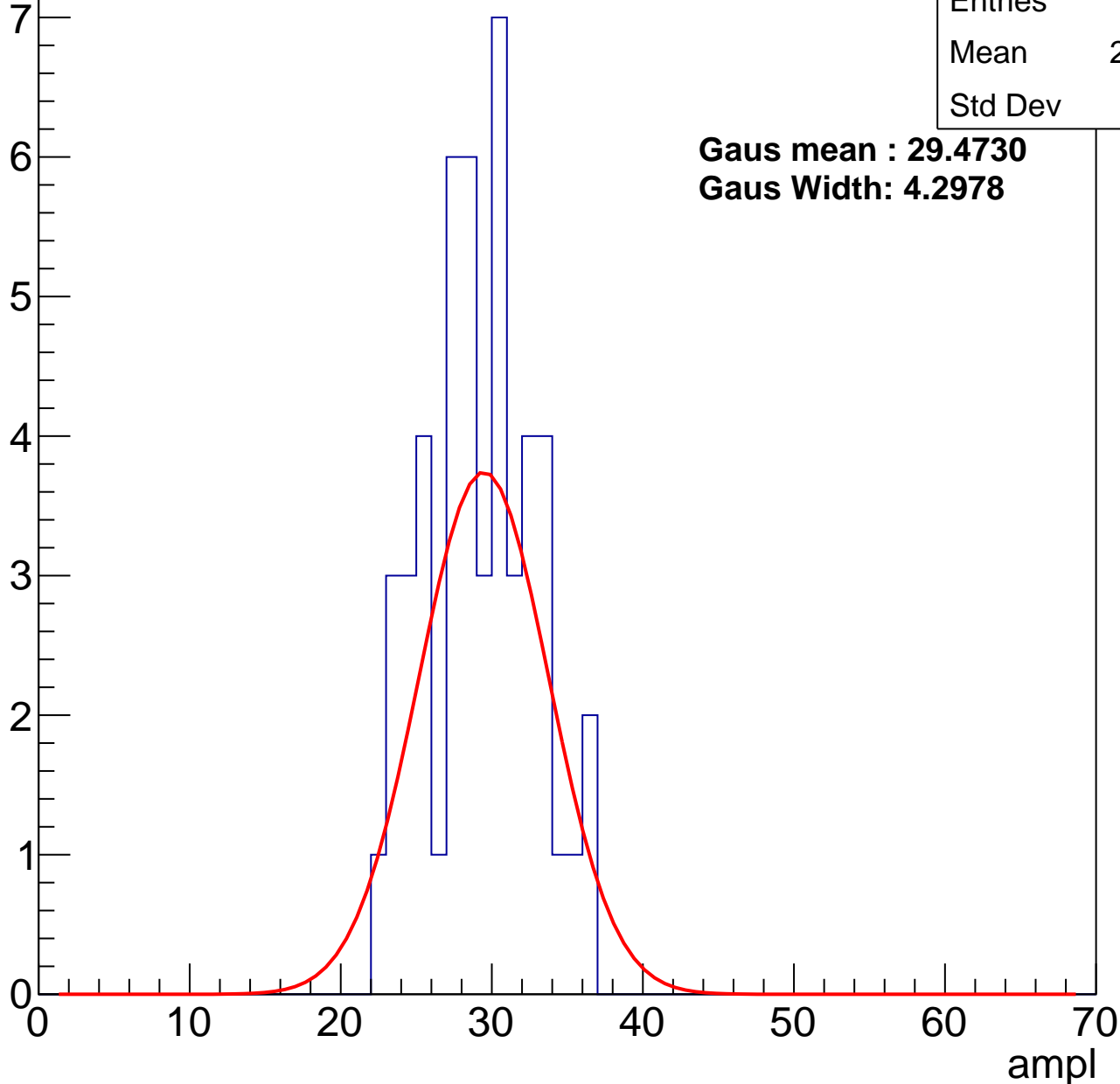
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	28.78
Std Dev	3.53

Gaus mean : 29.4730

Gaus Width: 4.2978



B1L100S, U14-ch20, adc1

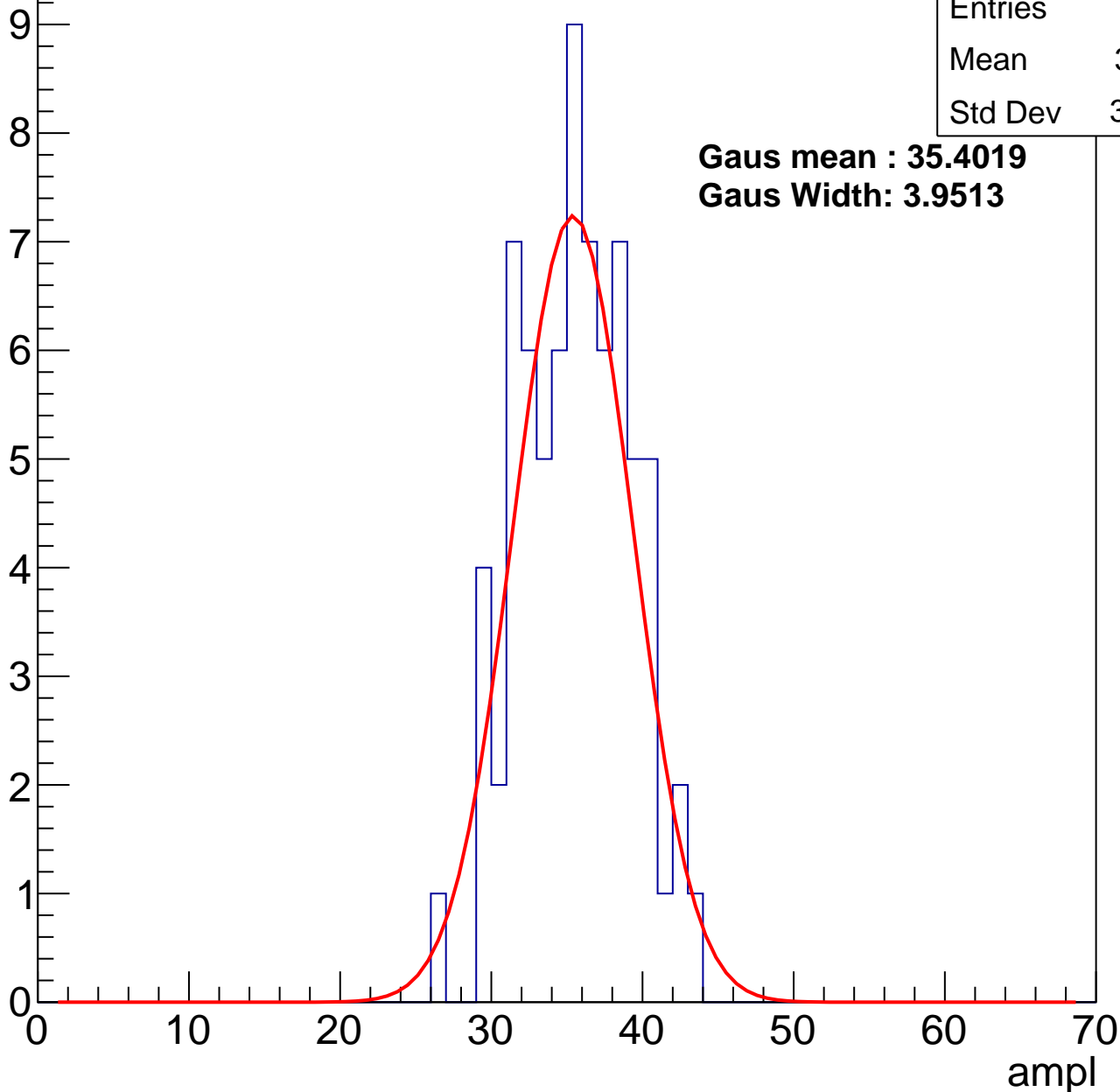
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.11
Std Dev	3.608

Gaus mean : 35.4019

Gaus Width: 3.9513



B1L100S, U14-ch20, adc2

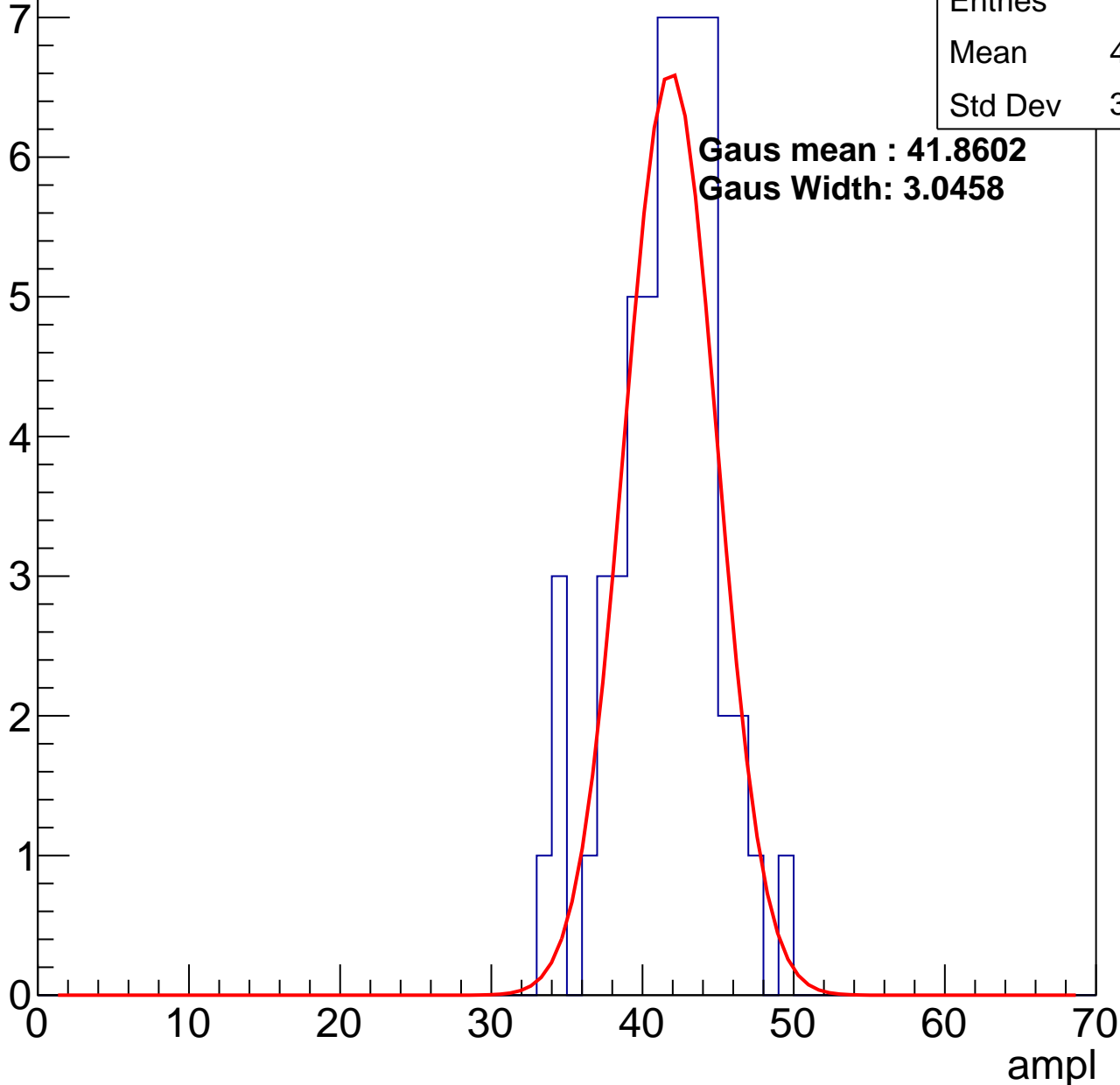
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	41.07
Std Dev	3.346

Gaus mean : 41.8602

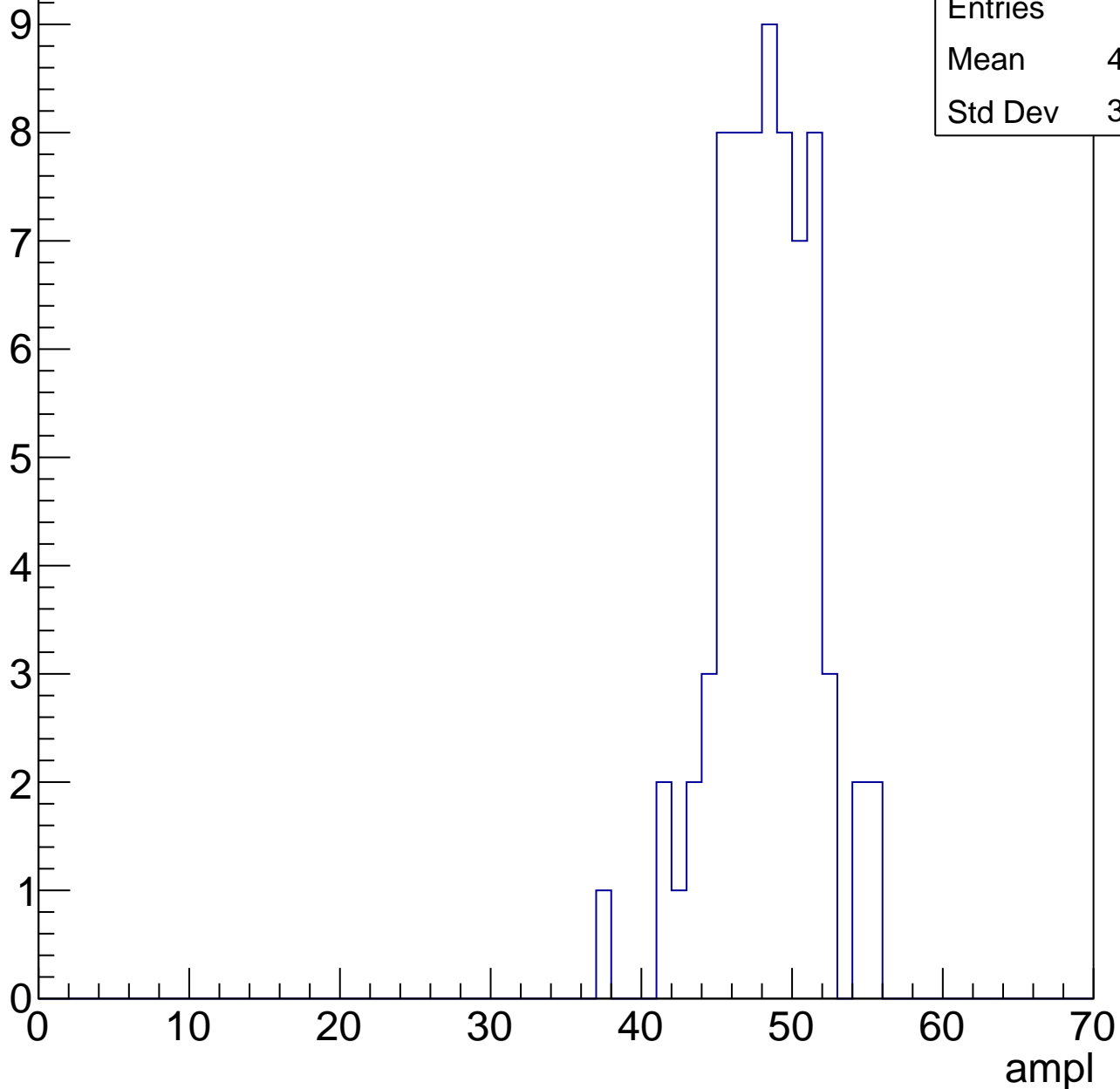
Gaus Width: 3.0458



B1L100S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



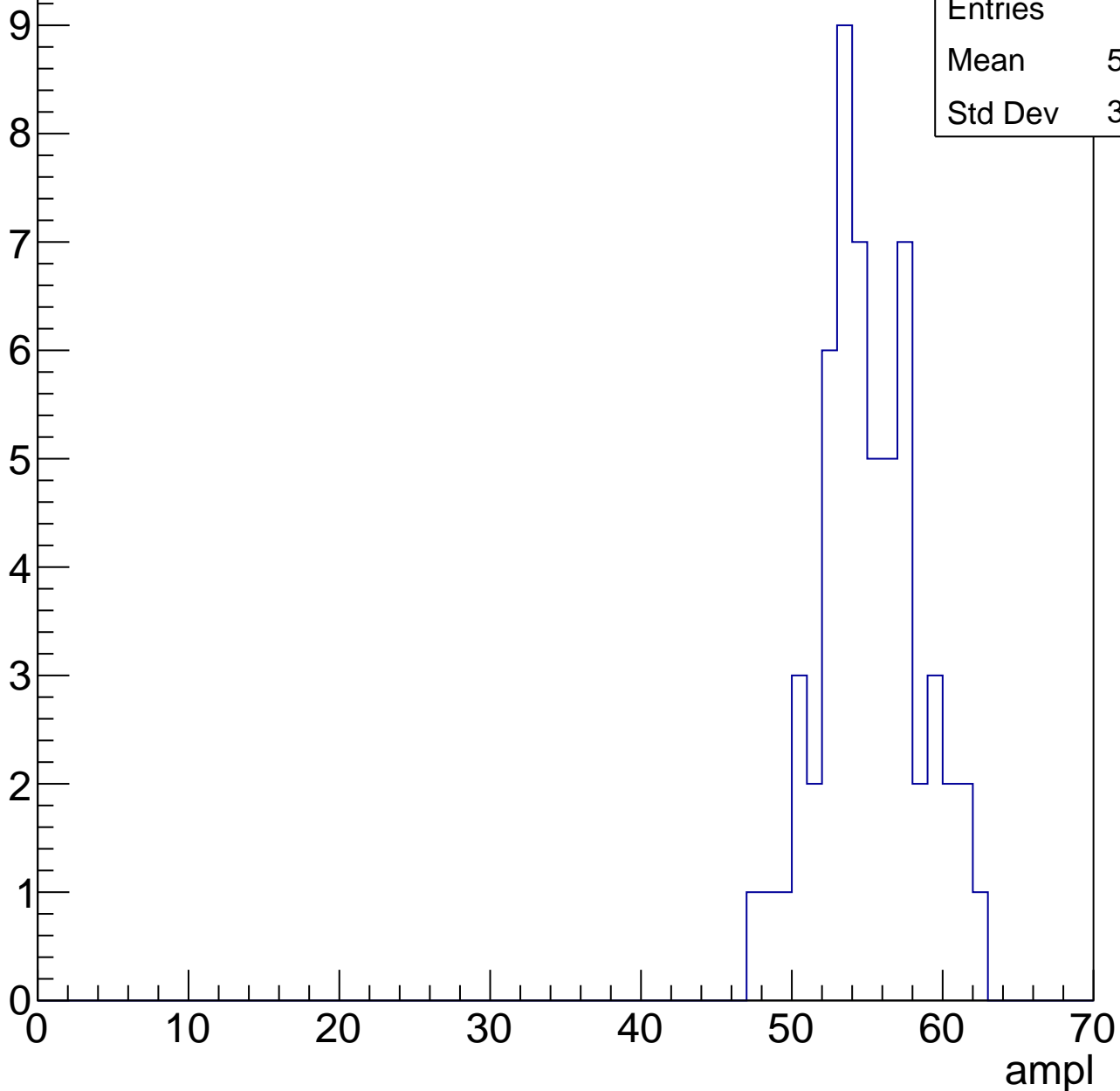
Entries	72
Mean	47.76
Std Dev	3.306

B1L100S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	54.63
Std Dev	3.285

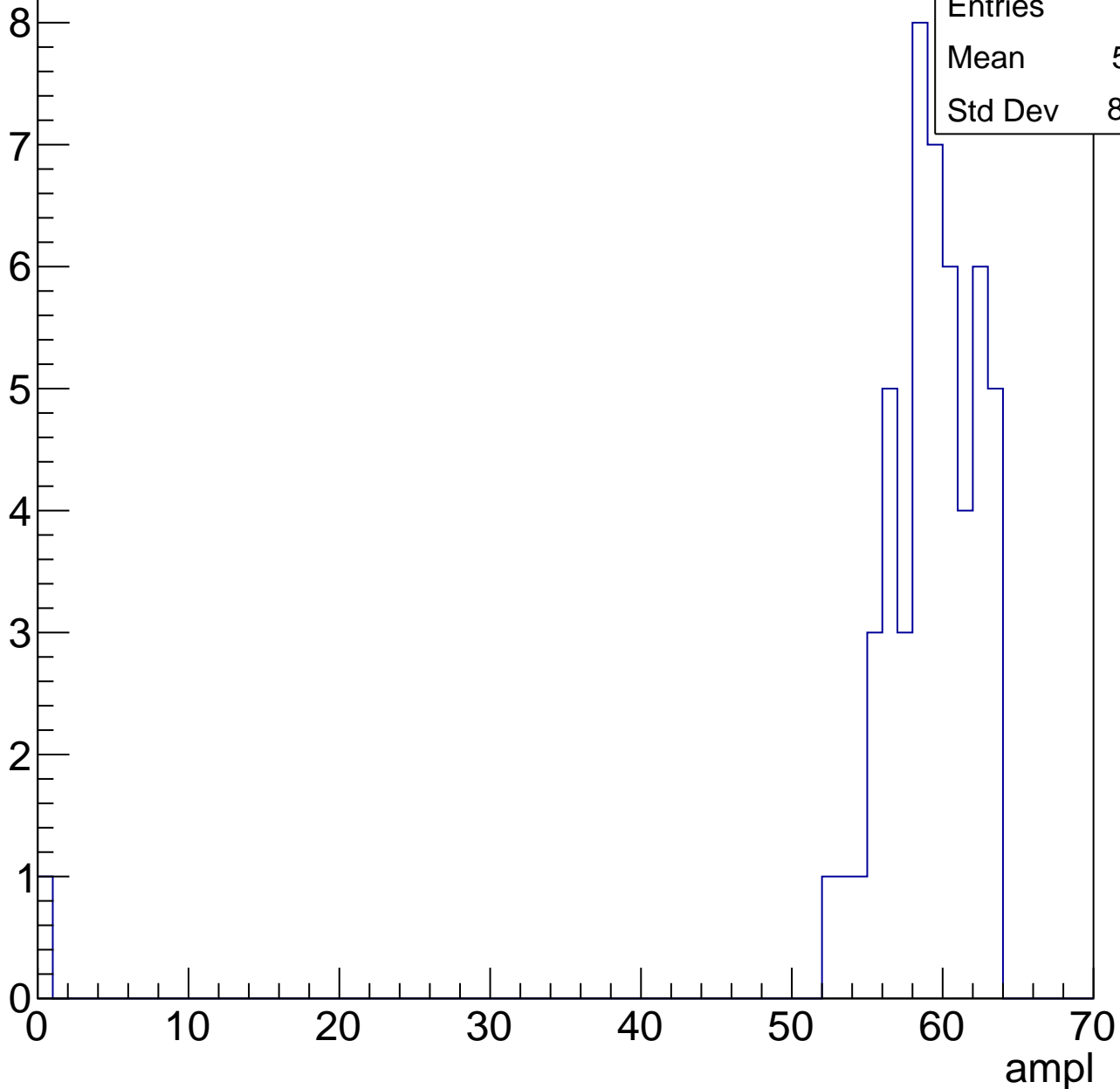


B1L100S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

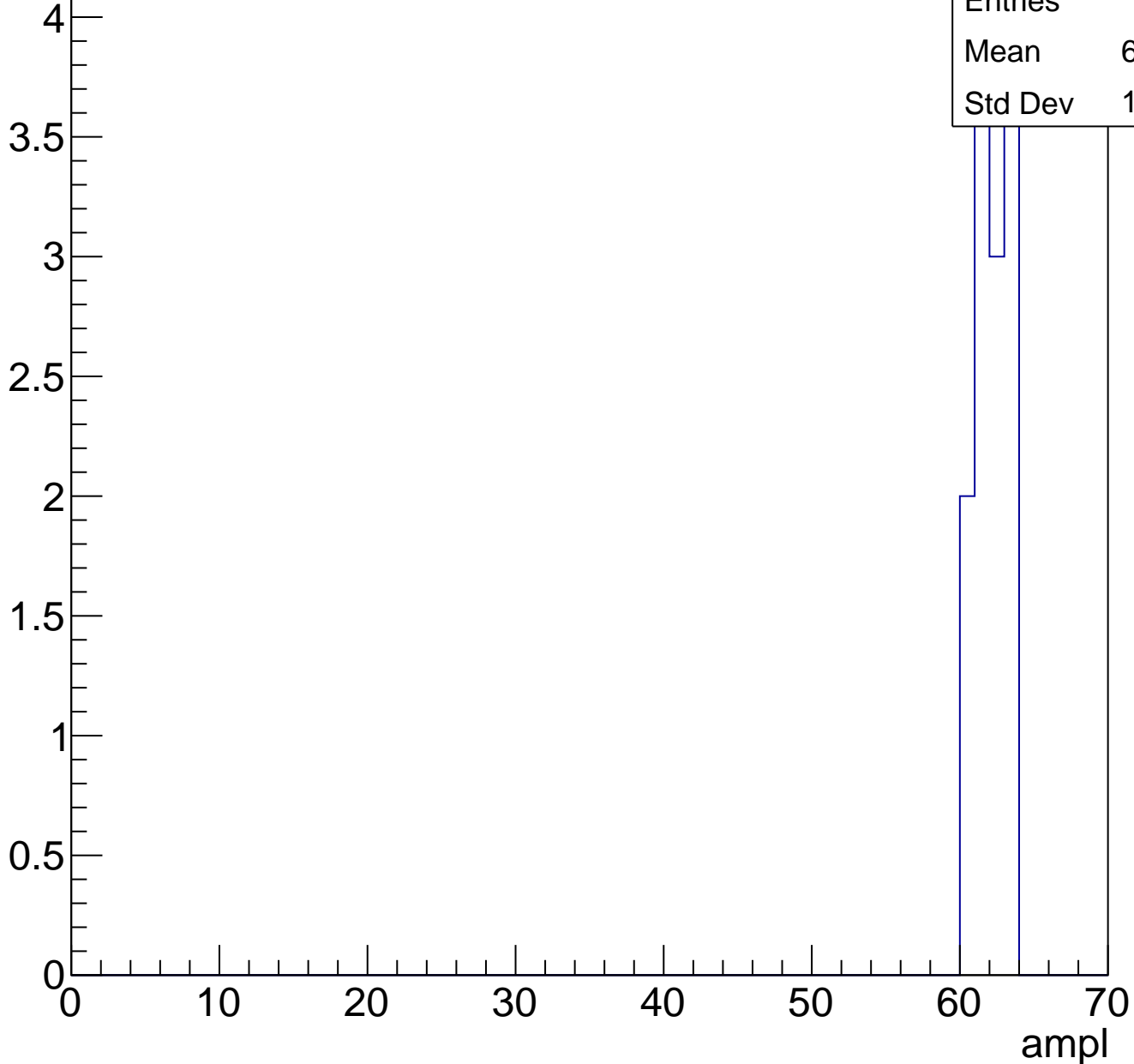
Entries	51
Mean	57.71
Std Dev	8.603



B1L100S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U14-ch21, adc0

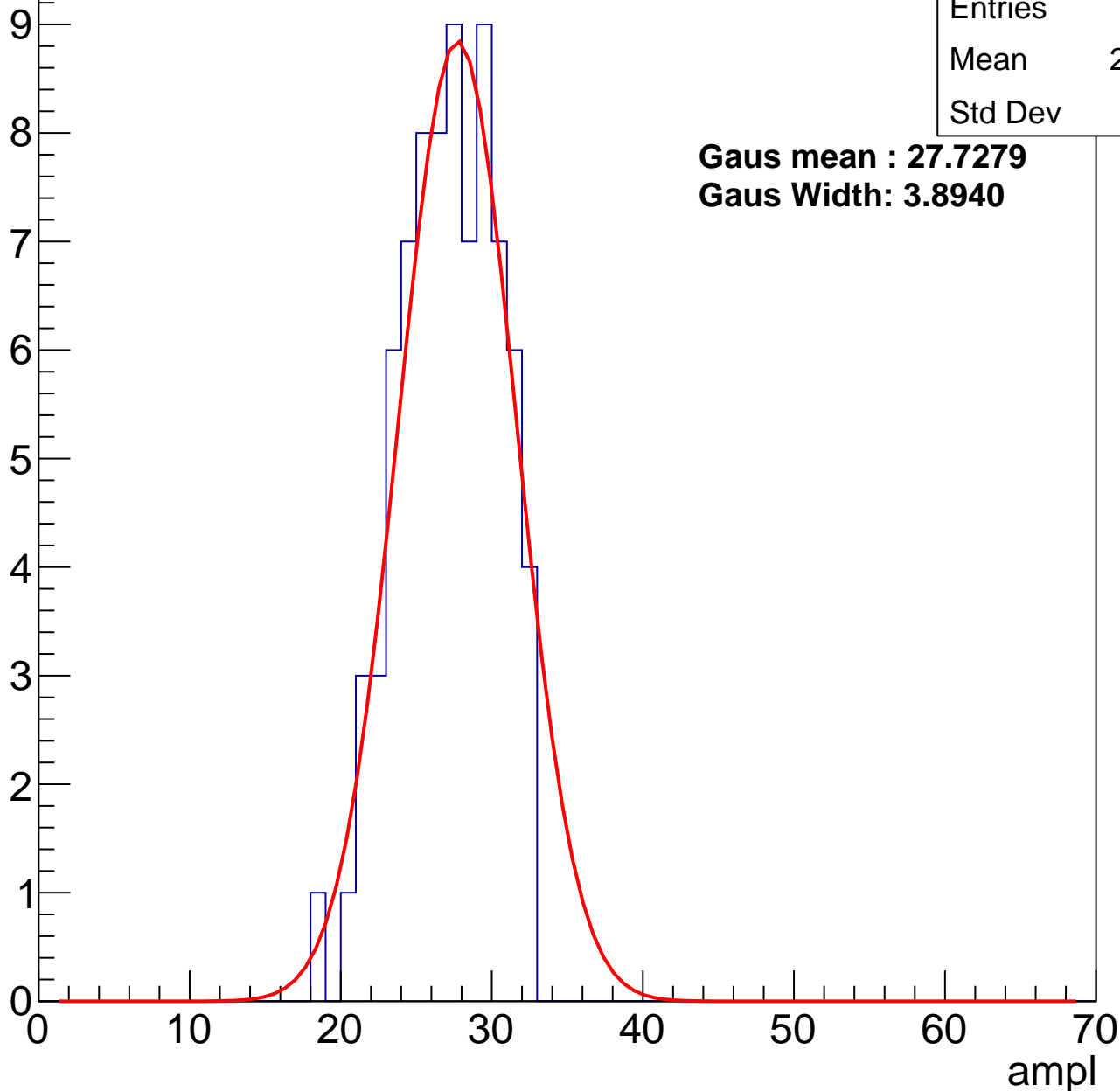
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	26.65
Std Dev	3.19

Gaus mean : 27.7279

Gaus Width: 3.8940



B1L100S, U14-ch21, adc1

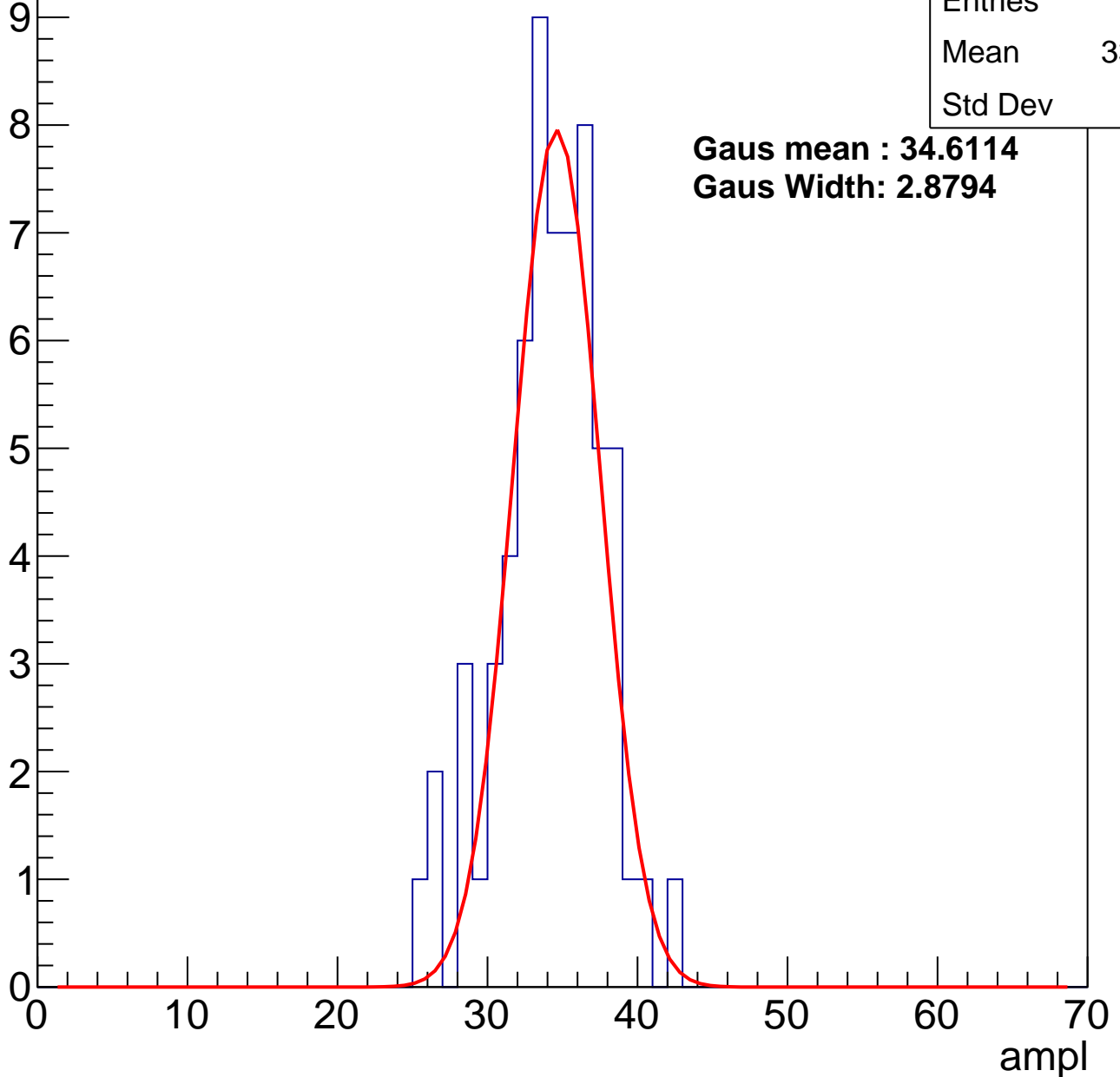
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	33.75
Std Dev	3.41

Gaus mean : 34.6114

Gaus Width: 2.8794



B1L100S, U14-ch21, adc2

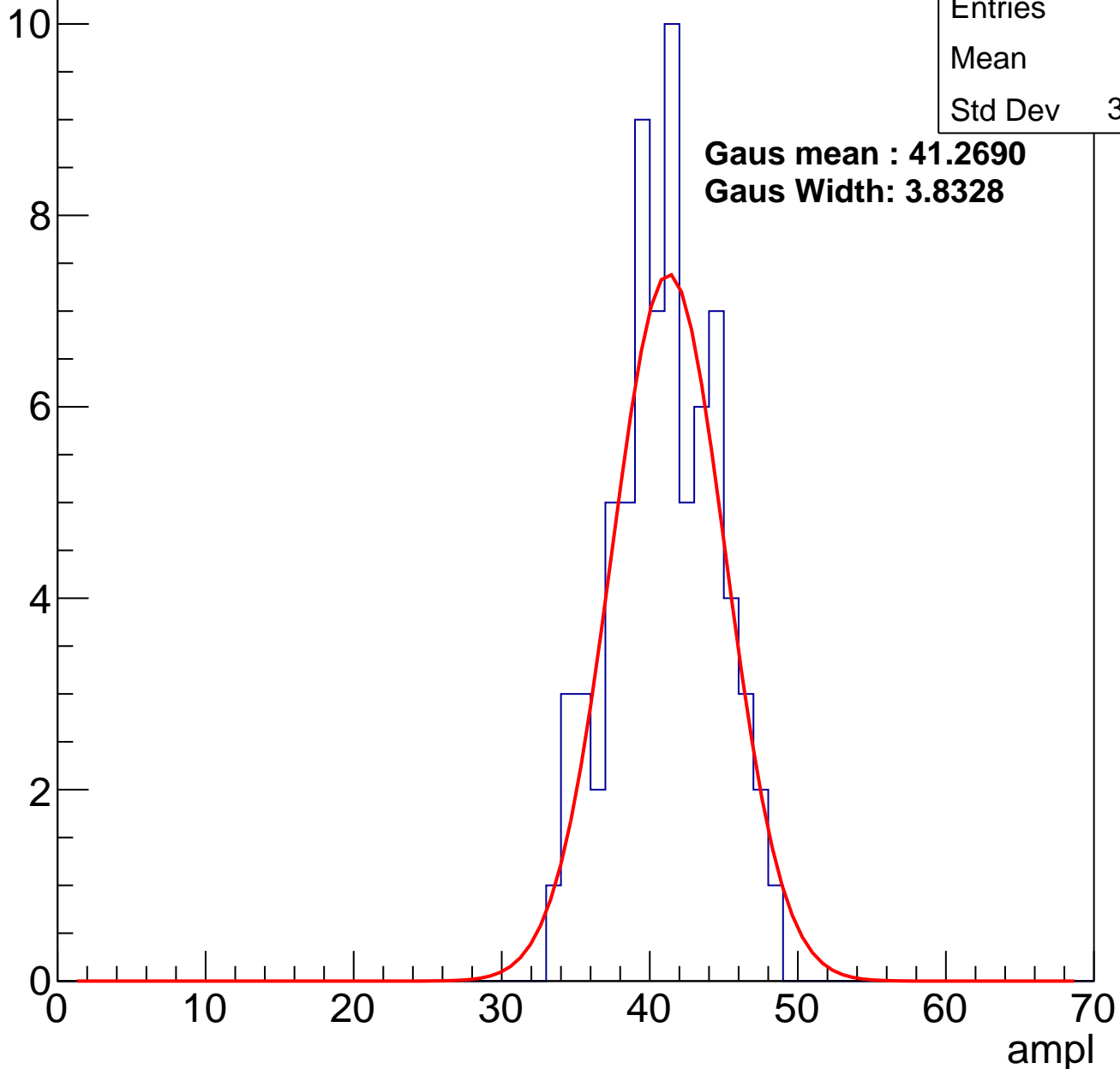
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	40.6
Std Dev	3.467

Gaus mean : 41.2690

Gaus Width: 3.8328

Entry

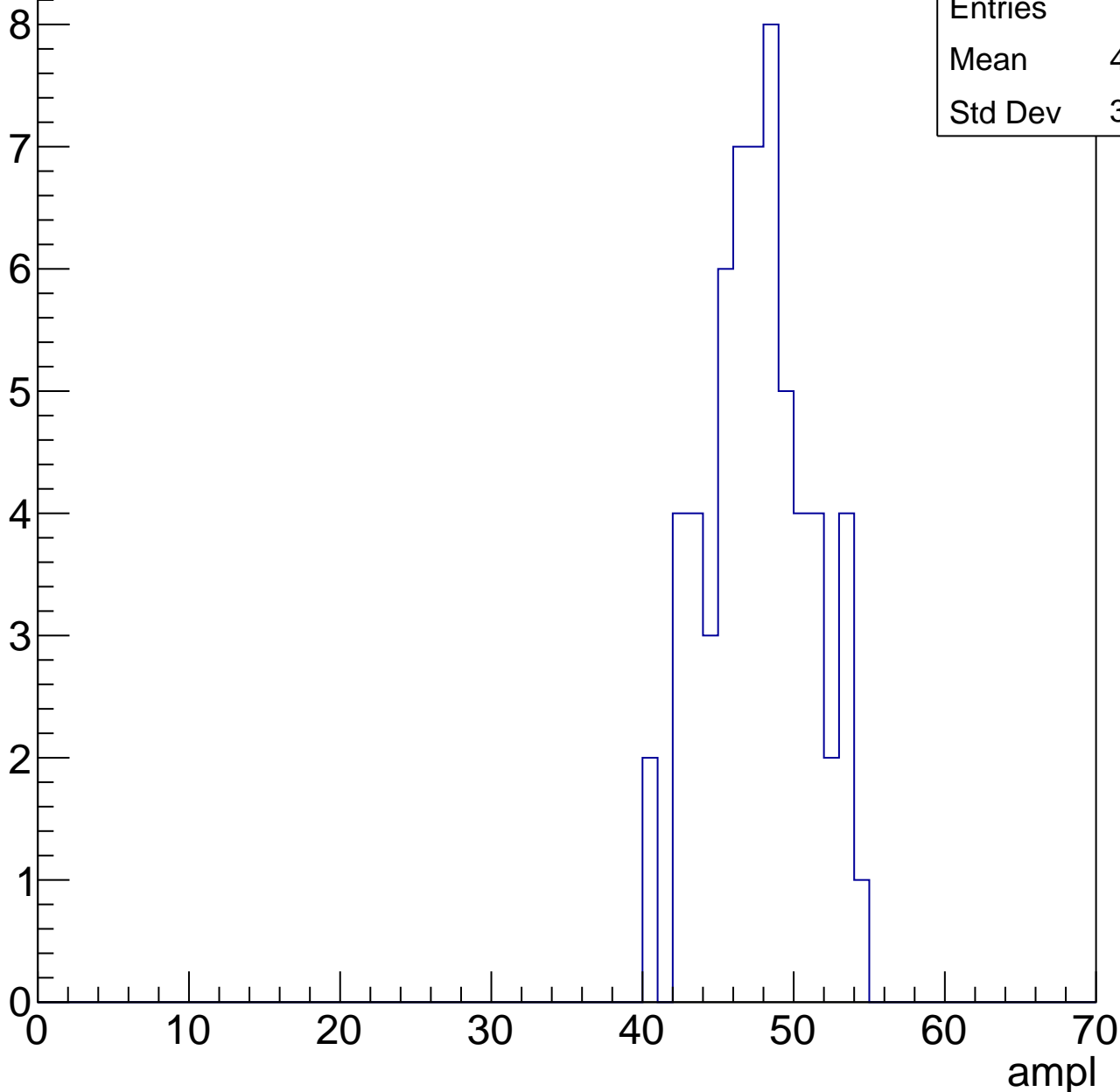


B1L100S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

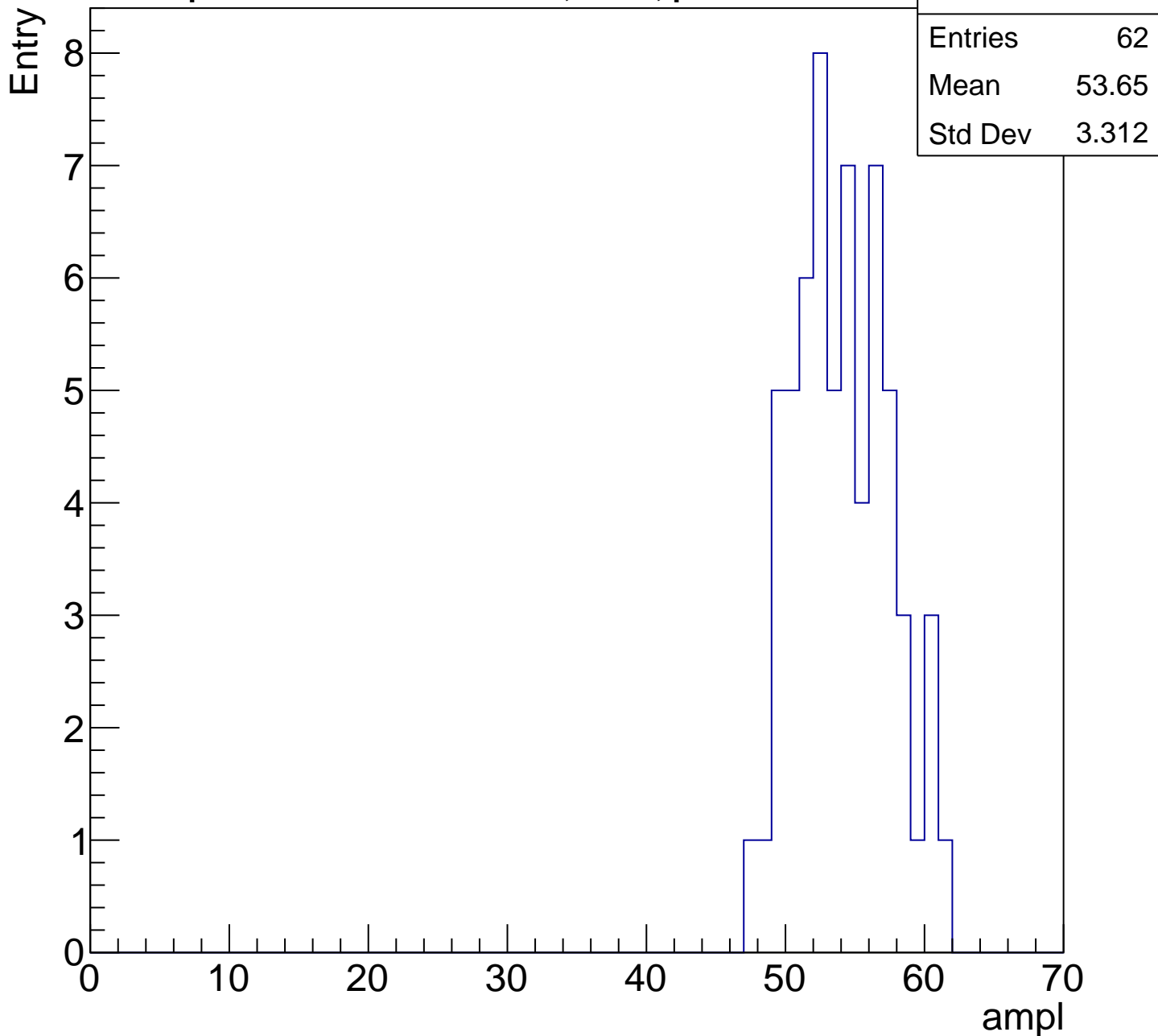
Entry

Entries	61
Mean	47.15
Std Dev	3.362



B1L100S, U14-ch21, adc4

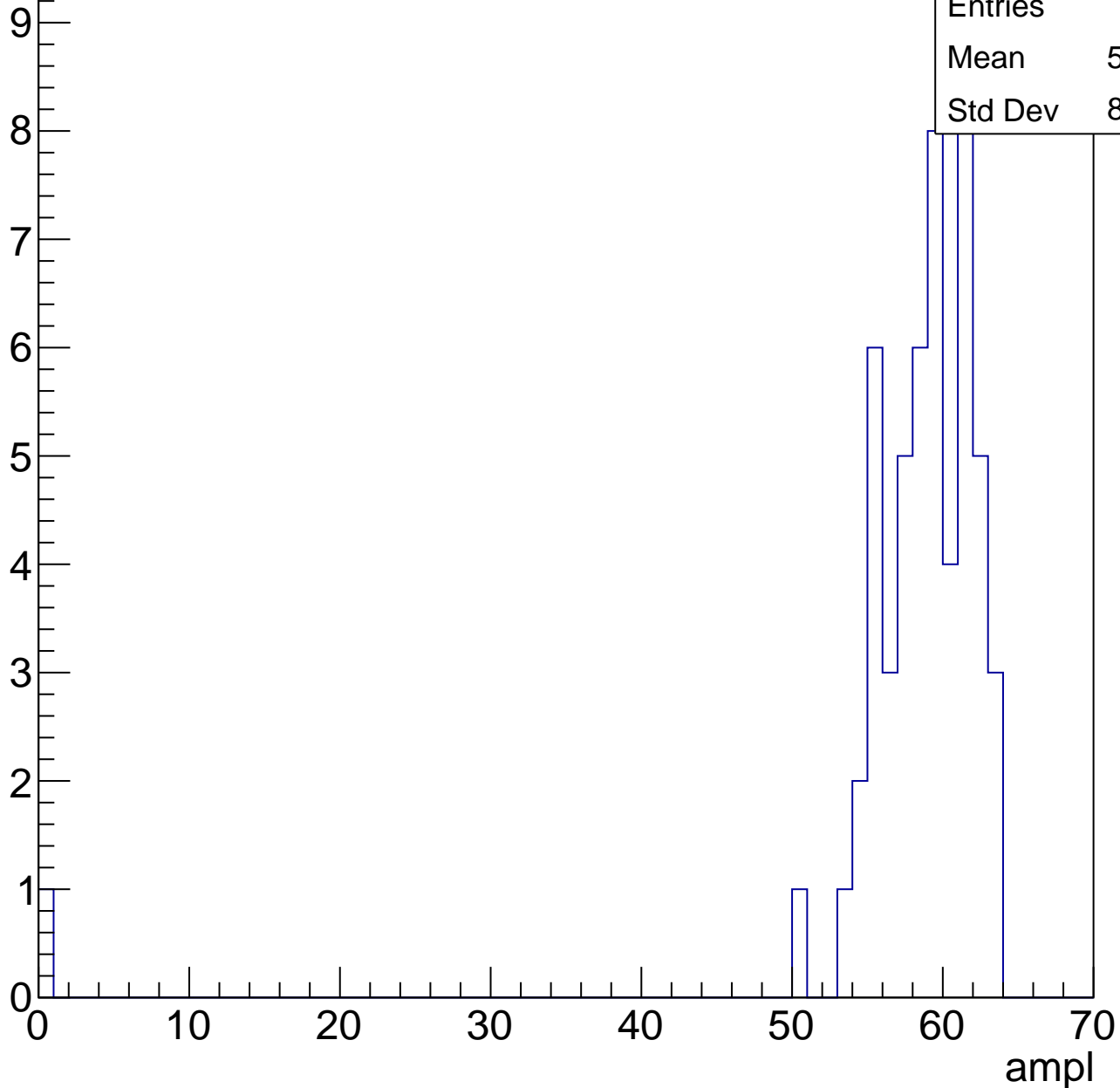
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

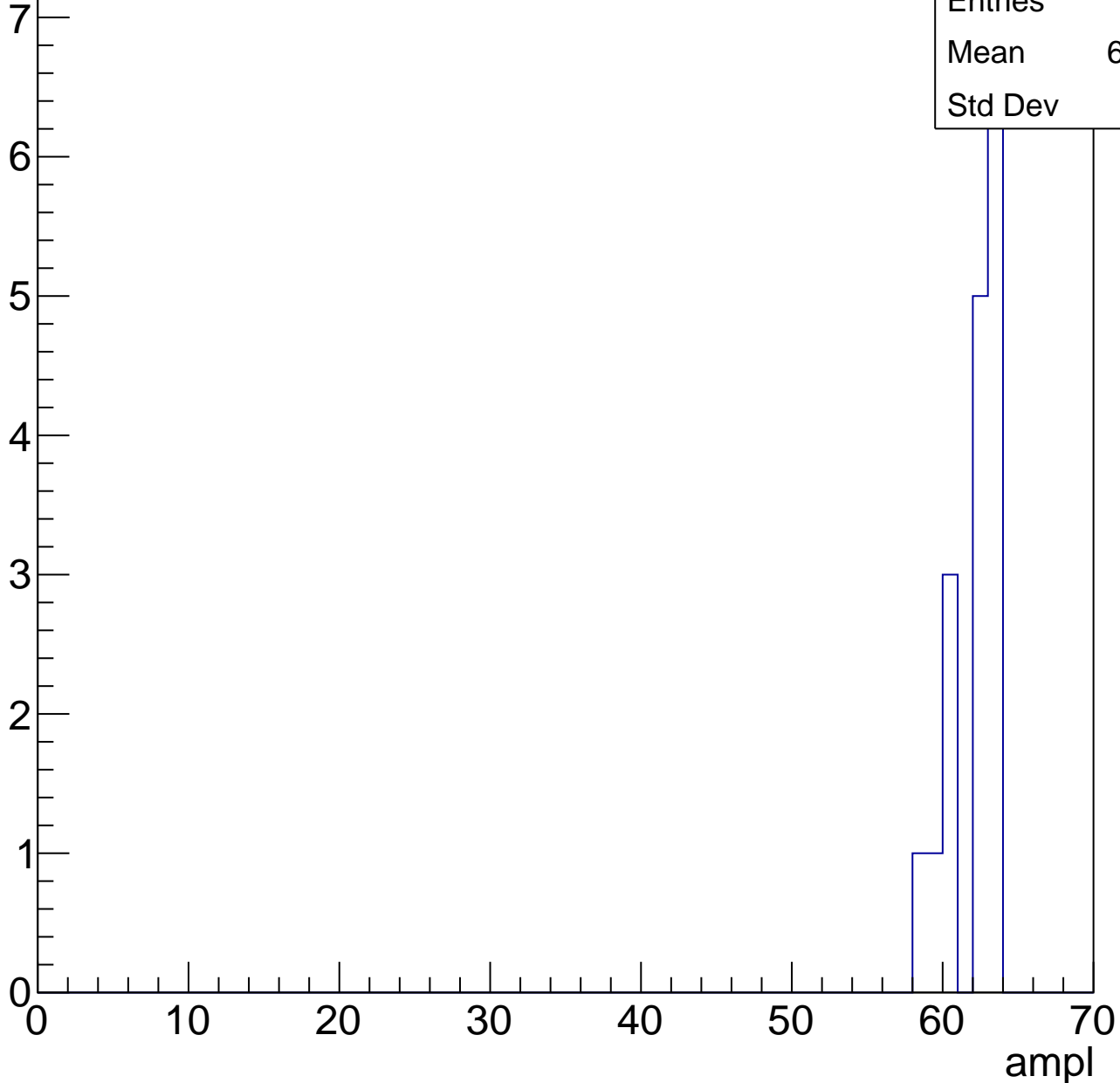


B1L100S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	61.65
Std Dev	1.57



B1L100S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch22, adc0

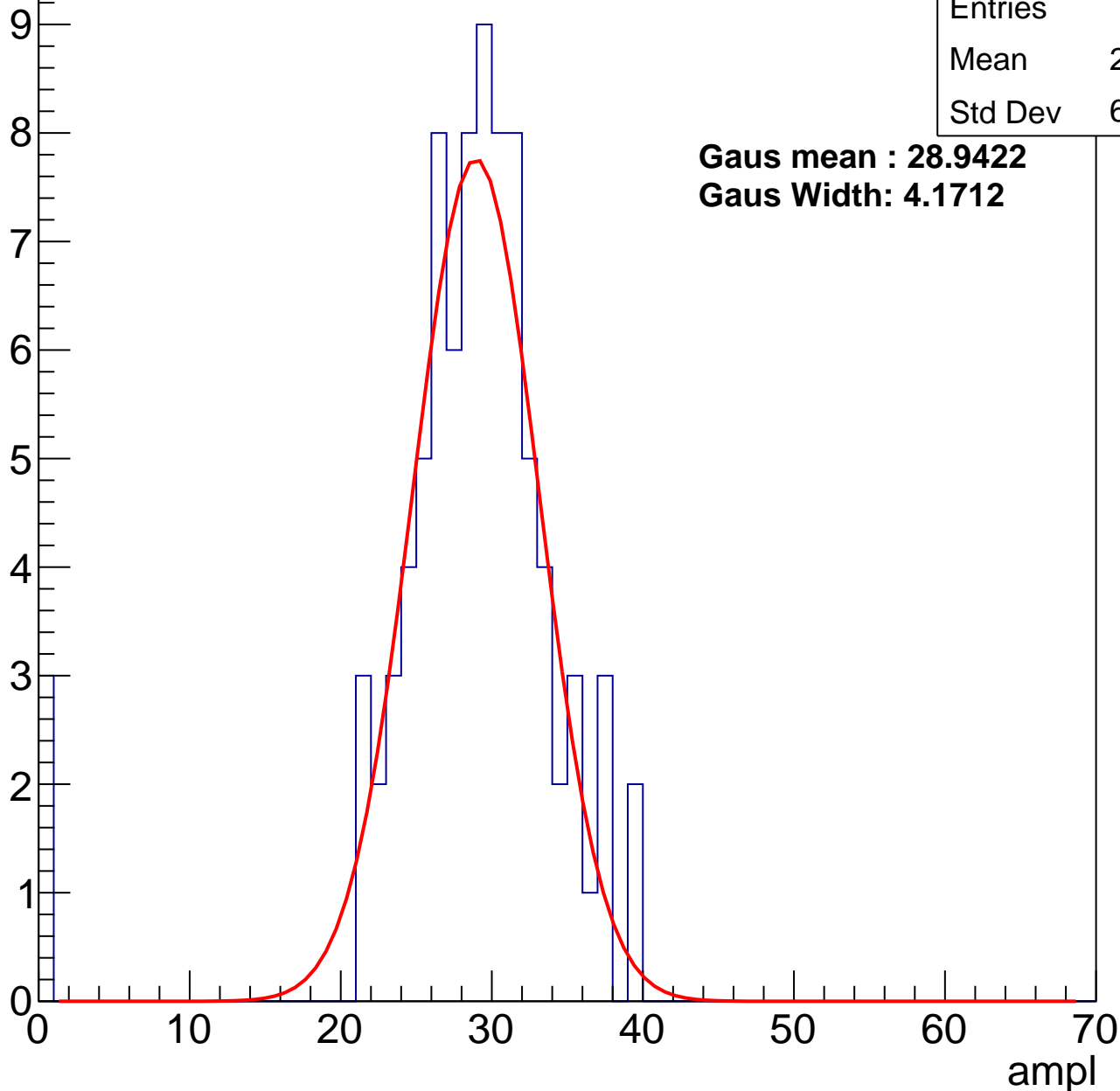
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	27.93
Std Dev	6.655

Gaus mean : 28.9422

Gaus Width: 4.1712



B1L100S, U14-ch22, adc1

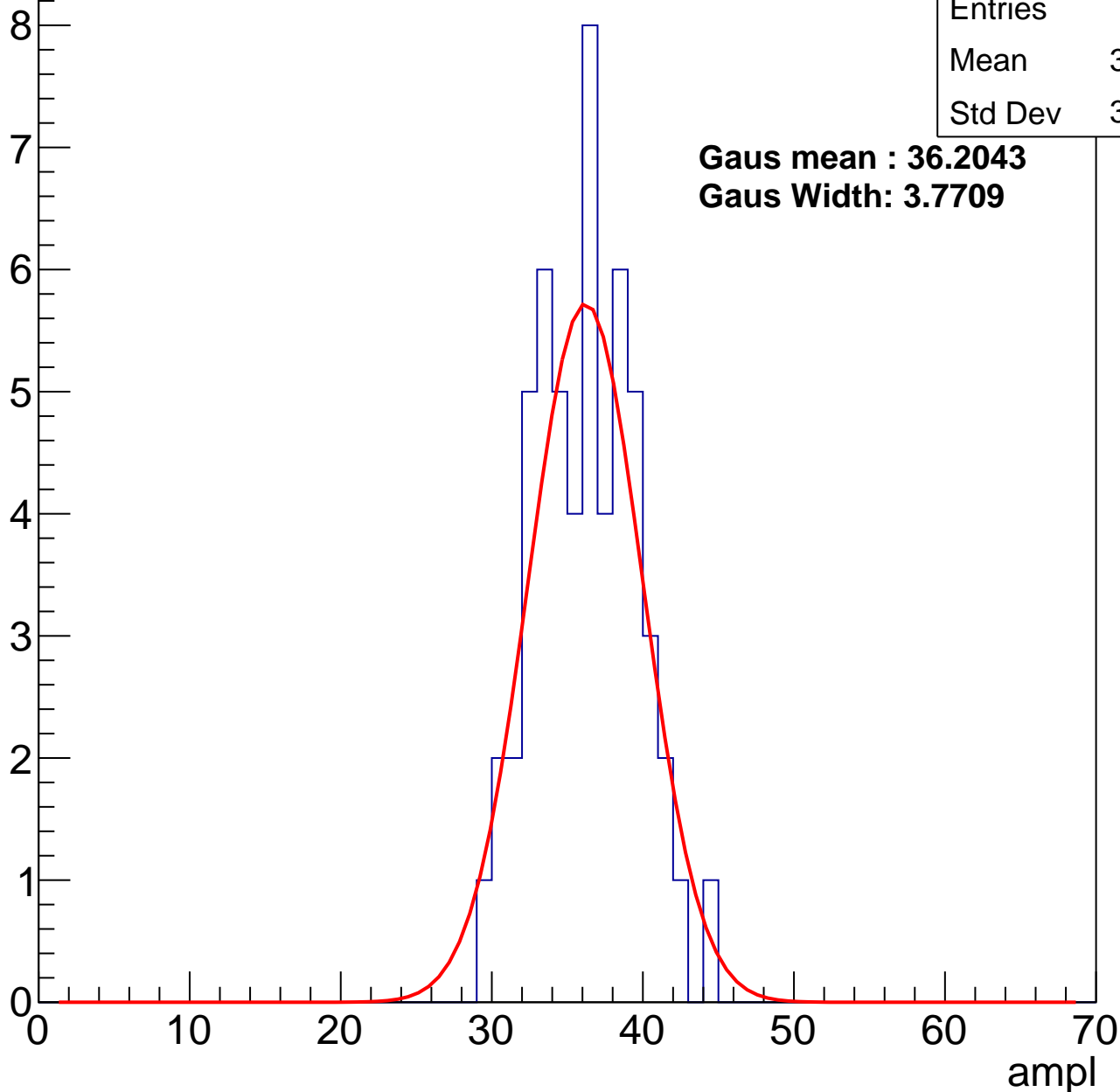
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.75
Std Dev	3.293

Gaus mean : 36.2043

Gaus Width: 3.7709



B1L100S, U14-ch22, adc2

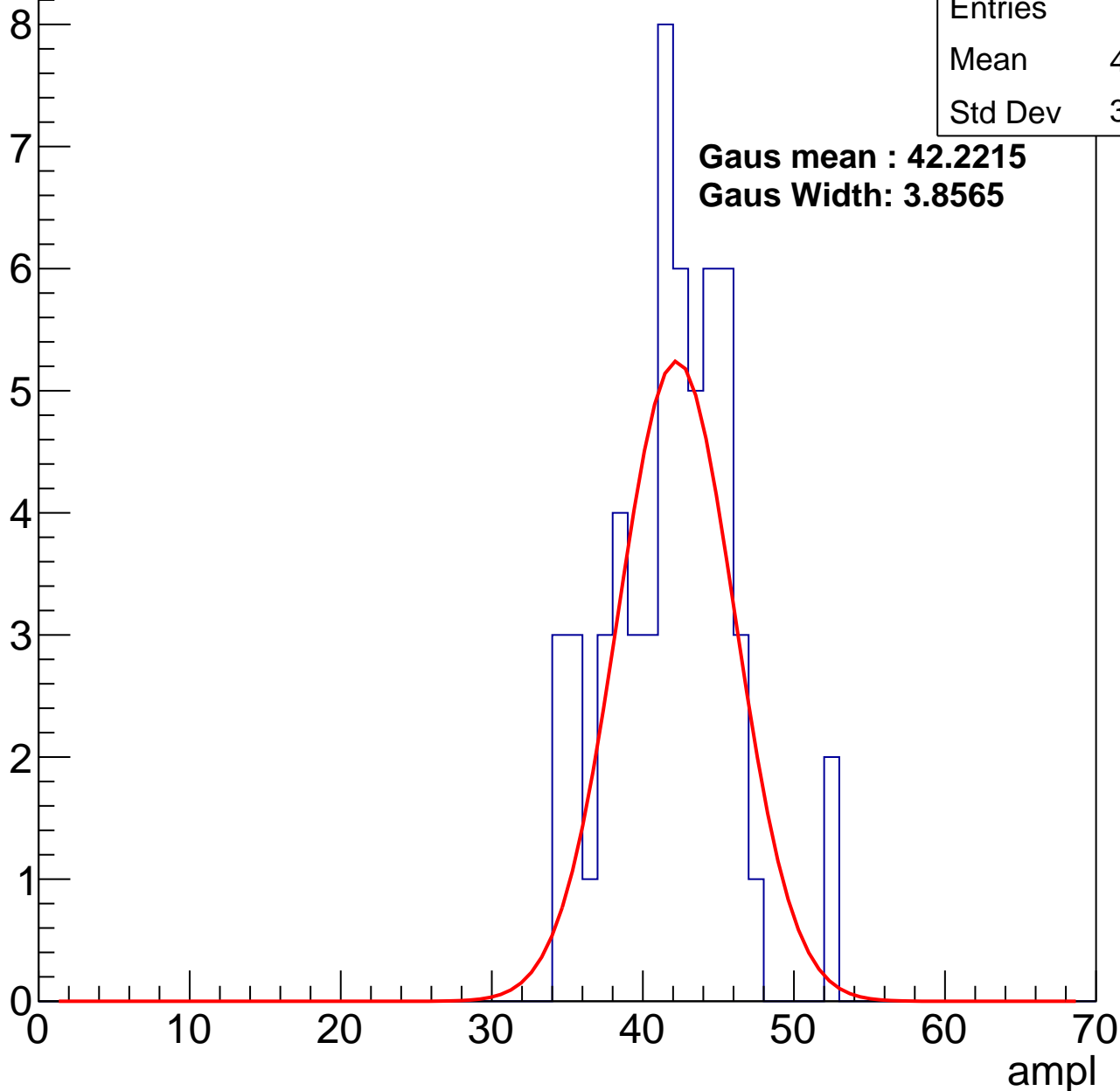
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	41.42
Std Dev	3.969

Gaus mean : 42.2215

Gaus Width: 3.8565

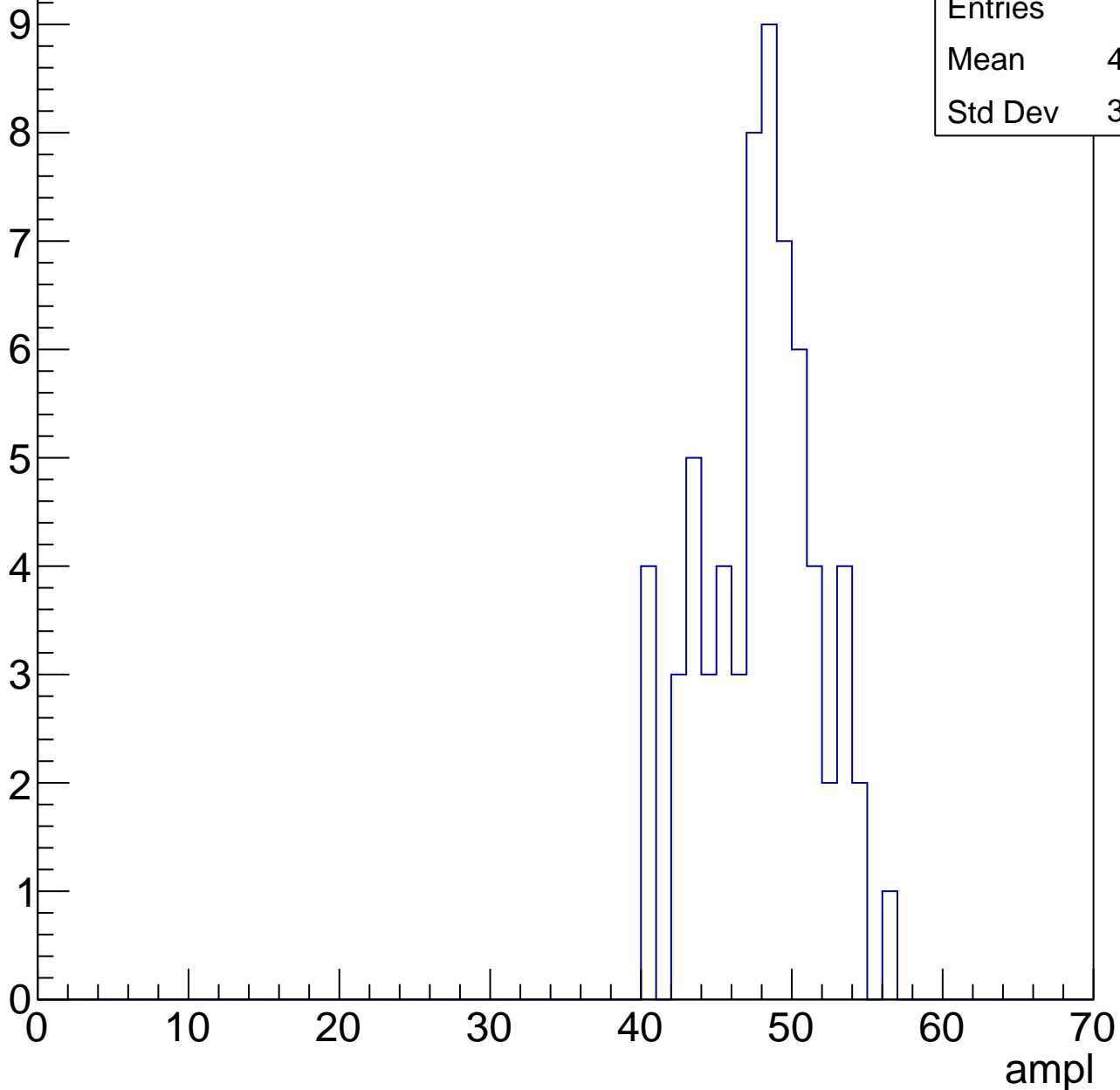


B1L100S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	47.48
Std Dev	3.742

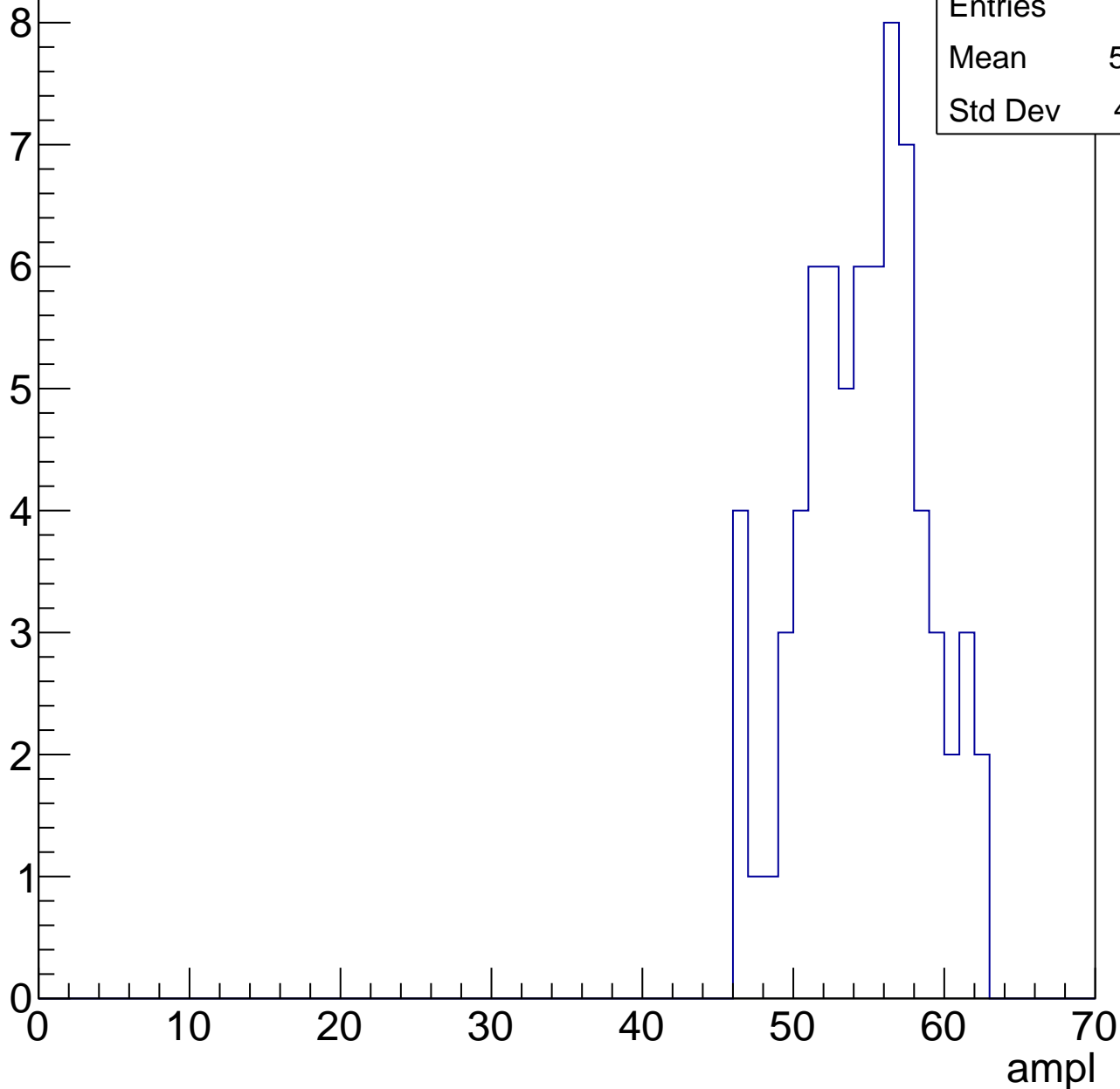


B1L100S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	54.17
Std Dev	4.021

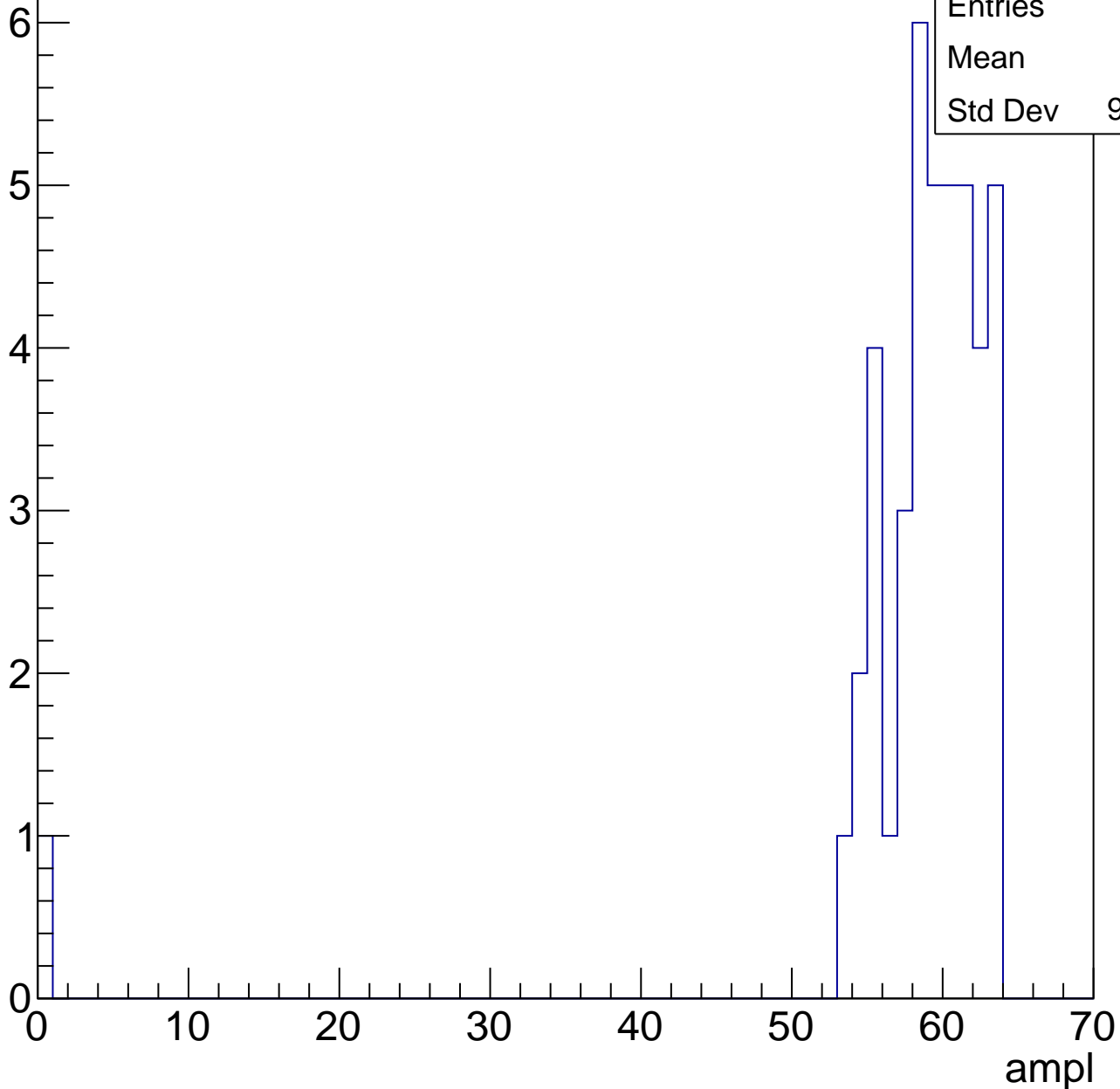


B1L100S, U14-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	57.6
Std Dev	9.406

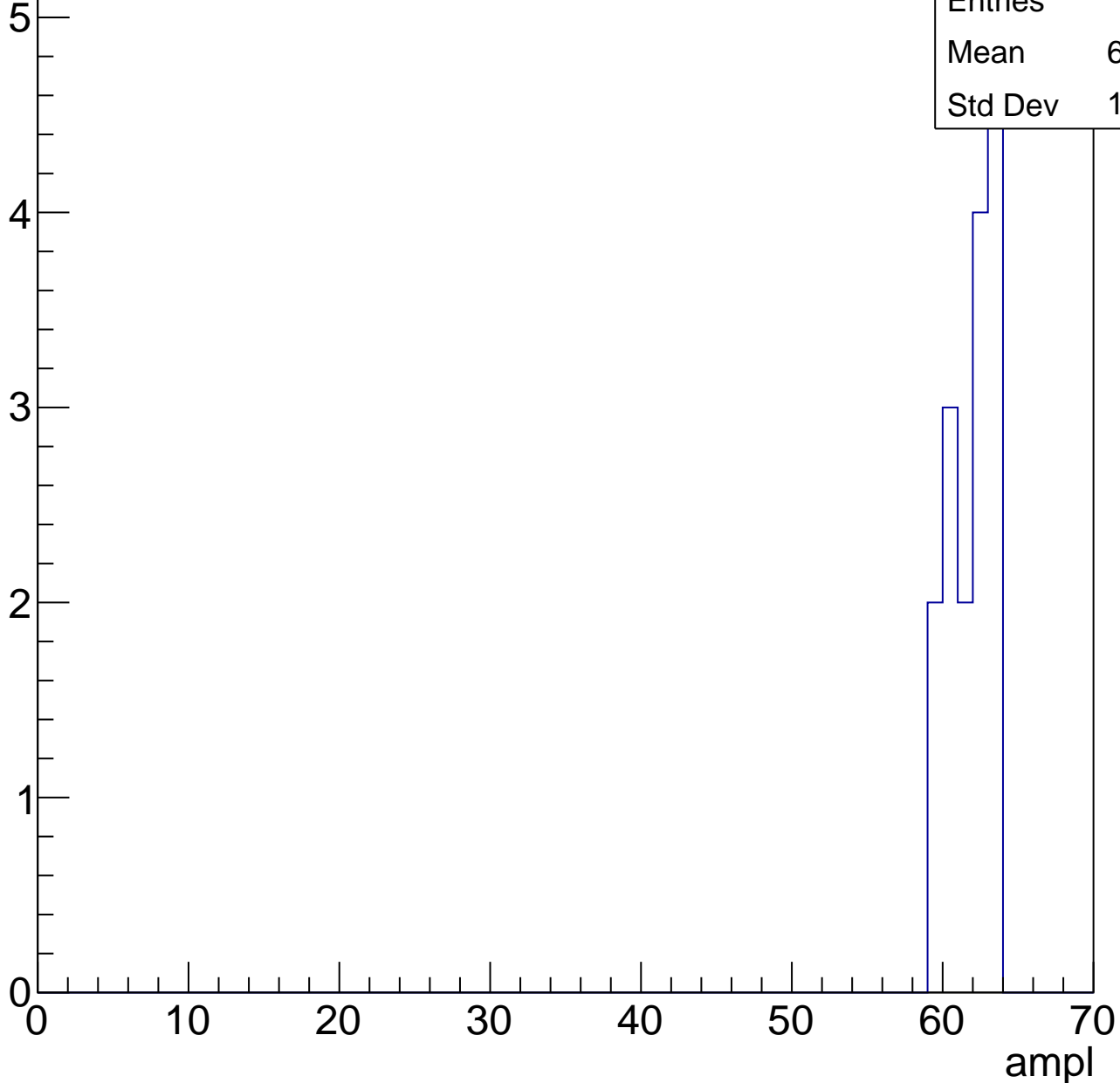


B1L100S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	61.44
Std Dev	1.413



B1L100S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch23, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	30.17
Std Dev	3.647

Gaus mean : 30.2834

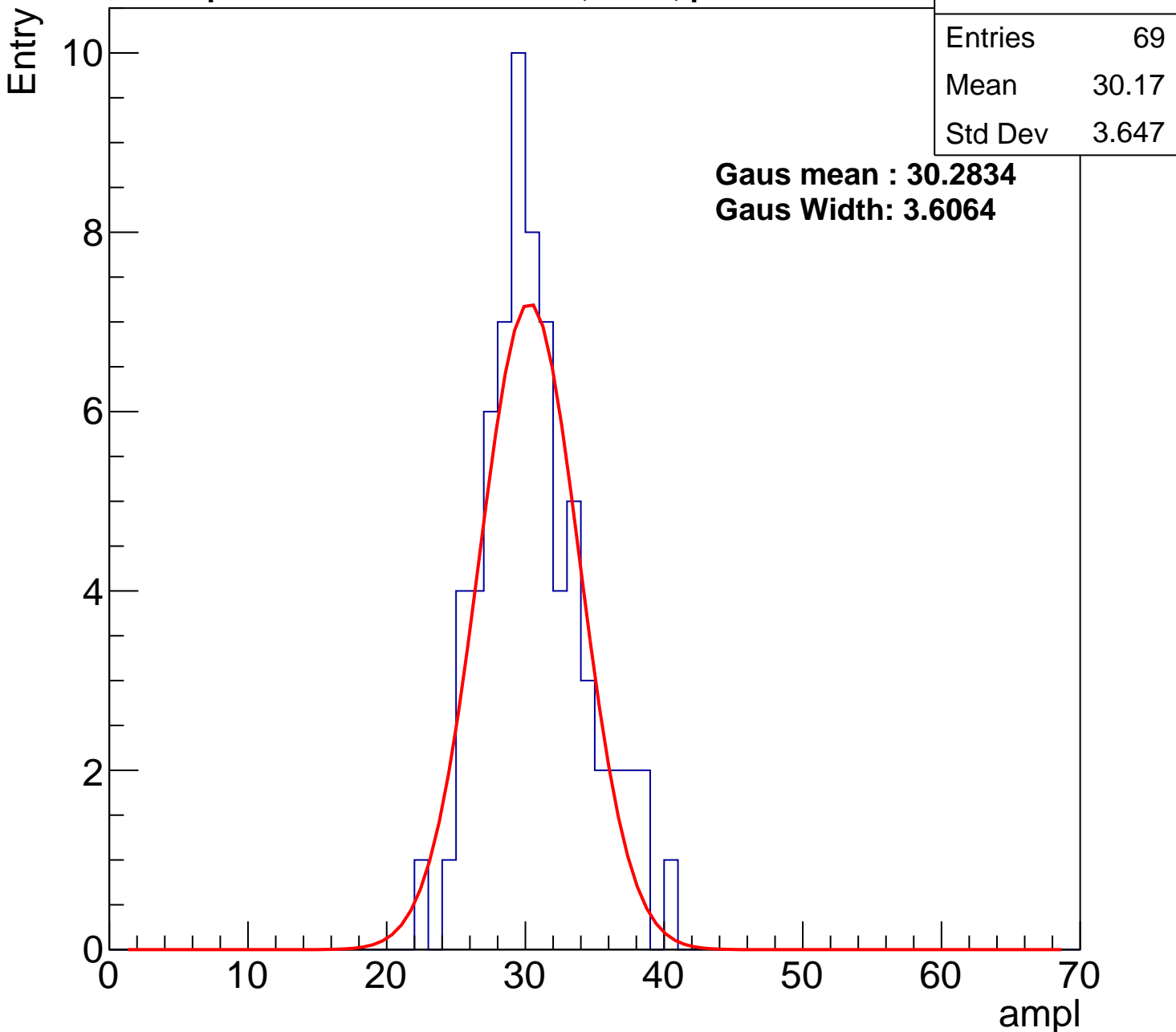
Gaus Width: 3.6064

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch23, adc1

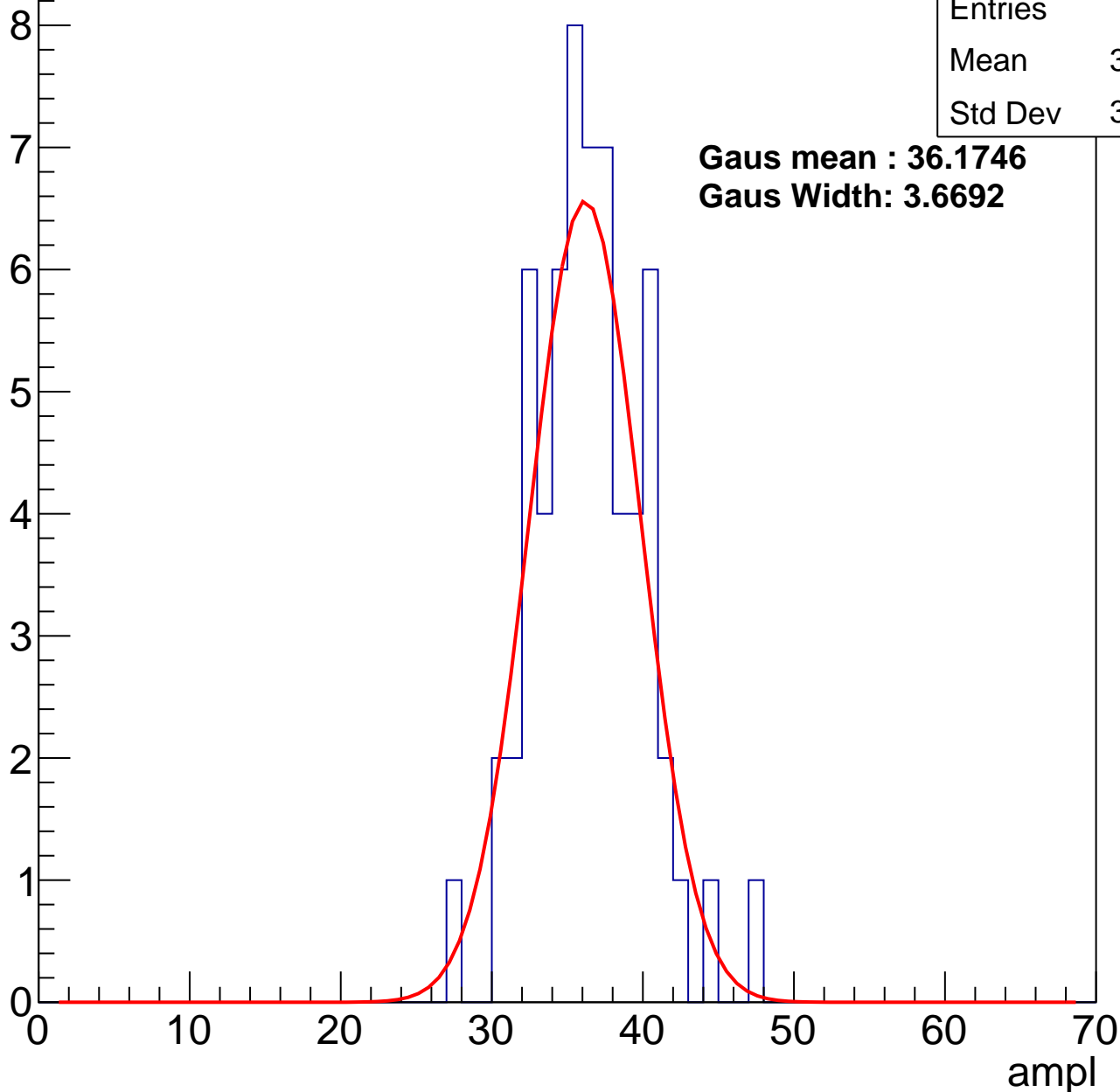
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.98
Std Dev	3.576

Gaus mean : 36.1746

Gaus Width: 3.6692



B1L100S, U14-ch23, adc2

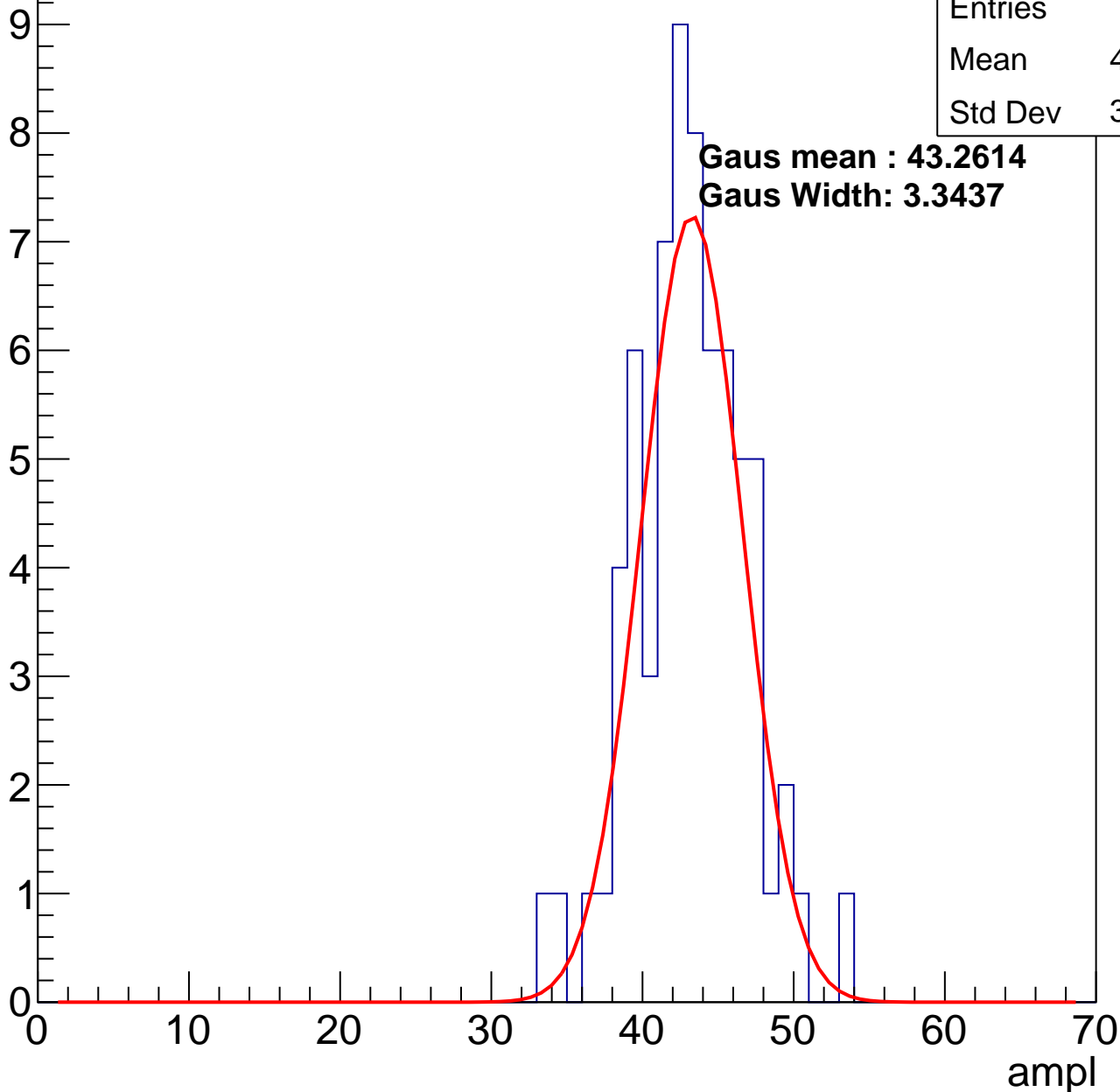
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.69
Std Dev	3.679

Gaus mean : 43.2614

Gaus Width: 3.3437

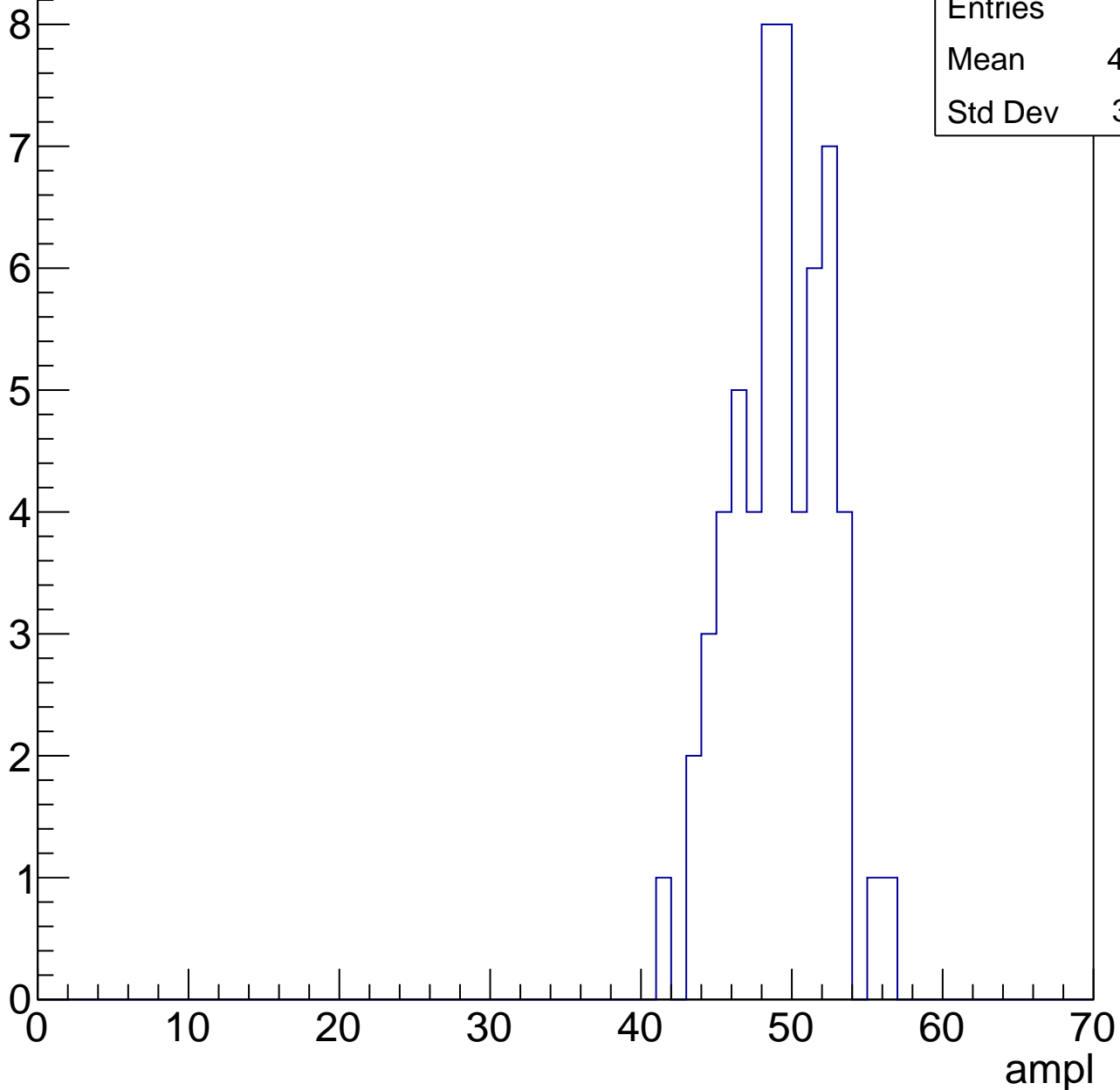


B1L100S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.72
Std Dev	3.161



B1L100S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	55.69
Std Dev	3.52

Entry

10

8

6

4

2

0

0

10

20

30

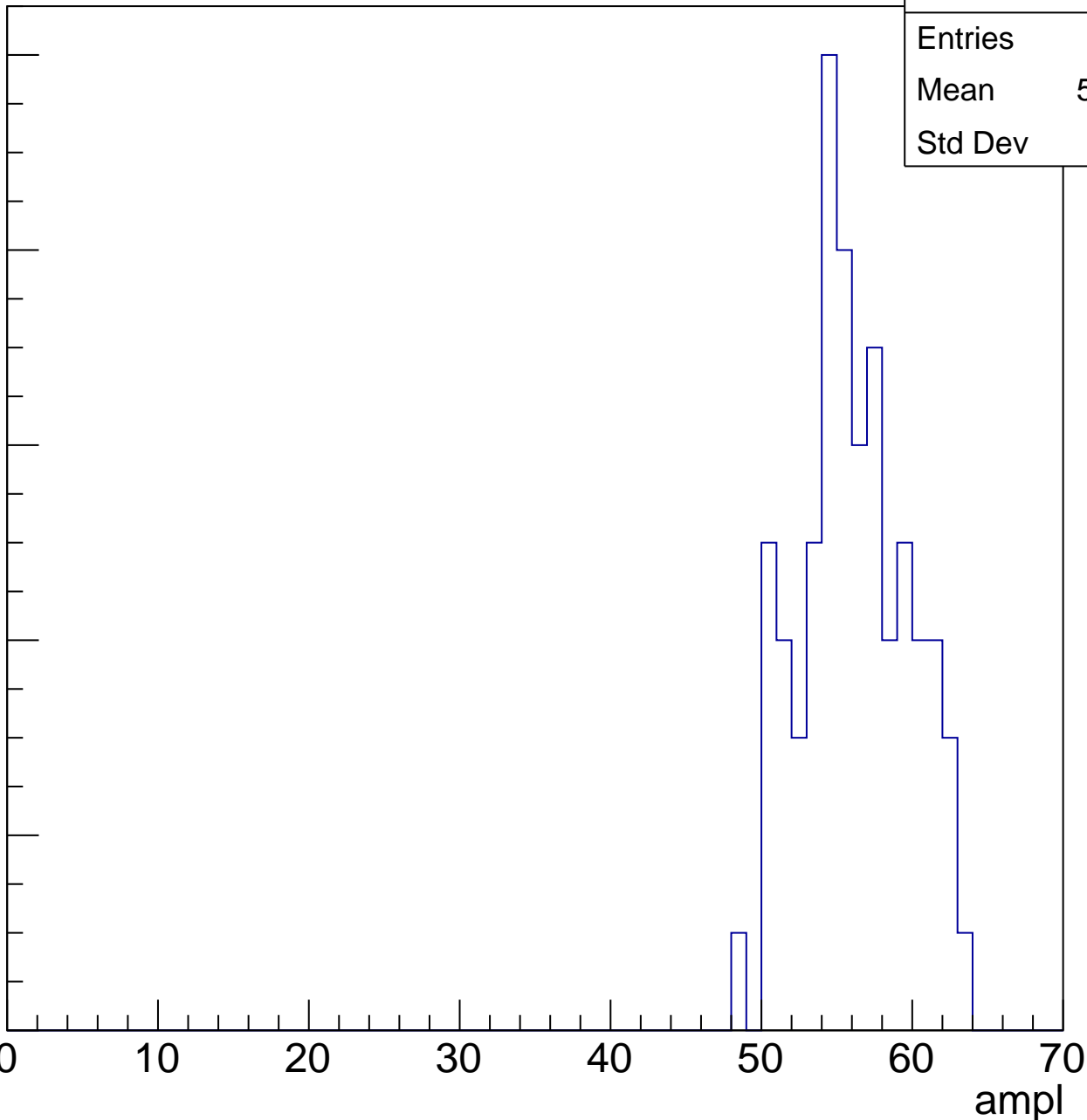
40

50

60

70

ampl

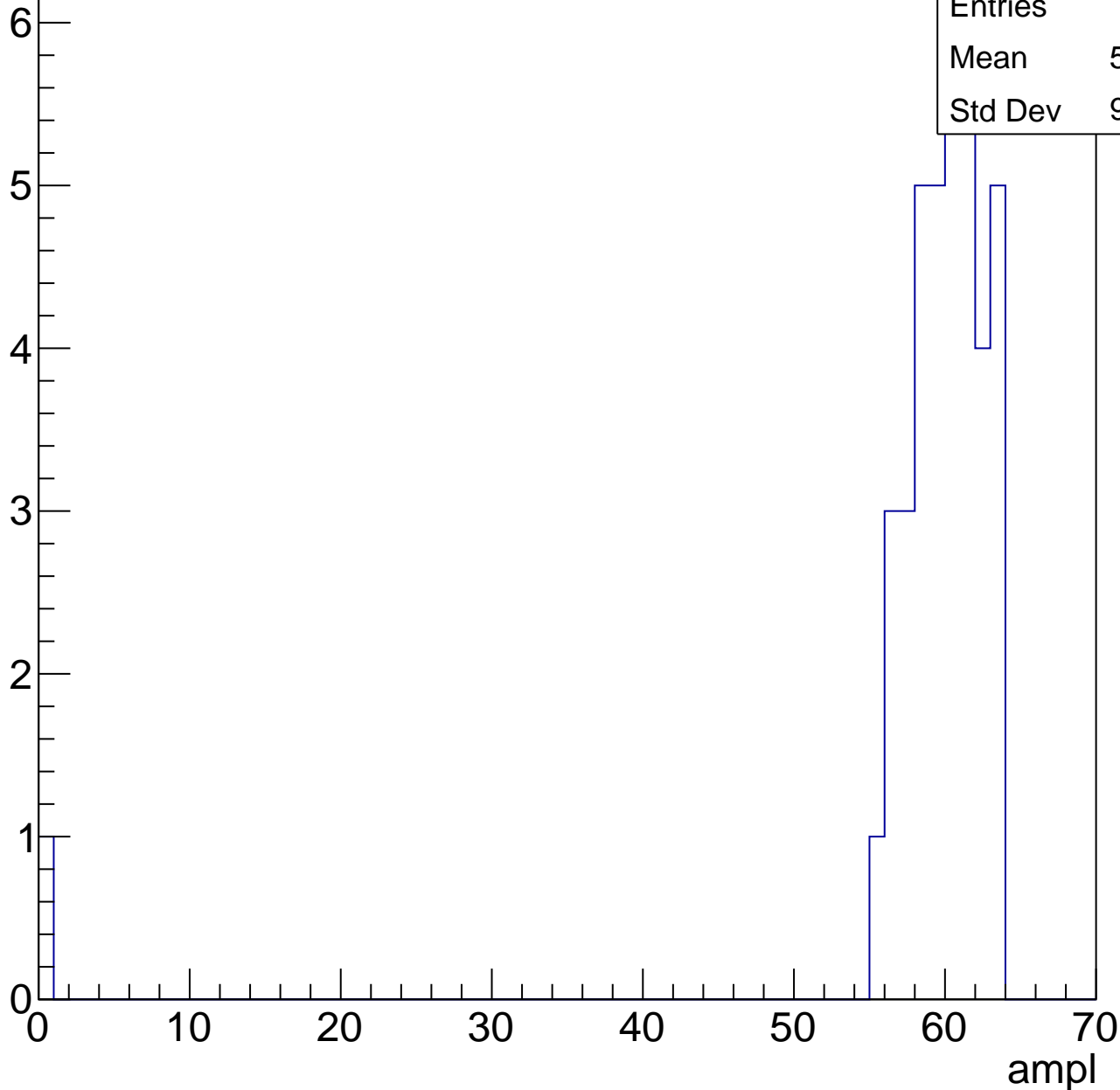


B1L100S, U14-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

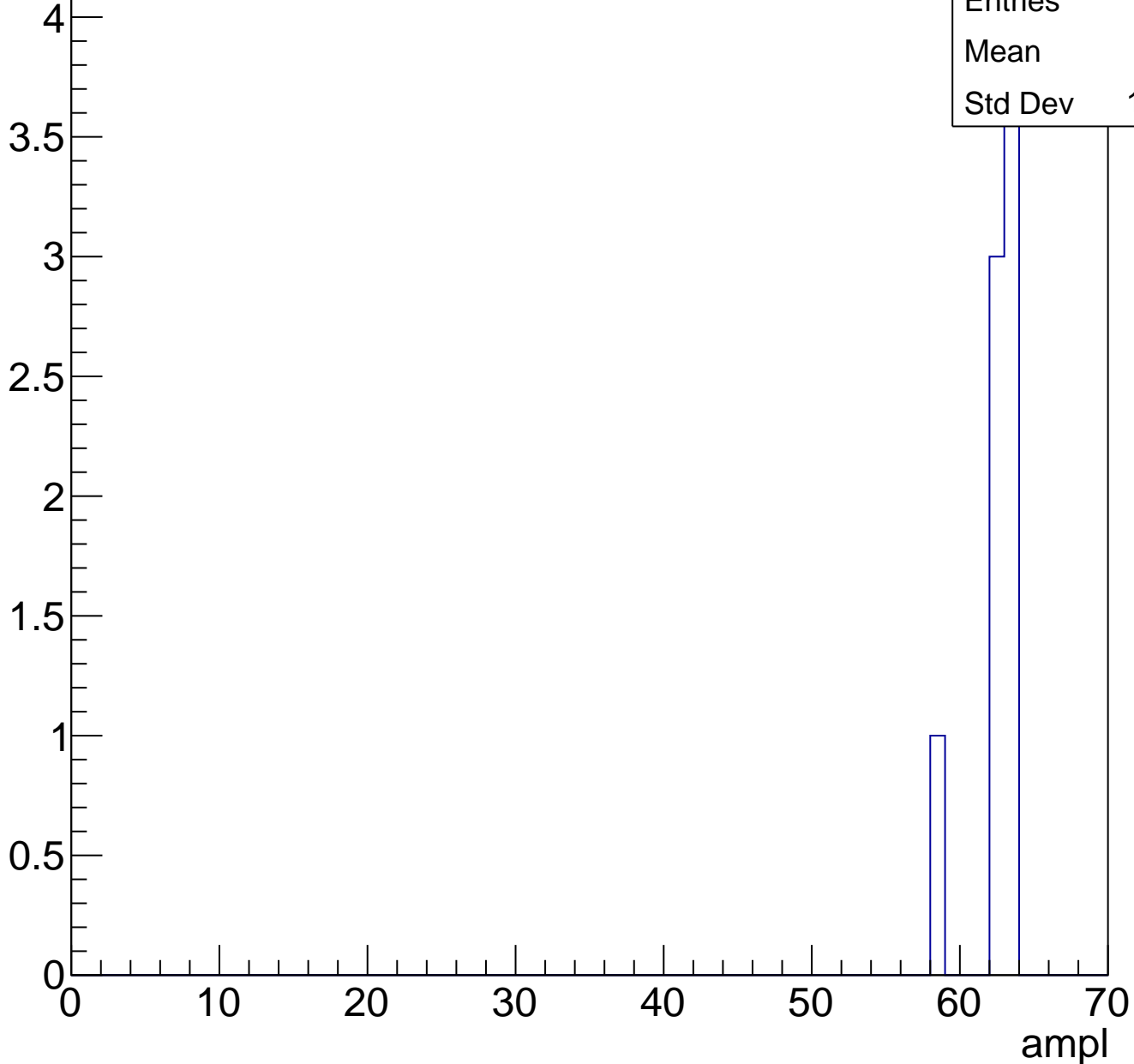
Entries	39
Mean	58.15
Std Dev	9.686



B1L100S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch24, adc0

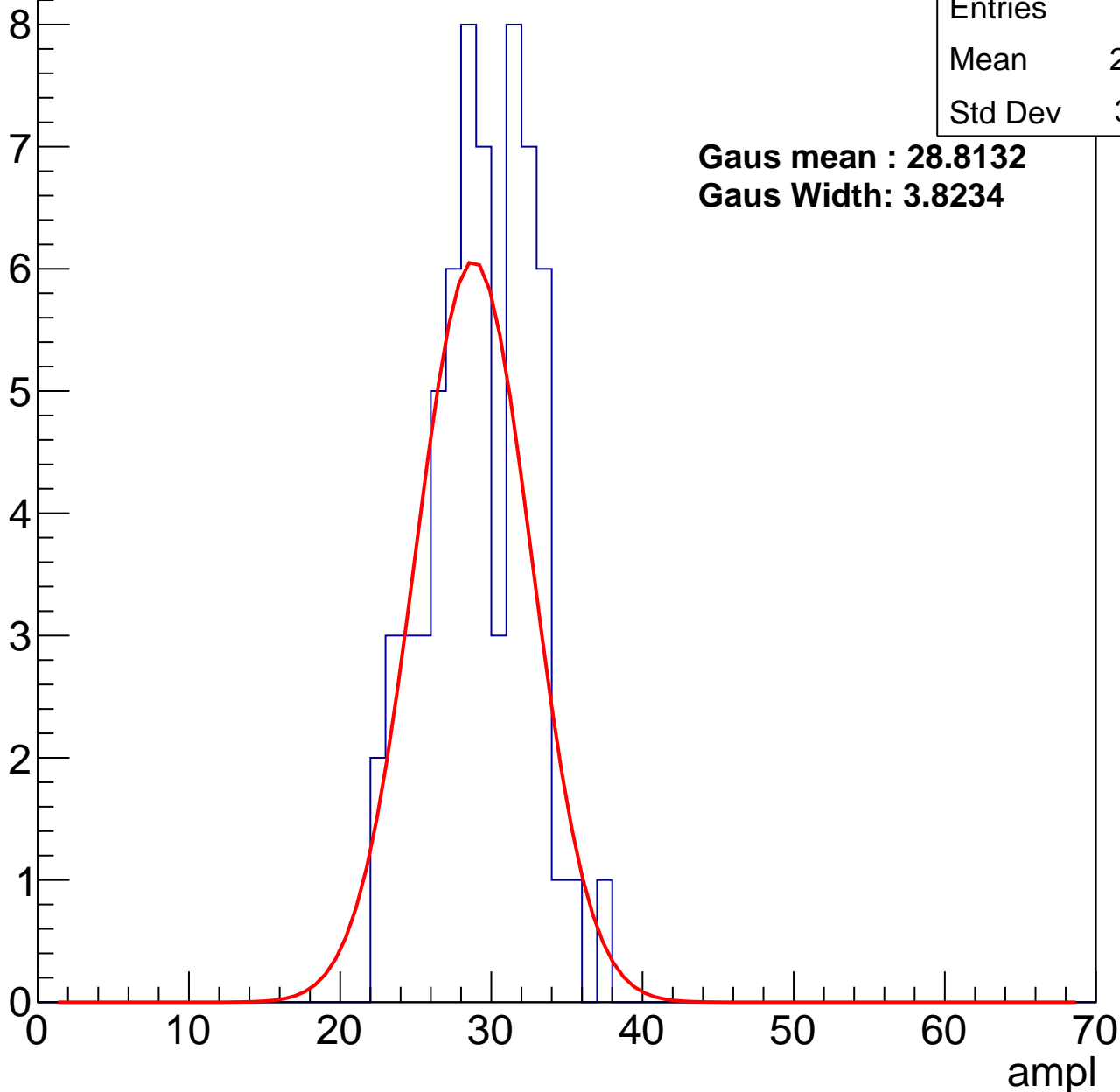
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.83
Std Dev	3.361

Gaus mean : 28.8132

Gaus Width: 3.8234



B1L100S, U14-ch24, adc1

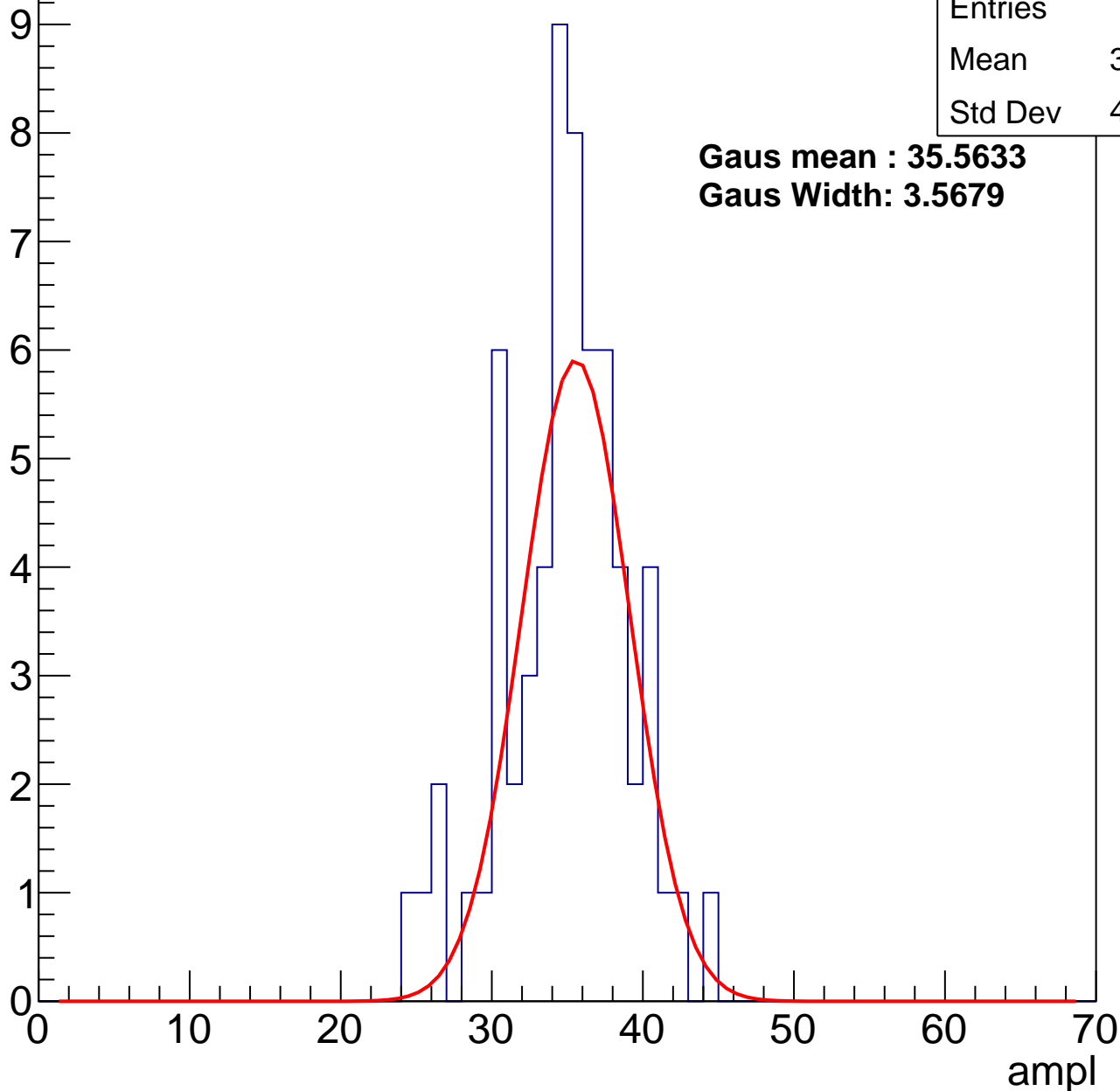
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	34.43
Std Dev	4.074

Gaus mean : 35.5633

Gaus Width: 3.5679



B1L100S, U14-ch24, adc2

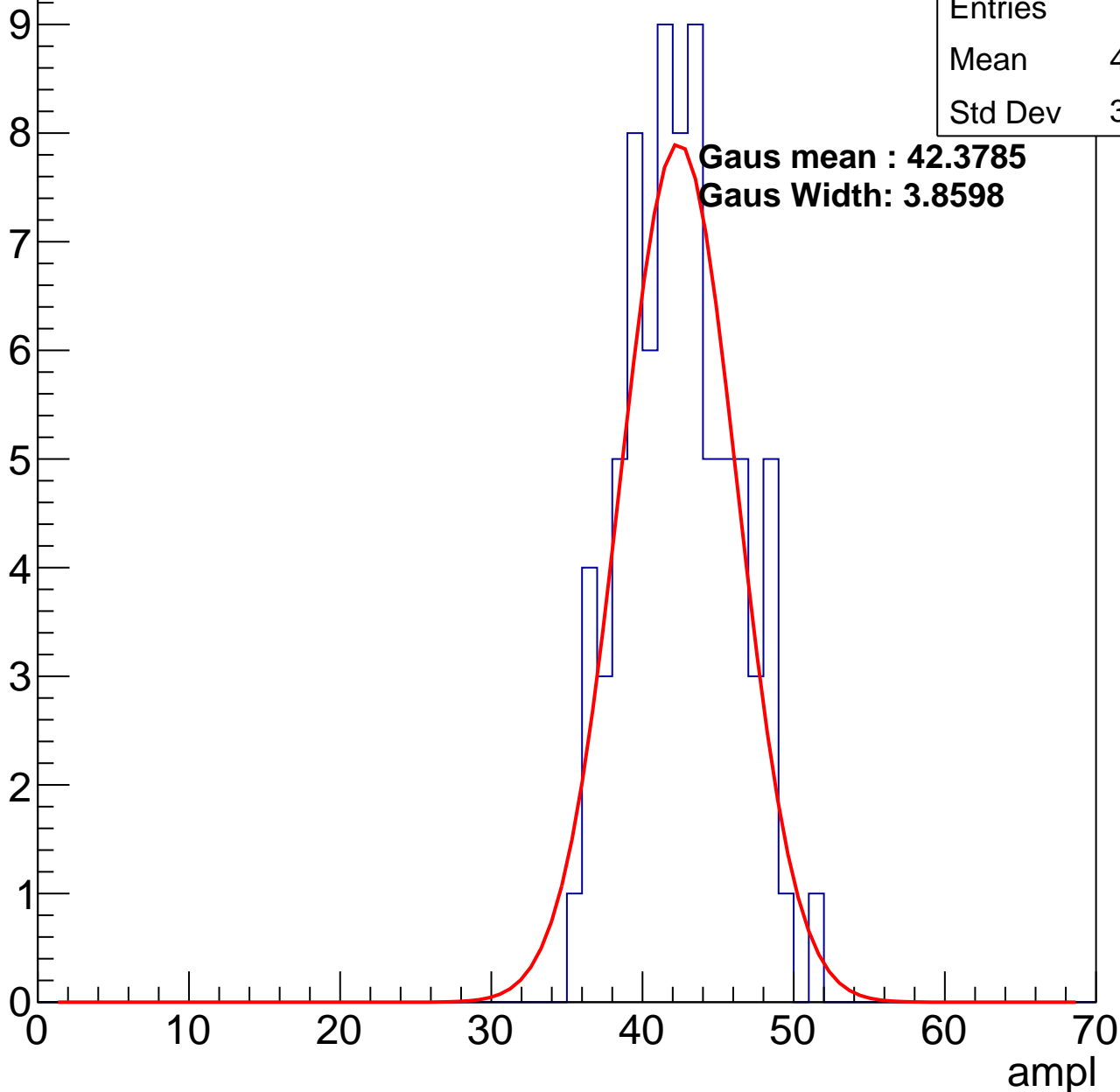
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	42.05
Std Dev	3.566

Gaus mean : 42.3785

Gaus Width: 3.8598

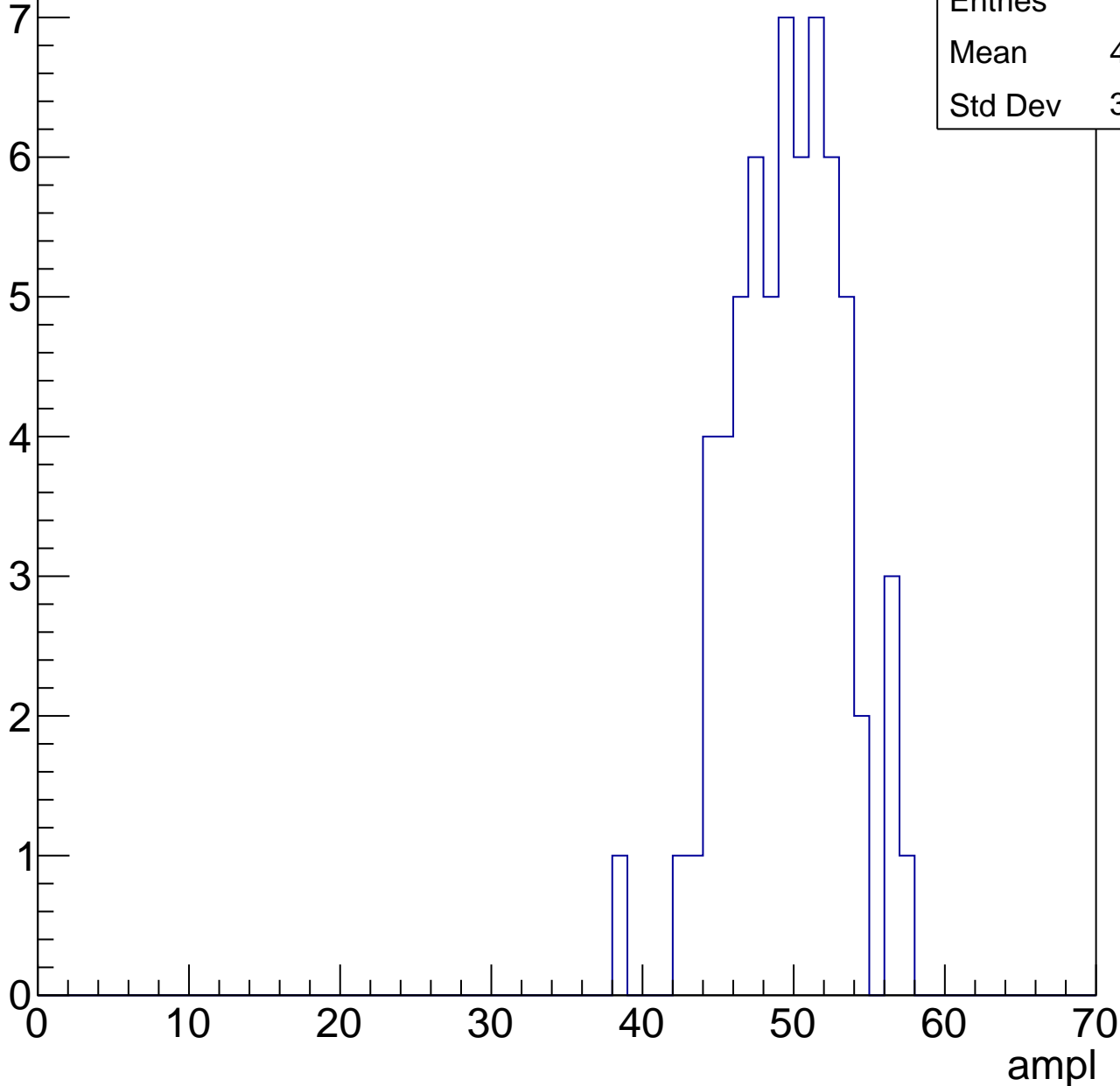


B1L100S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

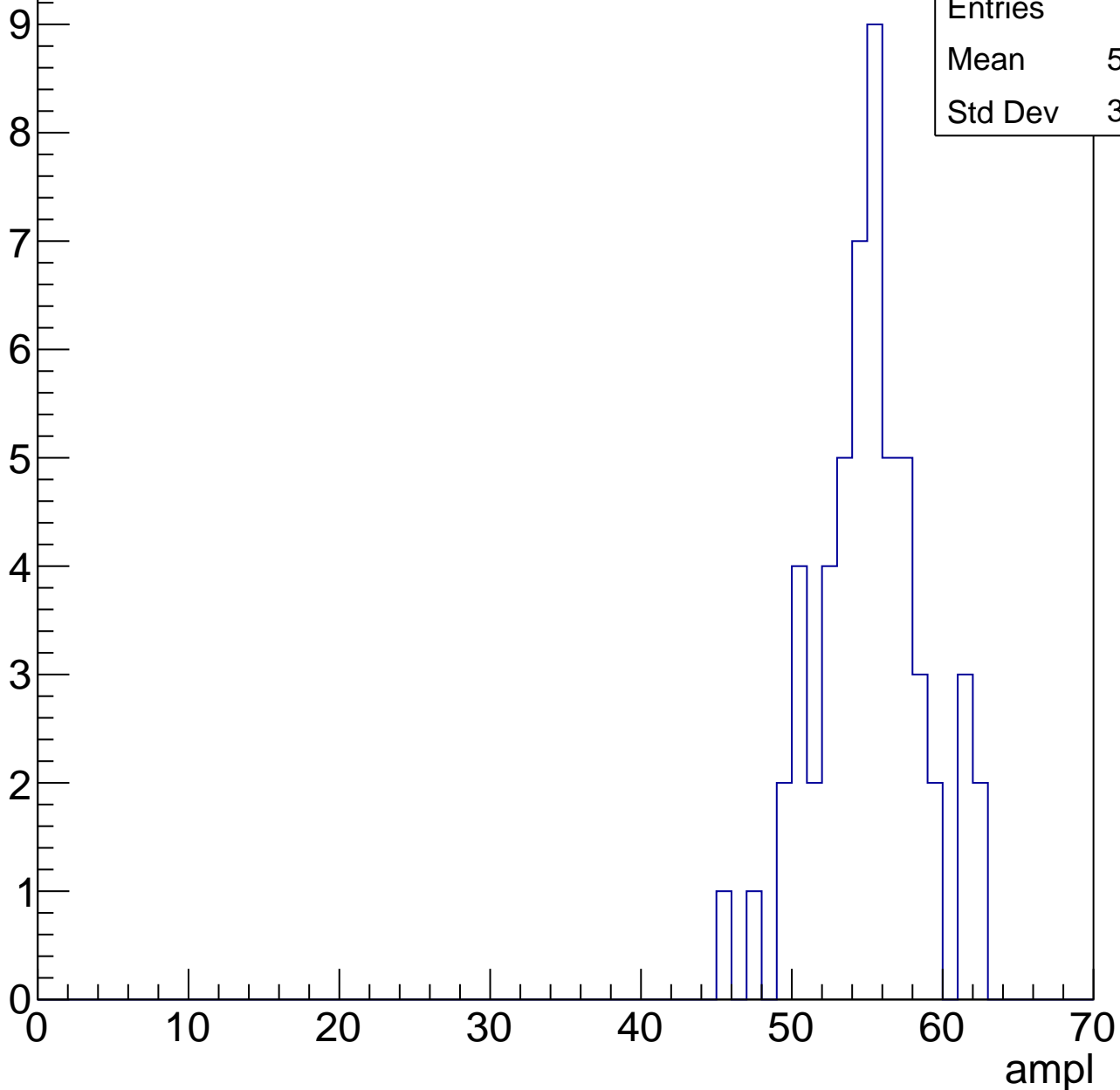
Entries	64
Mean	49.08
Std Dev	3.692



B1L100S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



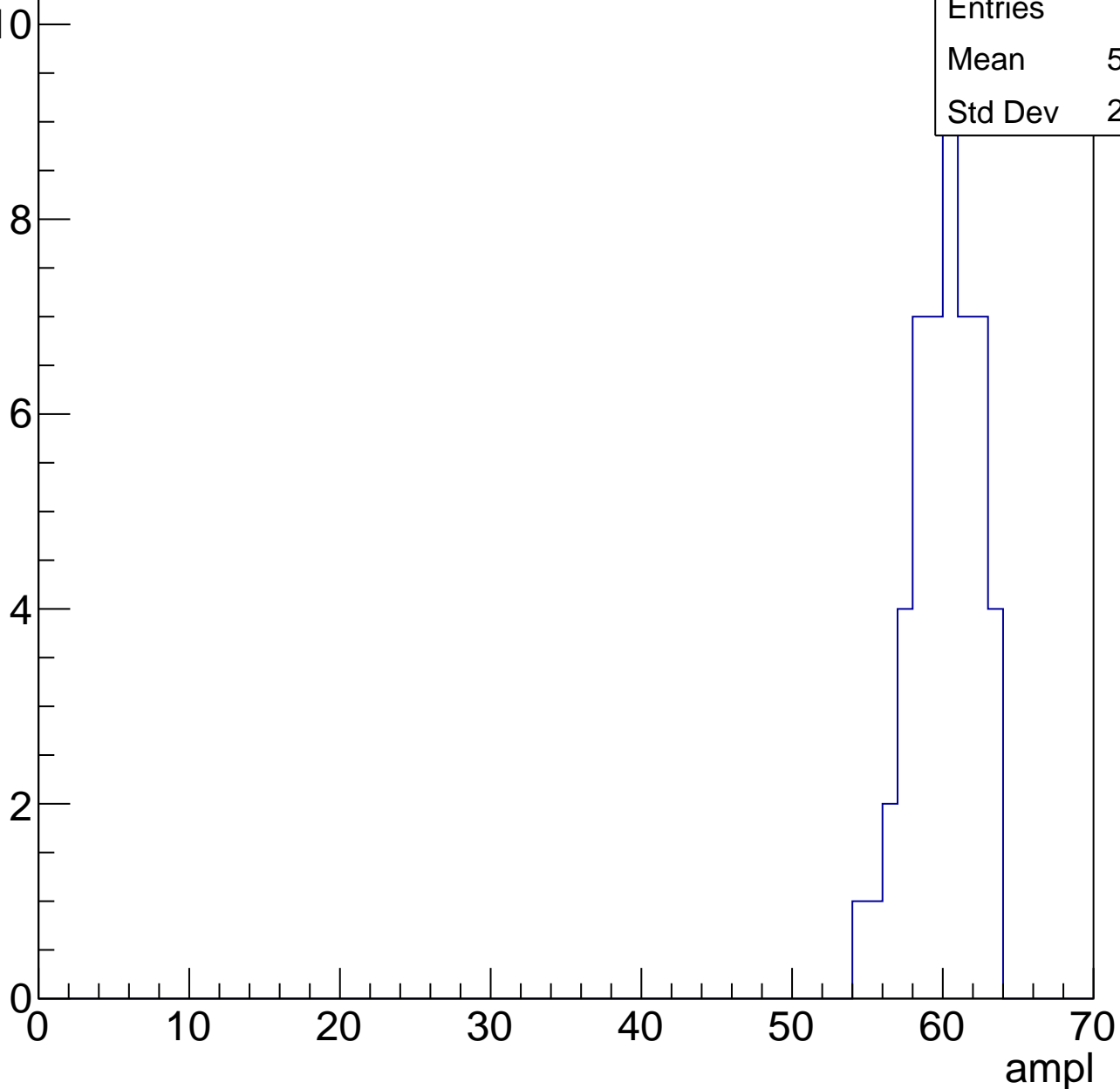
Entries	55
Mean	54.58
Std Dev	3.596

B1L100S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	59.62
Std Dev	2.134

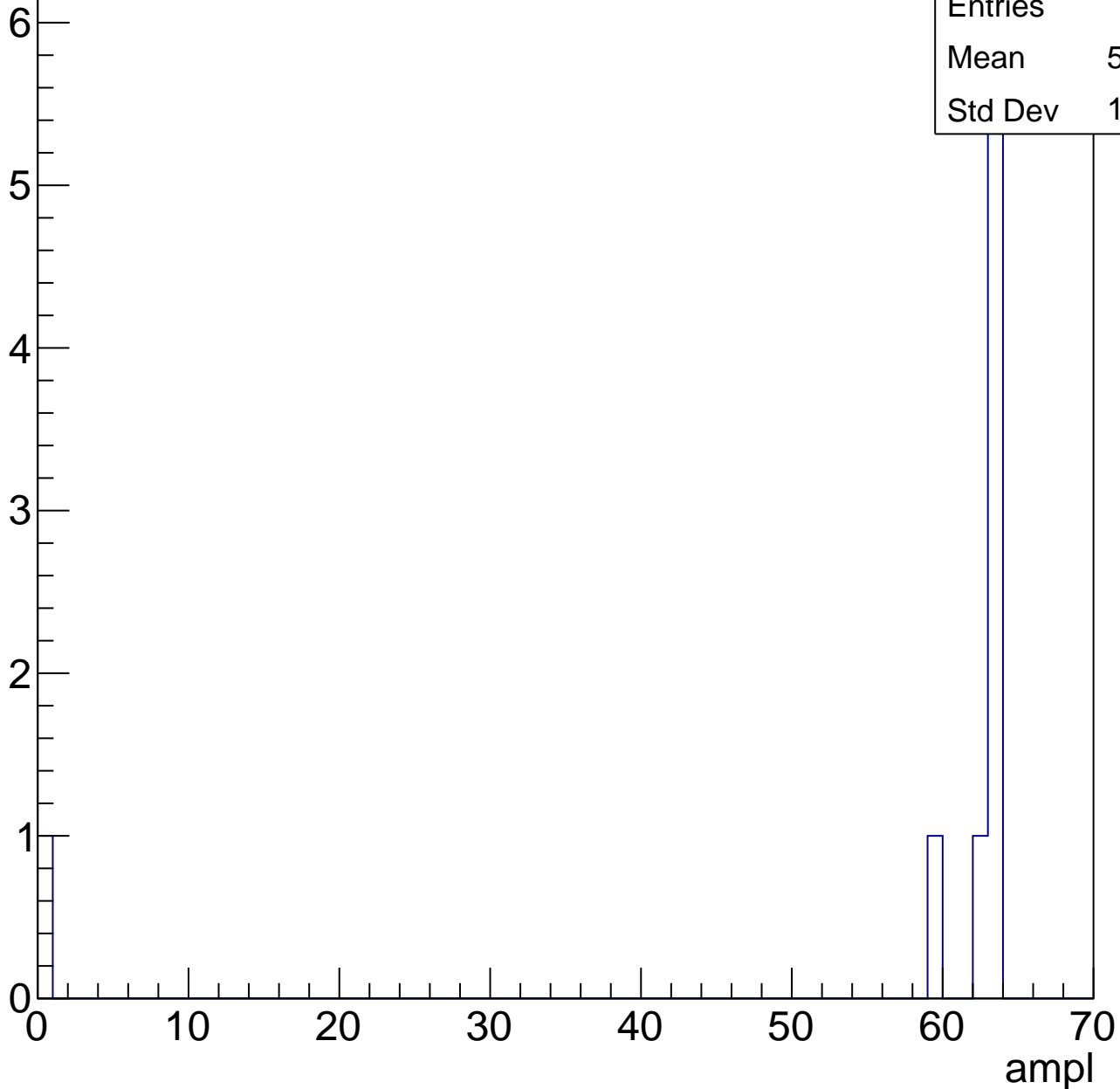


B1L100S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	55.44
Std Dev	19.64



B1L100S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch25, adc0

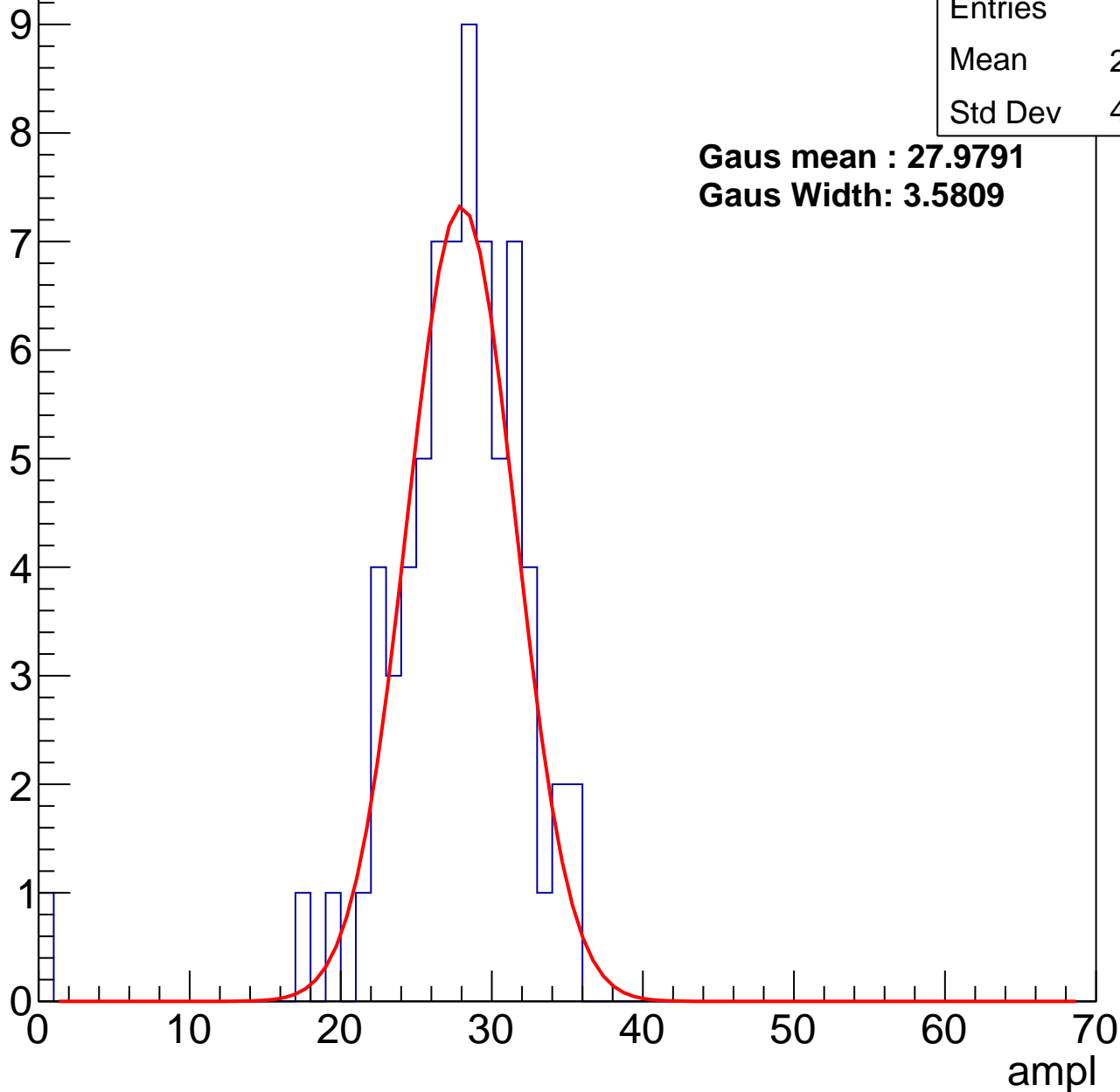
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.14
Std Dev	4.885

Gaus mean : 27.9791

Gaus Width: 3.5809



B1L100S, U14-ch25, adc1

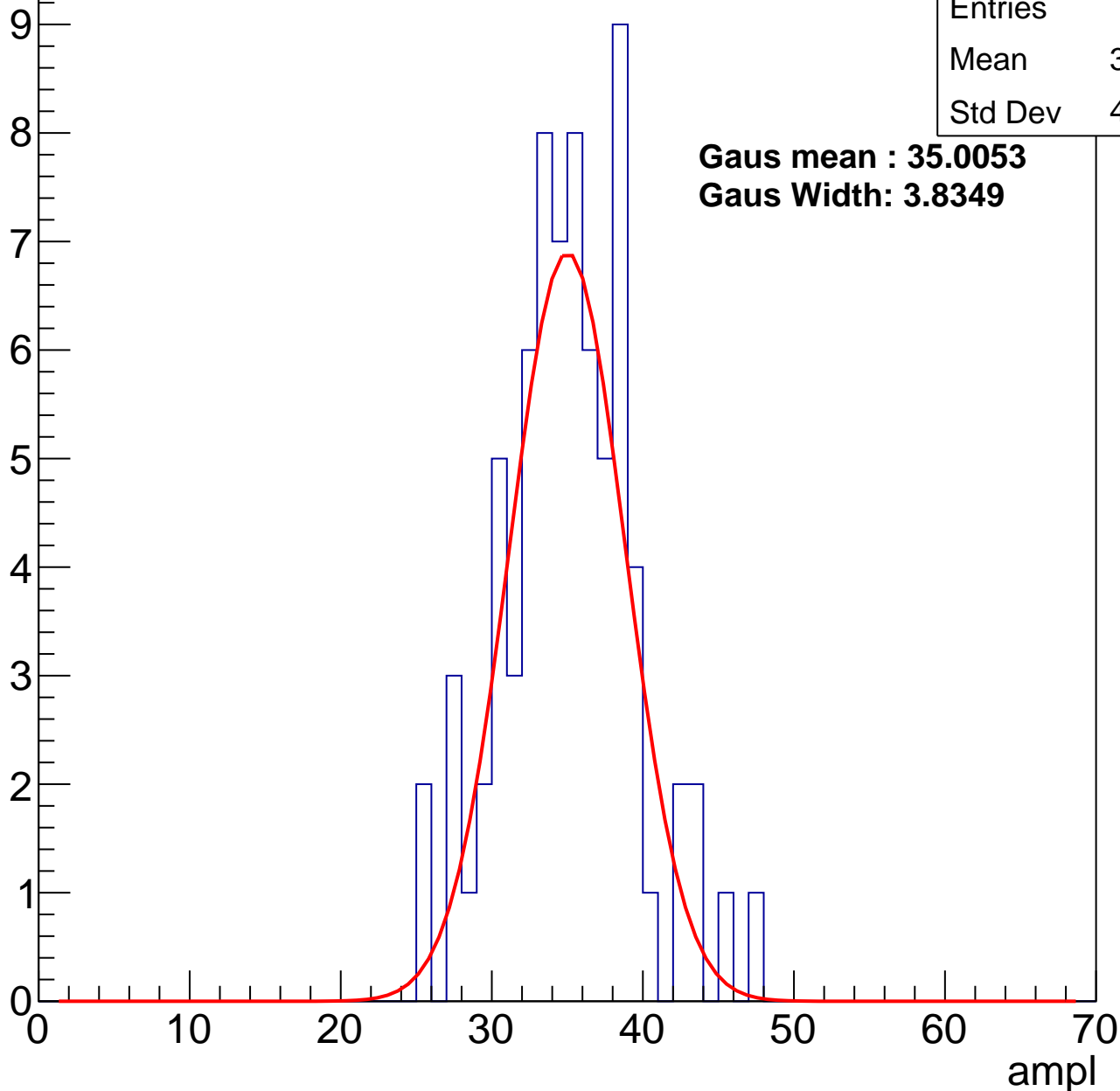
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	34.67
Std Dev	4.348

Gaus mean : 35.0053

Gaus Width: 3.8349



B1L100S, U14-ch25, adc2

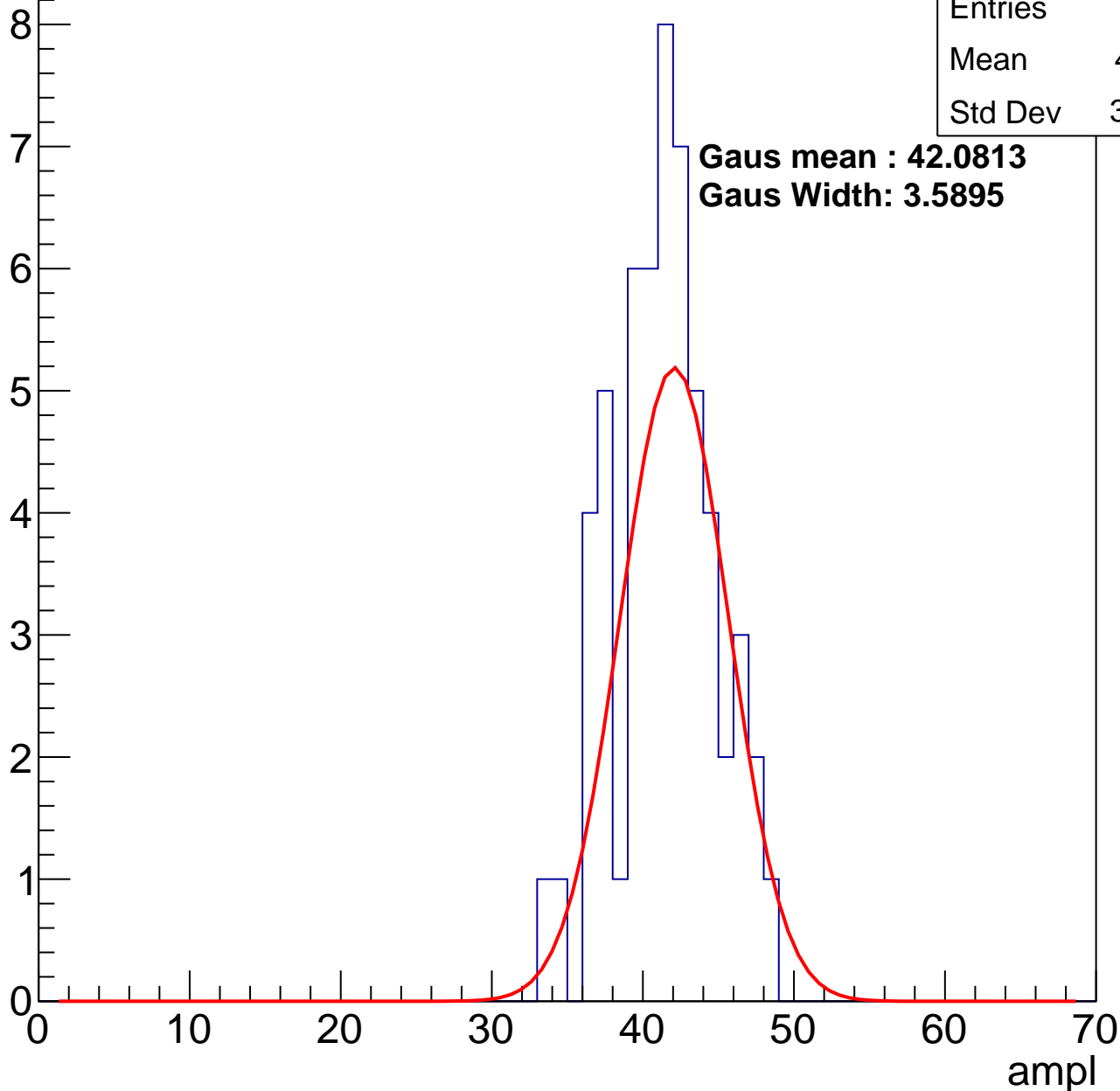
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	40.91
Std Dev	3.334

Gaus mean : 42.0813

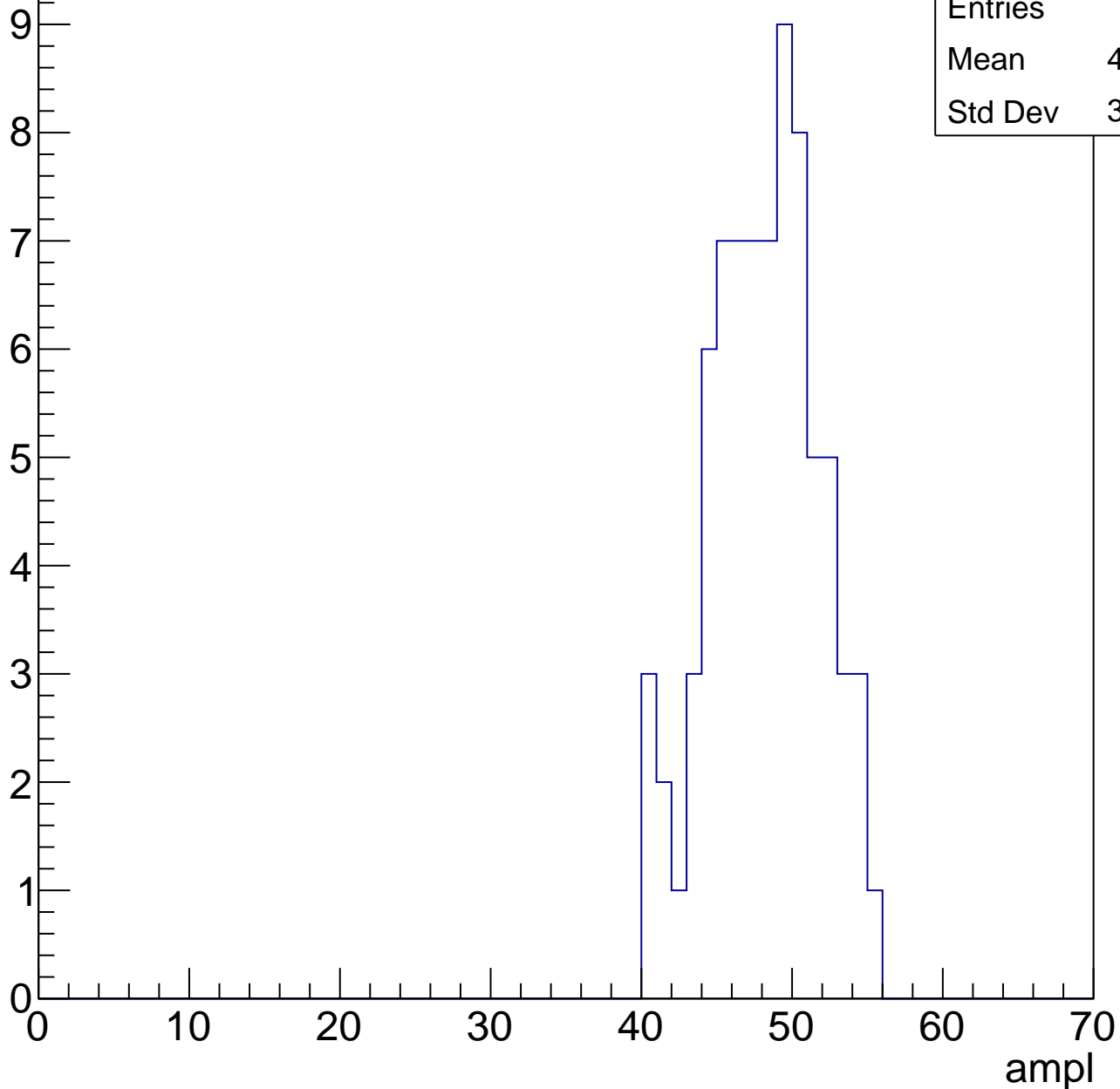
Gaus Width: 3.5895



B1L100S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

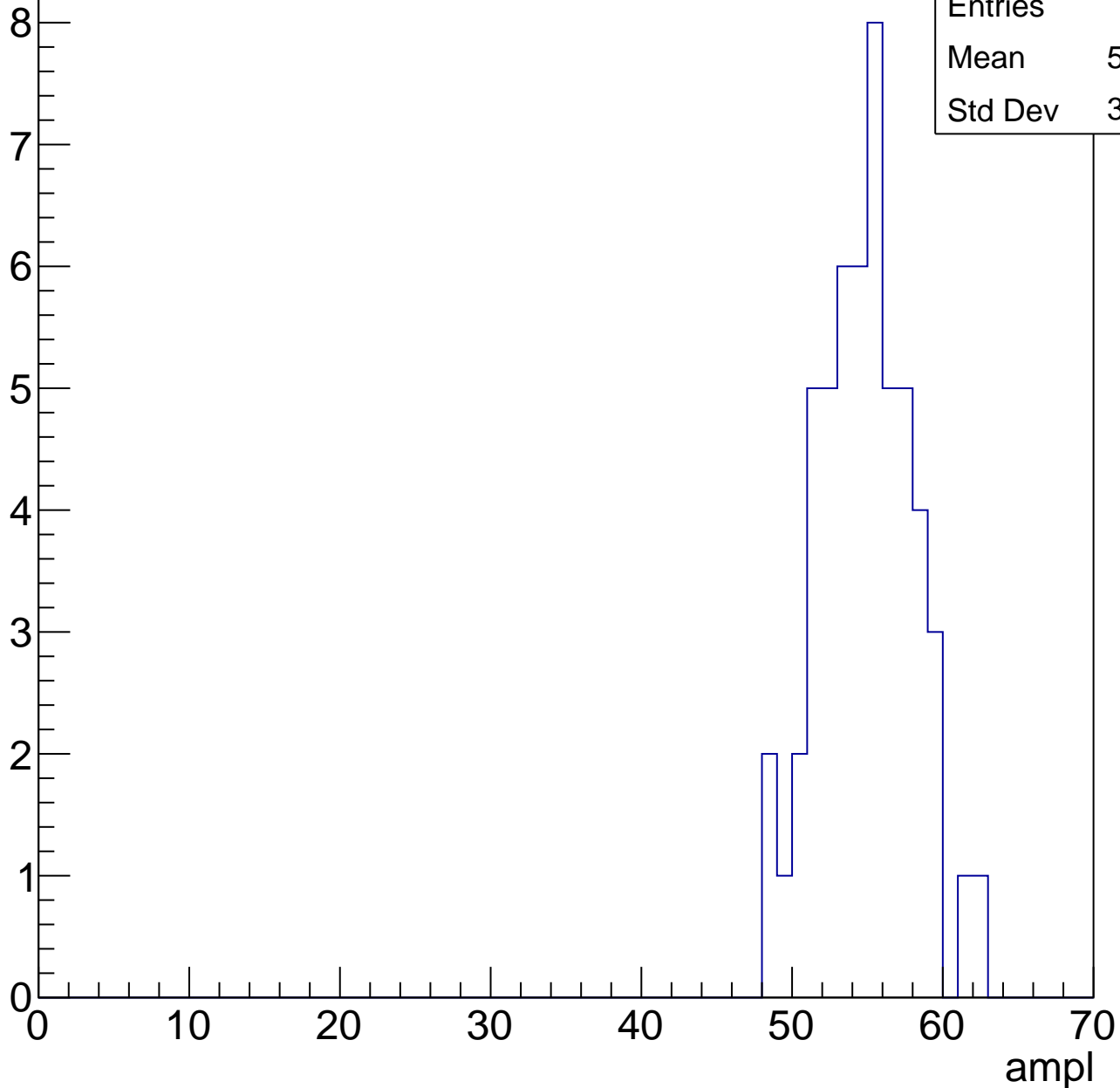


Entries	77
Mean	47.68
Std Dev	3.576

B1L100S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

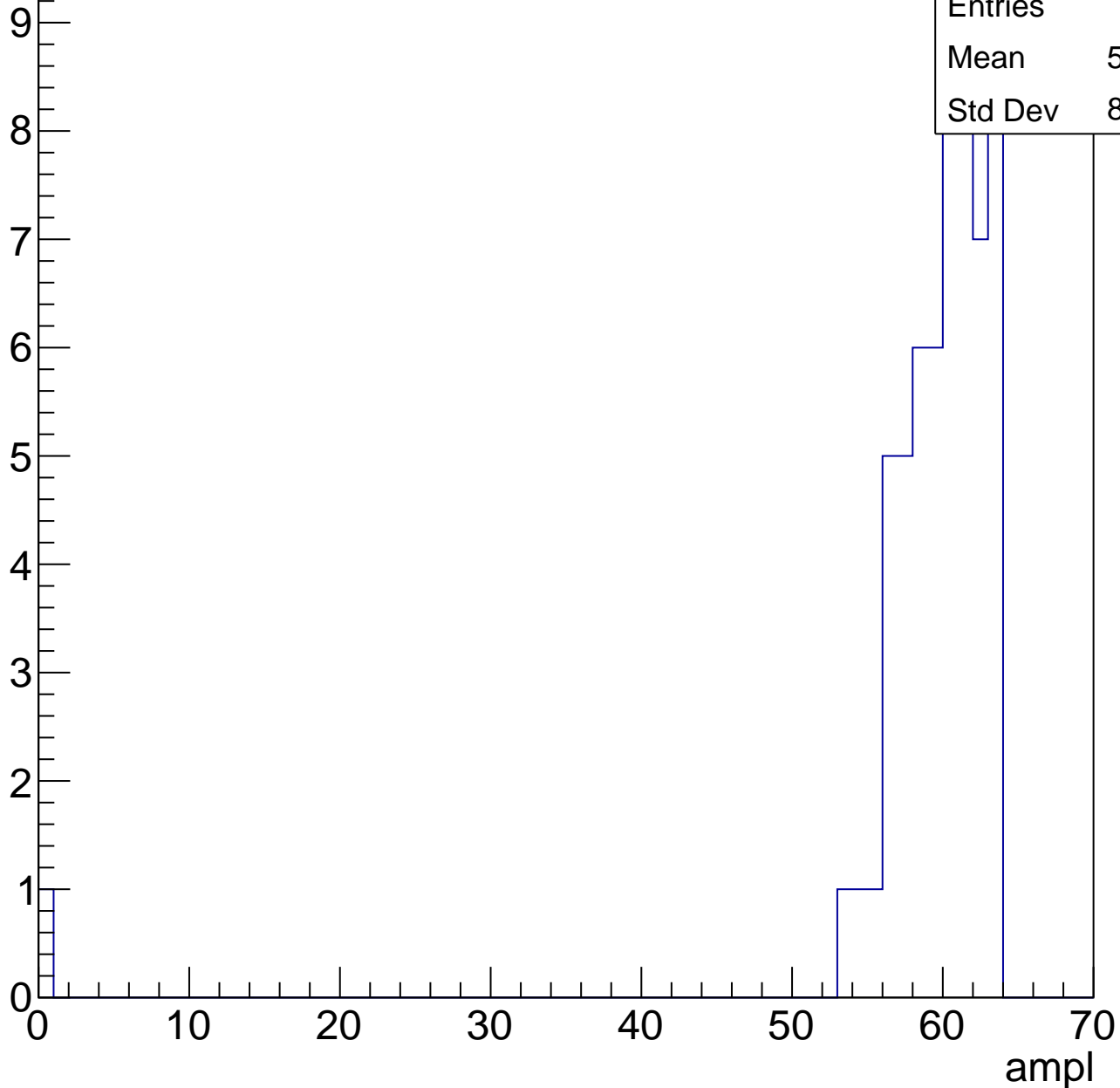
Entry



B1L100S, U14-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch25, adc6

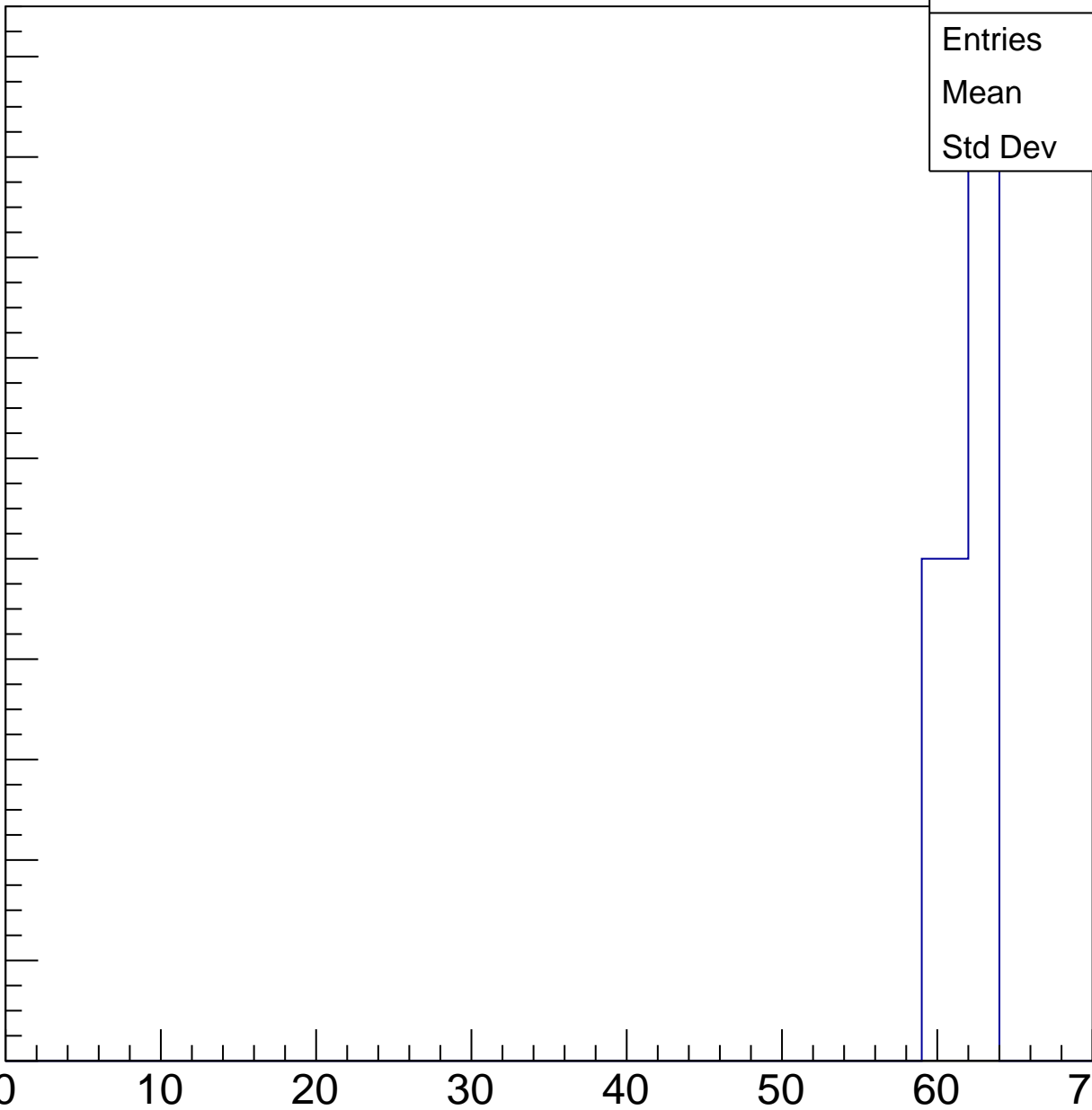
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.43
Std Dev	1.4

ampl



B1L100S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U14-ch26, adc0

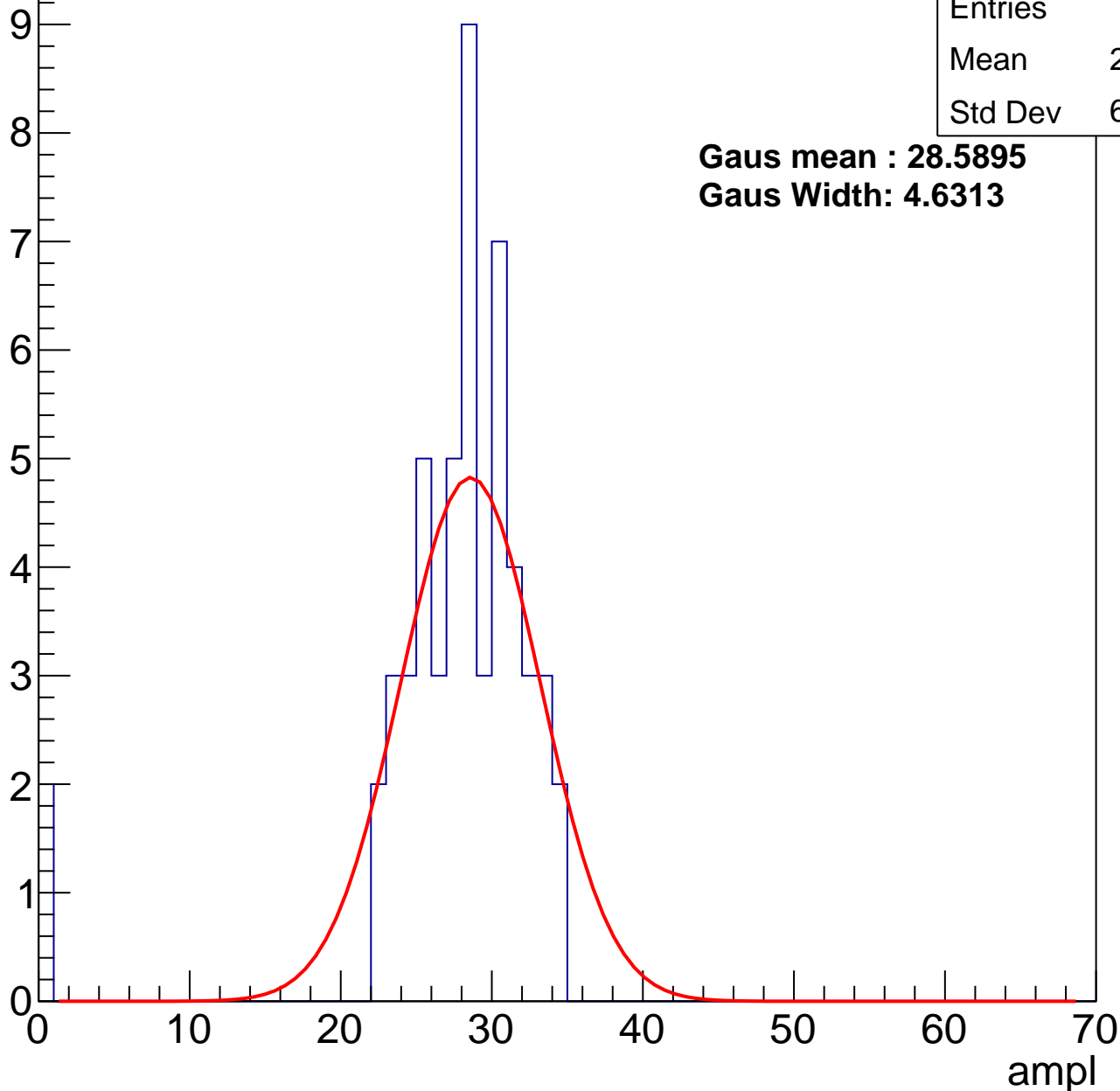
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	27.02
Std Dev	6.139

Gaus mean : 28.5895

Gaus Width: 4.6313



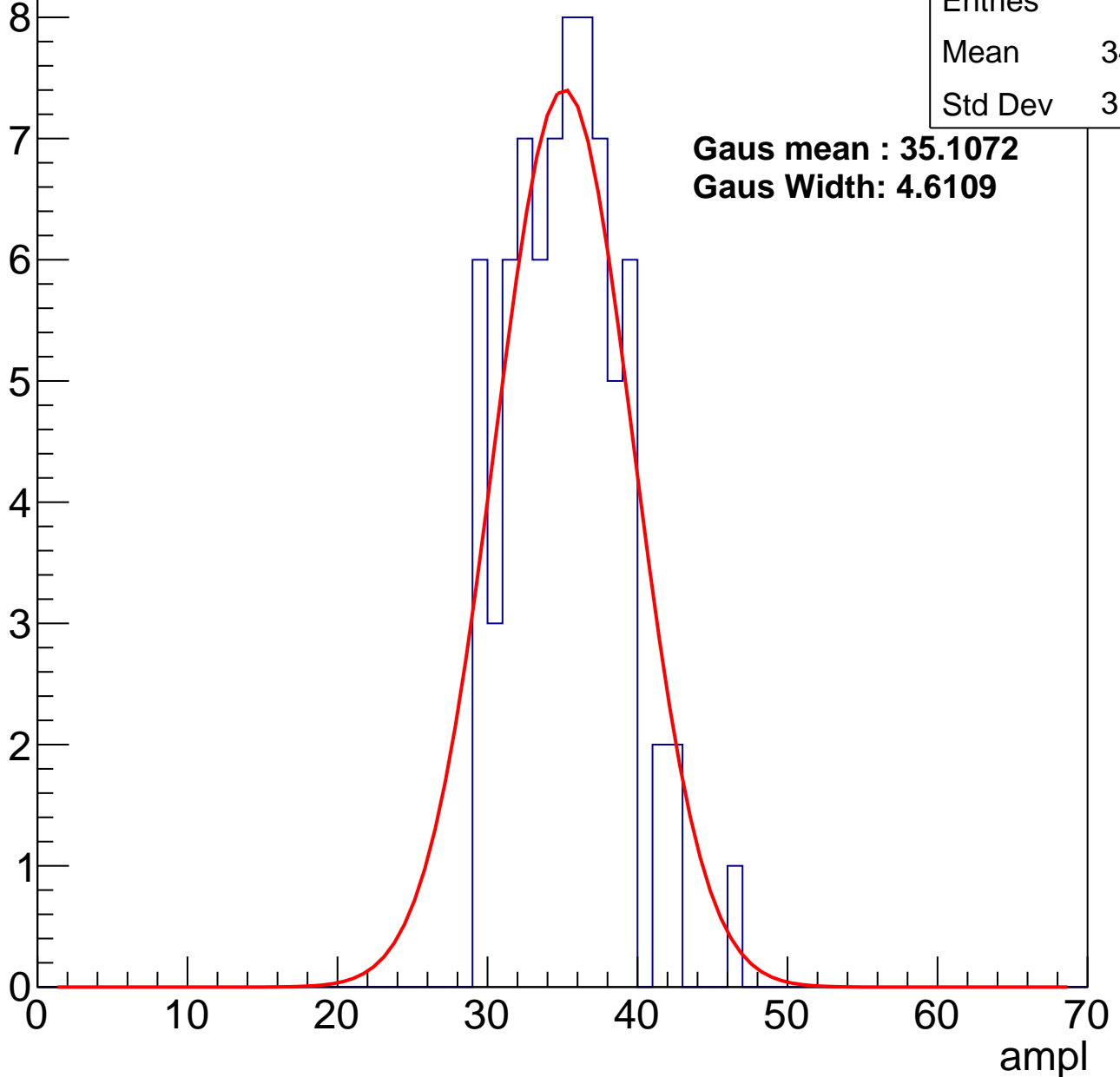
B1L100S, U14-ch26, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	34.77
Std Dev	3.574

Gaus mean : 35.1072
Gaus Width: 4.6109



B1L100S, U14-ch26, adc2

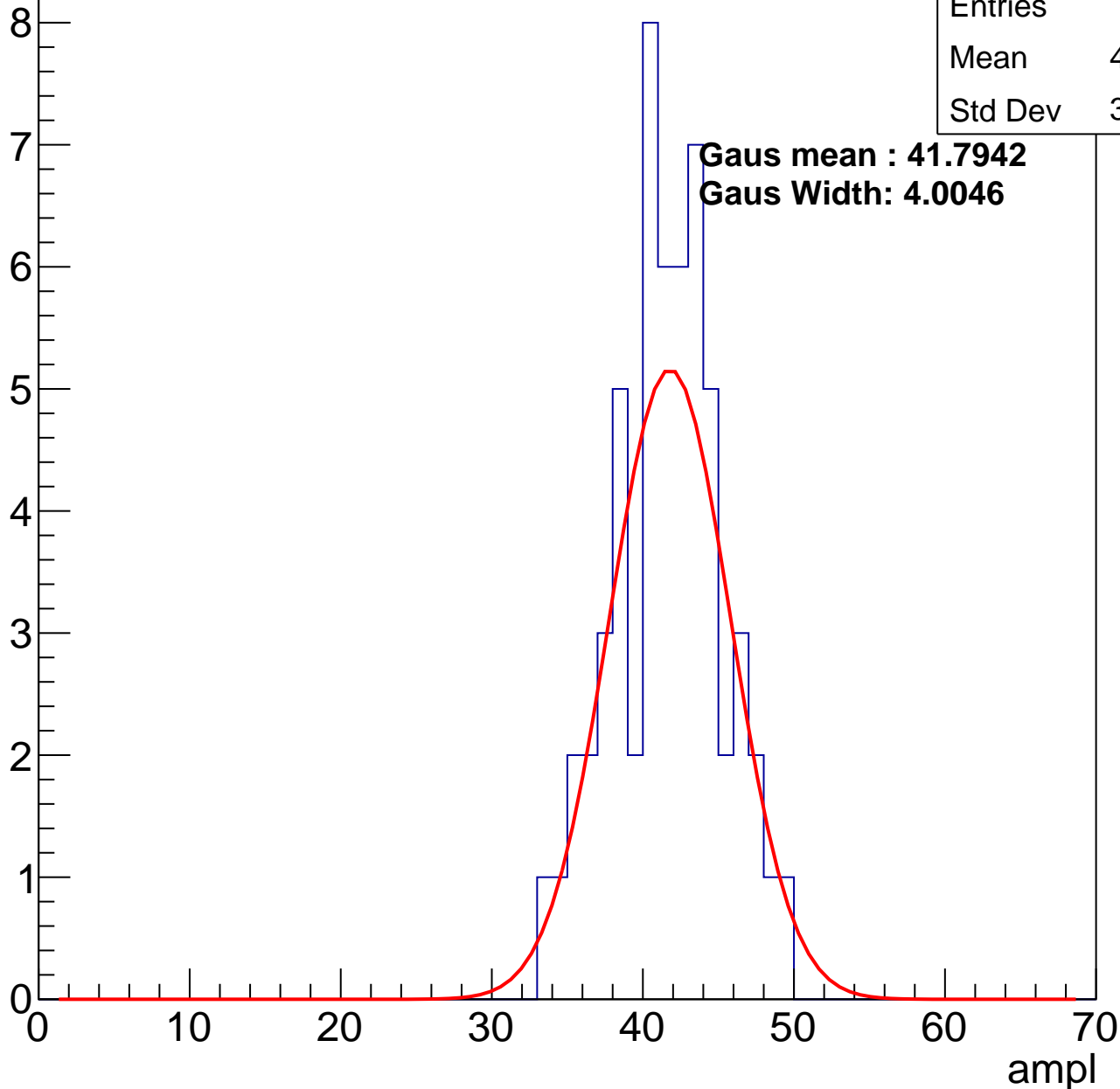
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	41.16
Std Dev	3.538

Gaus mean : 41.7942

Gaus Width: 4.0046

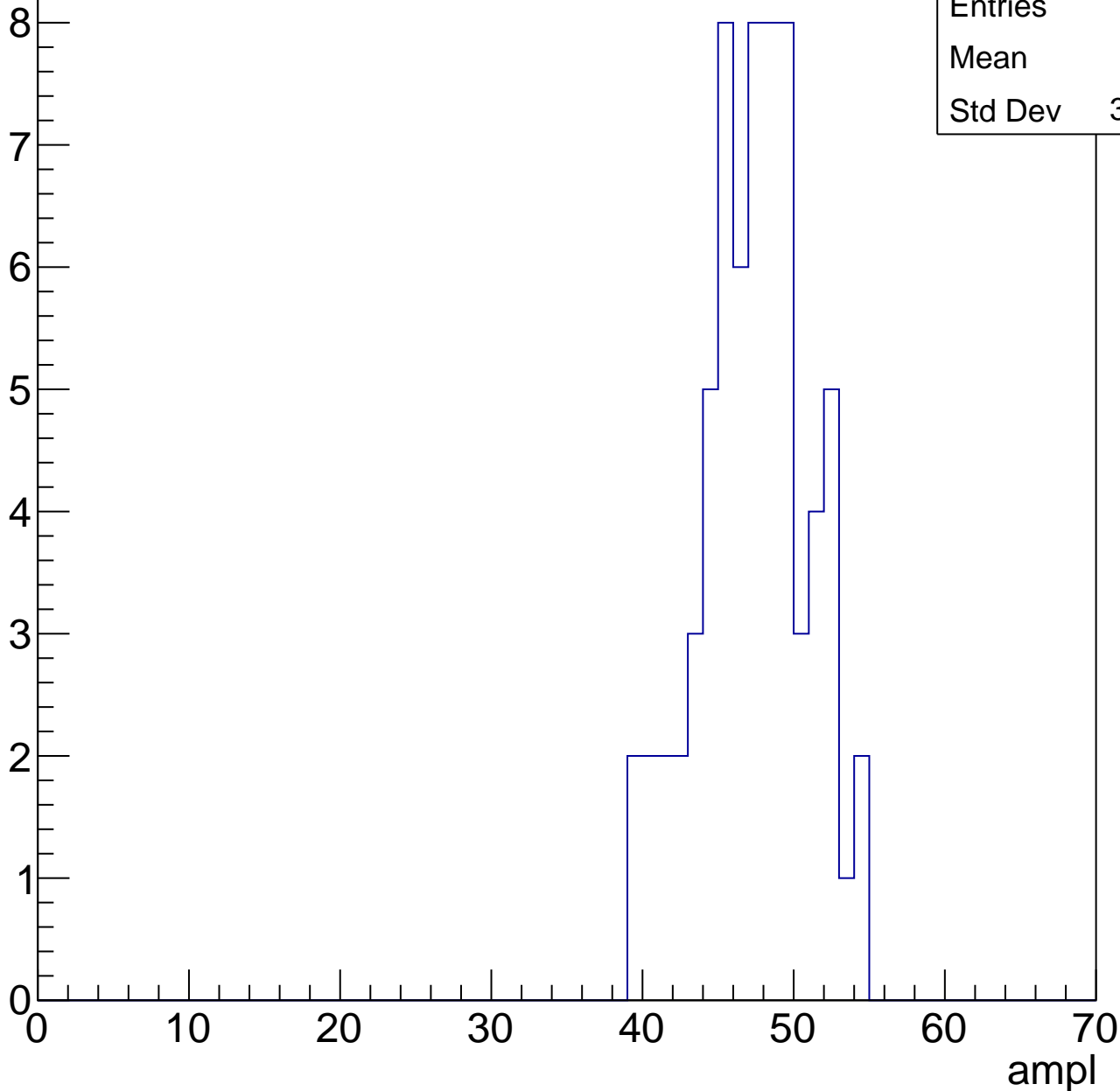


B1L100S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	46.9
Std Dev	3.547

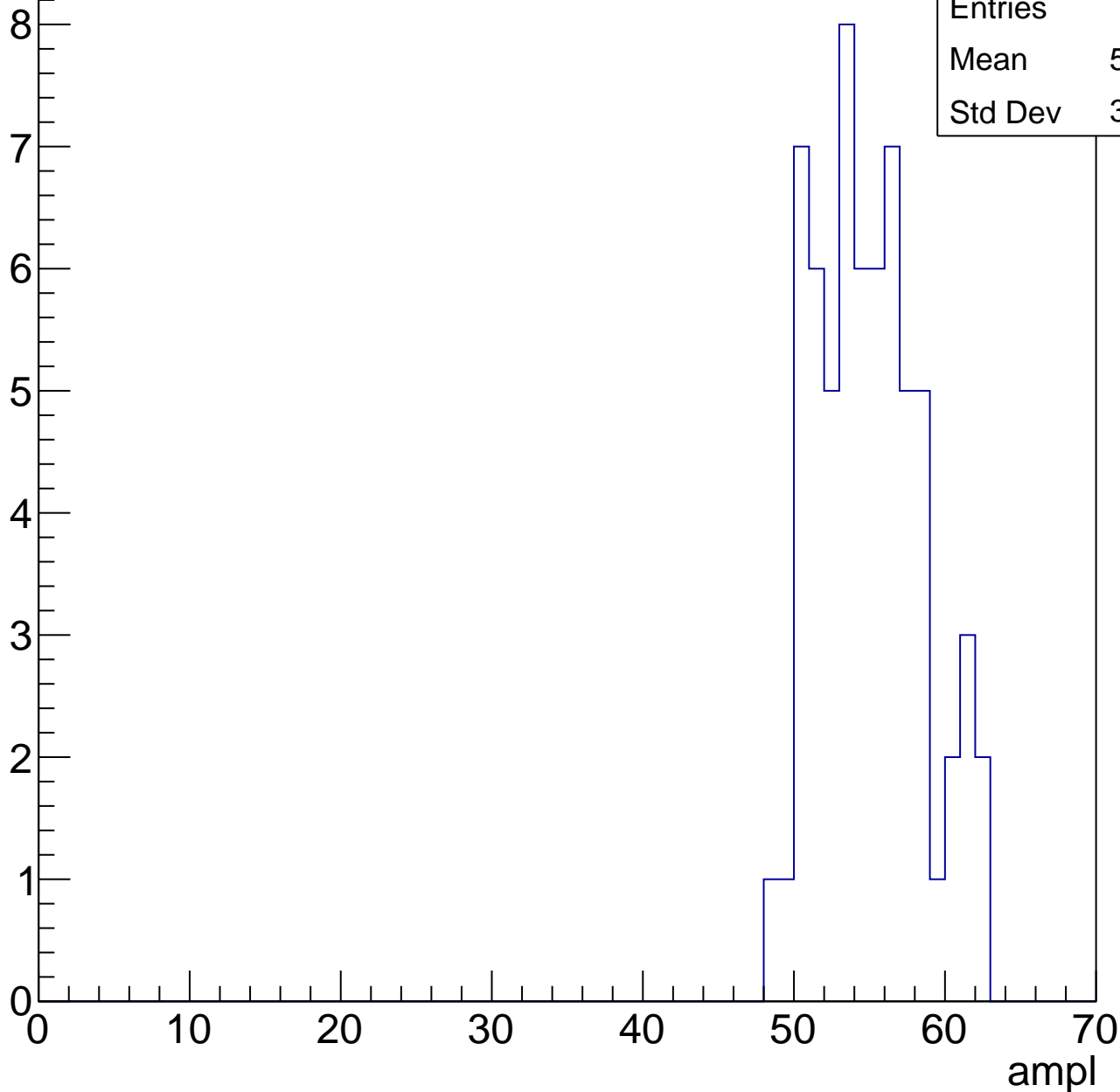


B1L100S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	54.52
Std Dev	3.438

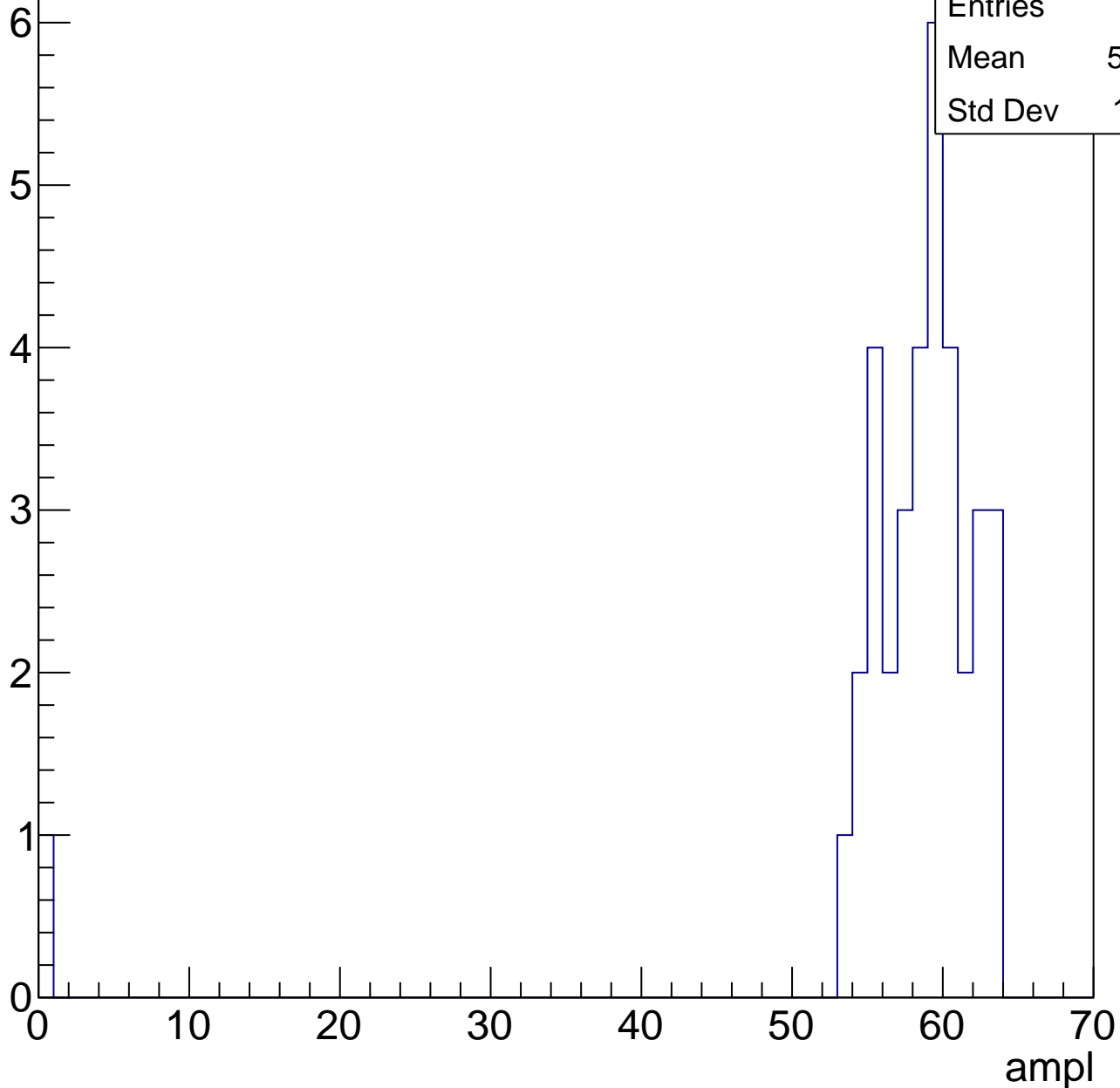


B1L100S, U14-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	56.77
Std Dev	10.11

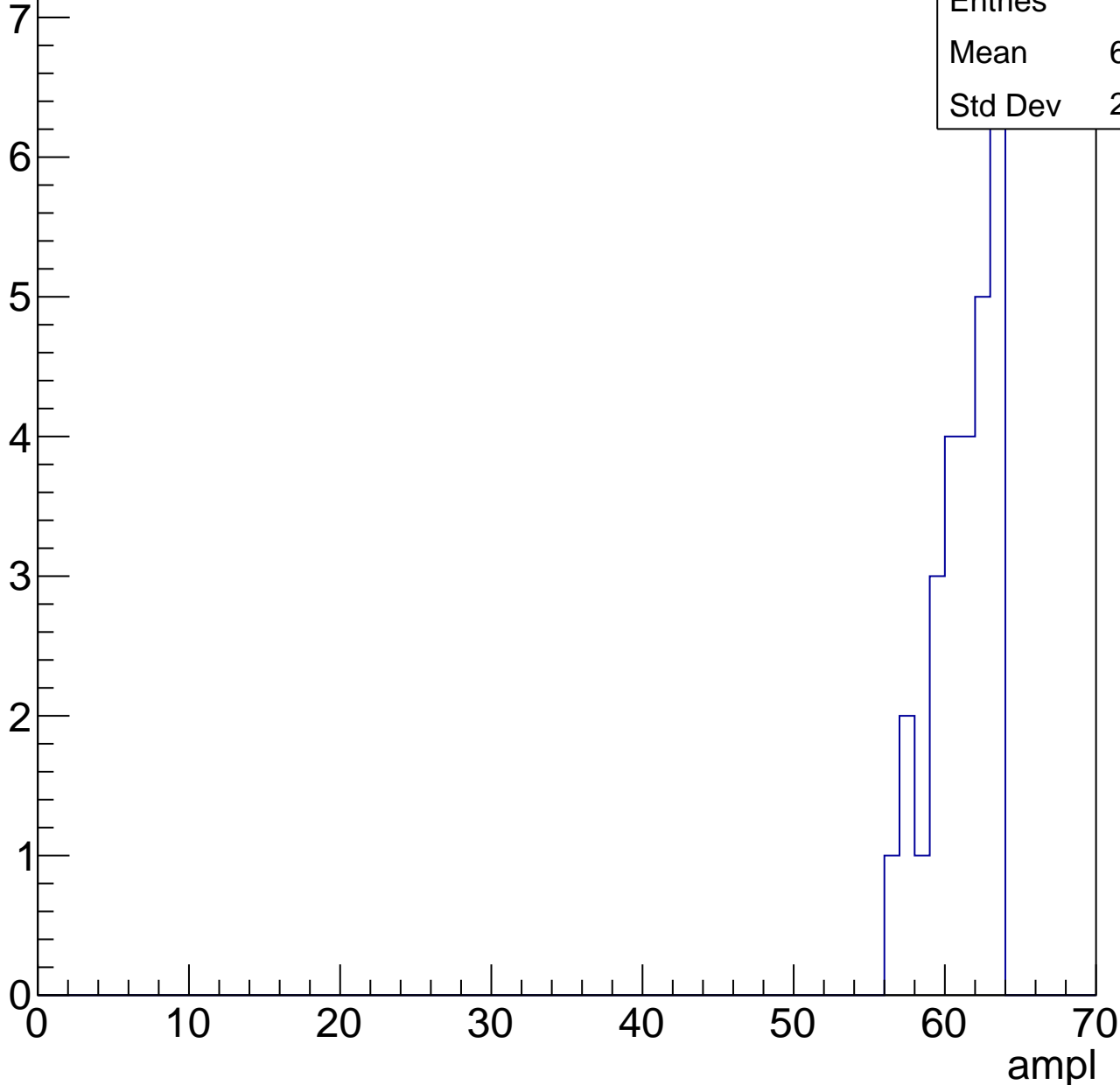


B1L100S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

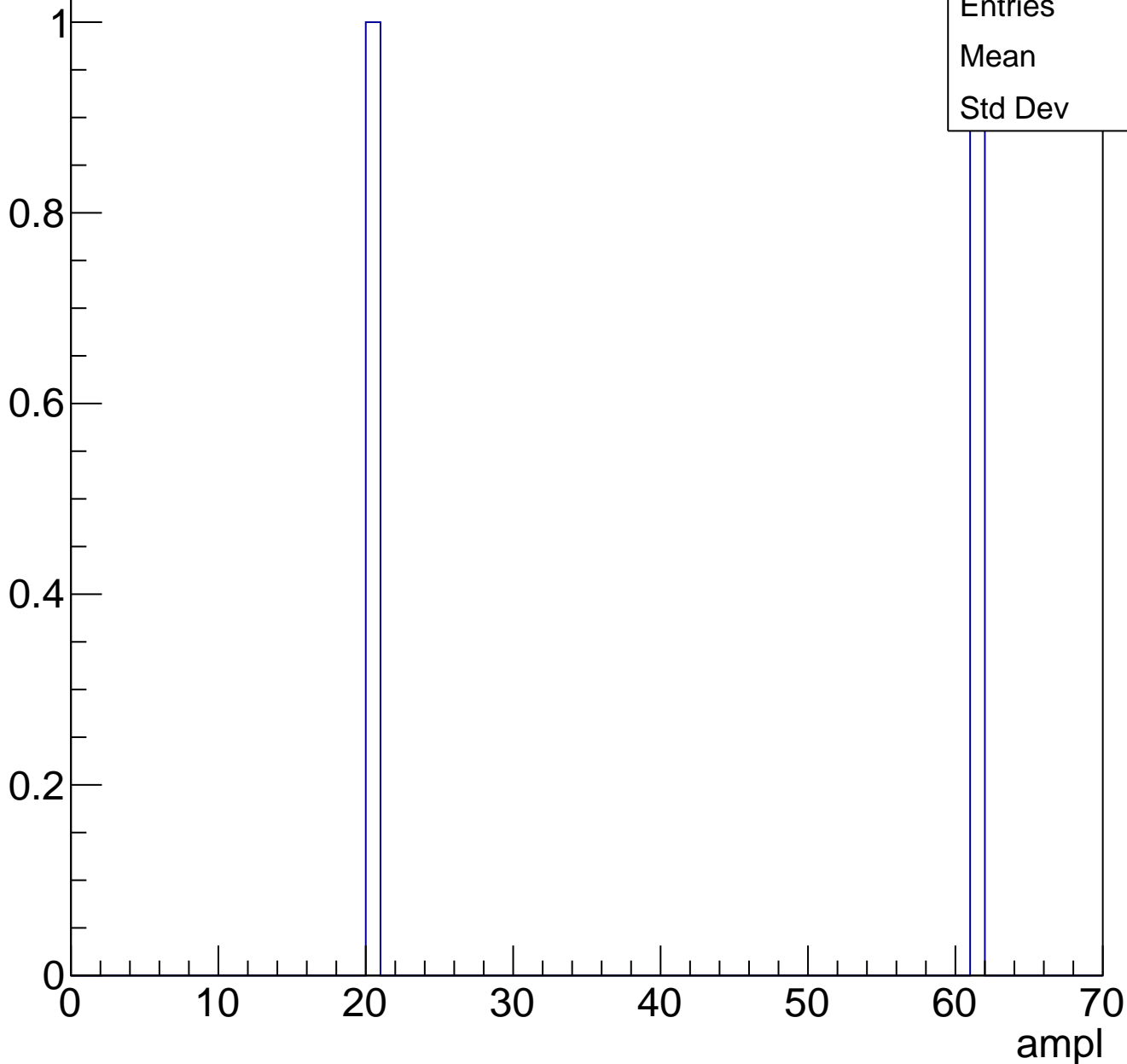
Entries	27
Mean	60.74
Std Dev	2.047



B1L100S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch27, adc0

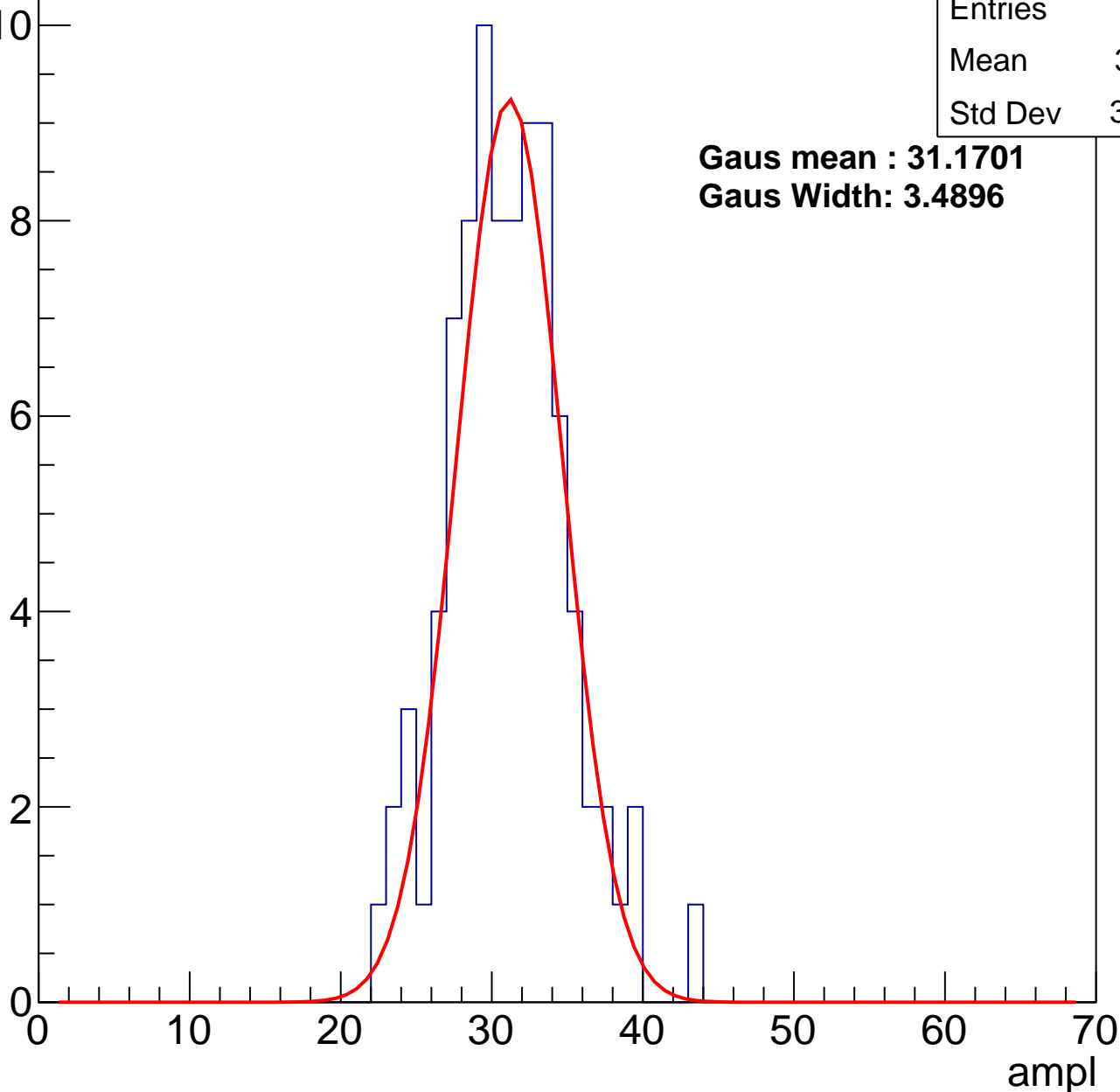
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	30.61
Std Dev	3.868

Gaus mean : 31.1701

Gaus Width: 3.4896



B1L100S, U14-ch27, adc1

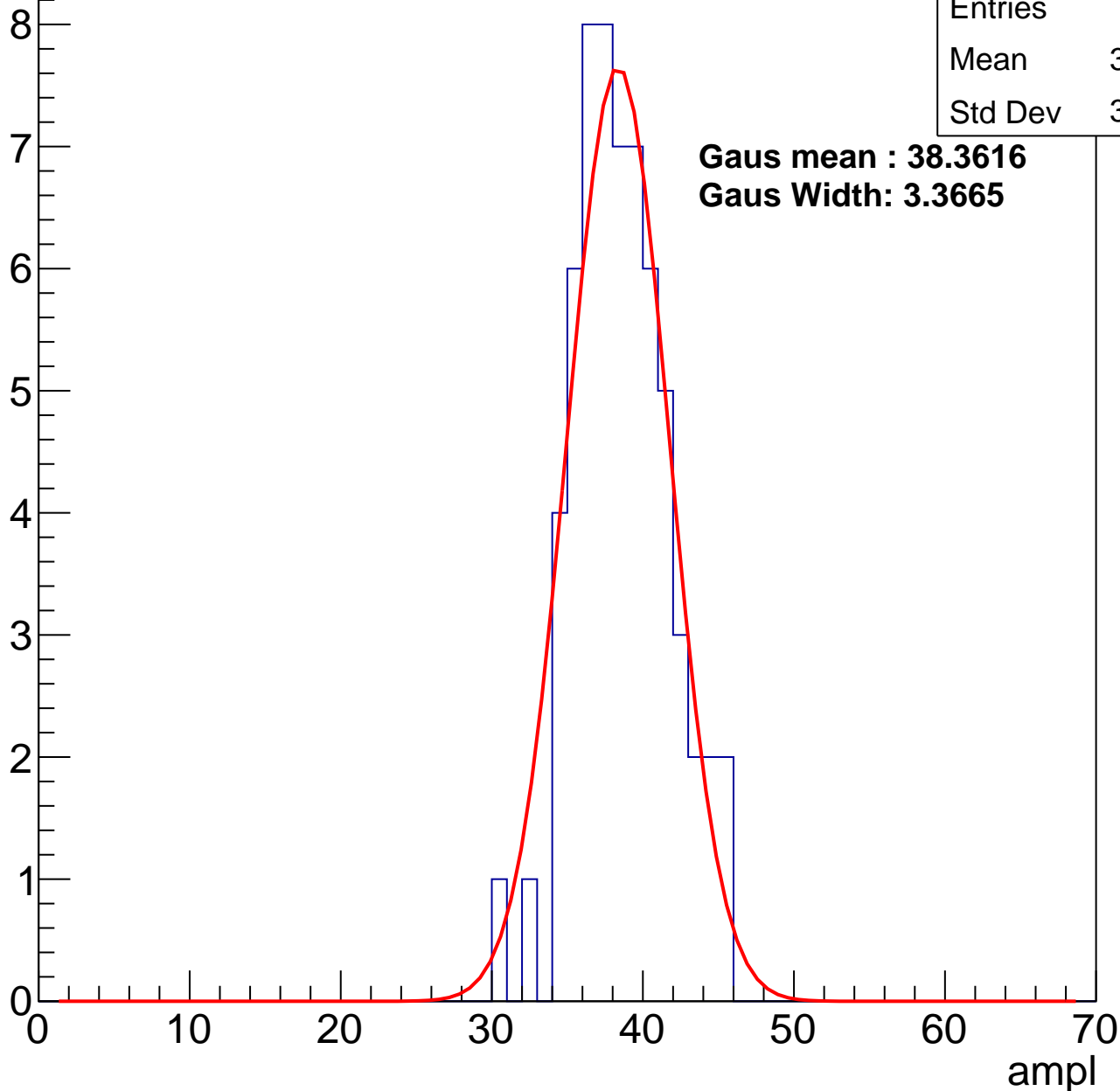
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	38.16
Std Dev	3.112

Gaus mean : 38.3616

Gaus Width: 3.3665



B1L100S, U14-ch27, adc2

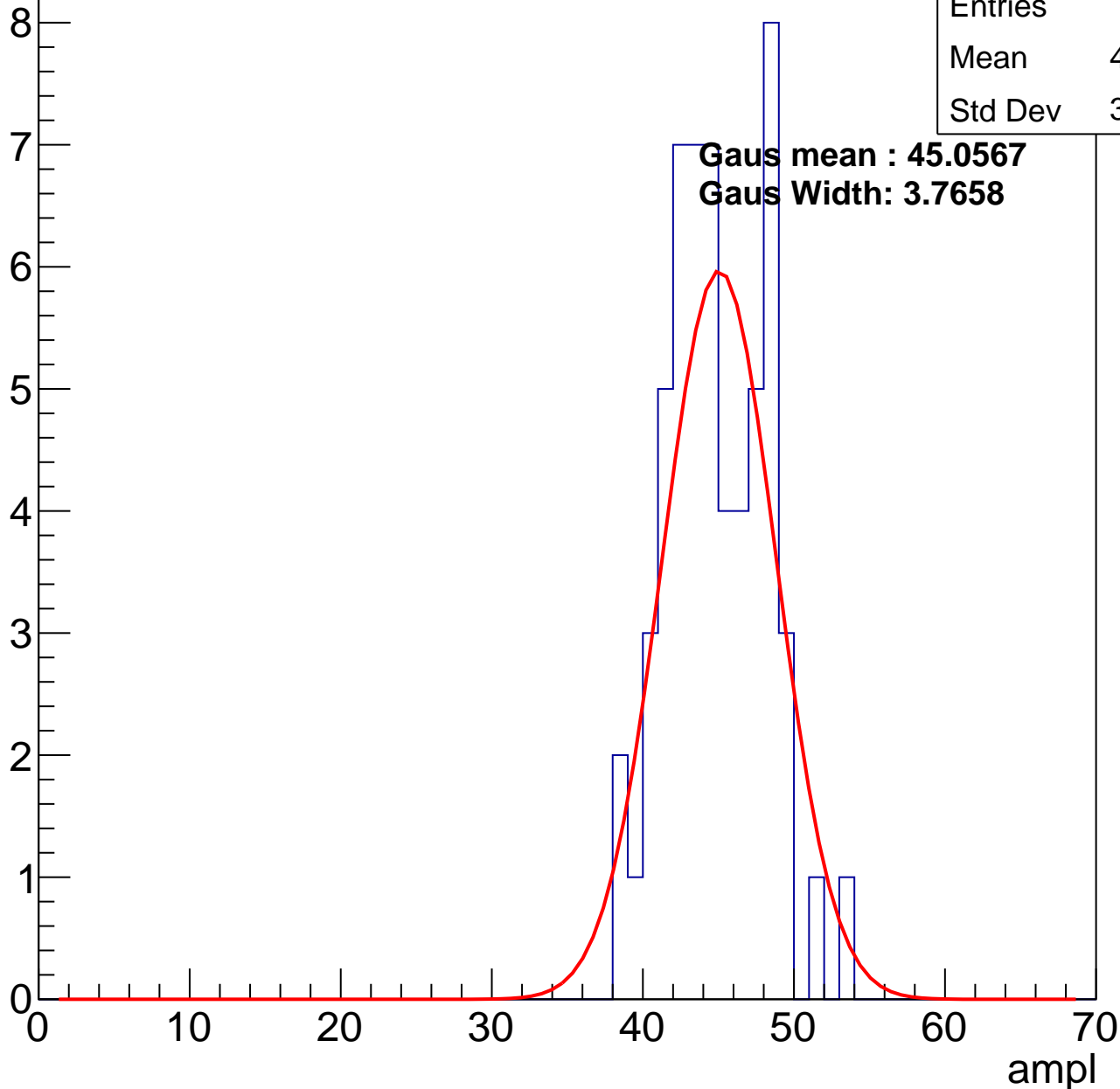
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	44.43
Std Dev	3.249

Gaus mean : 45.0567

Gaus Width: 3.7658



B1L100S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	50.22
Std Dev	3.707

Entry

10

8

6

4

2

0

0

10

20

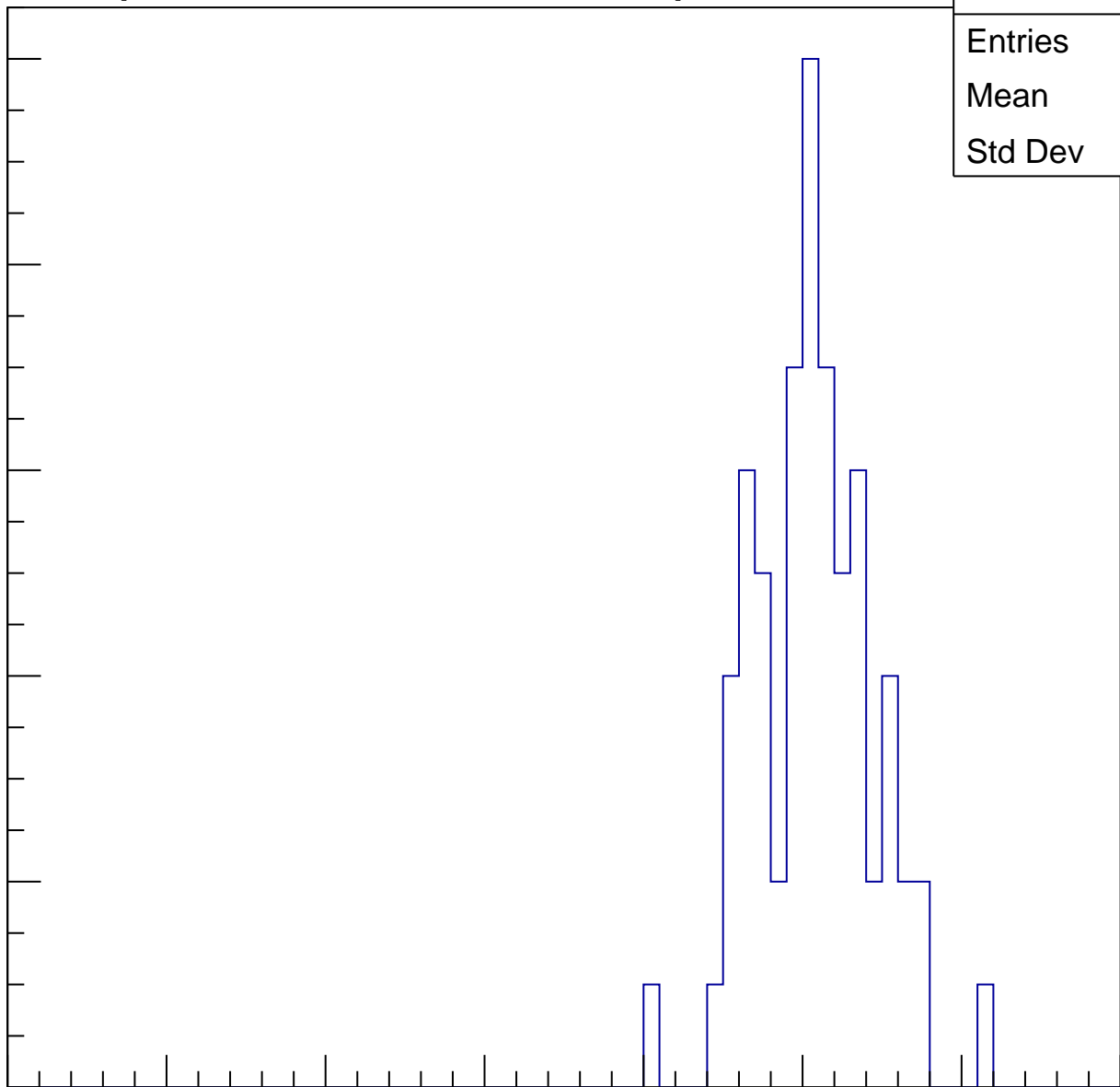
30

40

50

60

ampl

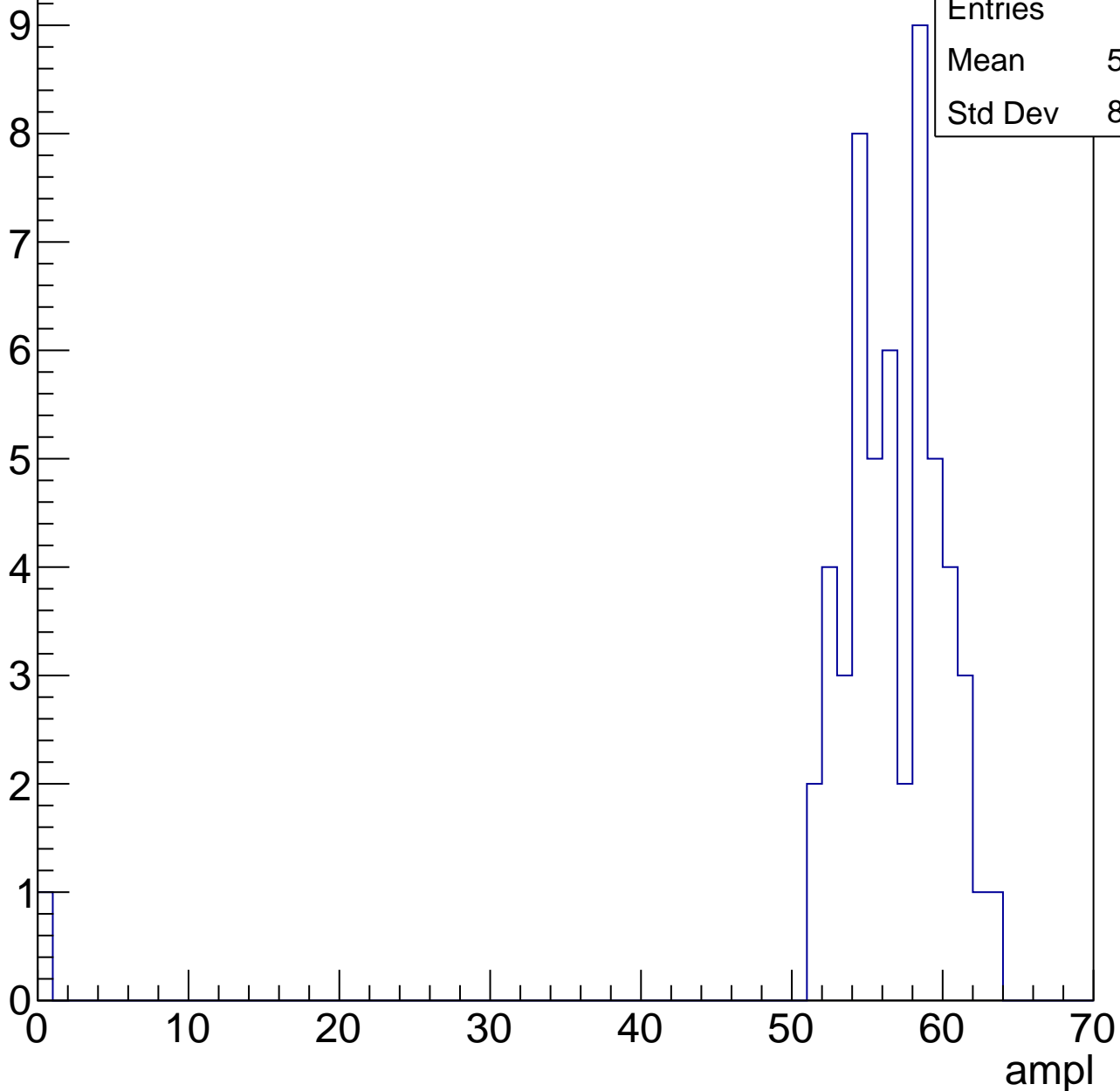


B1L100S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

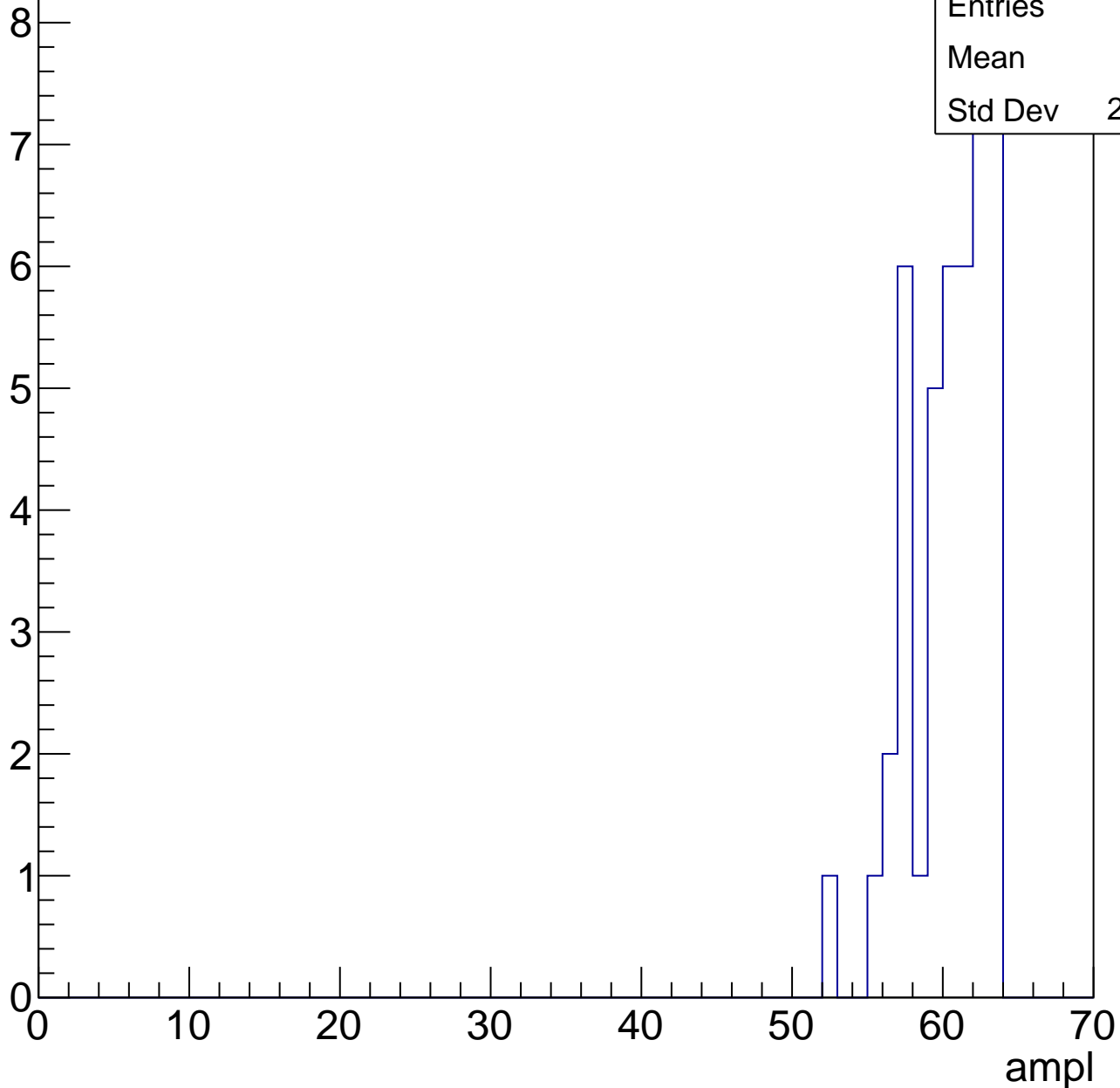
Entries	54
Mean	55.39
Std Dev	8.166



B1L100S, U14-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch28, adc0

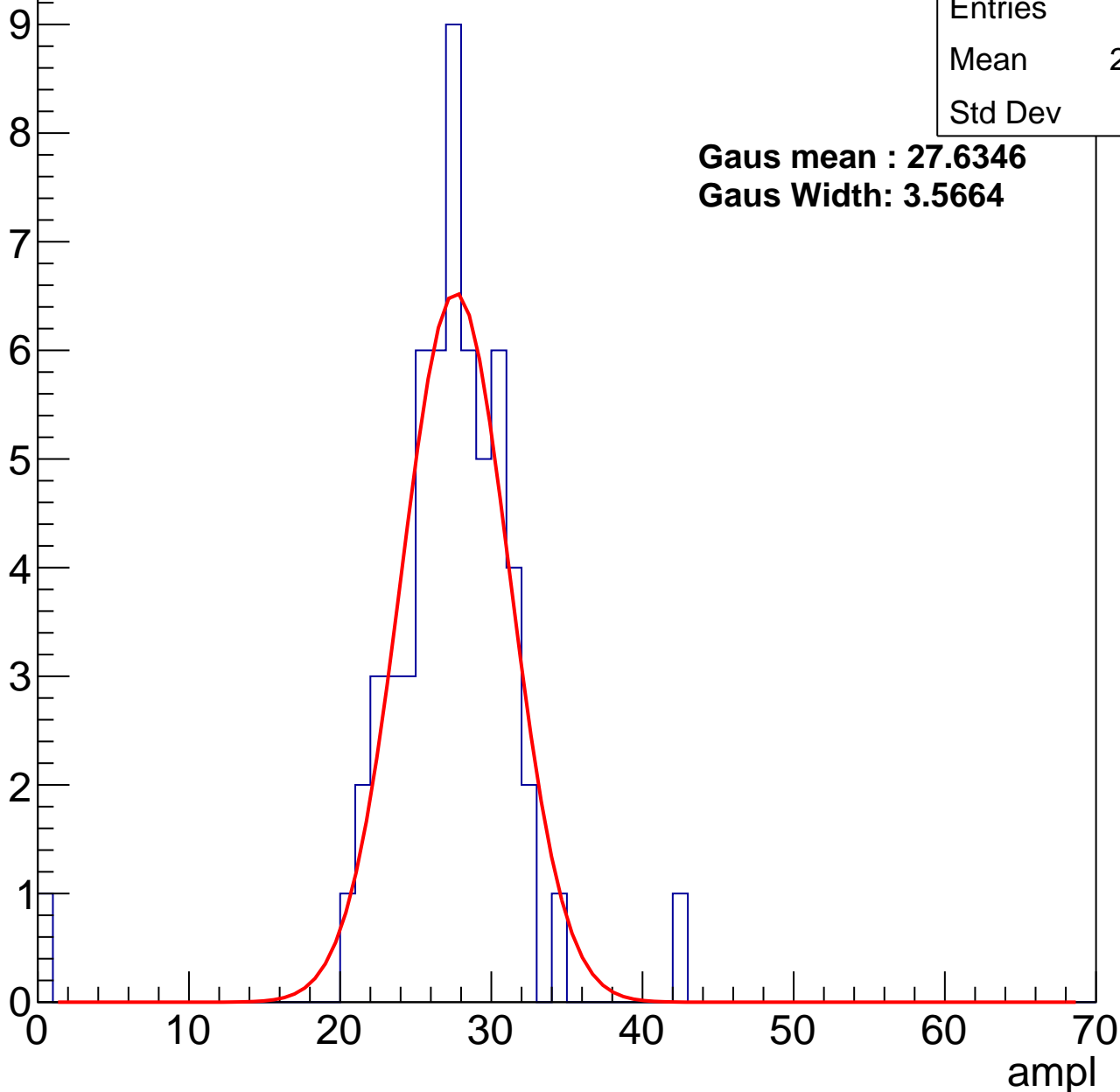
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	26.69
Std Dev	5.03

Gaus mean : 27.6346

Gaus Width: 3.5664



B1L100S, U14-ch28, adc1

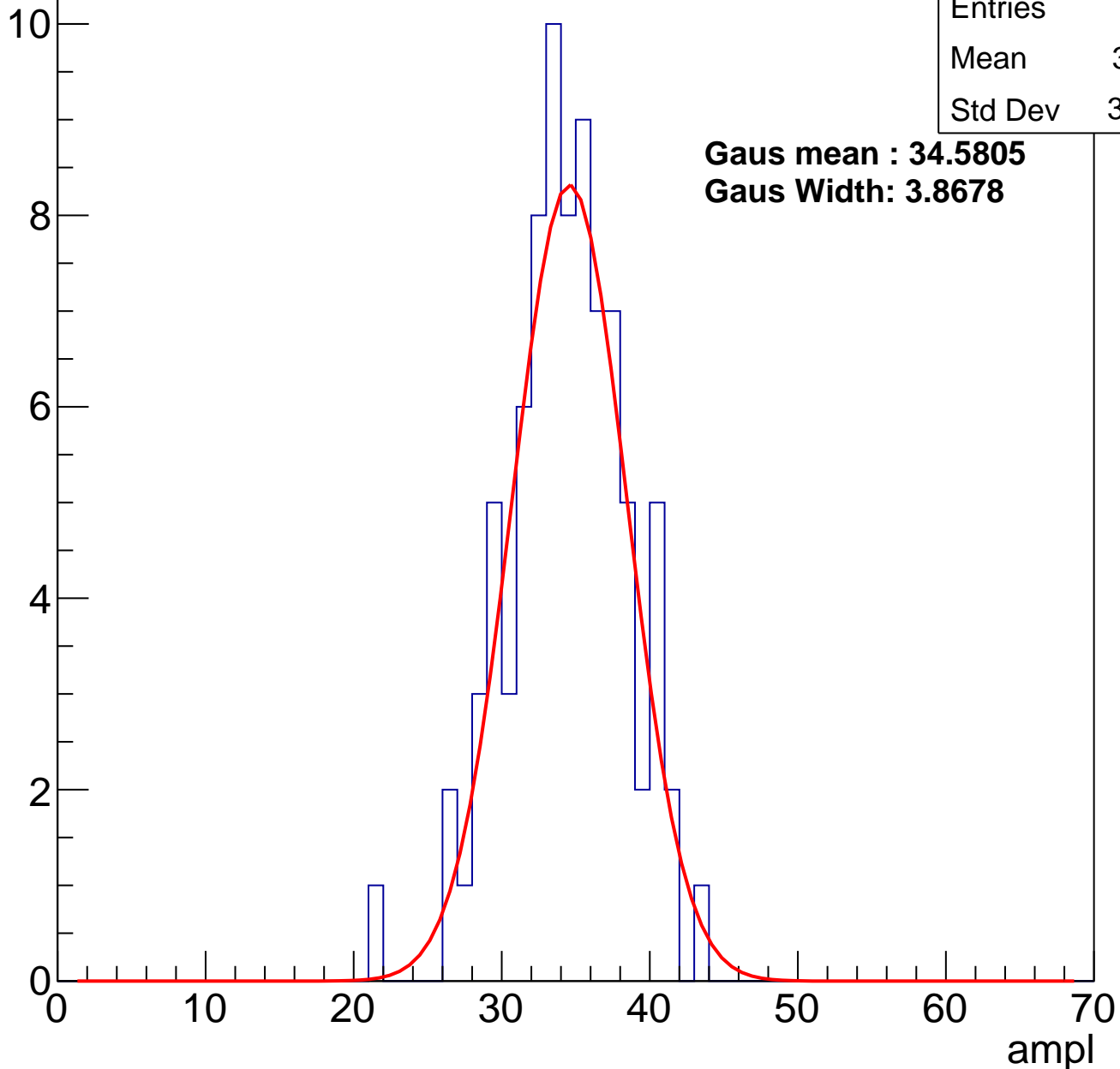
calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	33.91
Std Dev	3.928

Gaus mean : 34.5805

Gaus Width: 3.8678

Entry



B1L100S, U14-ch28, adc2

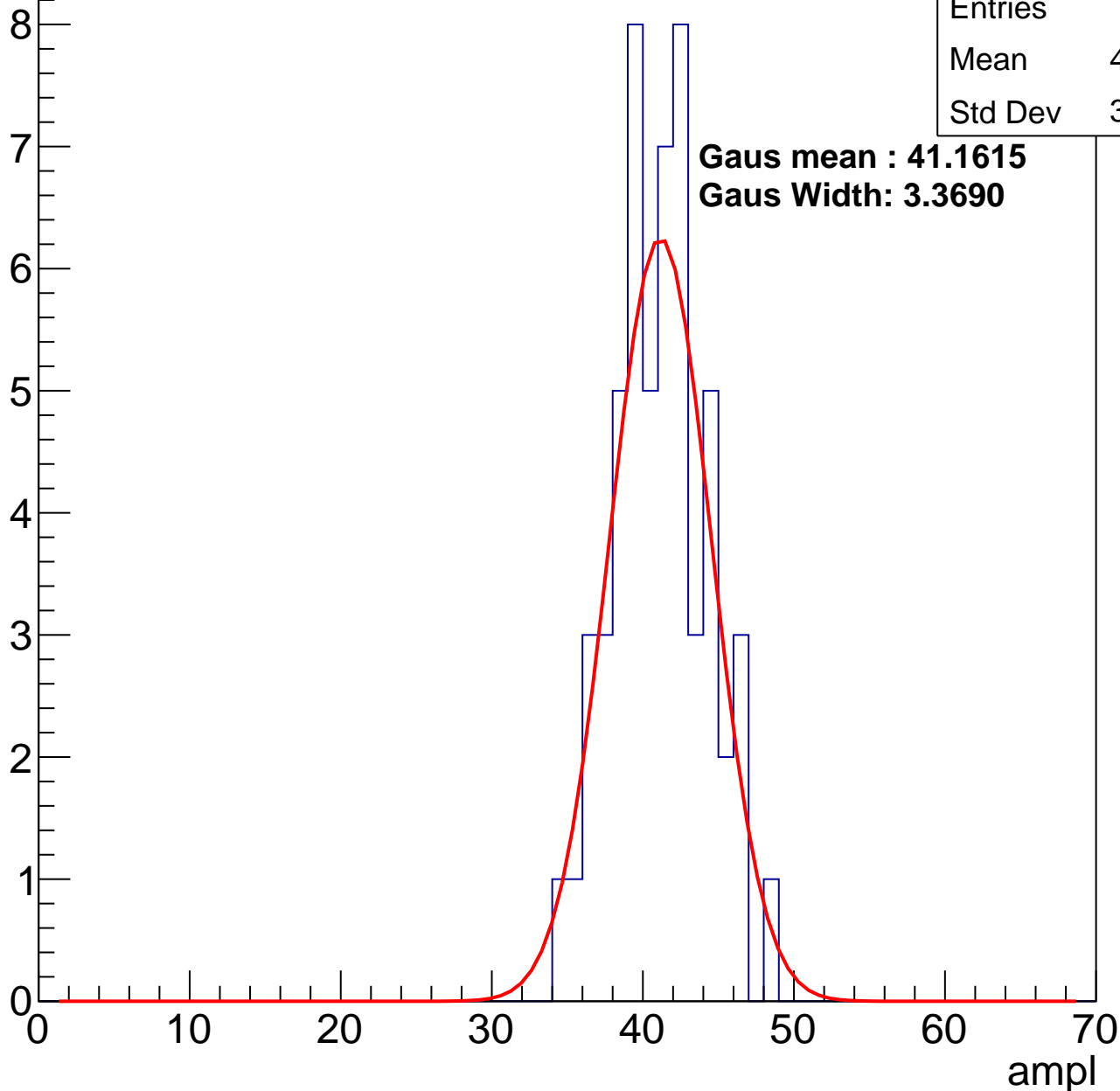
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	40.69
Std Dev	3.032

Gaus mean : 41.1615

Gaus Width: 3.3690

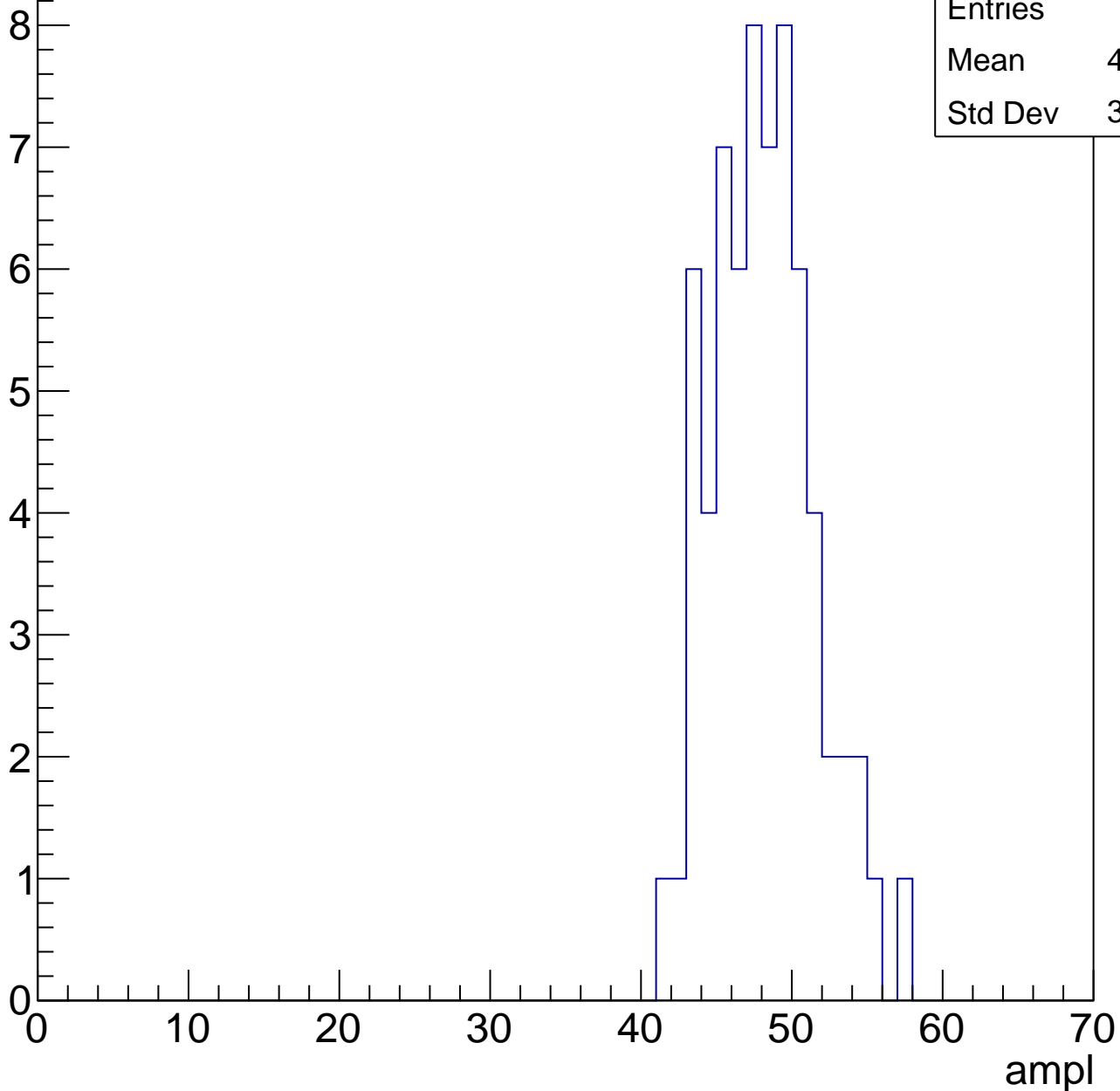


B1L100S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	47.67
Std Dev	3.345

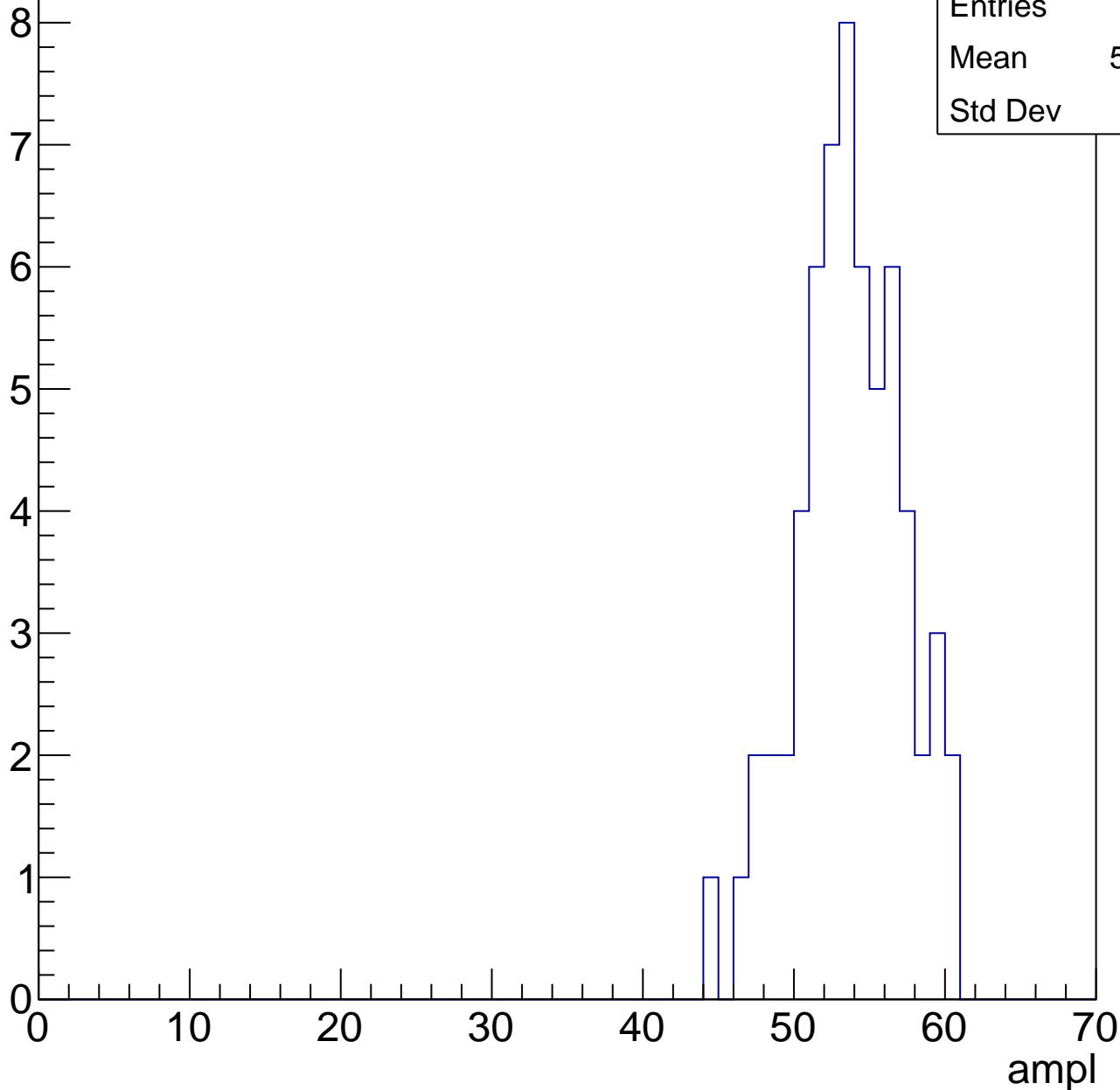


B1L100S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	53.25
Std Dev	3.5

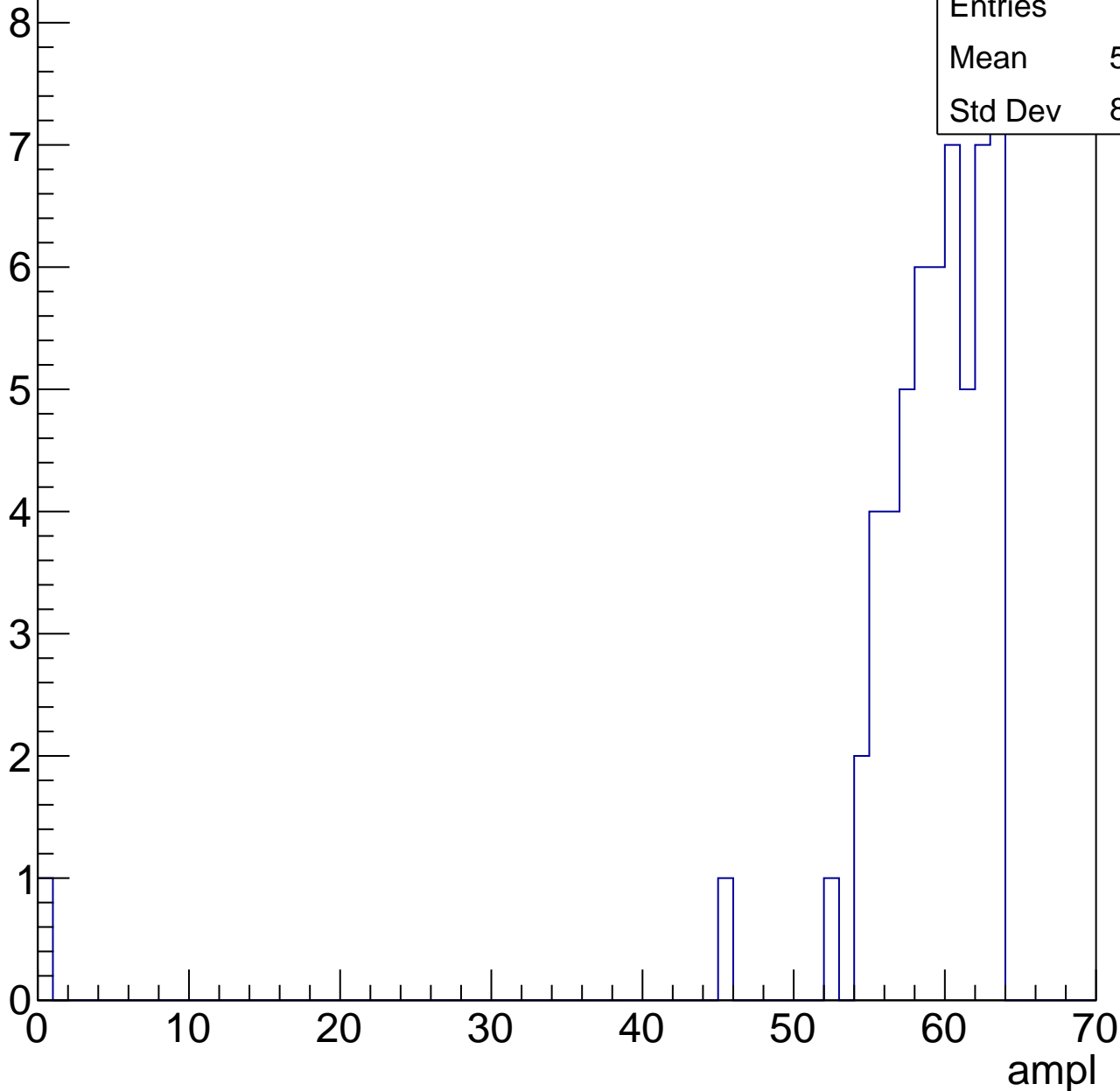


B1L100S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	57.88
Std Dev	8.427



B1L100S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

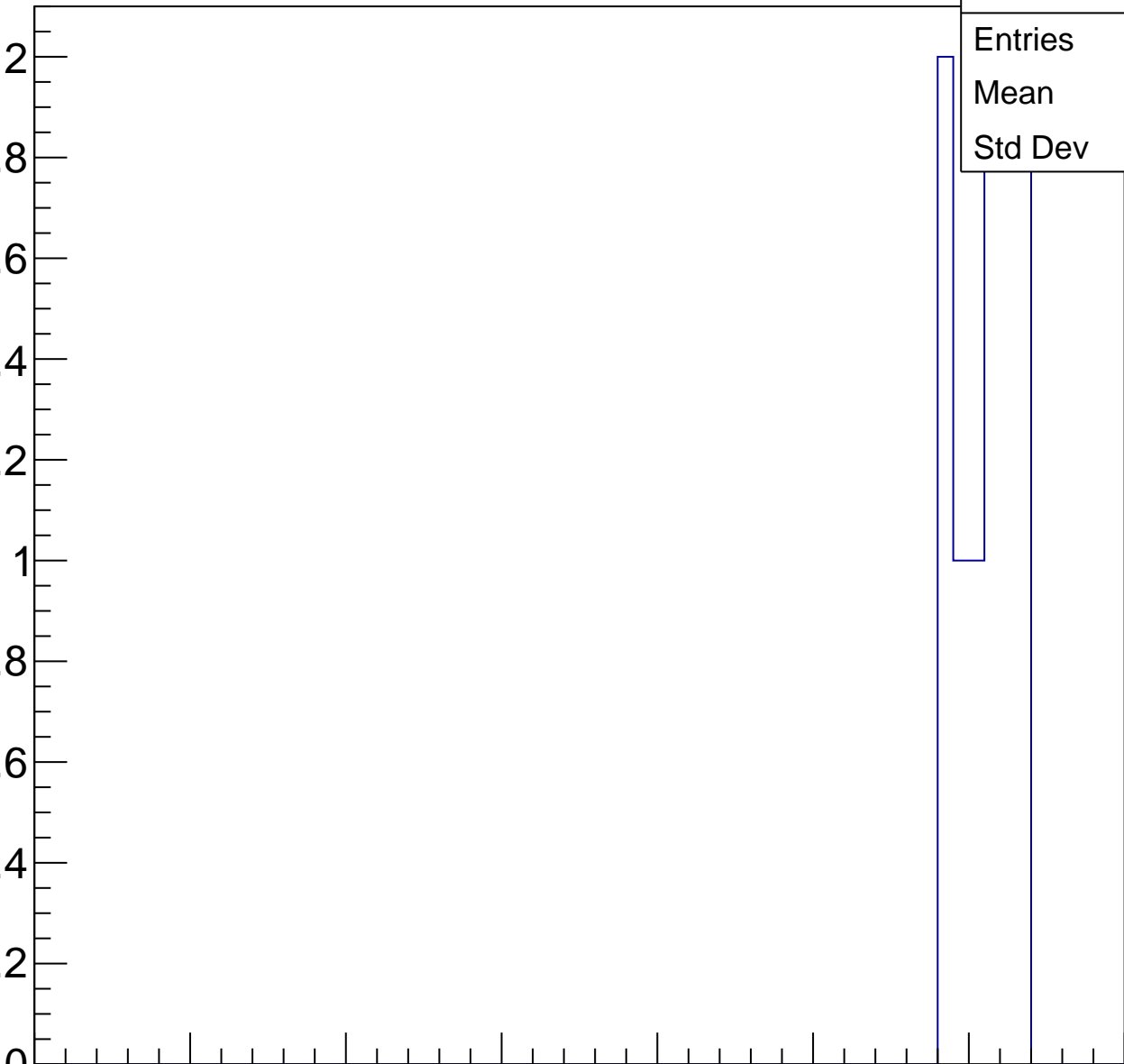
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60.7
Std Dev	1.792

0 10 20 30 40 50 60 70

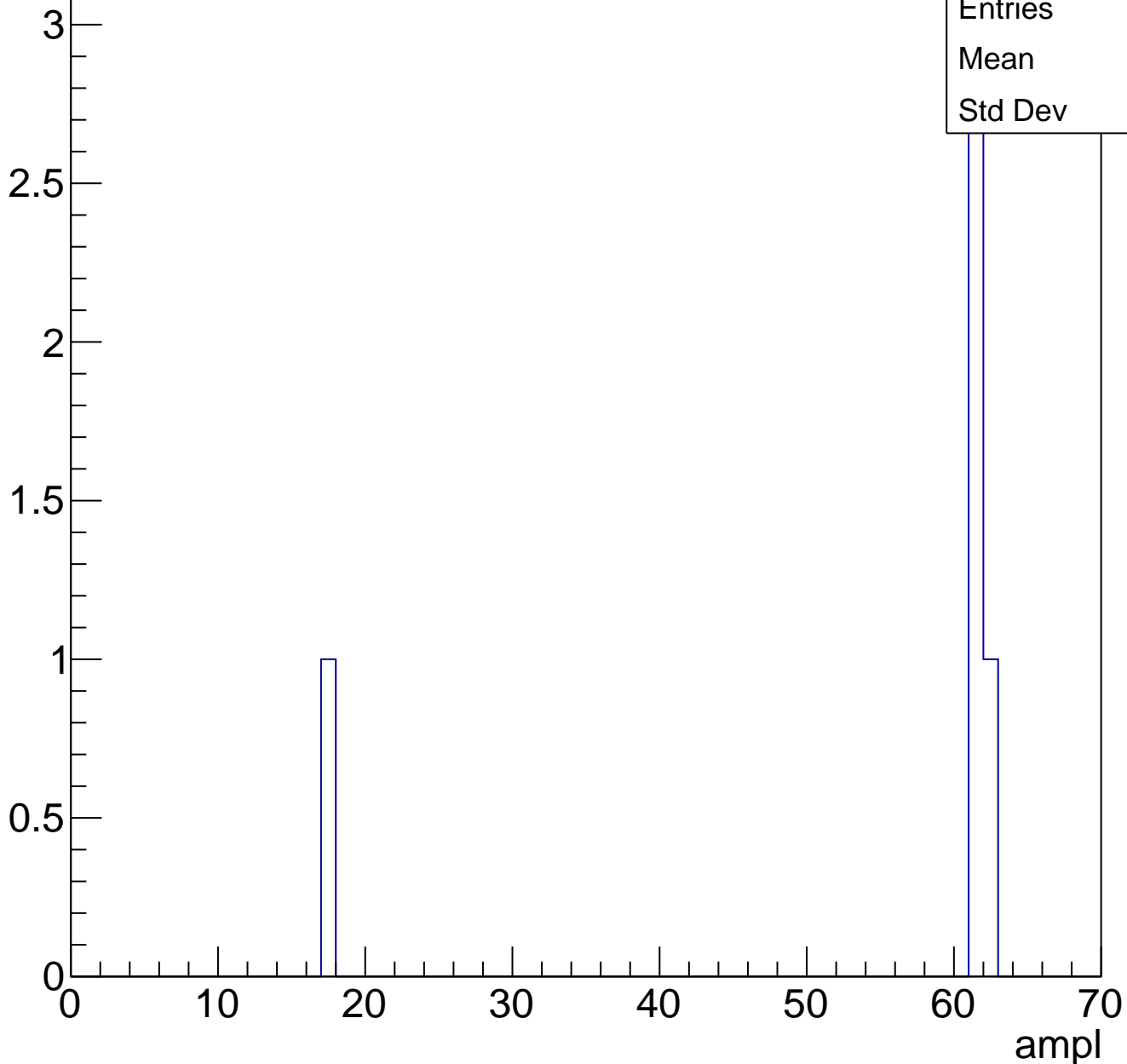
ampl



B1L100S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch29, adc0

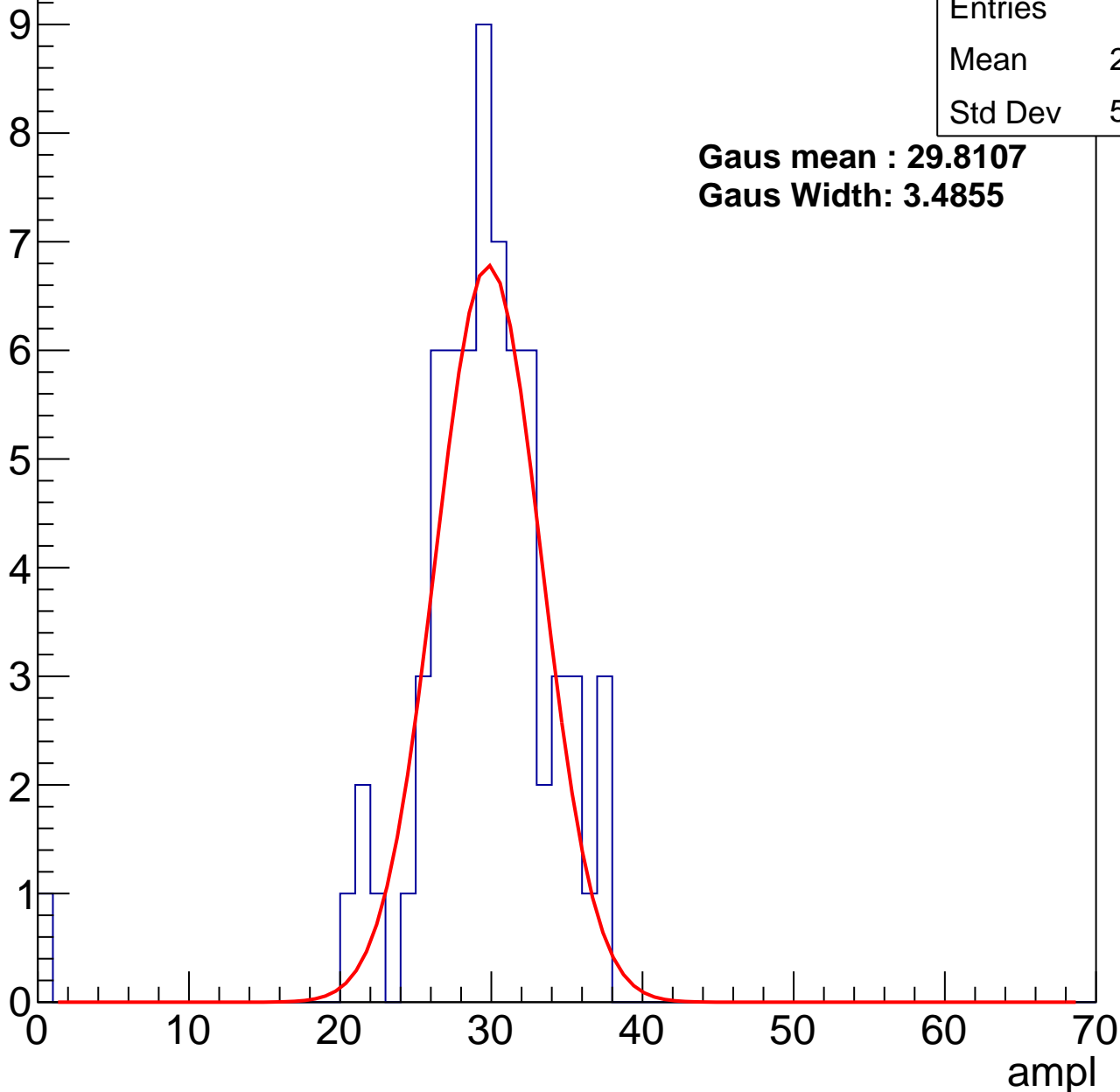
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.93
Std Dev	5.178

Gaus mean : 29.8107

Gaus Width: 3.4855



B1L100S, U14-ch29, adc1

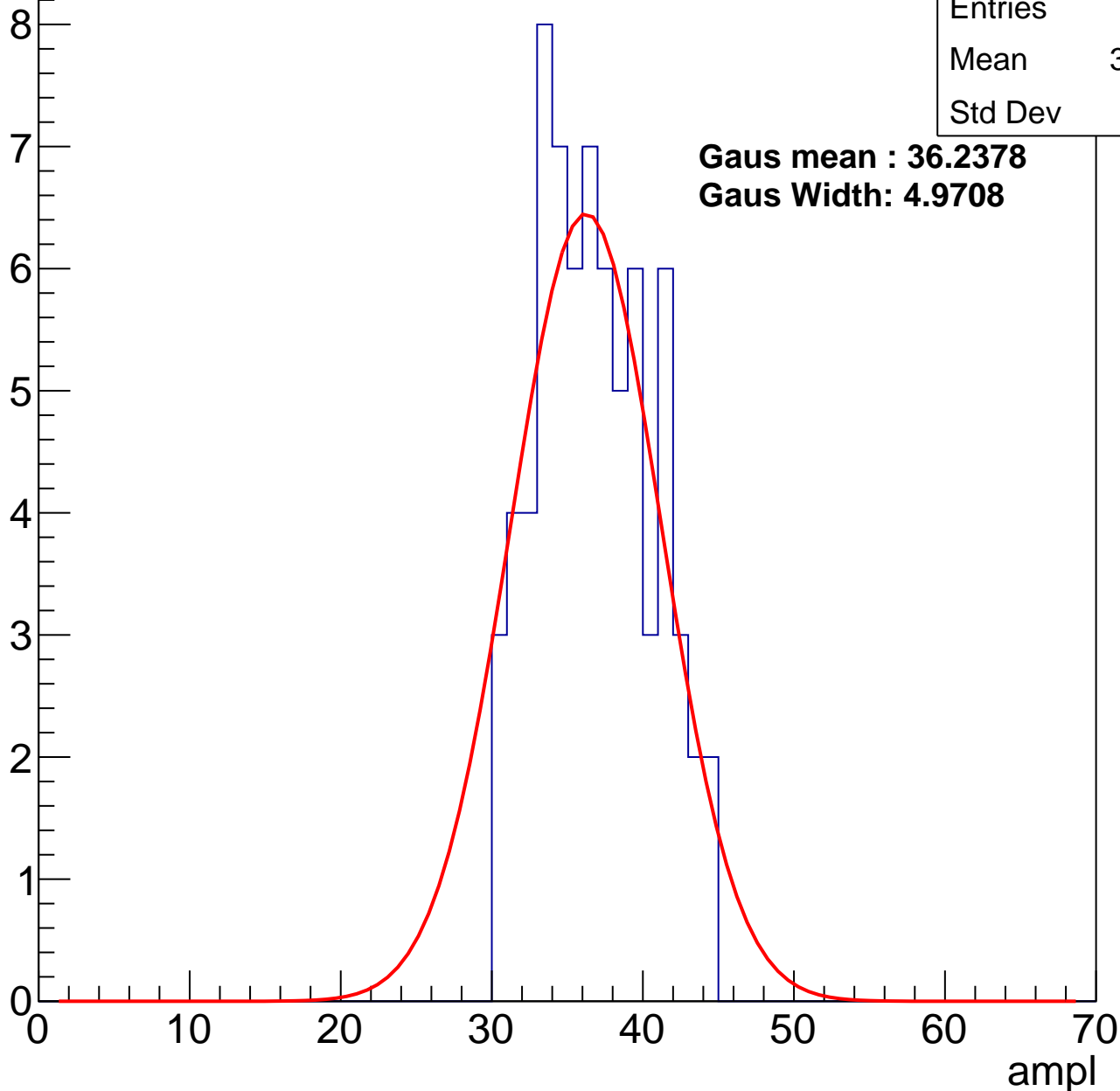
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.36
Std Dev	3.69

Gaus mean : 36.2378

Gaus Width: 4.9708



B1L100S, U14-ch29, adc2

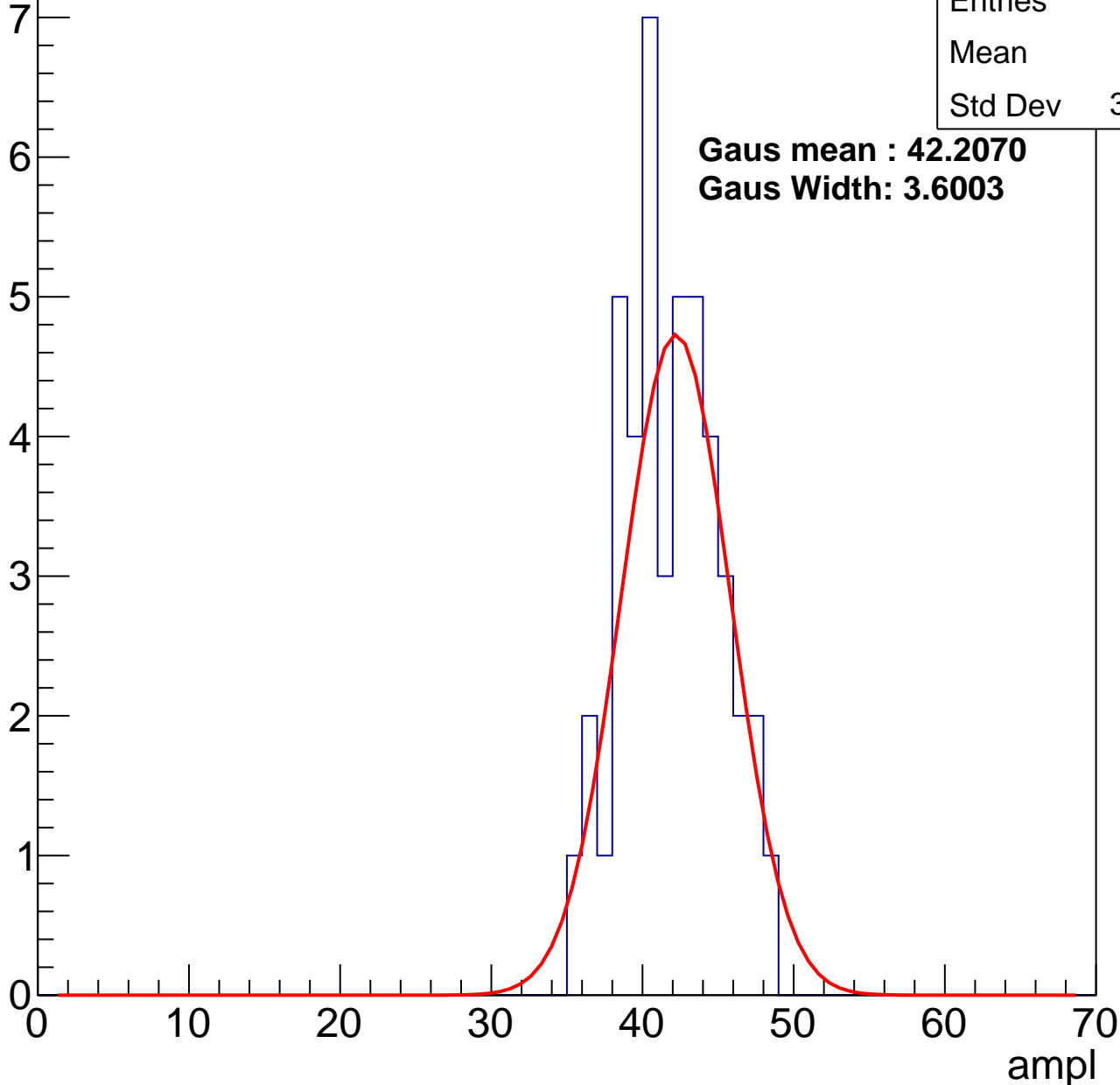
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	41.4
Std Dev	3.137

Gaus mean : 42.2070

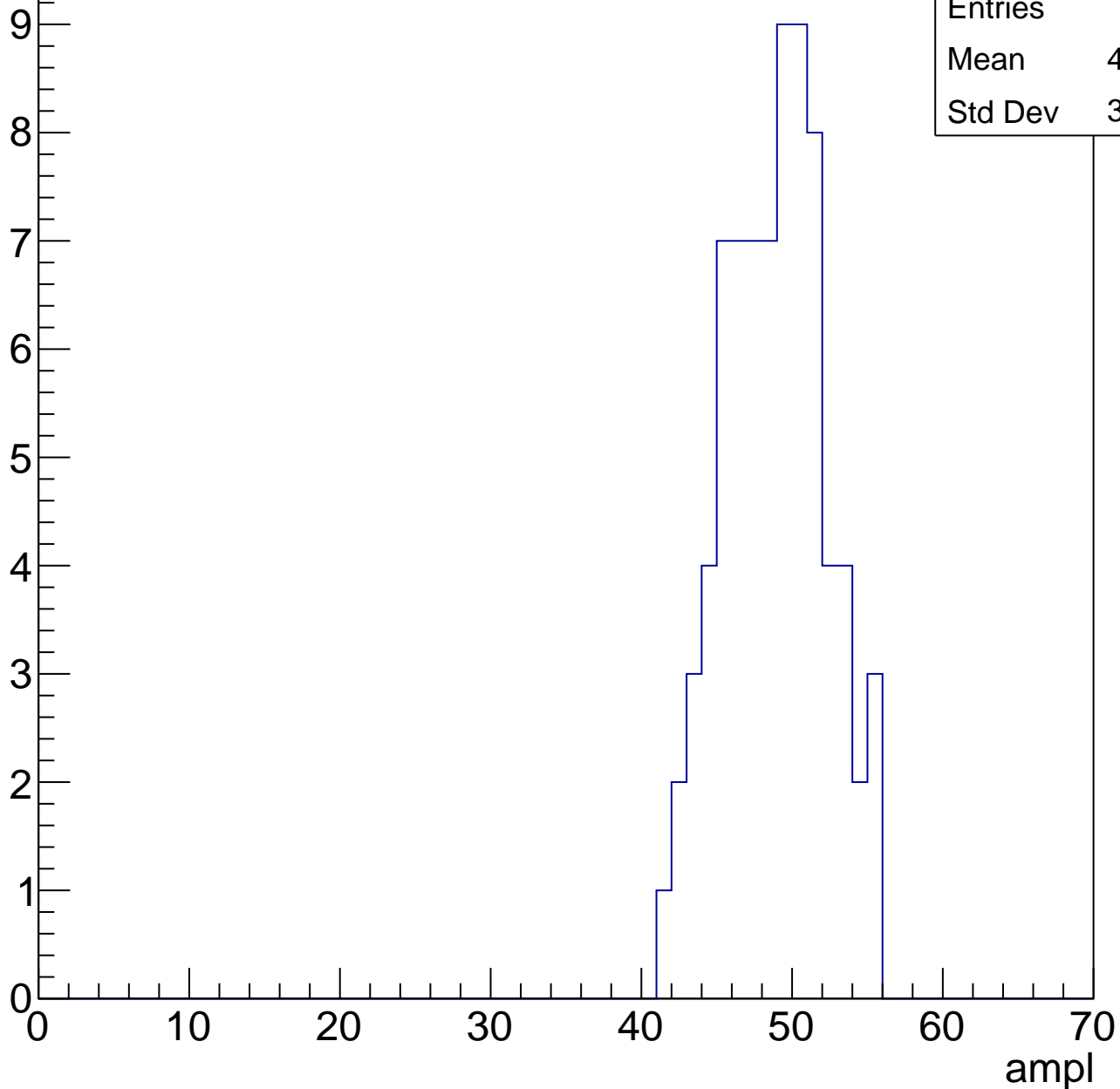
Gaus Width: 3.6003



B1L100S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

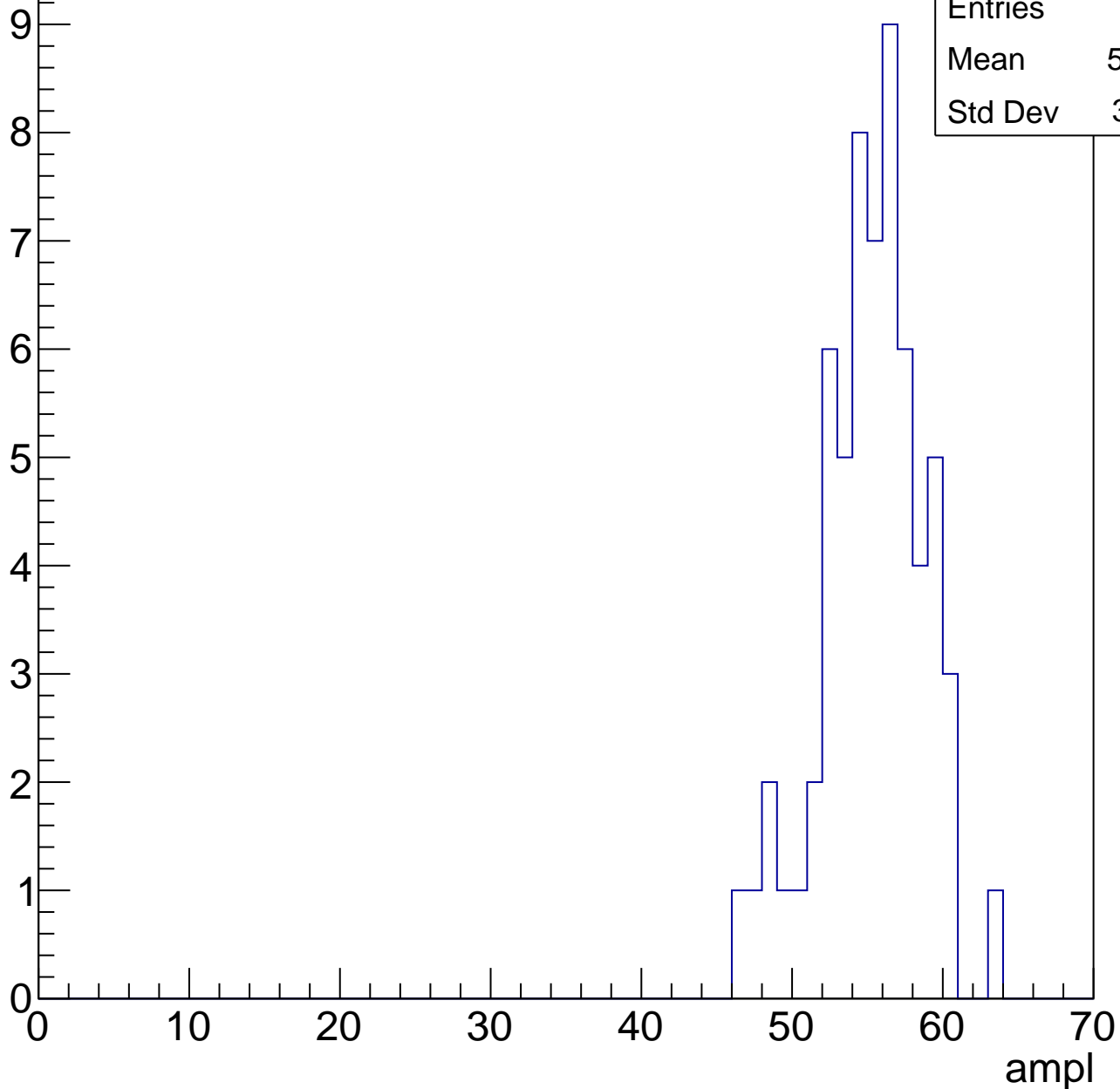


Entries	77
Mean	48.36
Std Dev	3.318

B1L100S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

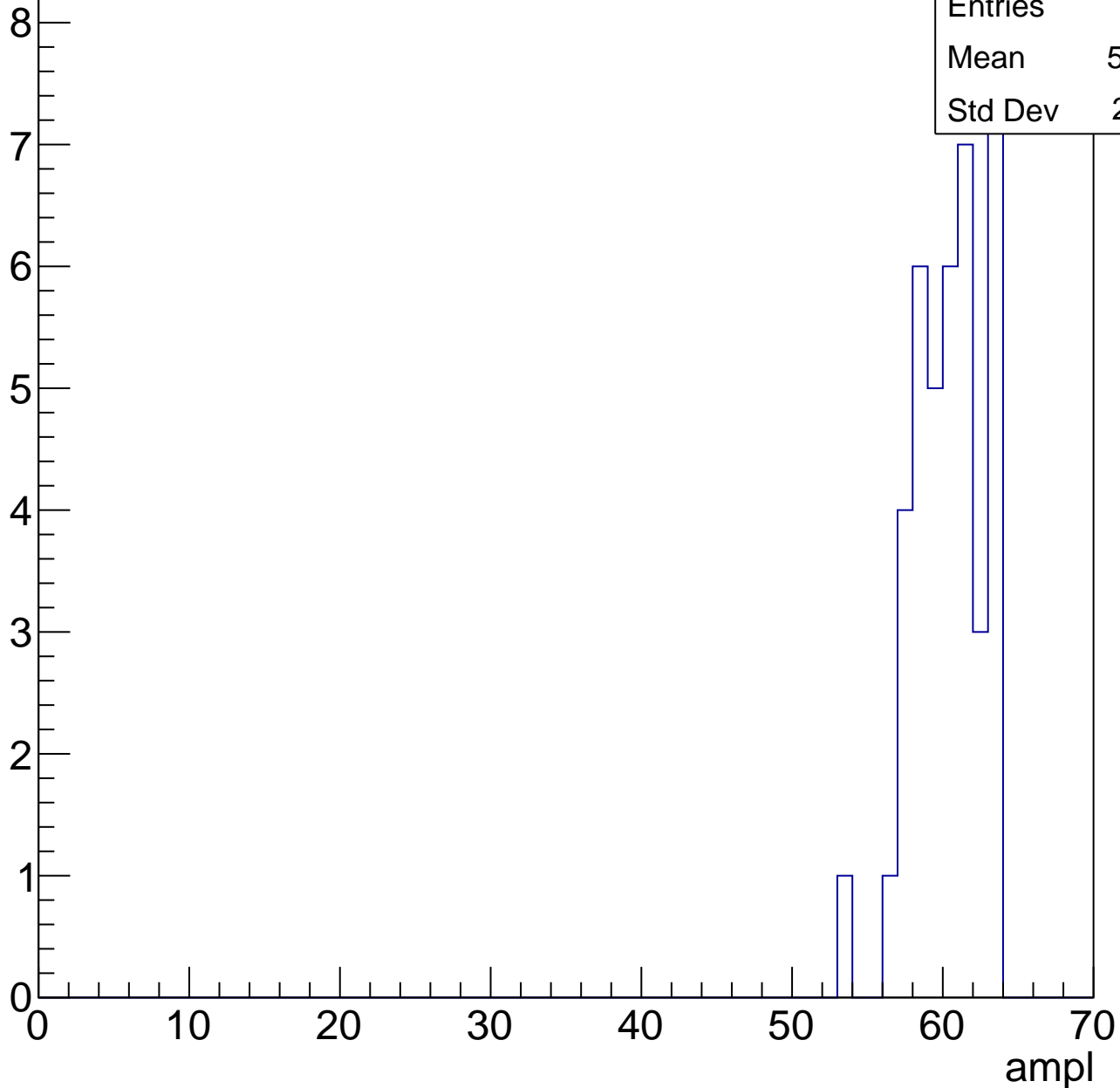


B1L100S, U14-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	59.93
Std Dev	2.321

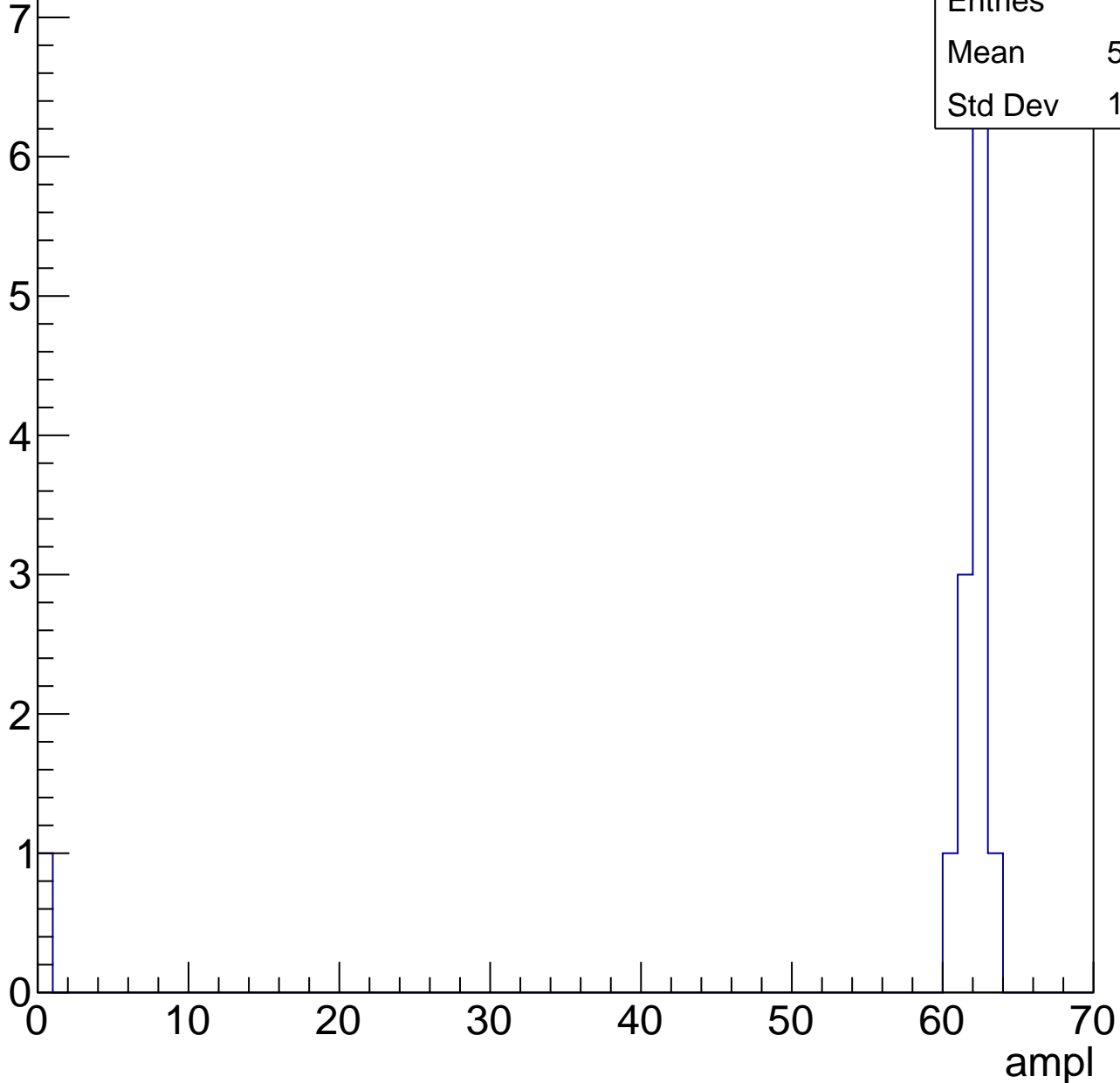


B1L100S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	56.92
Std Dev	16.45



B1L100S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch30, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	27.81
Std Dev	3.565

Gaus mean : 28.6033

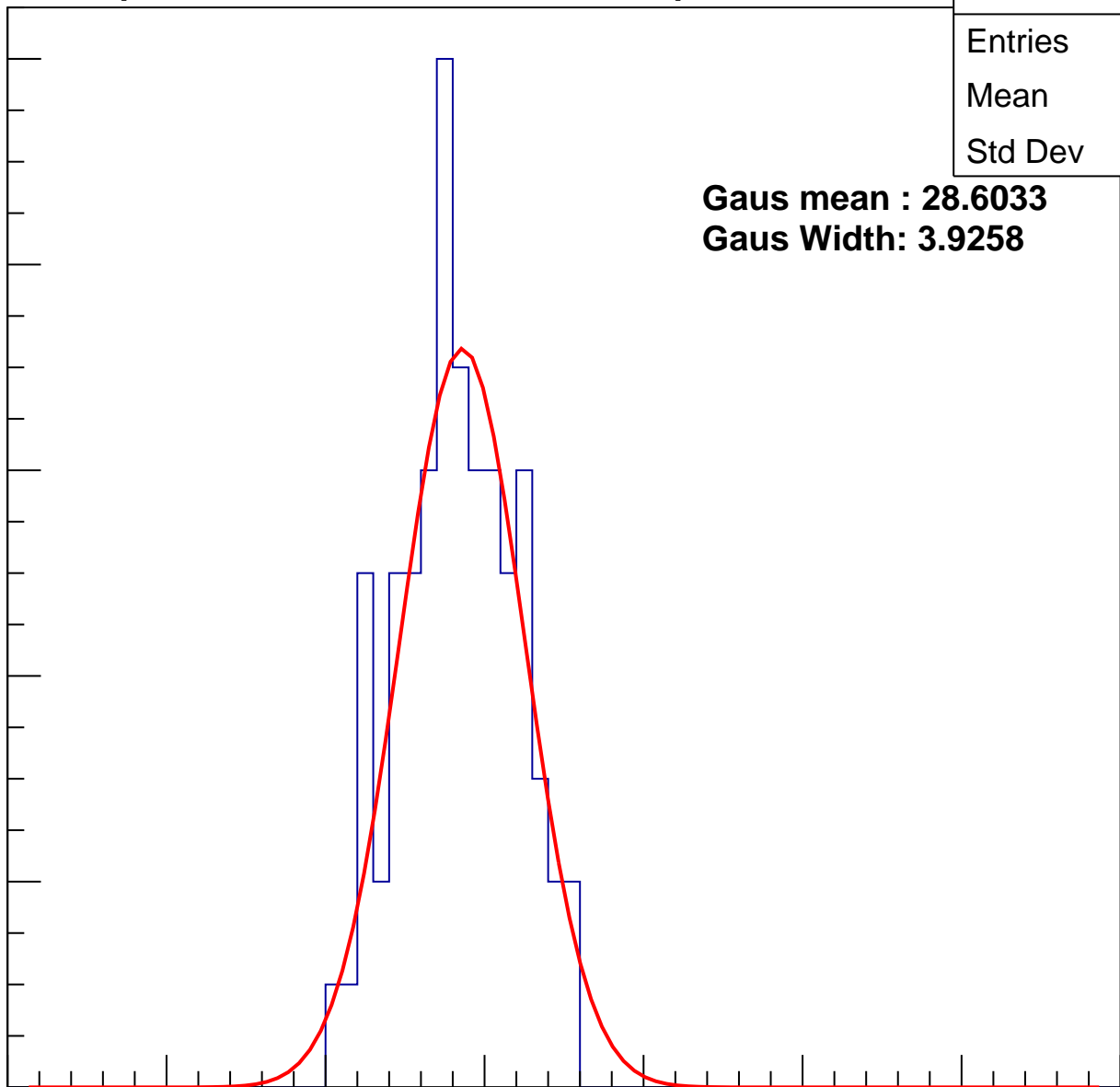
Gaus Width: 3.9258

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch30, adc1

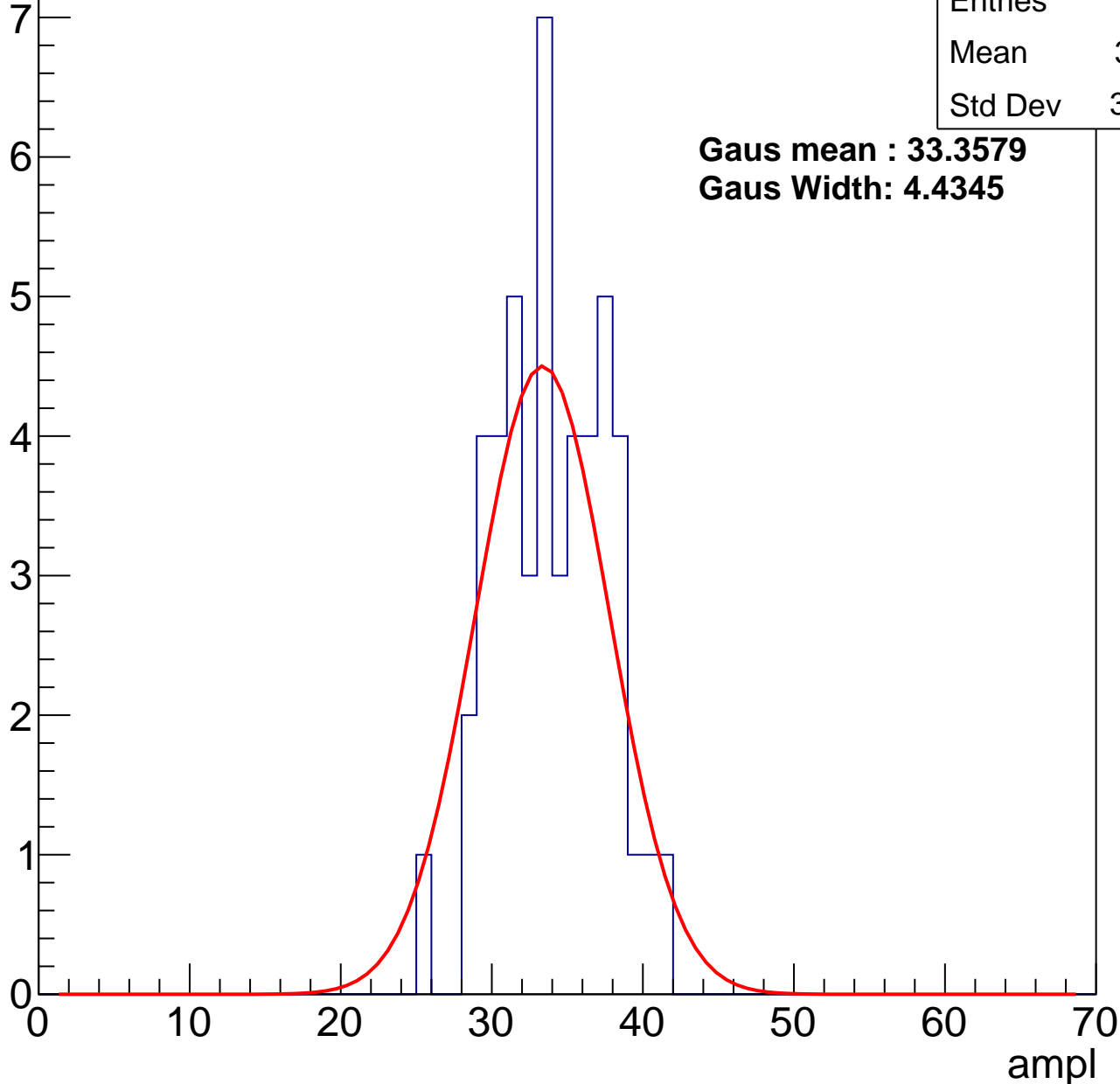
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	33.51
Std Dev	3.523

Gaus mean : 33.3579

Gaus Width: 4.4345



B1L100S, U14-ch30, adc2

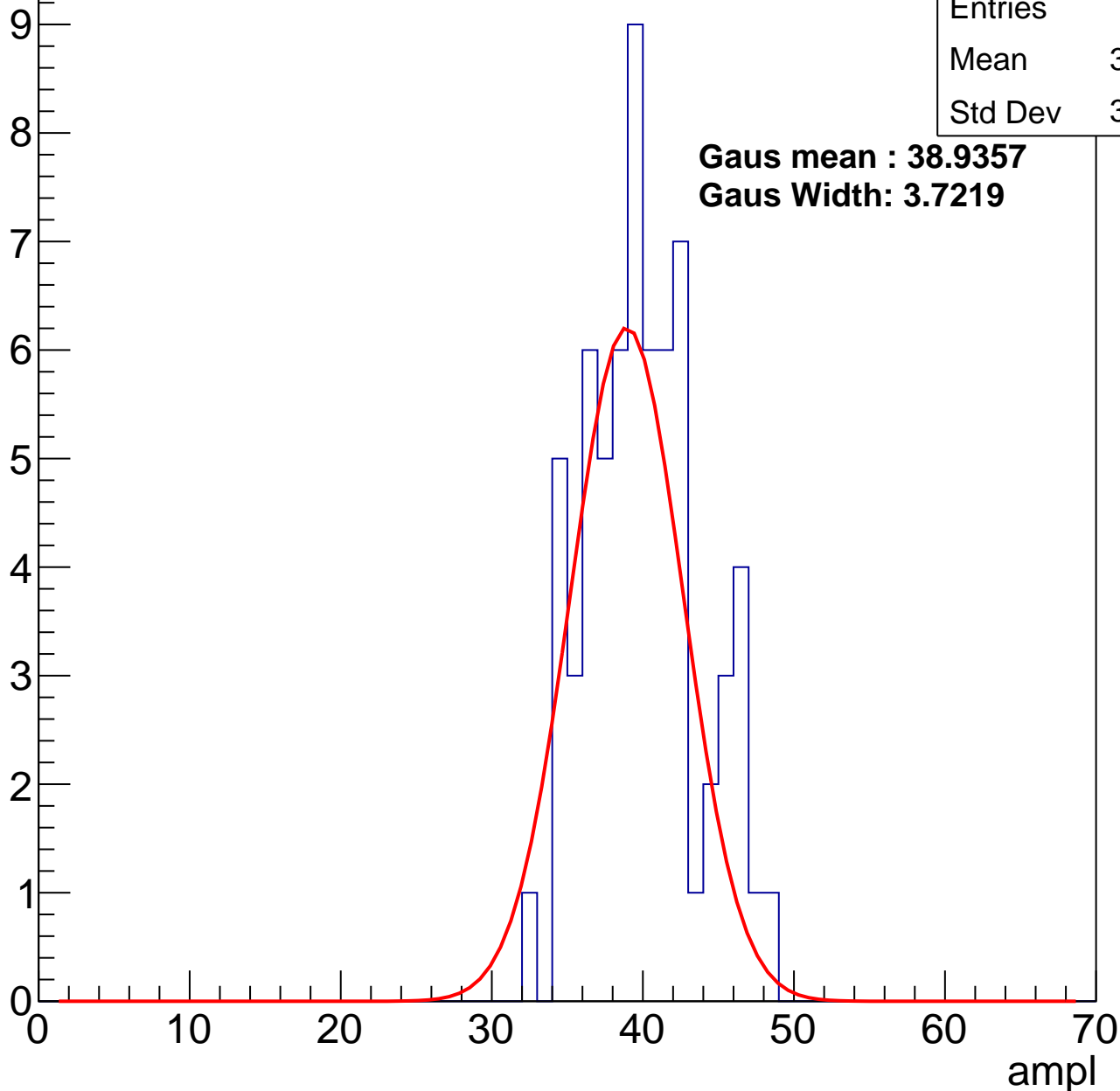
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	39.58
Std Dev	3.668

Gaus mean : 38.9357

Gaus Width: 3.7219

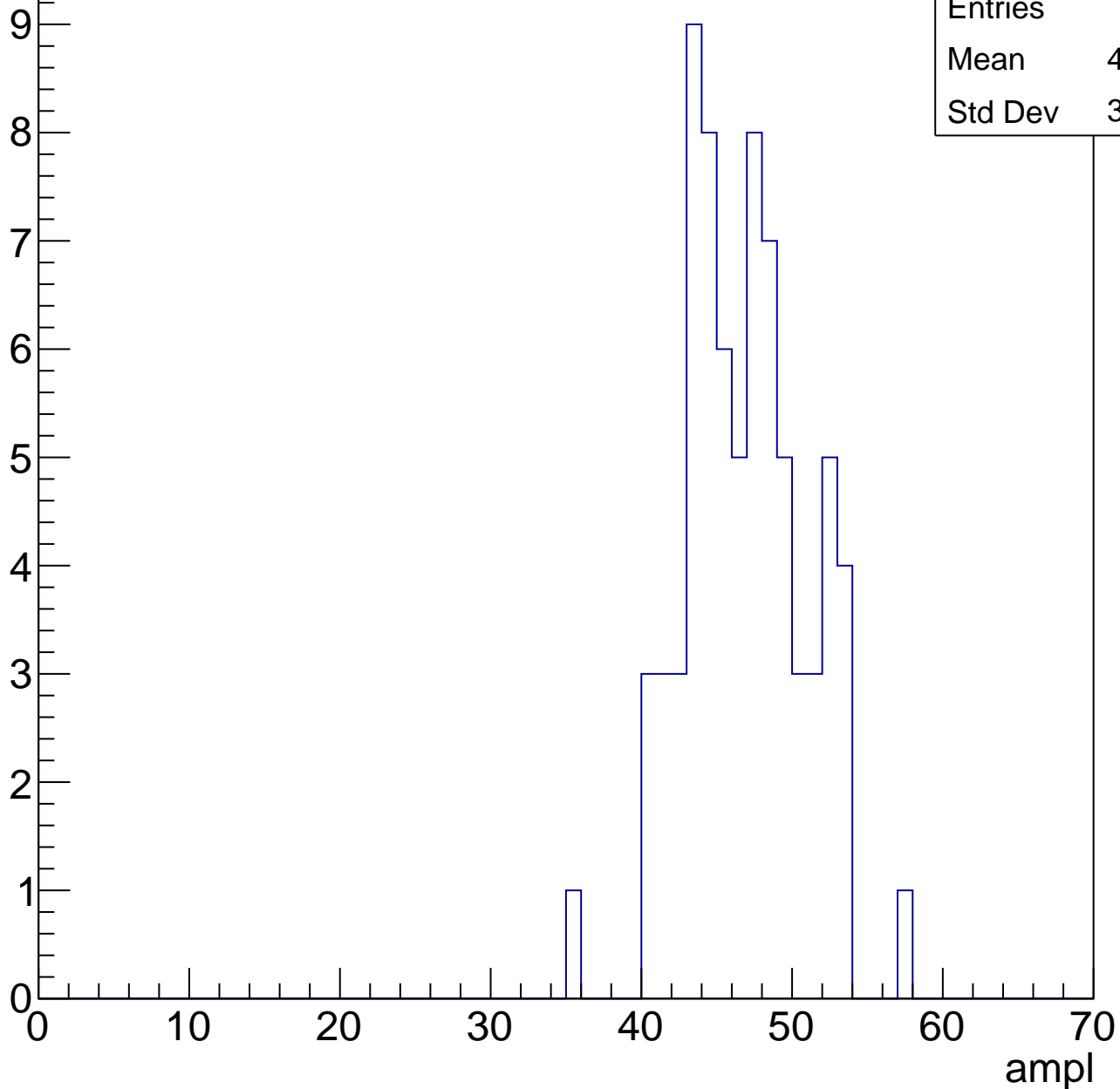


B1L100S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

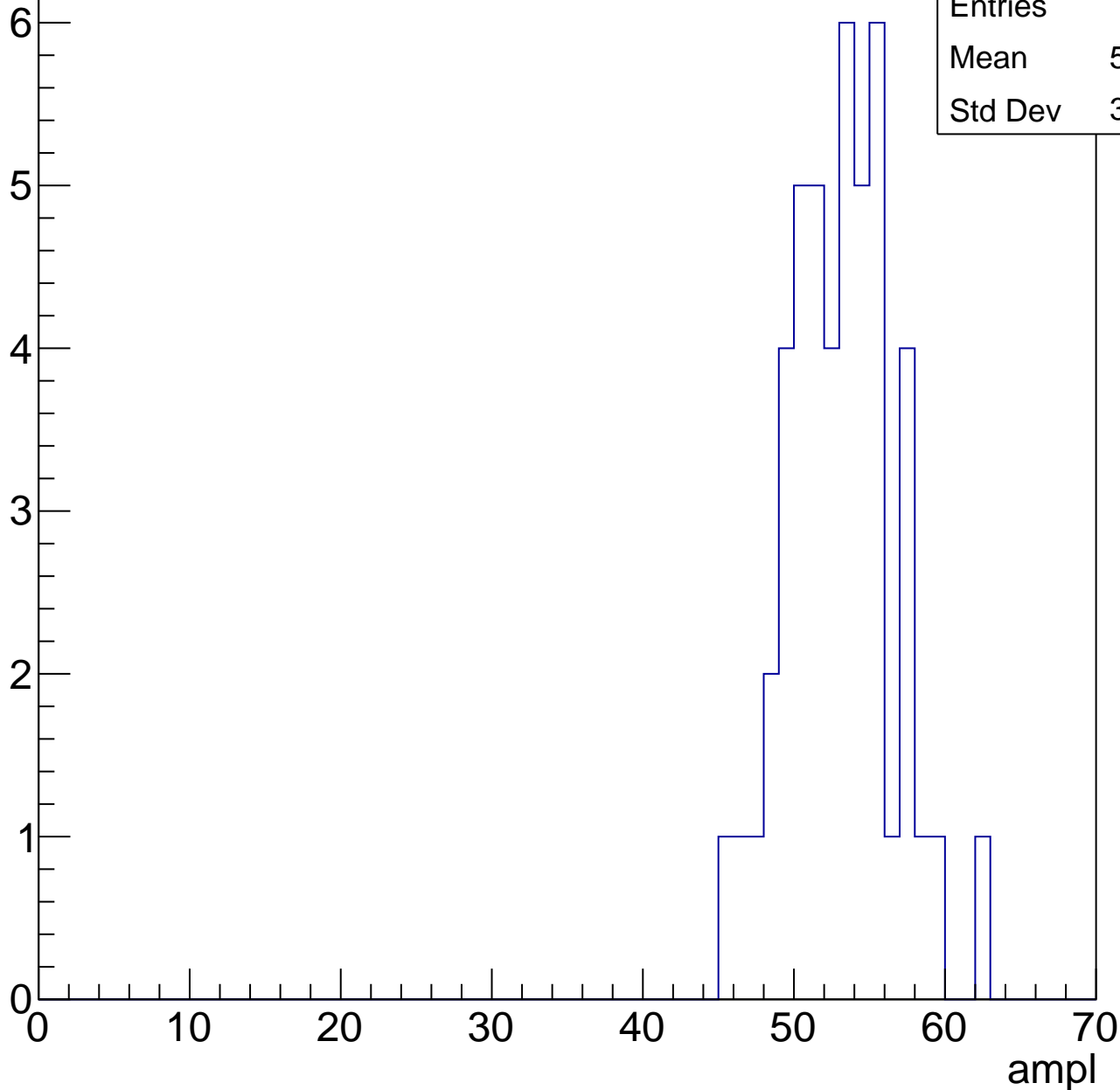
Entries	74
Mean	46.38
Std Dev	3.962



B1L100S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

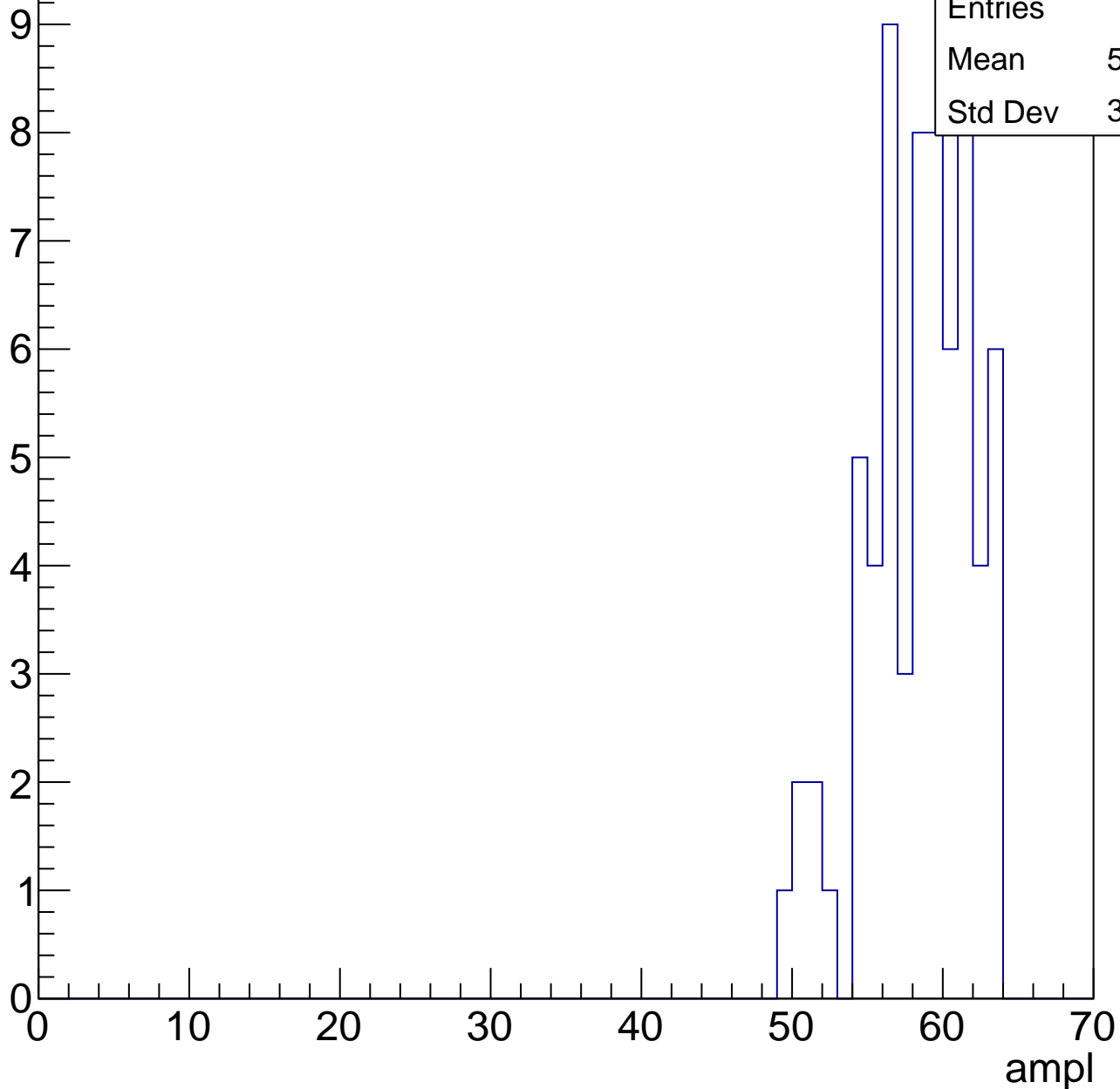
Entry



B1L100S, U14-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



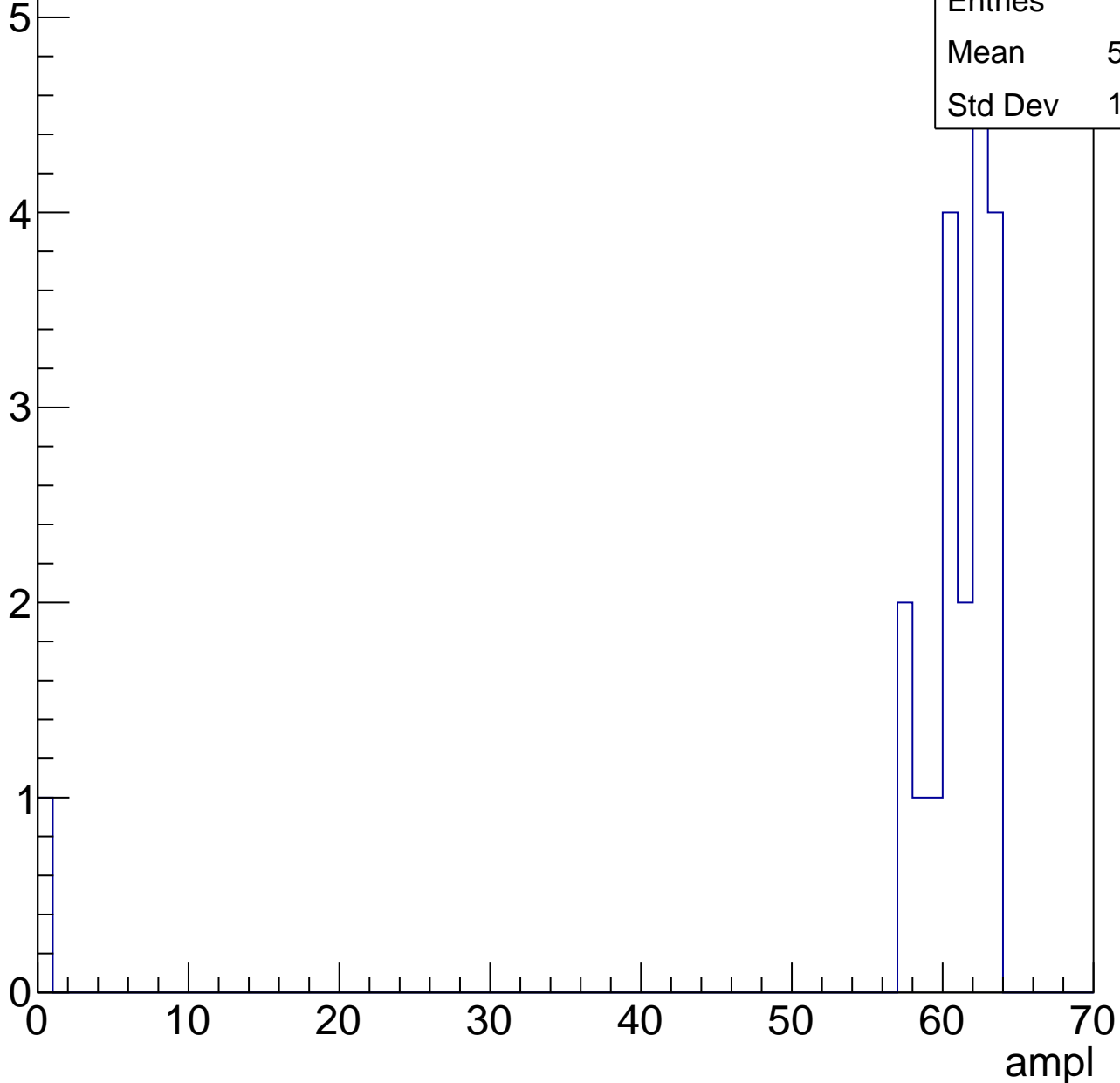
Entries	67
Mean	57.88
Std Dev	3.488

B1L100S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	57.75
Std Dev	13.38



B1L100S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch31, adc0

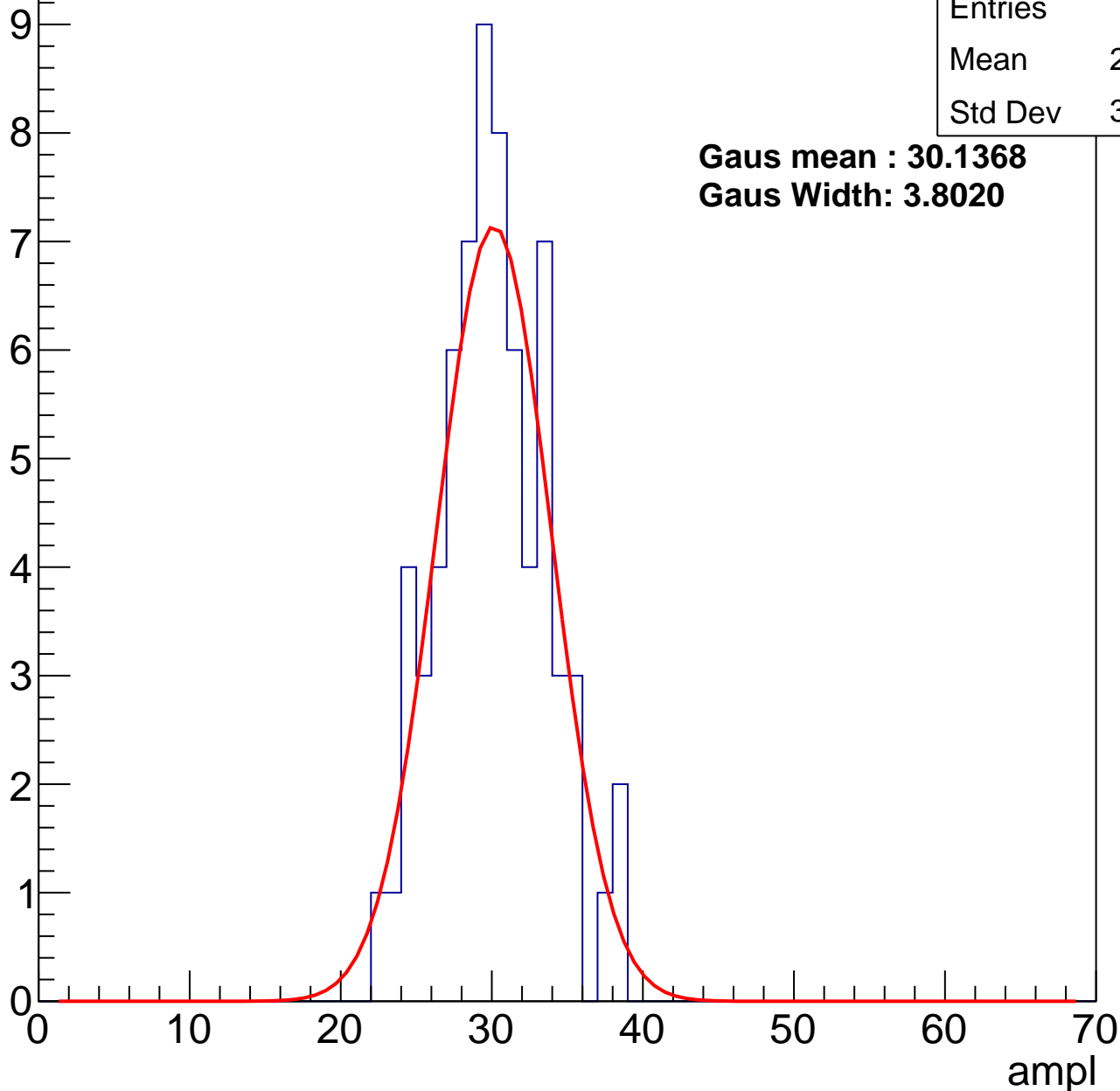
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.62
Std Dev	3.535

Gaus mean : 30.1368

Gaus Width: 3.8020



B1L100S, U14-ch31, adc1

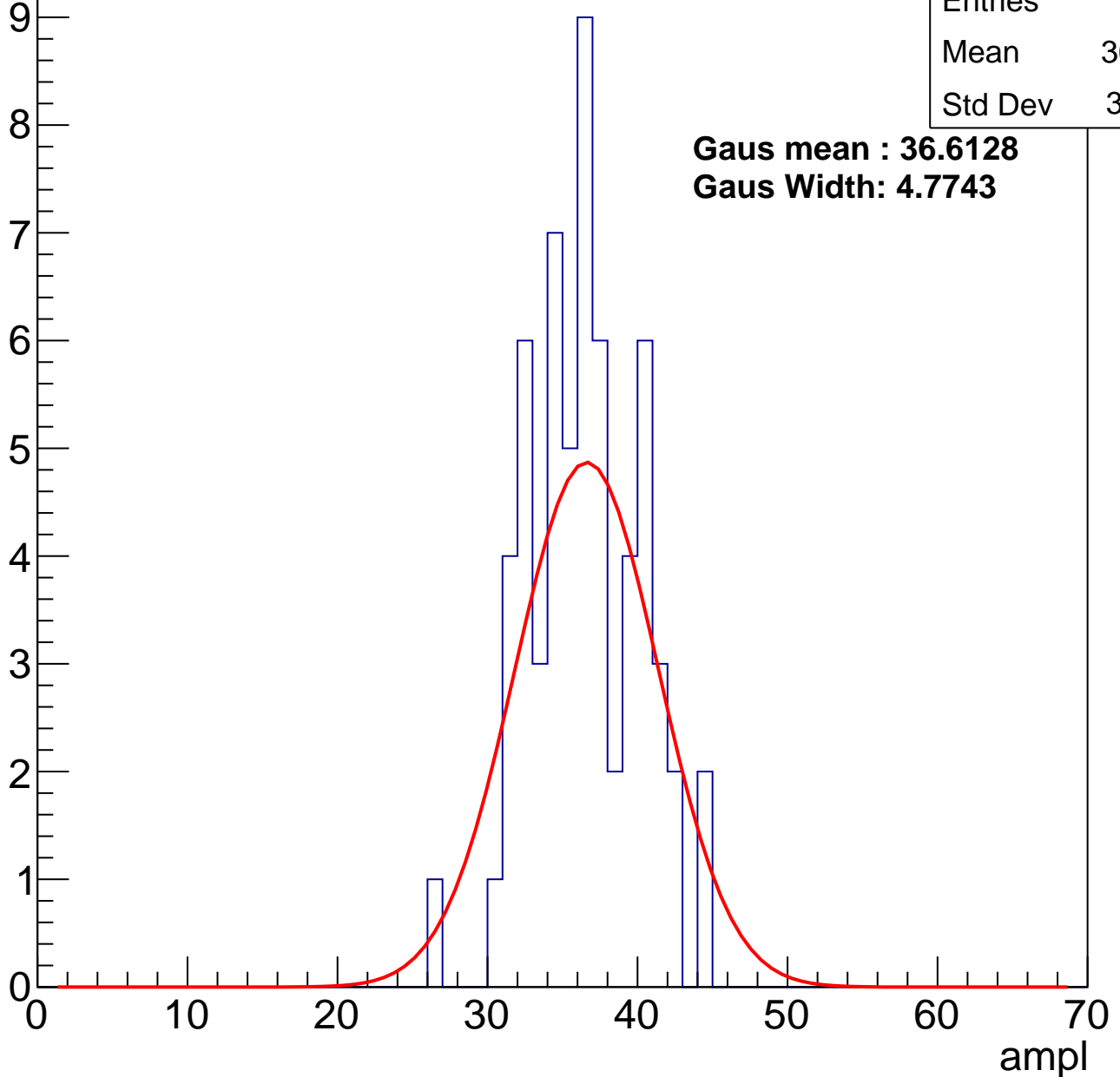
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.02
Std Dev	3.651

Gaus mean : 36.6128

Gaus Width: 4.7743



B1L100S, U14-ch31, adc2

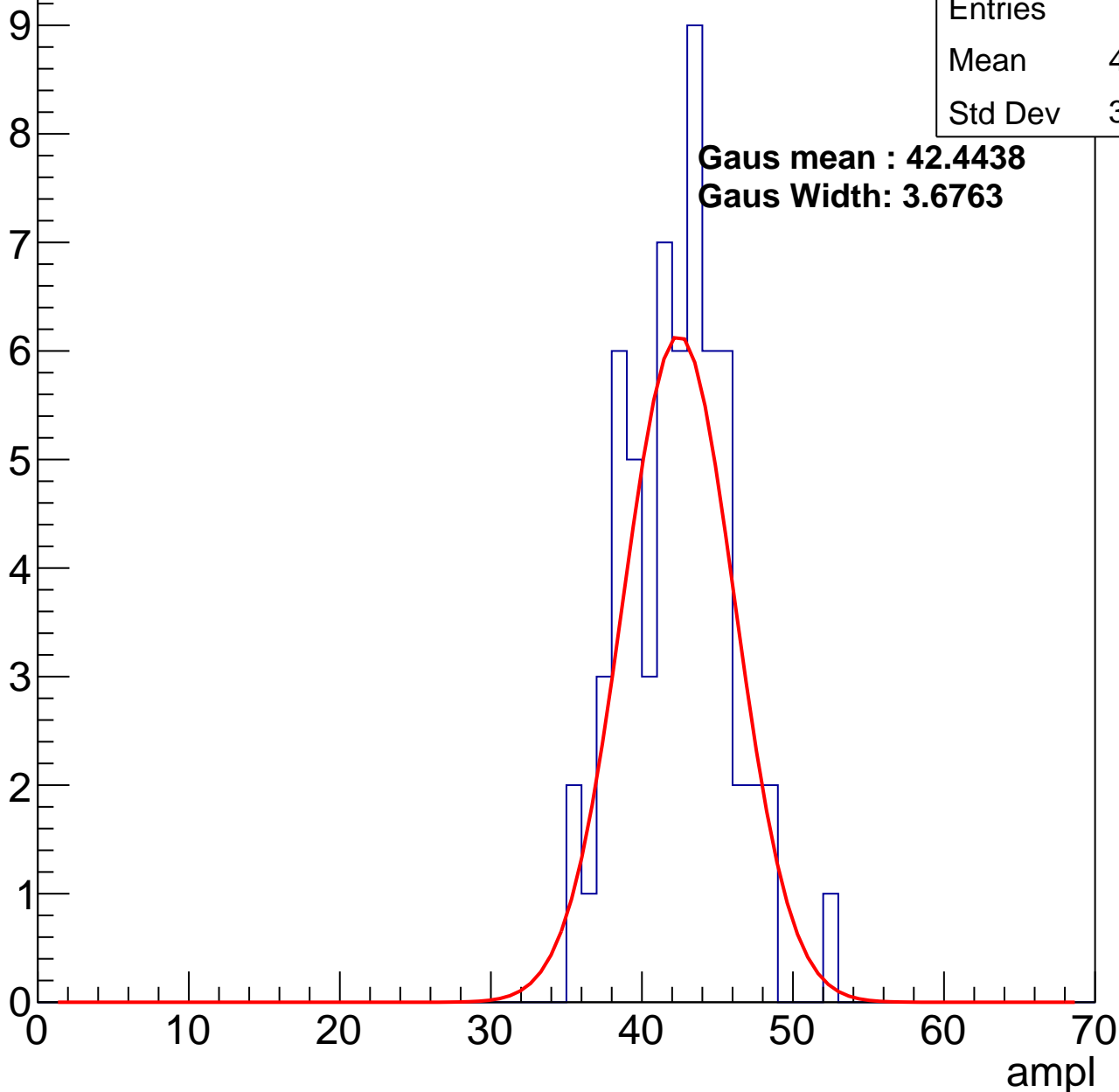
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.87
Std Dev	3.409

Gaus mean : 42.4438

Gaus Width: 3.6763

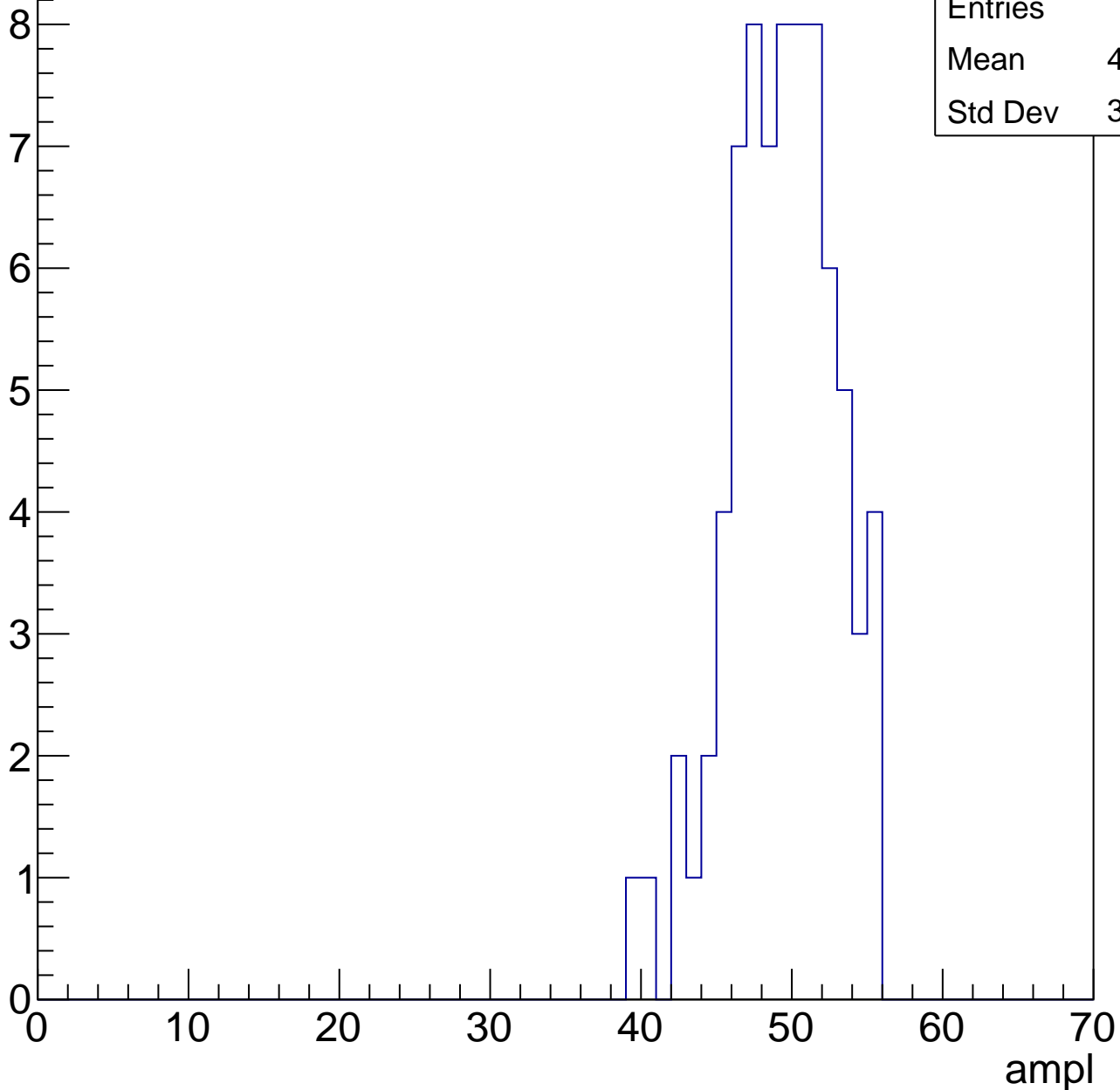


B1L100S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

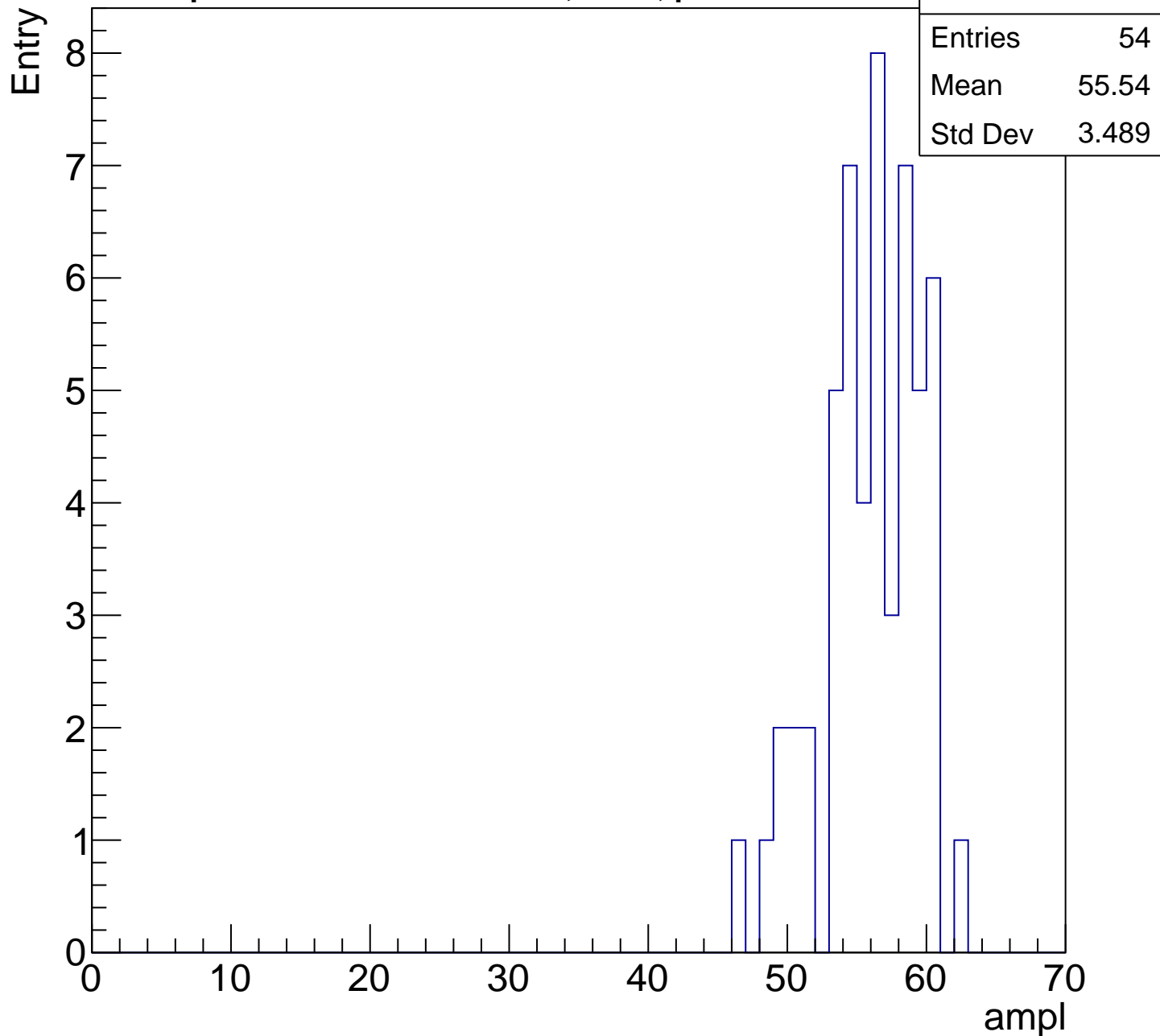
Entry

Entries	75
Mean	48.89
Std Dev	3.508



B1L100S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

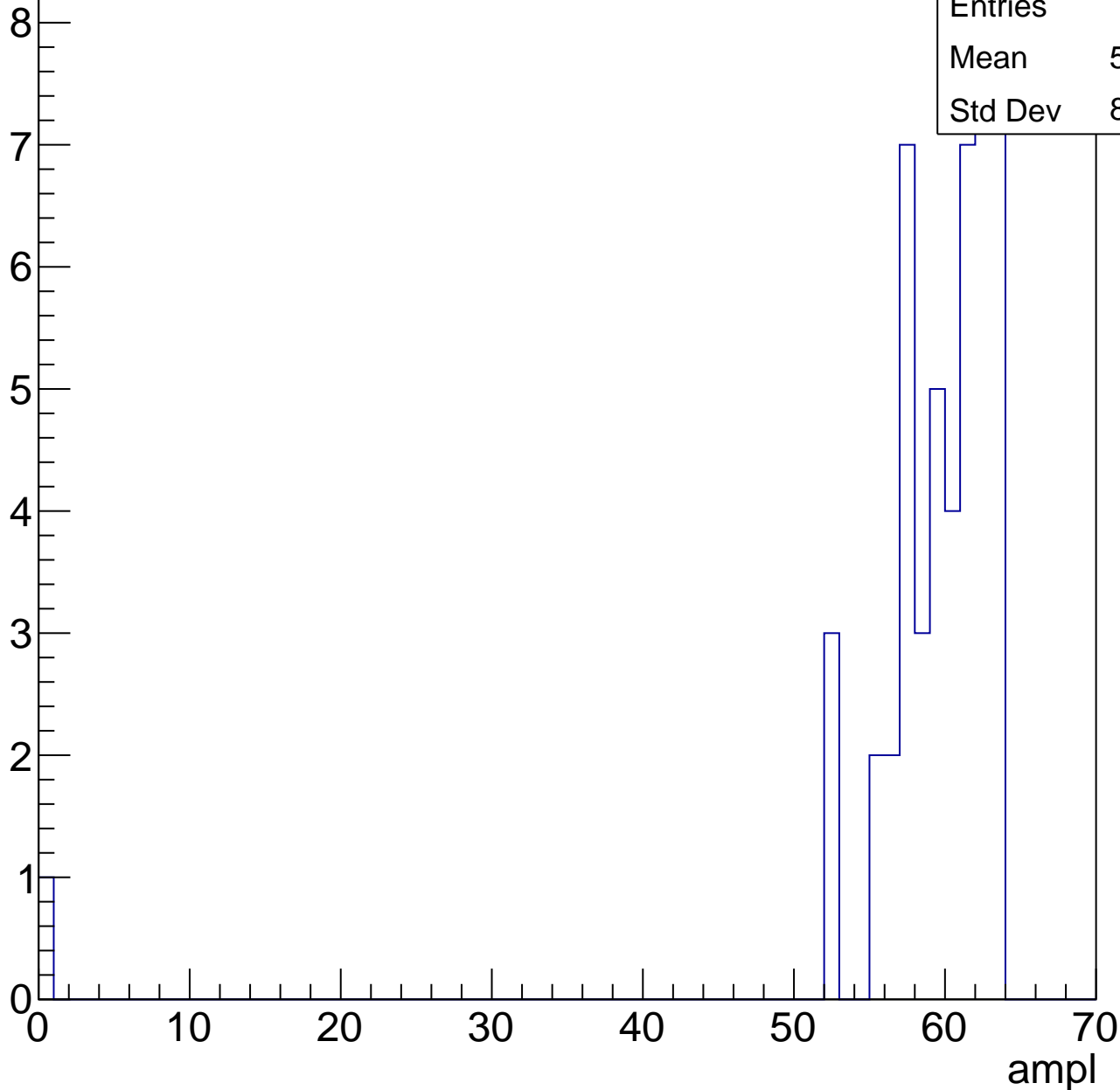


B1L100S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

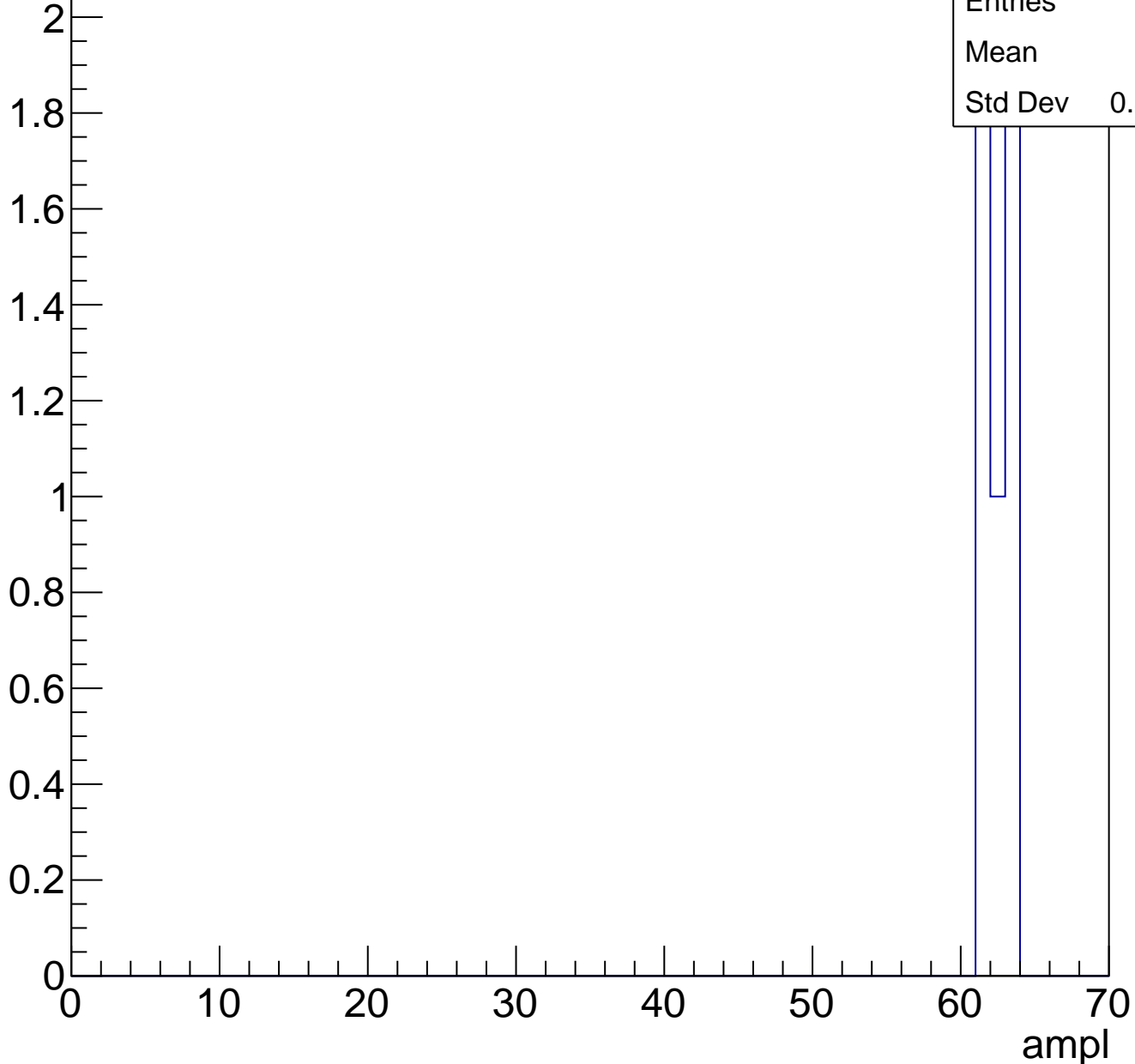
Entries	50
Mean	58.26
Std Dev	8.847



B1L100S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch32, adc0

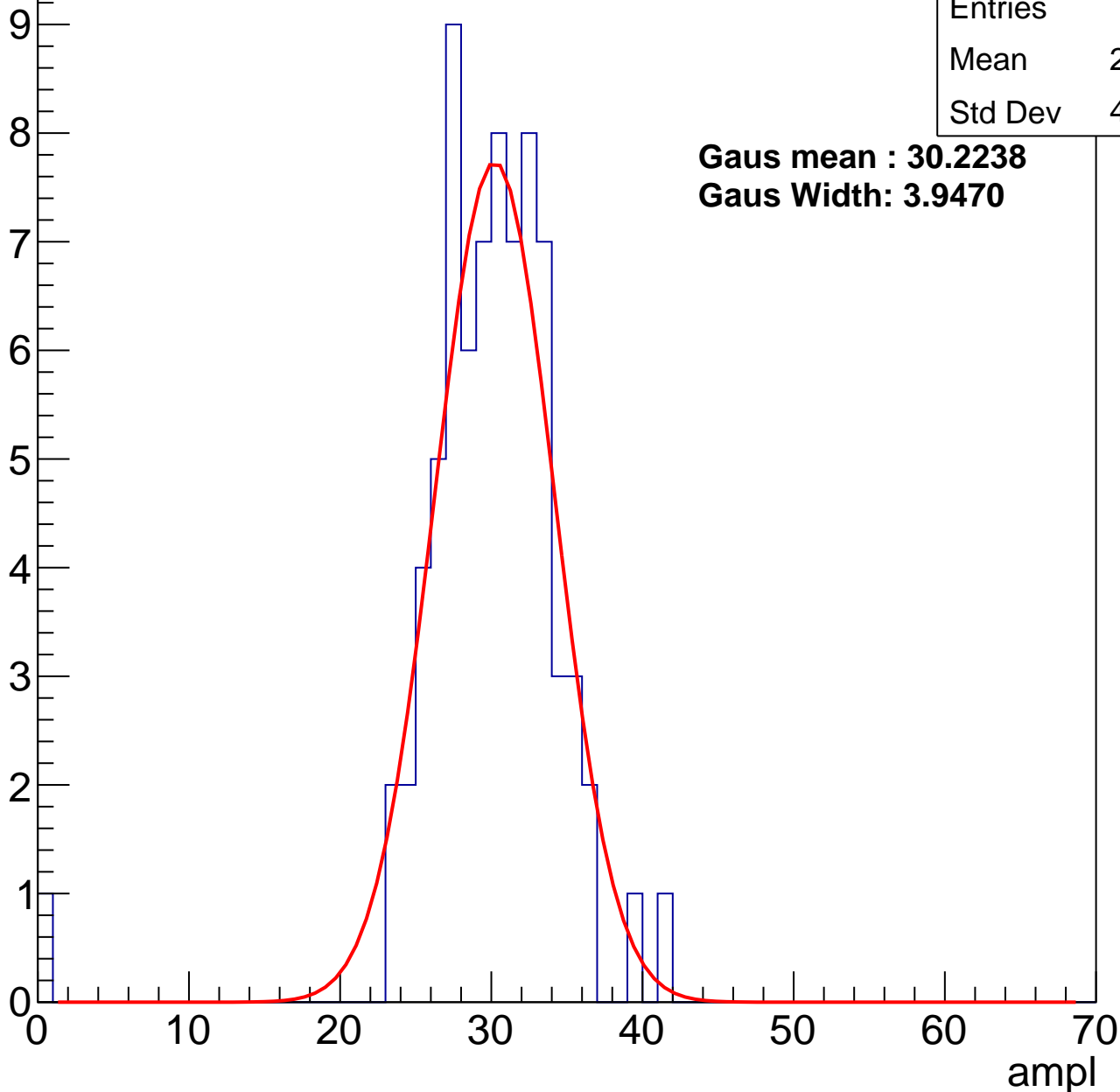
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	29.49
Std Dev	4.922

Gaus mean : 30.2238

Gaus Width: 3.9470



B1L100S, U14-ch32, adc1

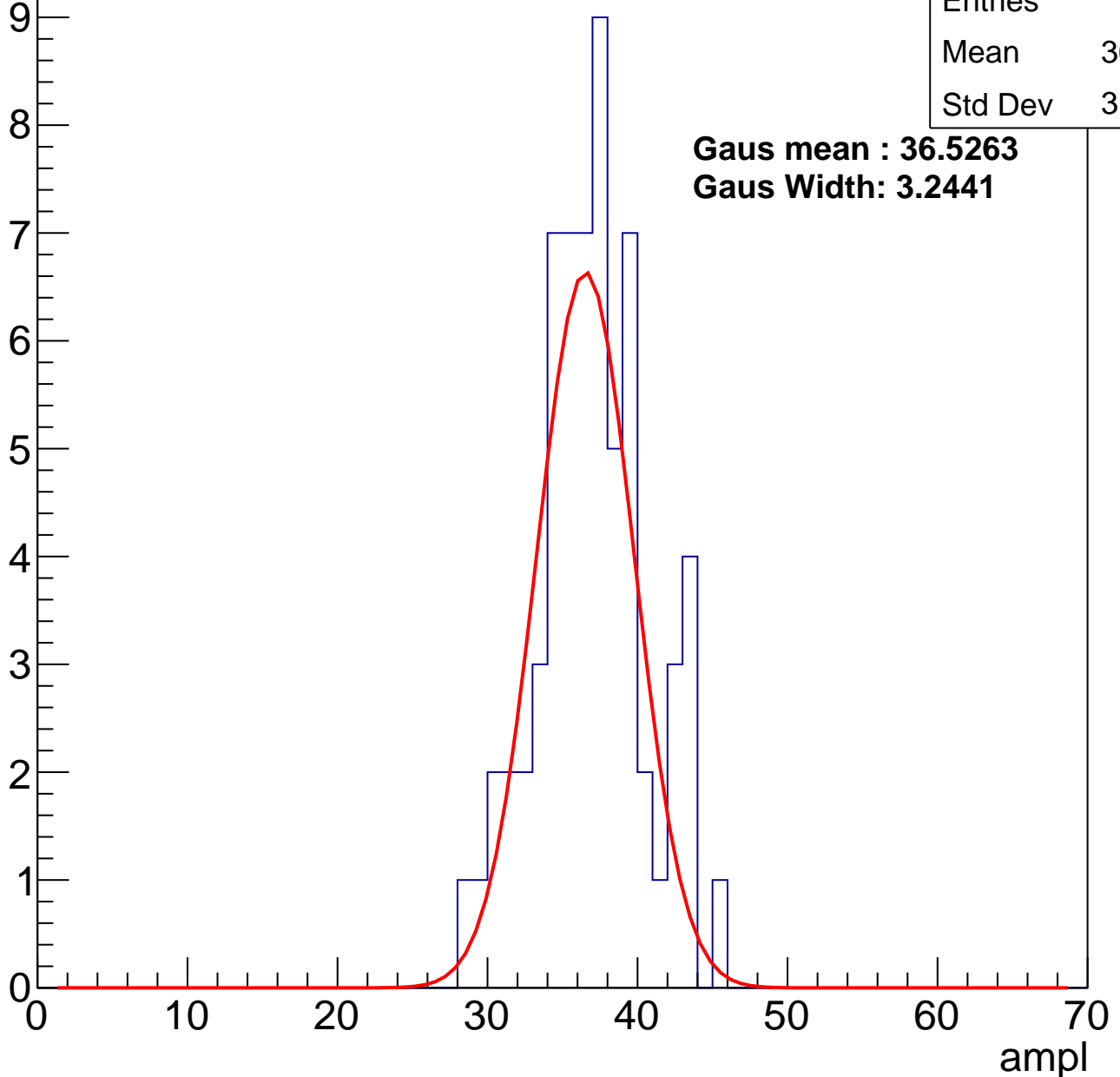
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.52
Std Dev	3.636

Gaus mean : 36.5263

Gaus Width: 3.2441



B1L100S, U14-ch32, adc2

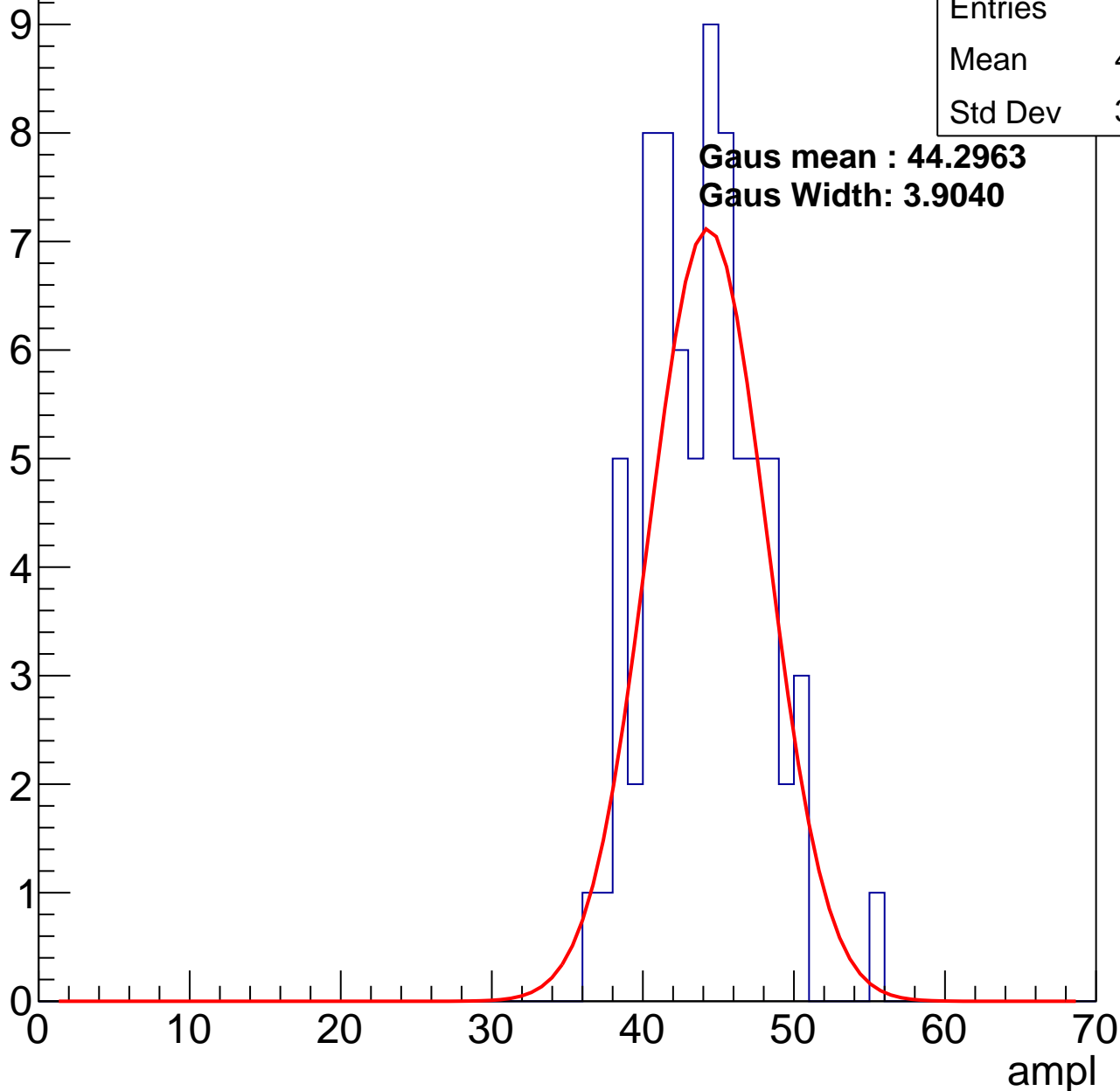
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.51
Std Dev	3.651

Gaus mean : 44.2963

Gaus Width: 3.9040

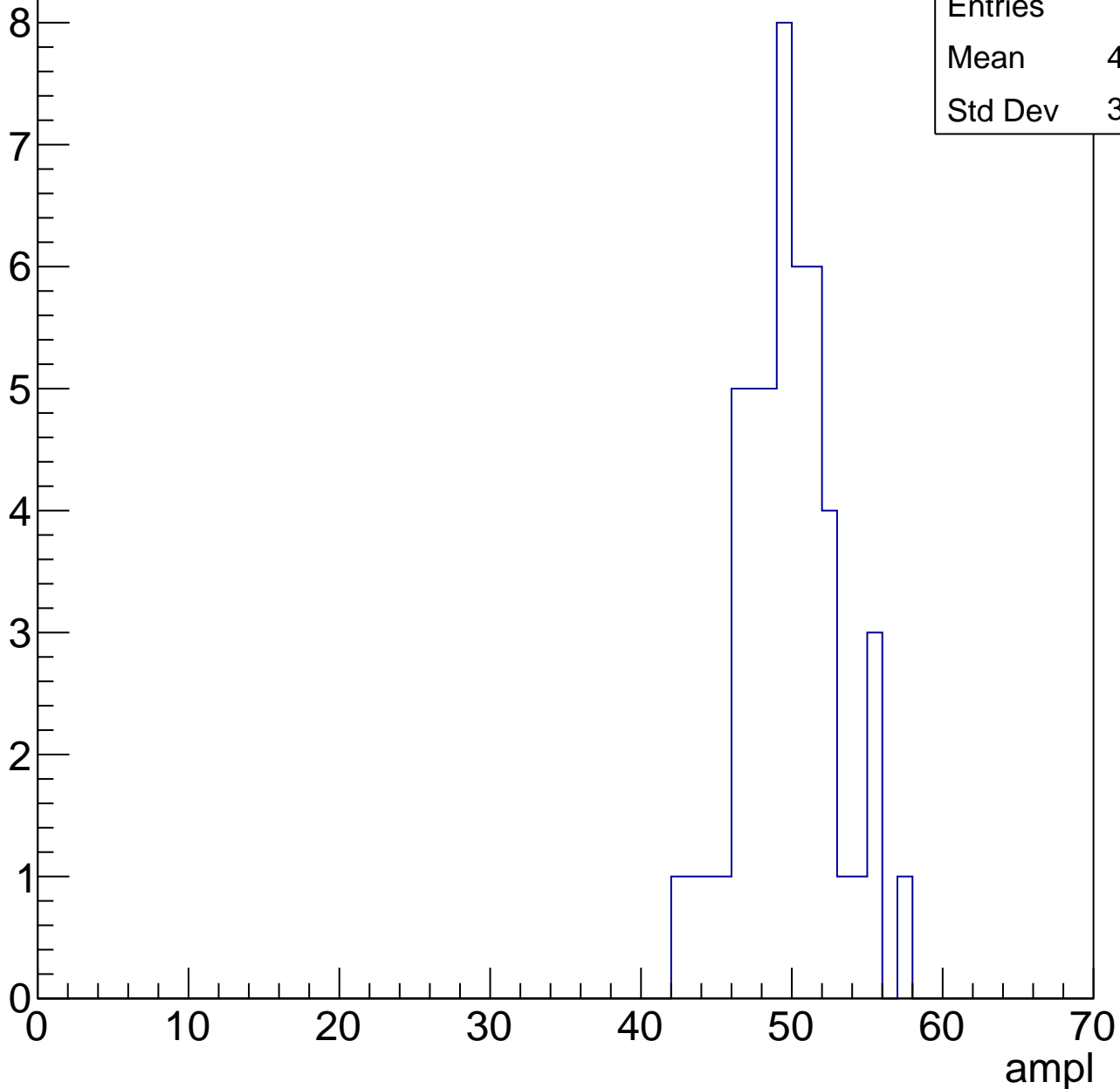


B1L100S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

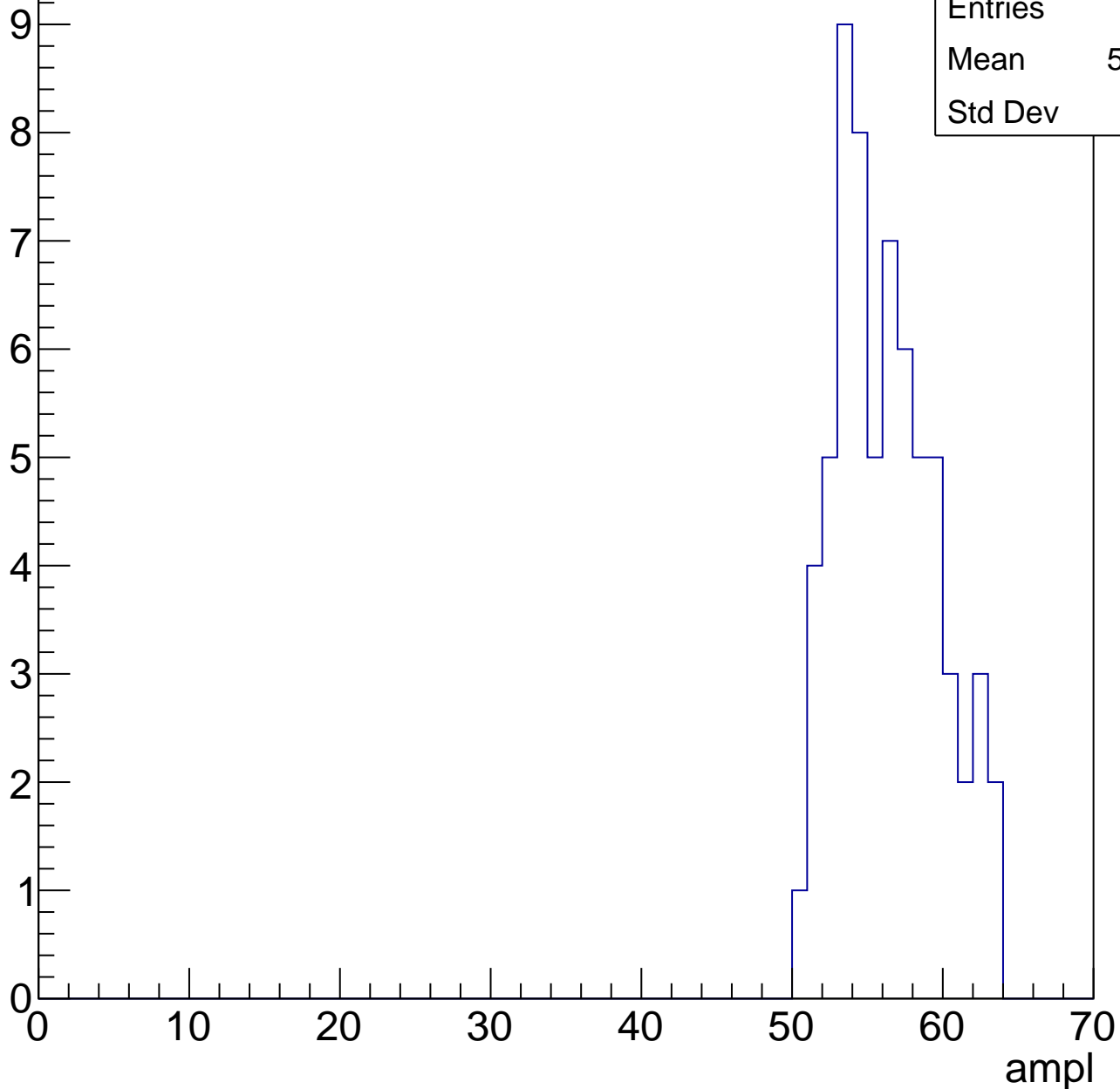
Entries	49
Mean	49.27
Std Dev	3.102



B1L100S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



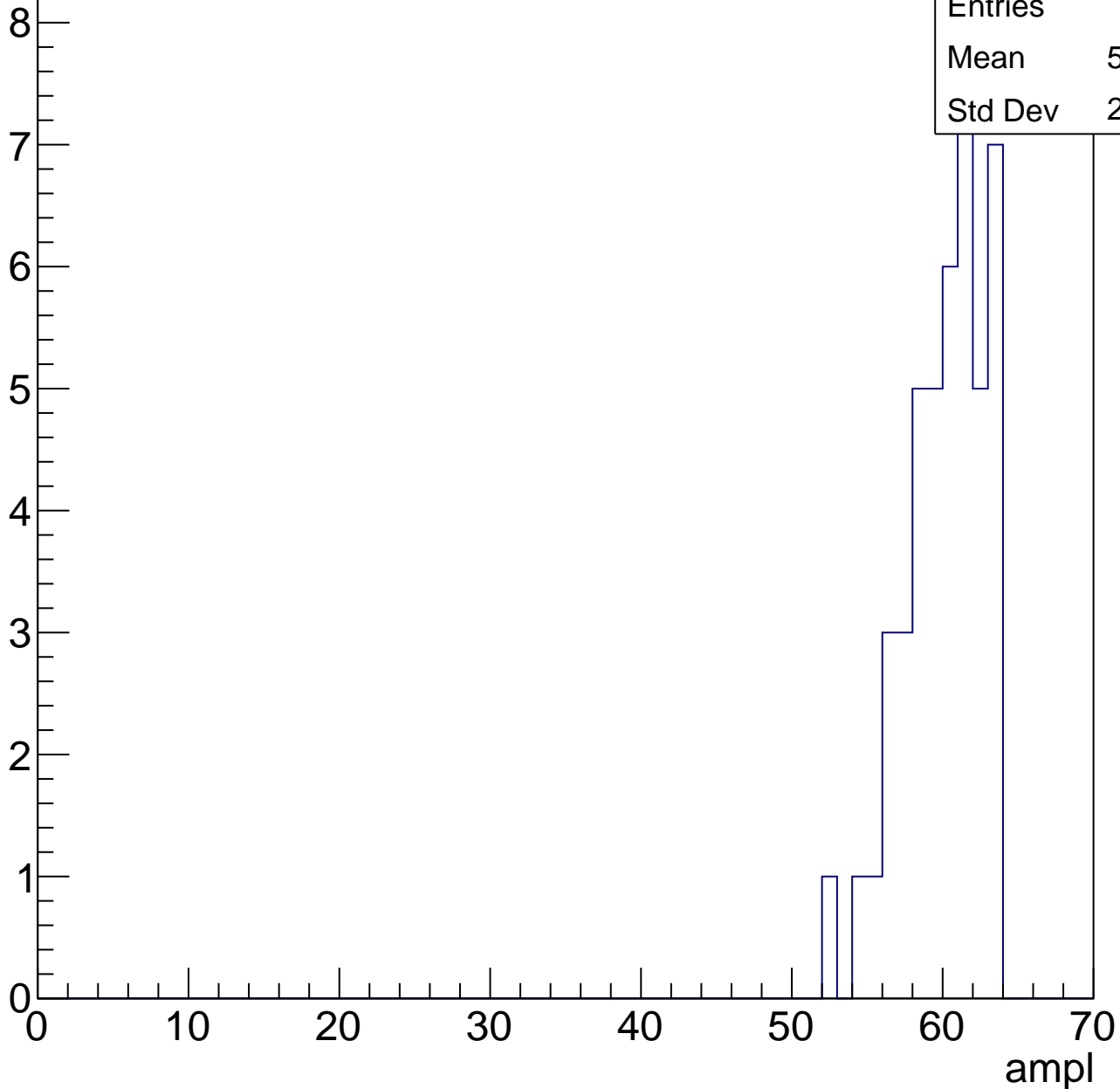
Entries	65
Mean	55.86
Std Dev	3.3

B1L100S, U14-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	59.64
Std Dev	2.626



B1L100S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

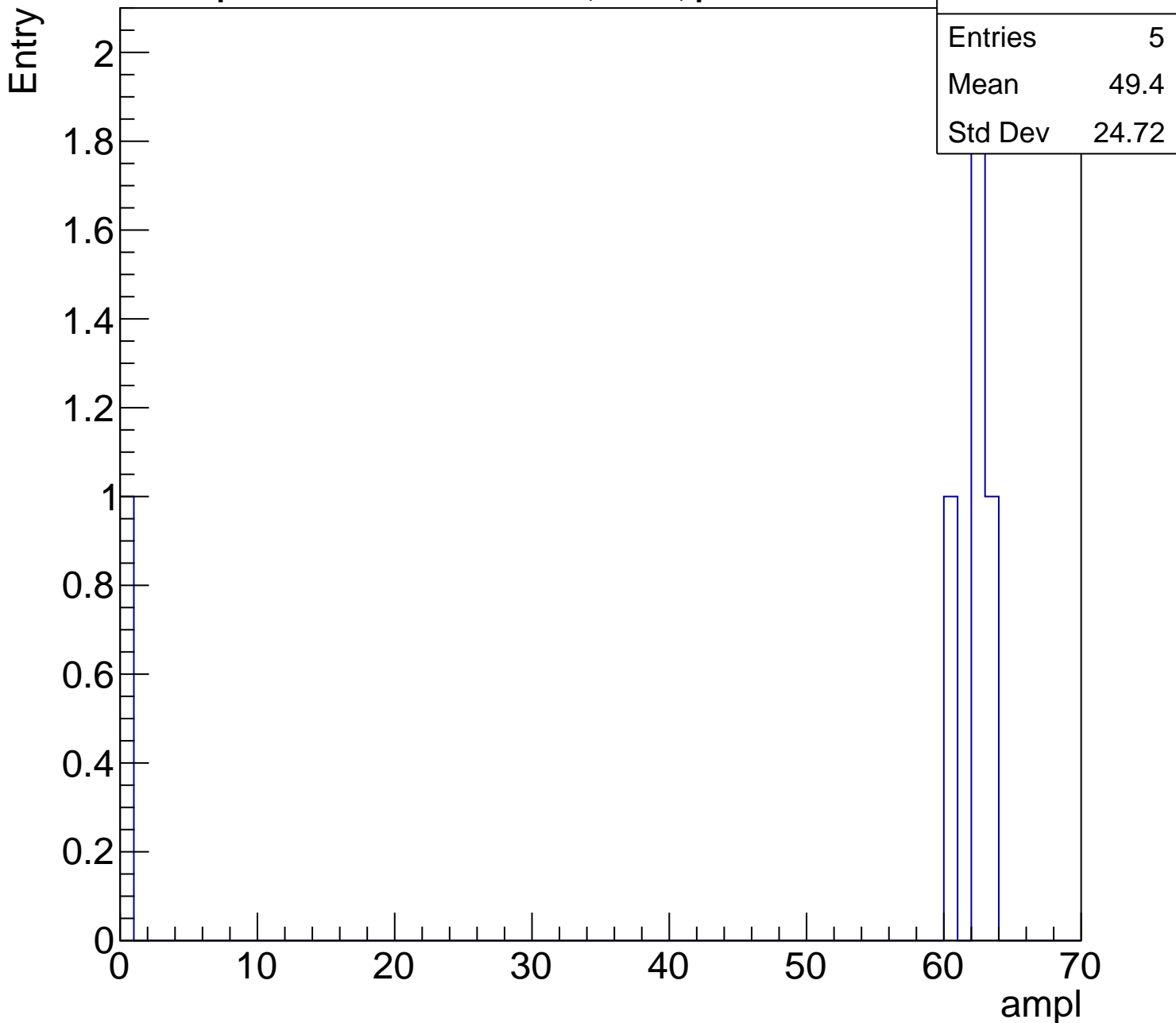
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.72

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U14-ch33, adc0

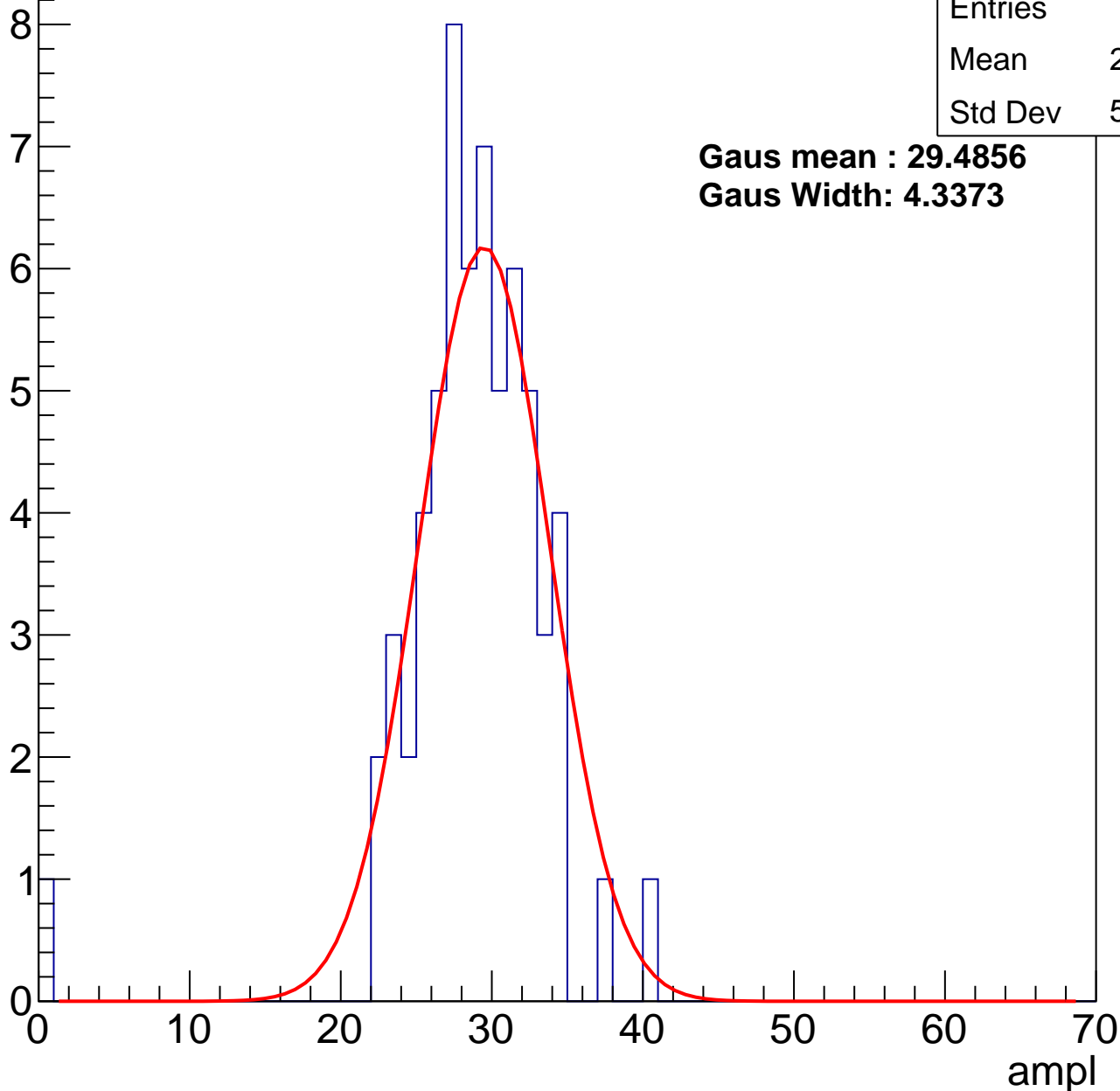
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.35
Std Dev	5.078

Gaus mean : 29.4856

Gaus Width: 4.3373



B1L100S, U14-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	35.92
Std Dev	3.148

Gaus mean : 36.5874

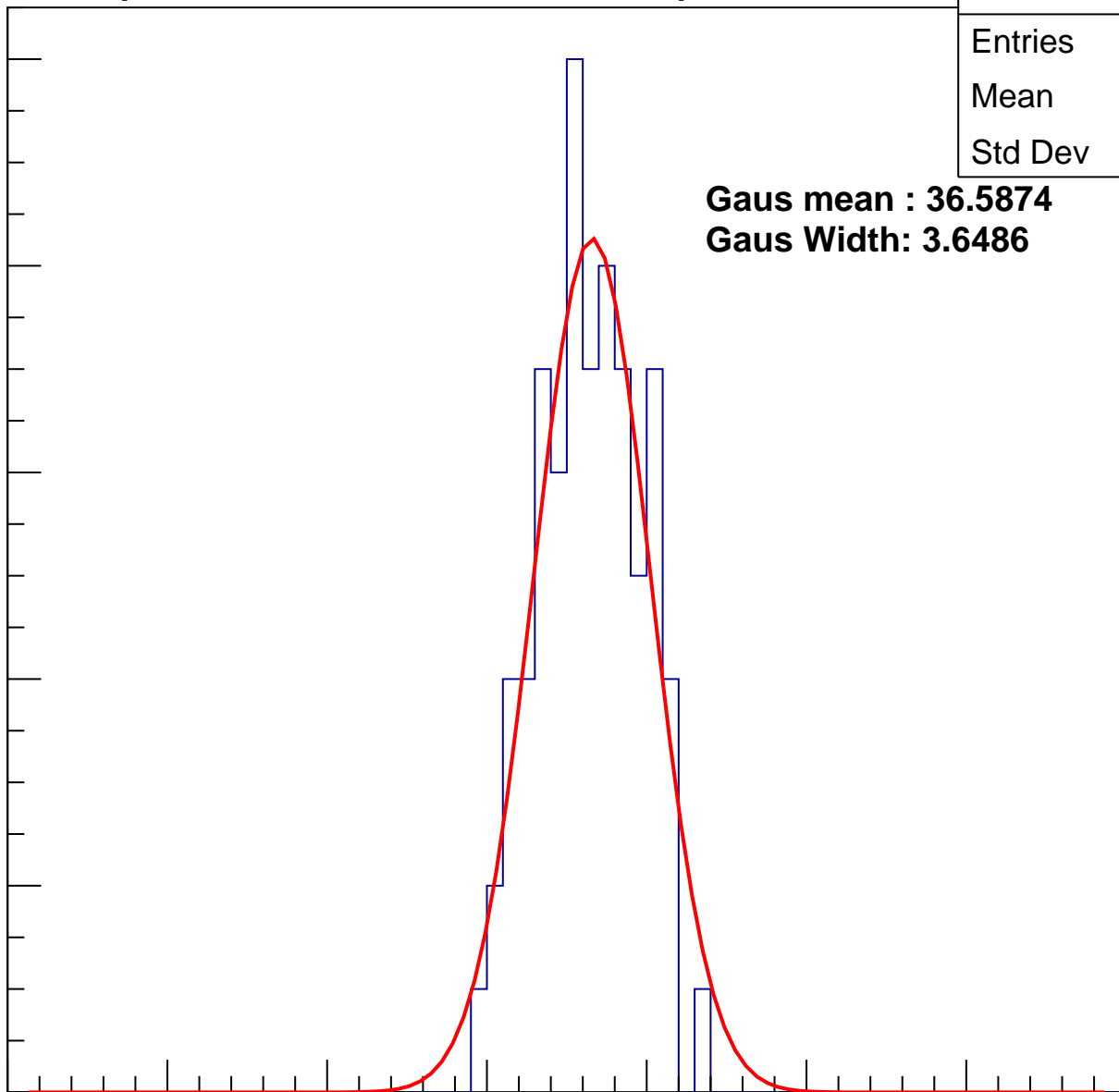
Gaus Width: 3.6486

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch33, adc2

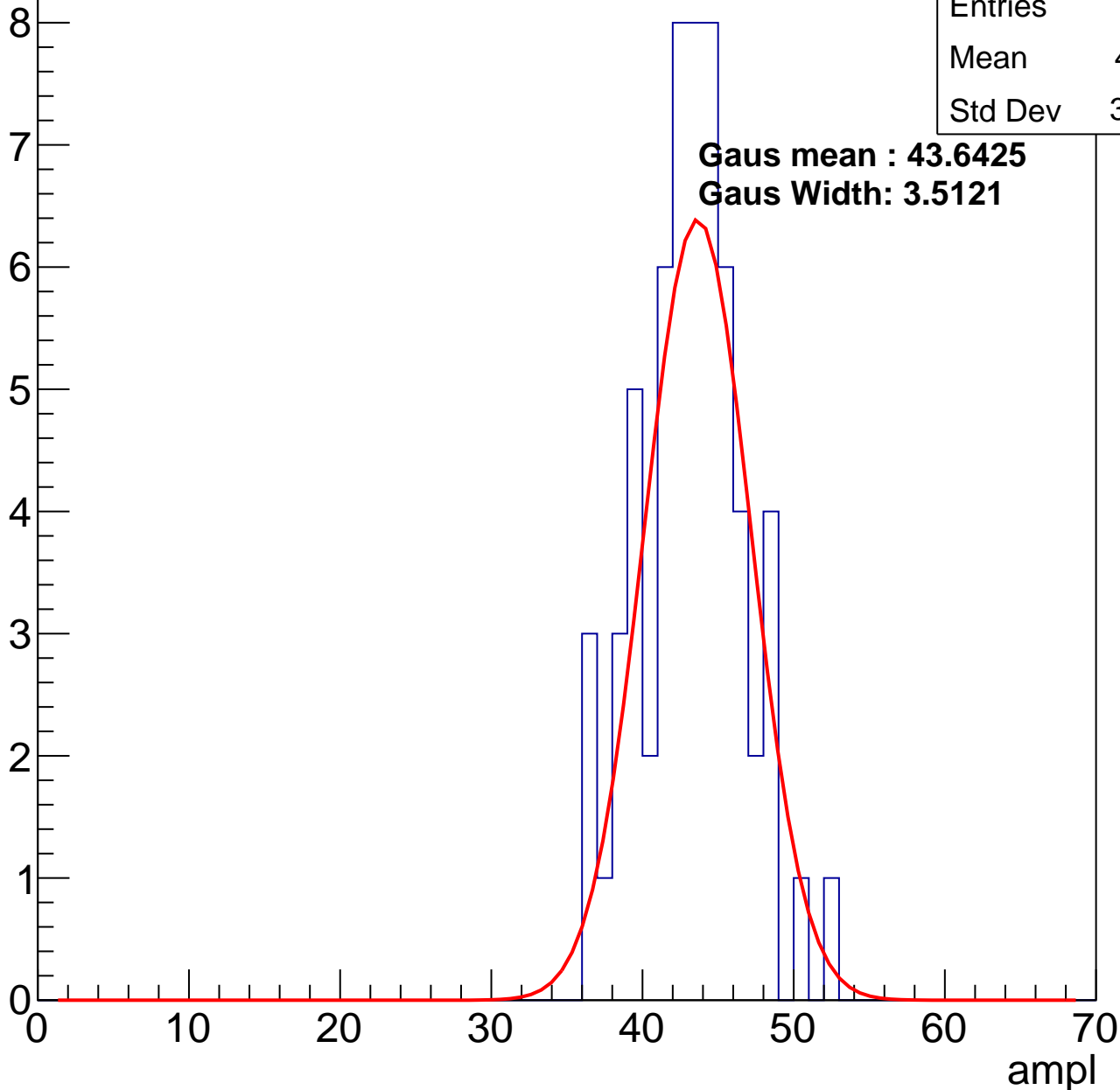
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.81
Std Dev	3.398

Gaus mean : 43.6425

Gaus Width: 3.5121

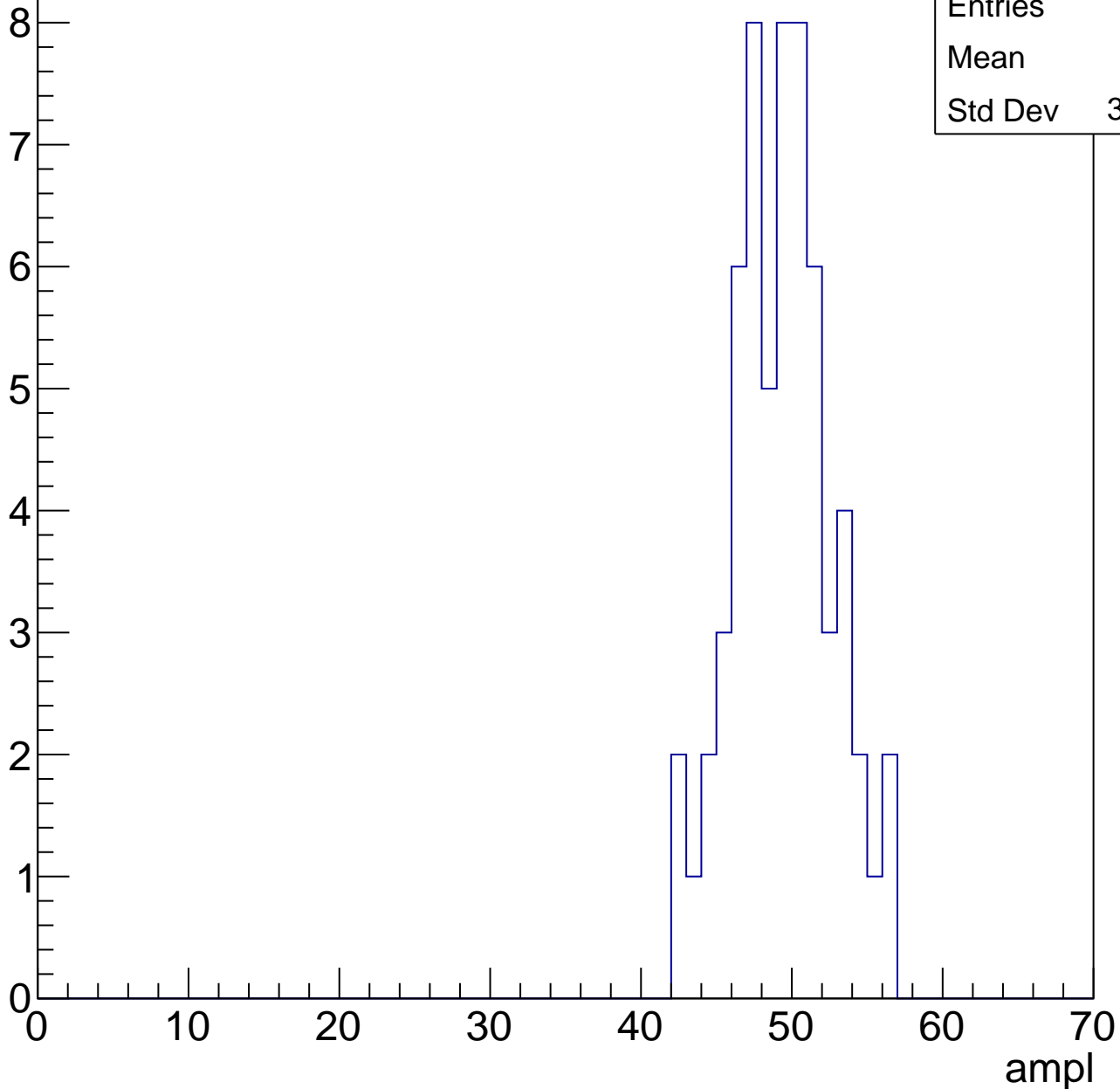


B1L100S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48.9
Std Dev	3.212

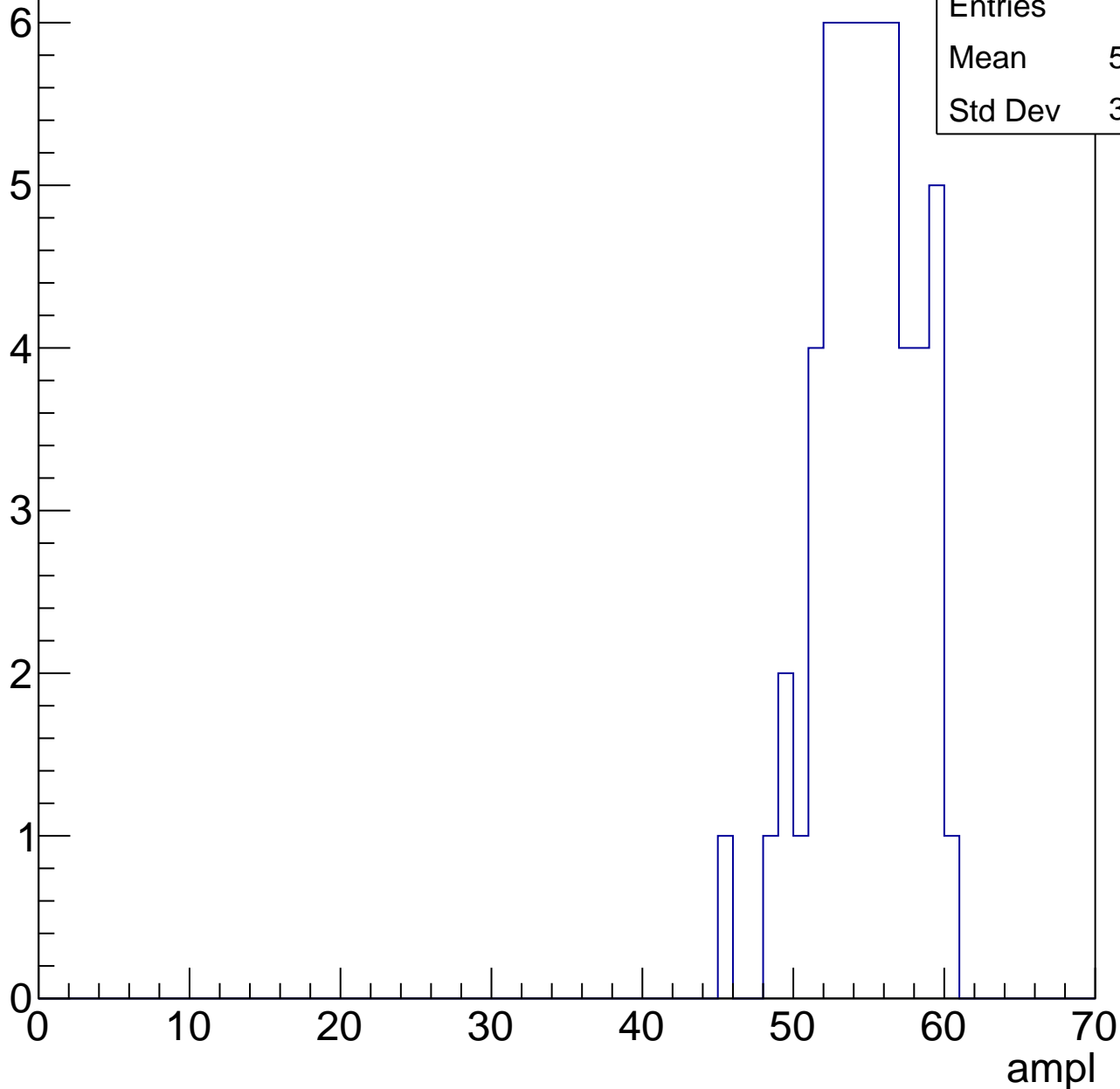


B1L100S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

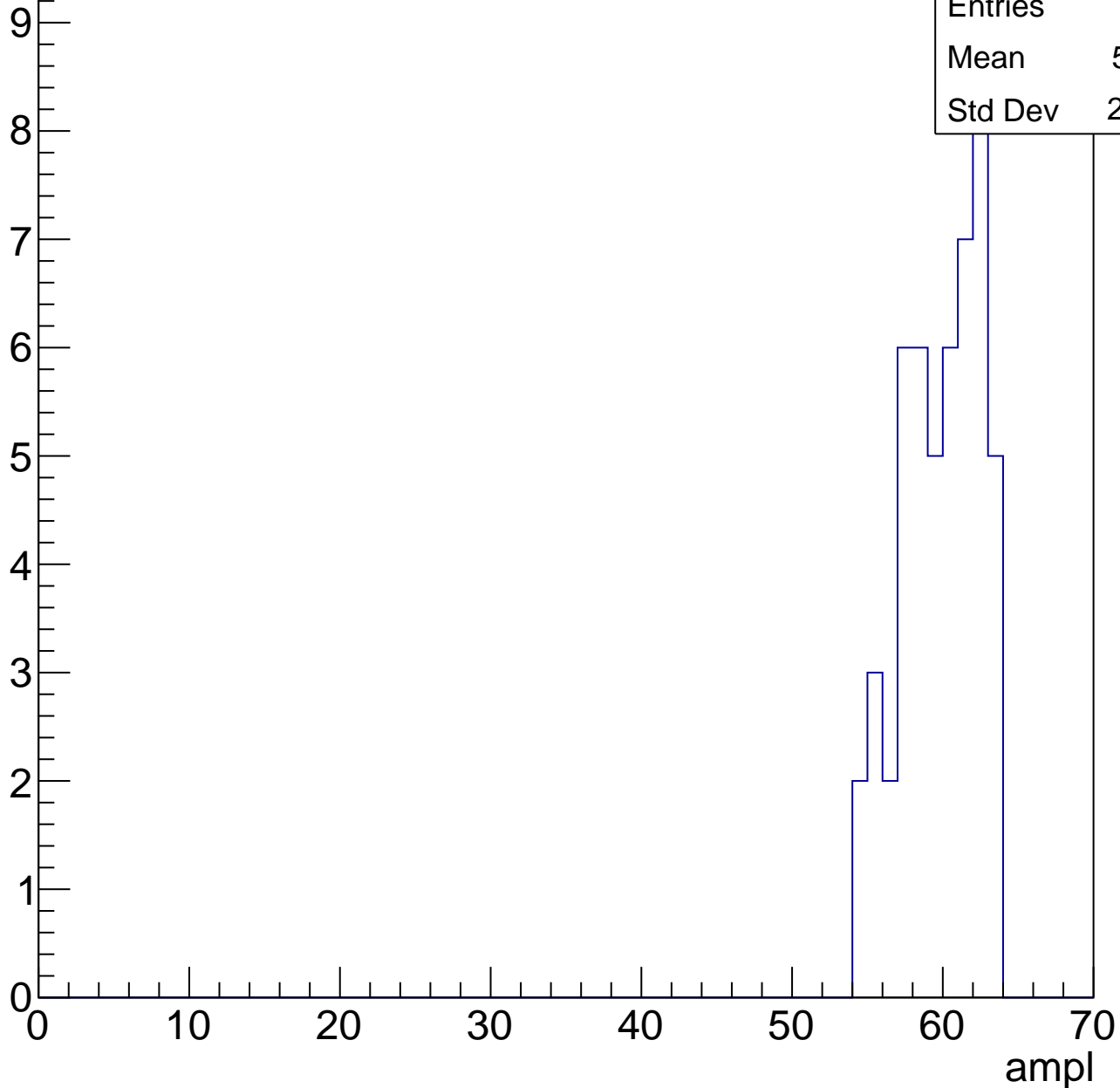
Entries	53
Mean	54.34
Std Dev	3.174



B1L100S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

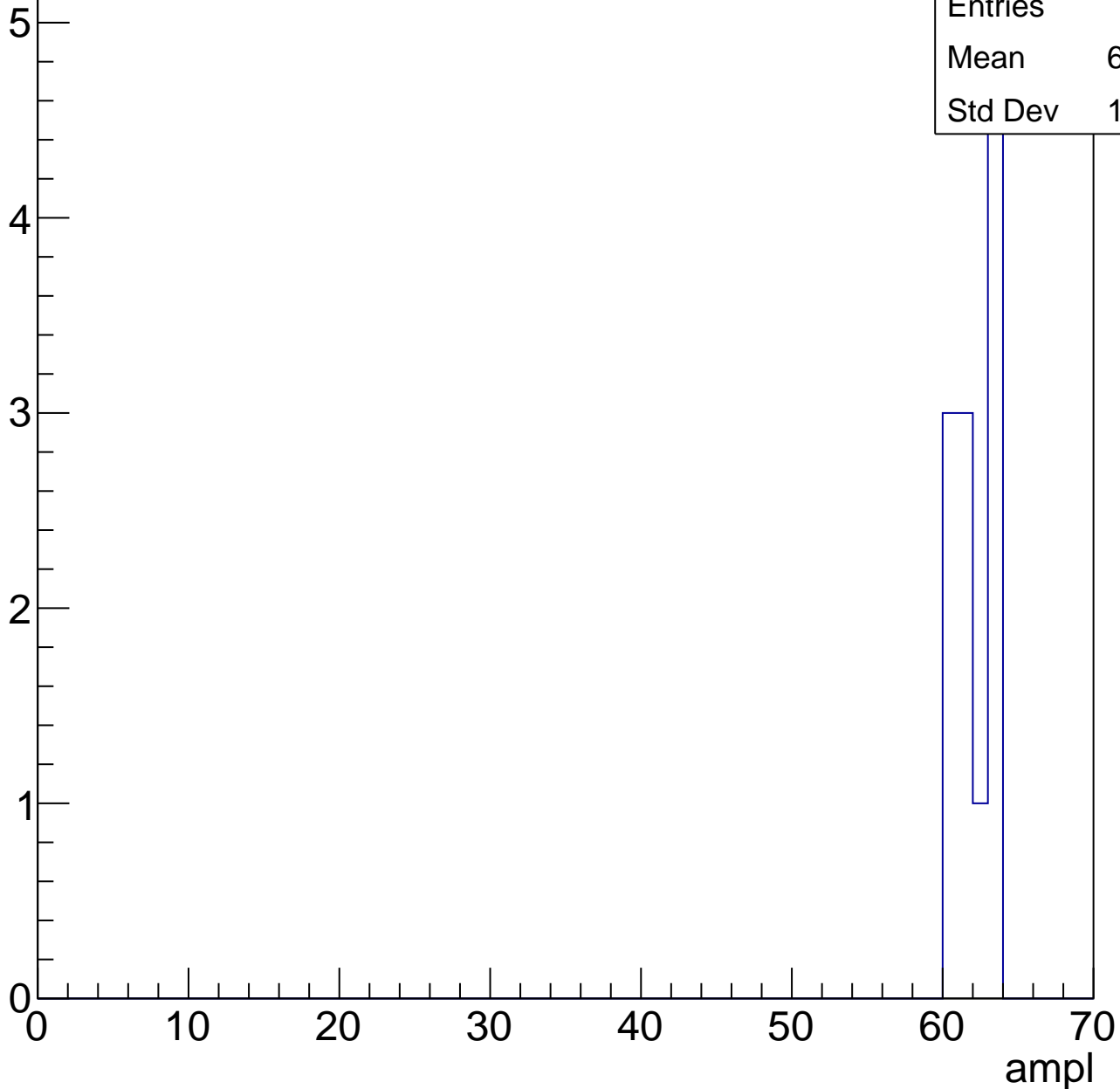


B1L100S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

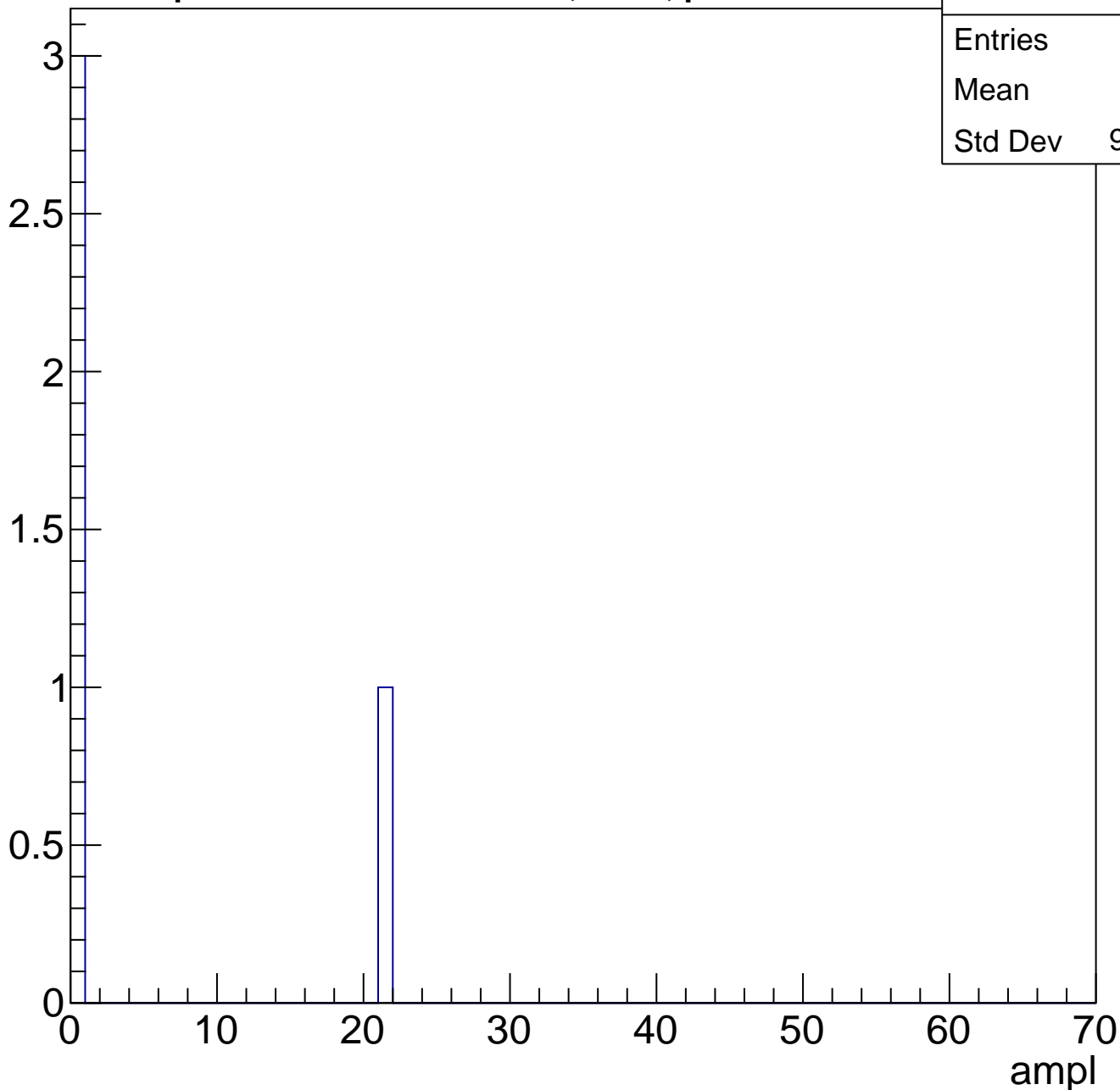
Entries	12
Mean	61.67
Std Dev	1.247



B1L100S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L100S, U14-ch34, adc0

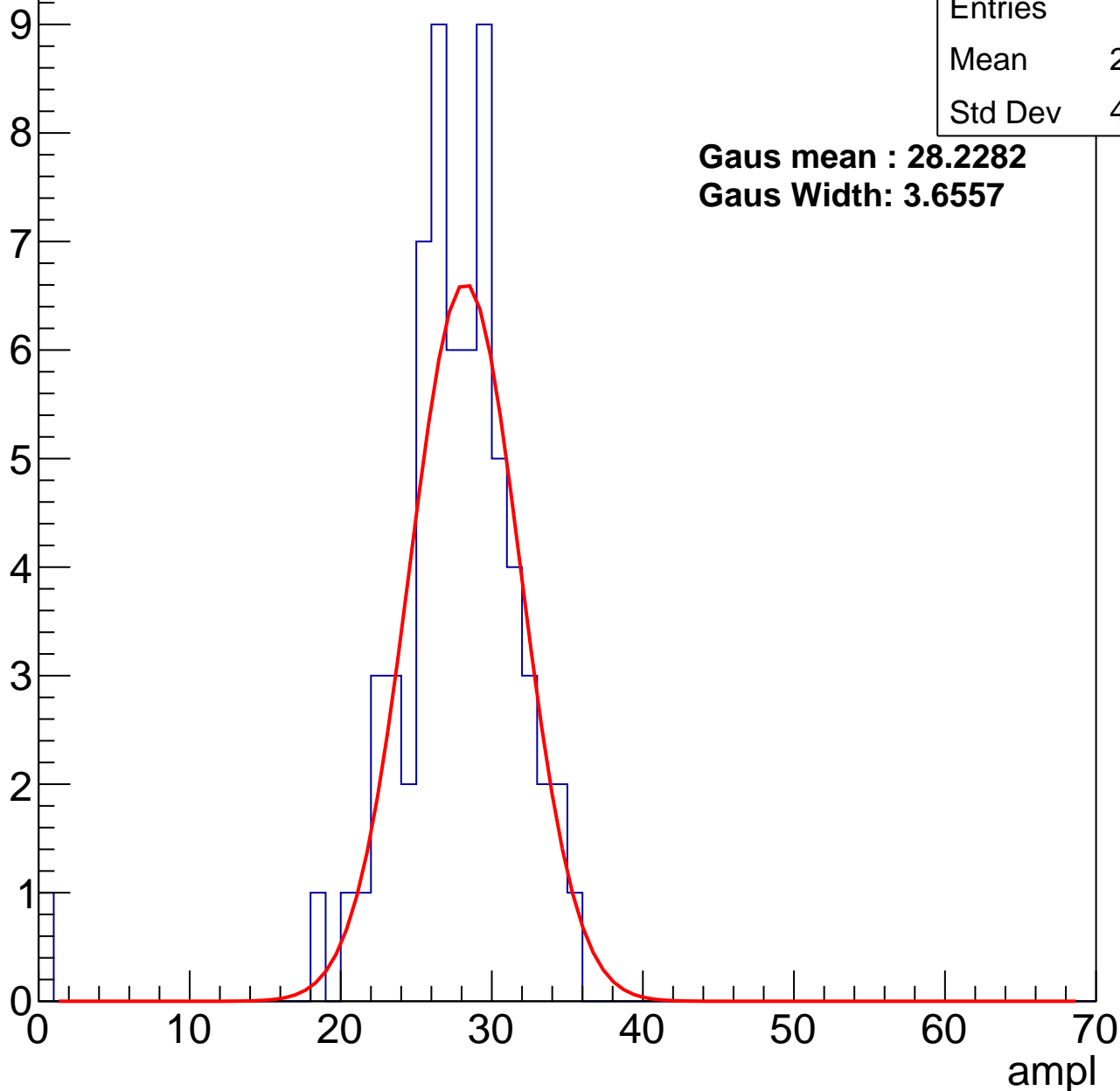
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	26.98
Std Dev	4.819

Gaus mean : 28.2282

Gaus Width: 3.6557



B1L100S, U14-ch34, adc1

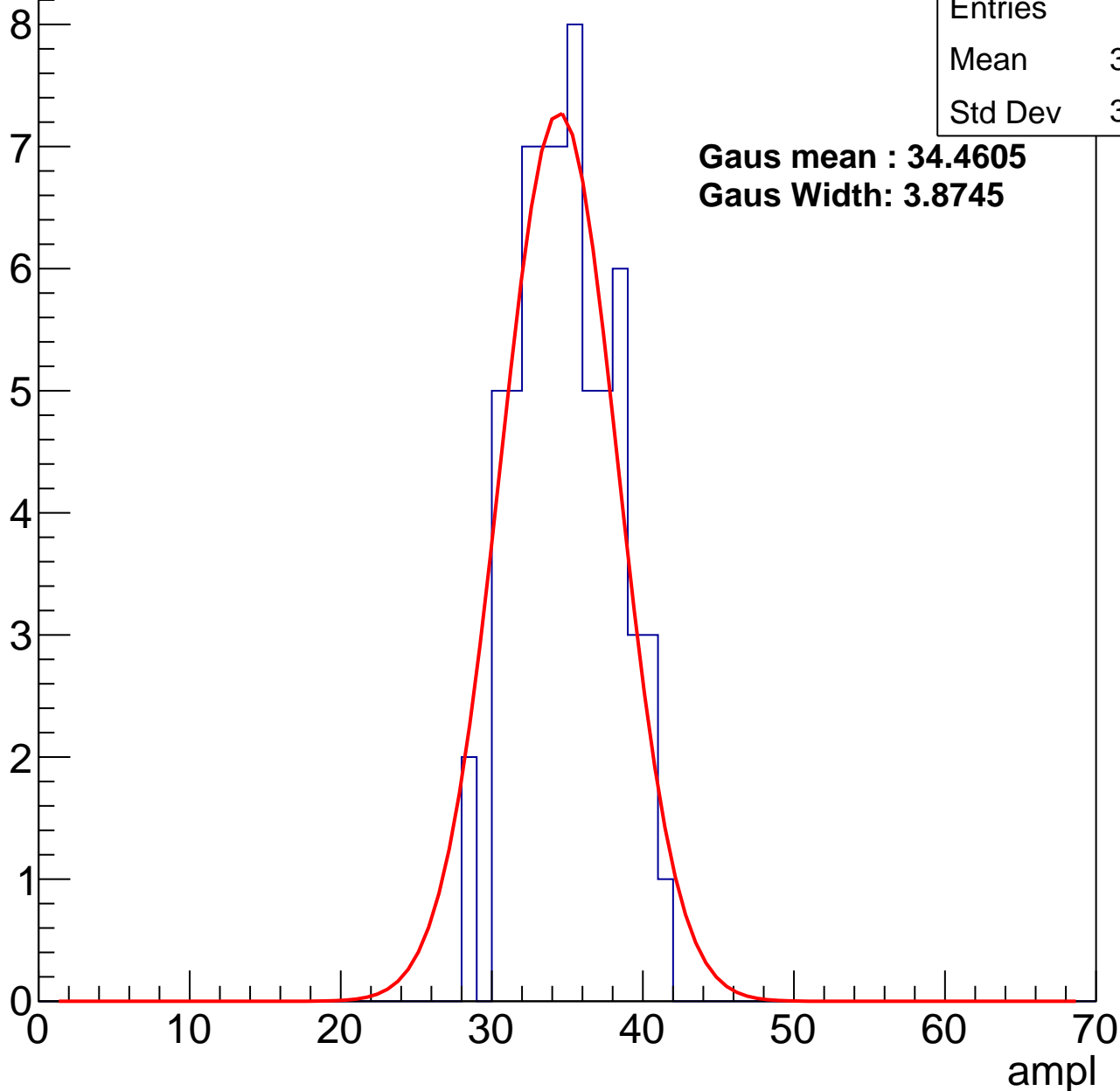
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	34.45
Std Dev	3.112

Gaus mean : 34.4605

Gaus Width: 3.8745



B1L100S, U14-ch34, adc2

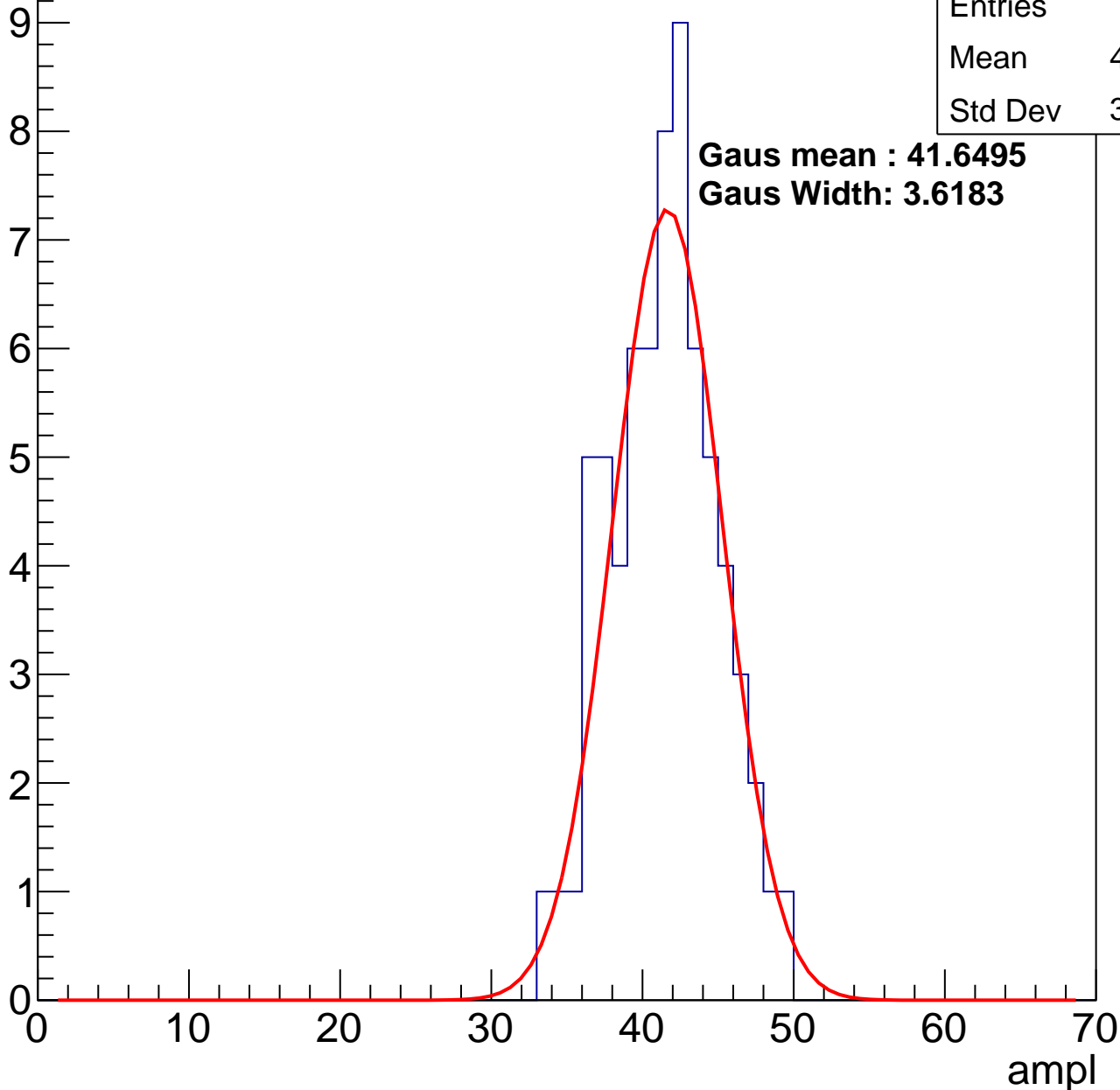
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	40.97
Std Dev	3.477

Gaus mean : 41.6495

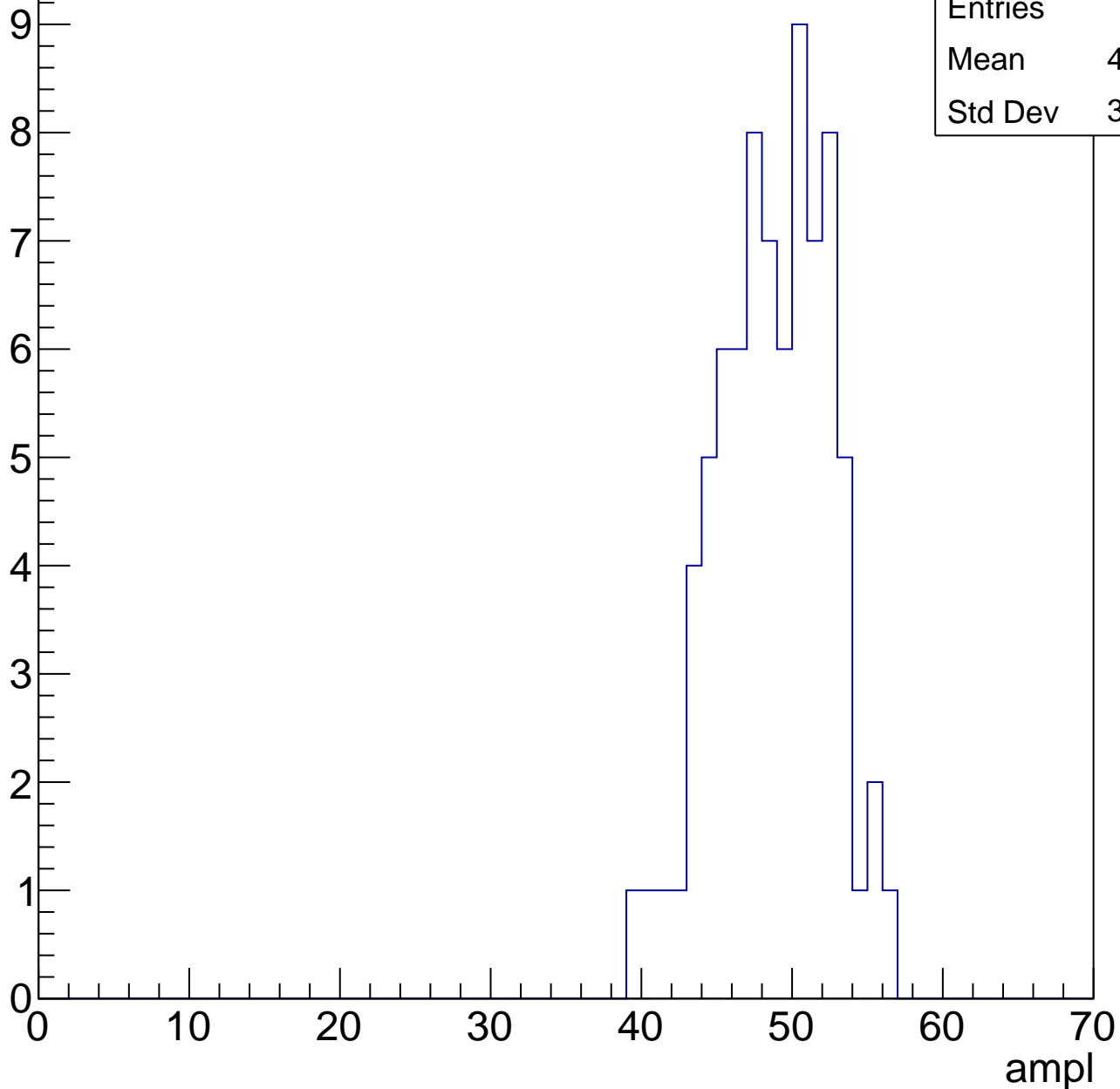
Gaus Width: 3.6183



B1L100S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

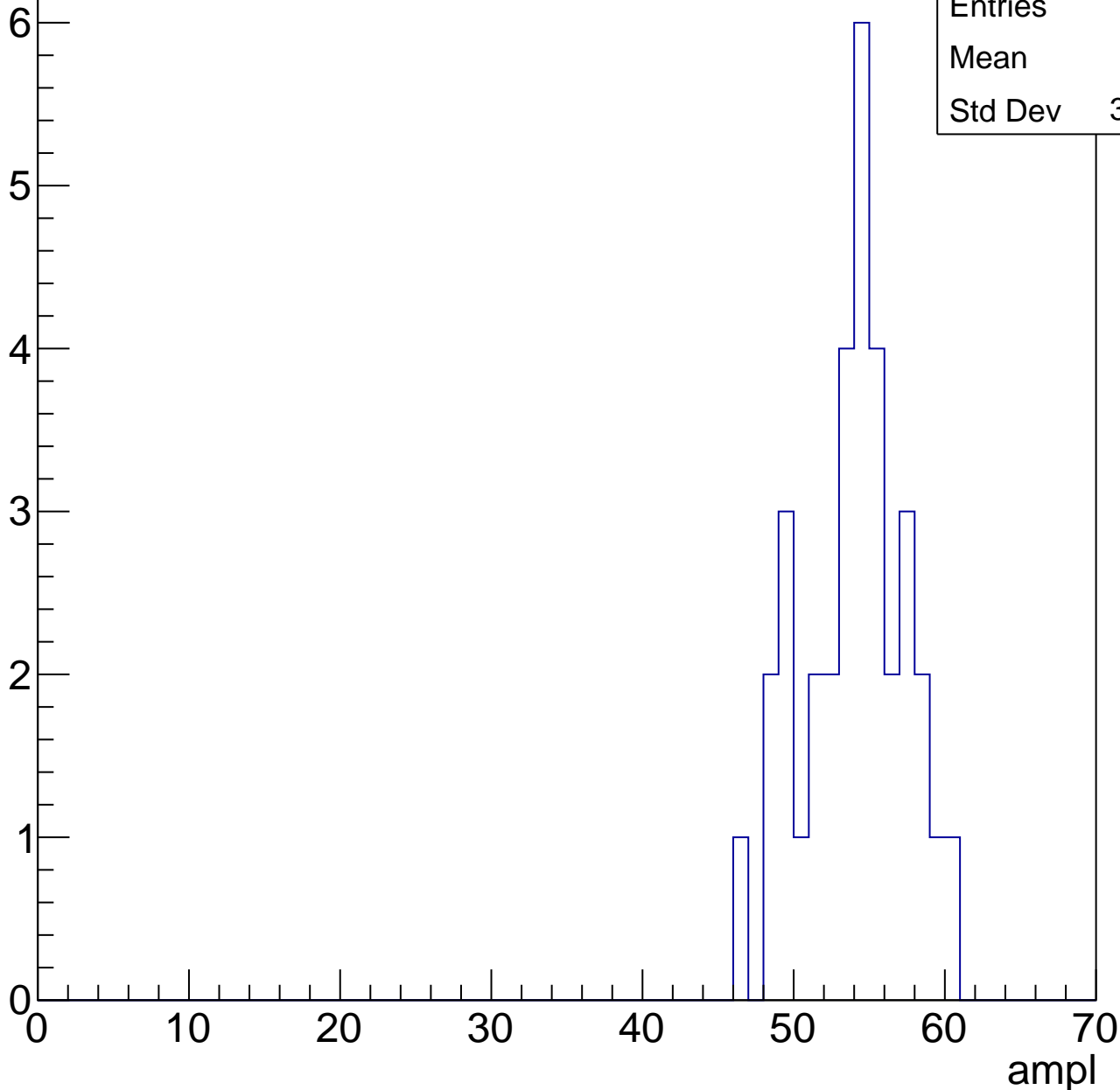


B1L100S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	53.5
Std Dev	3.345

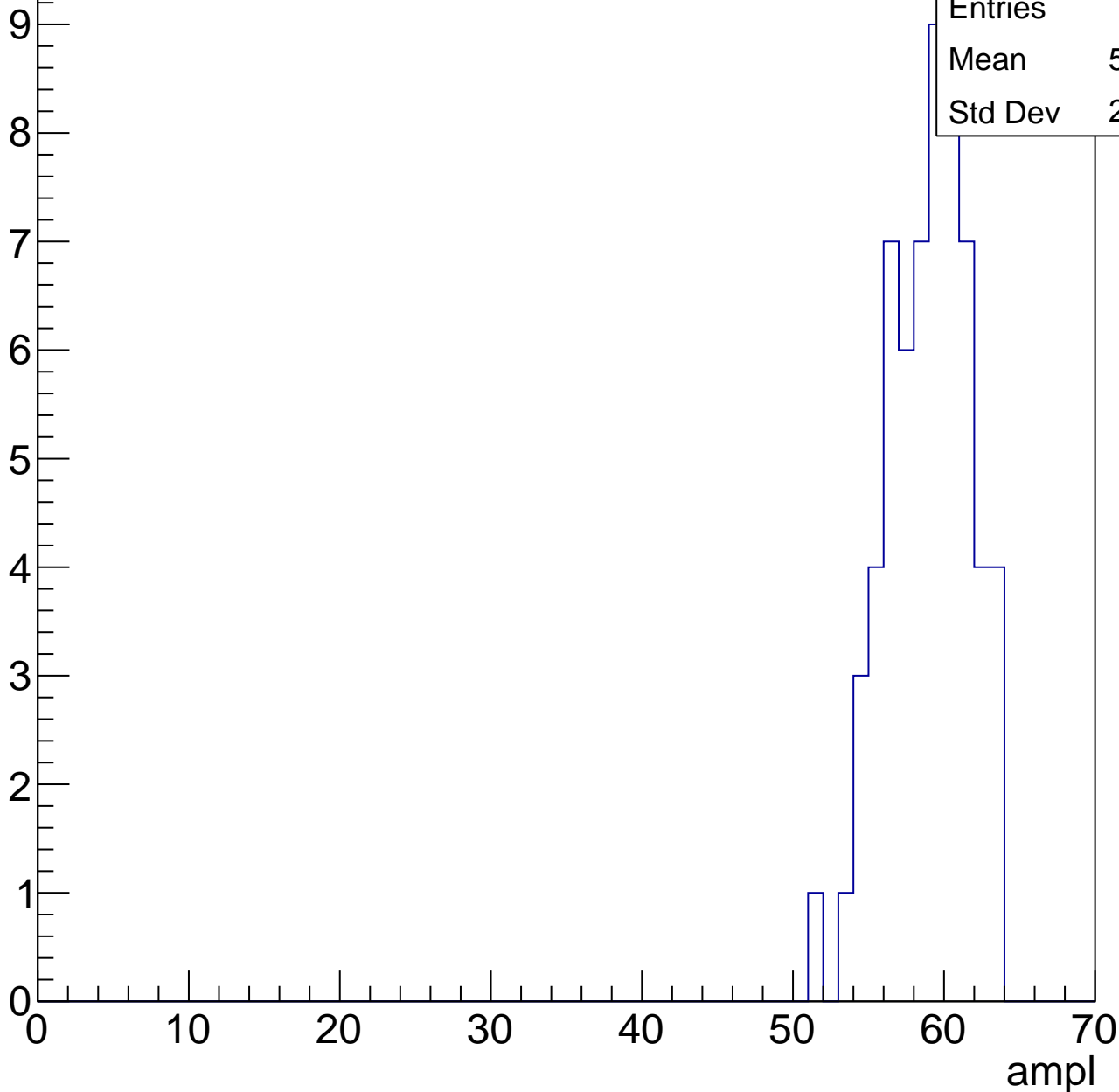


B1L100S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	58.43
Std Dev	2.713

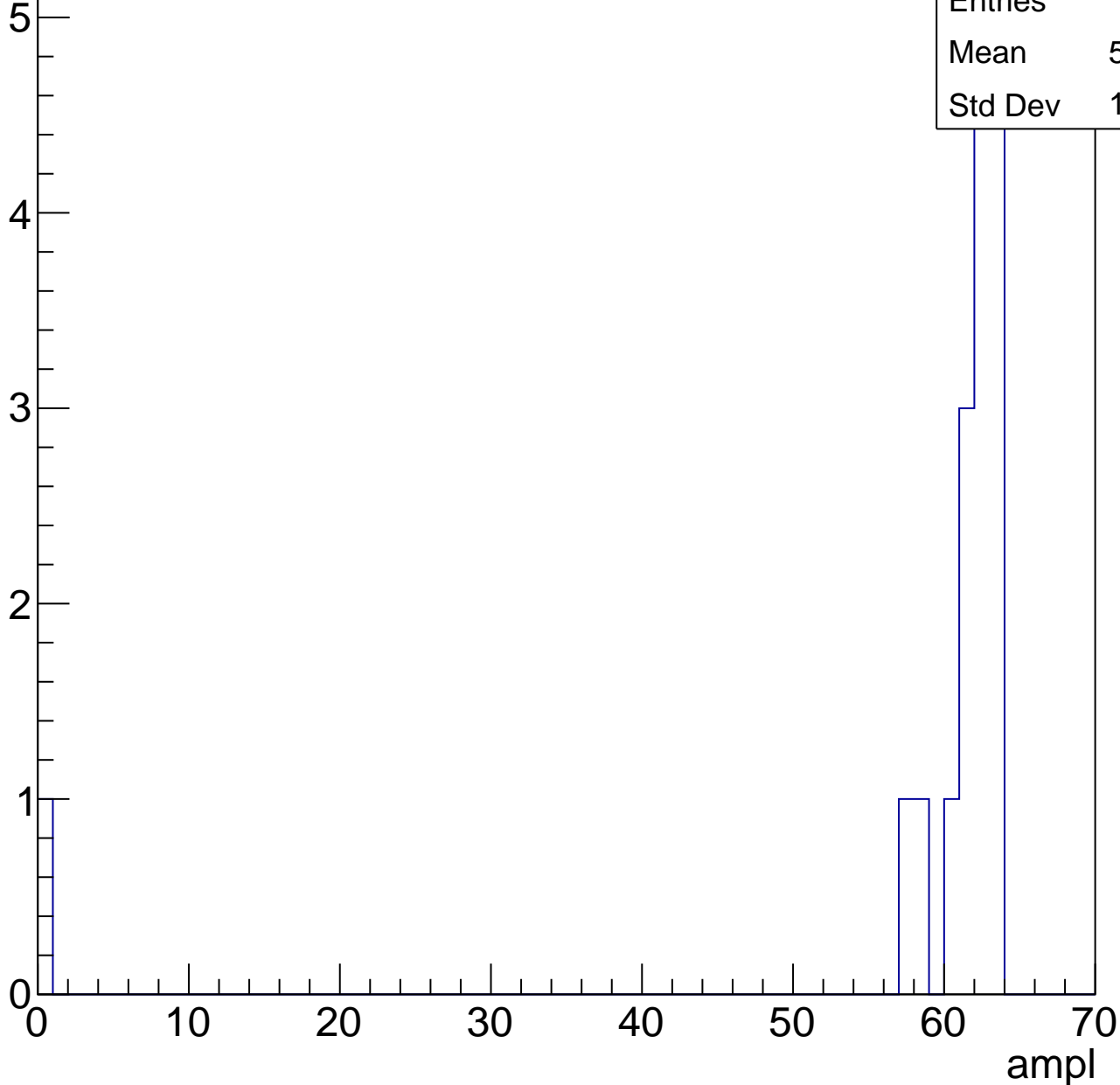


B1L100S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.82
Std Dev	14.55



B1L100S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch35, adc0

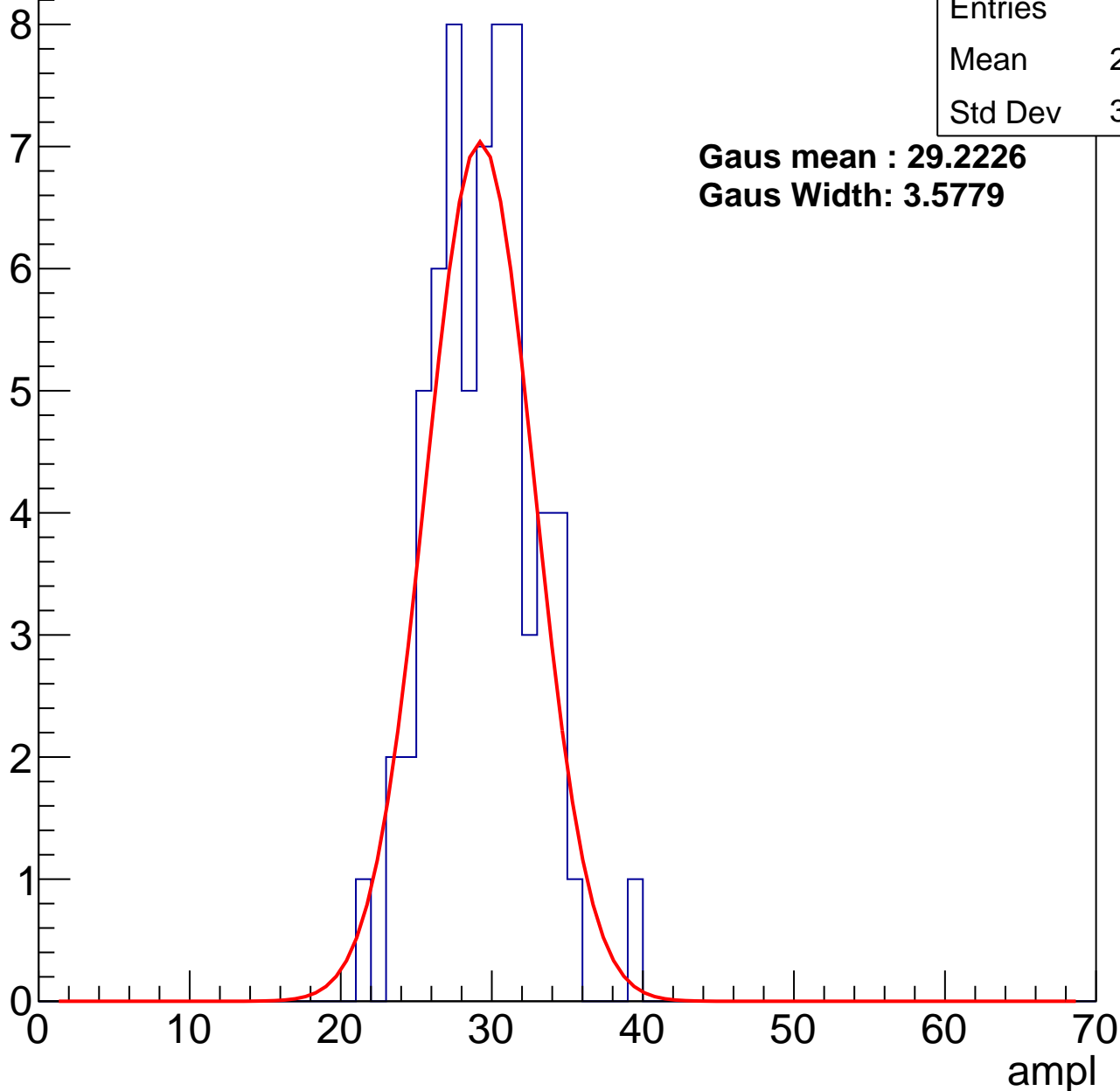
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.94
Std Dev	3.337

Gaus mean : 29.2226

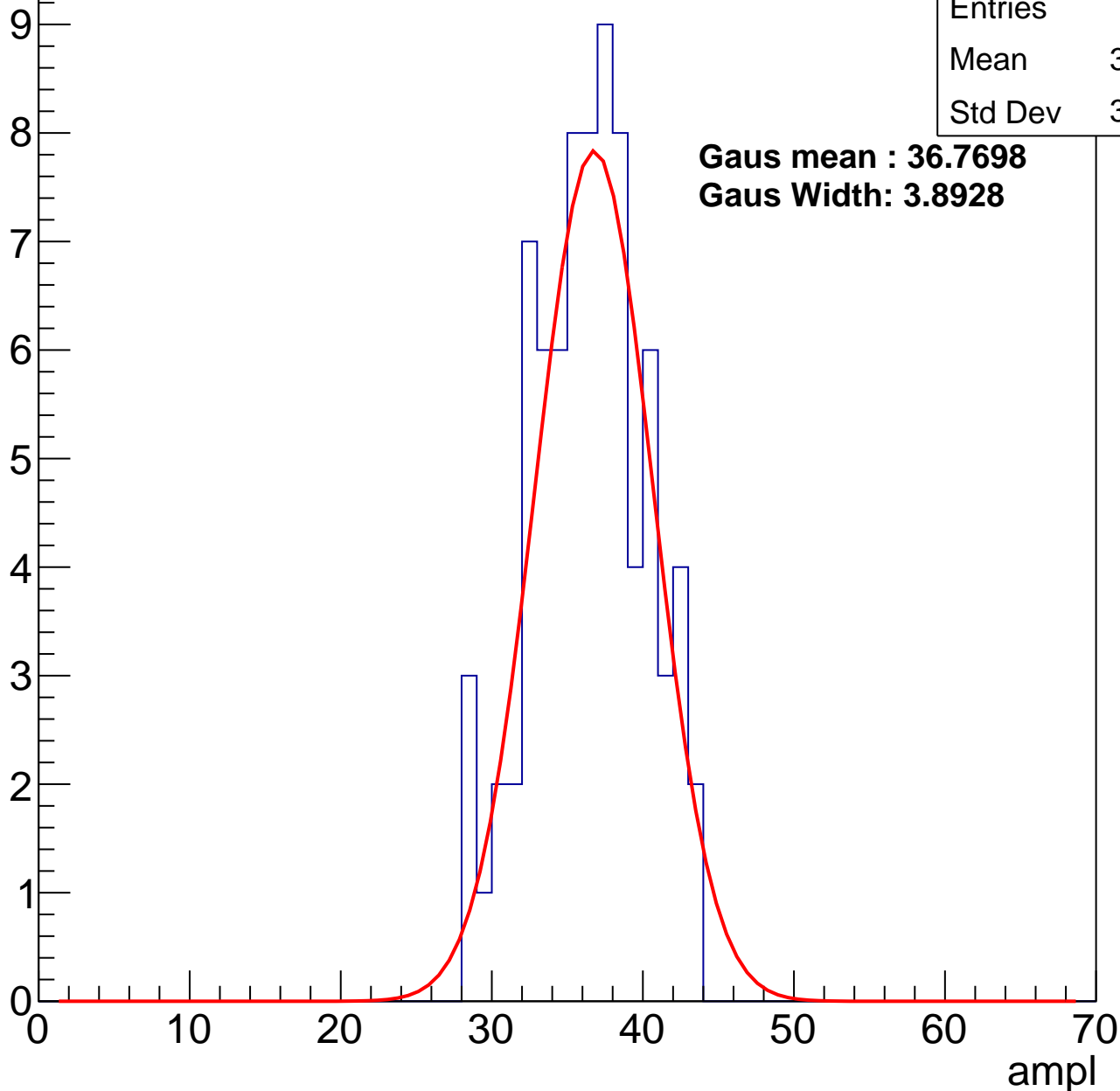
Gaus Width: 3.5779



B1L100S, U14-ch35, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	79
Mean	35.94
Std Dev	3.647

B1L100S, U14-ch35, adc2

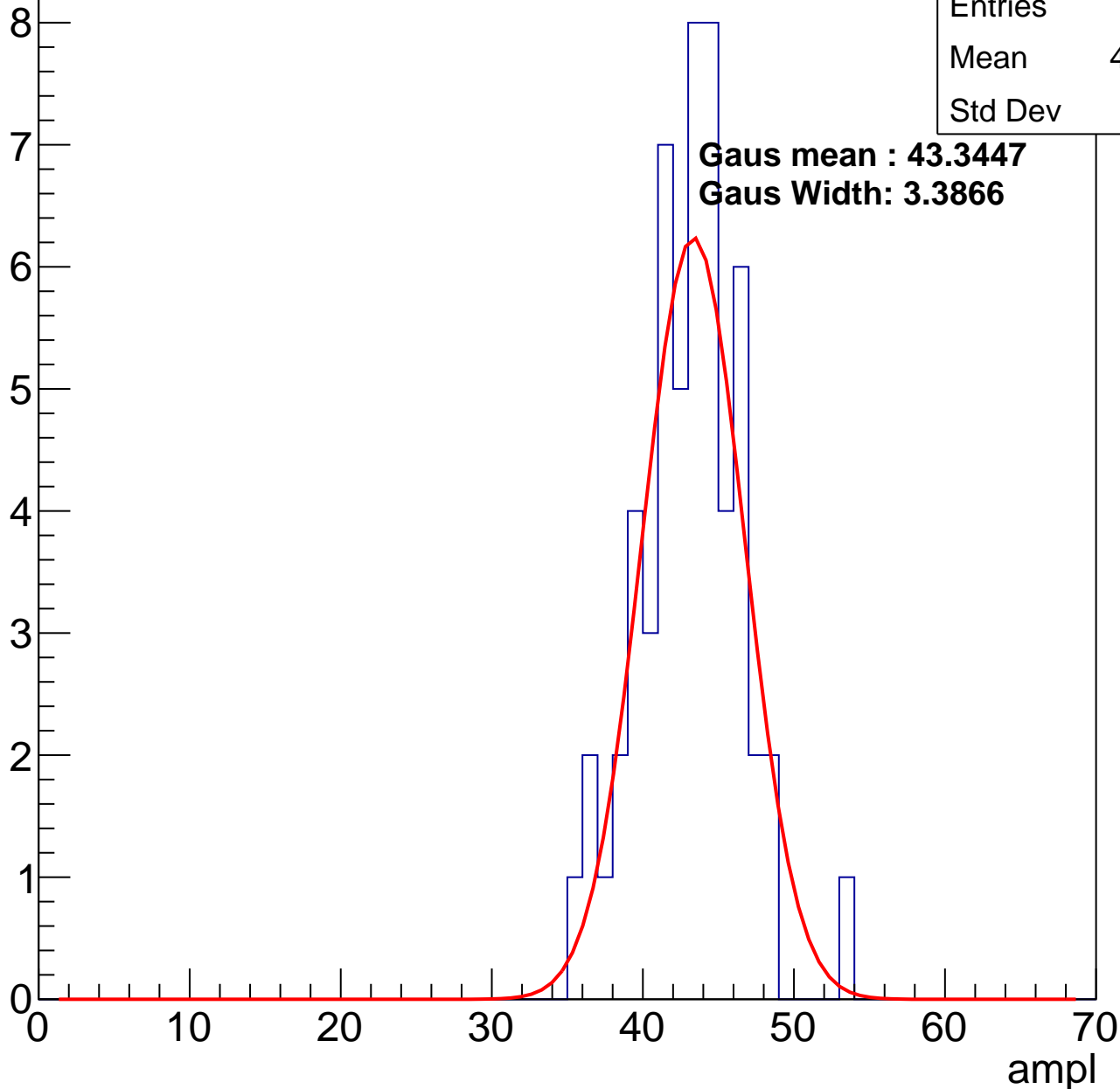
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.64
Std Dev	3.34

Gaus mean : 43.3447

Gaus Width: 3.3866

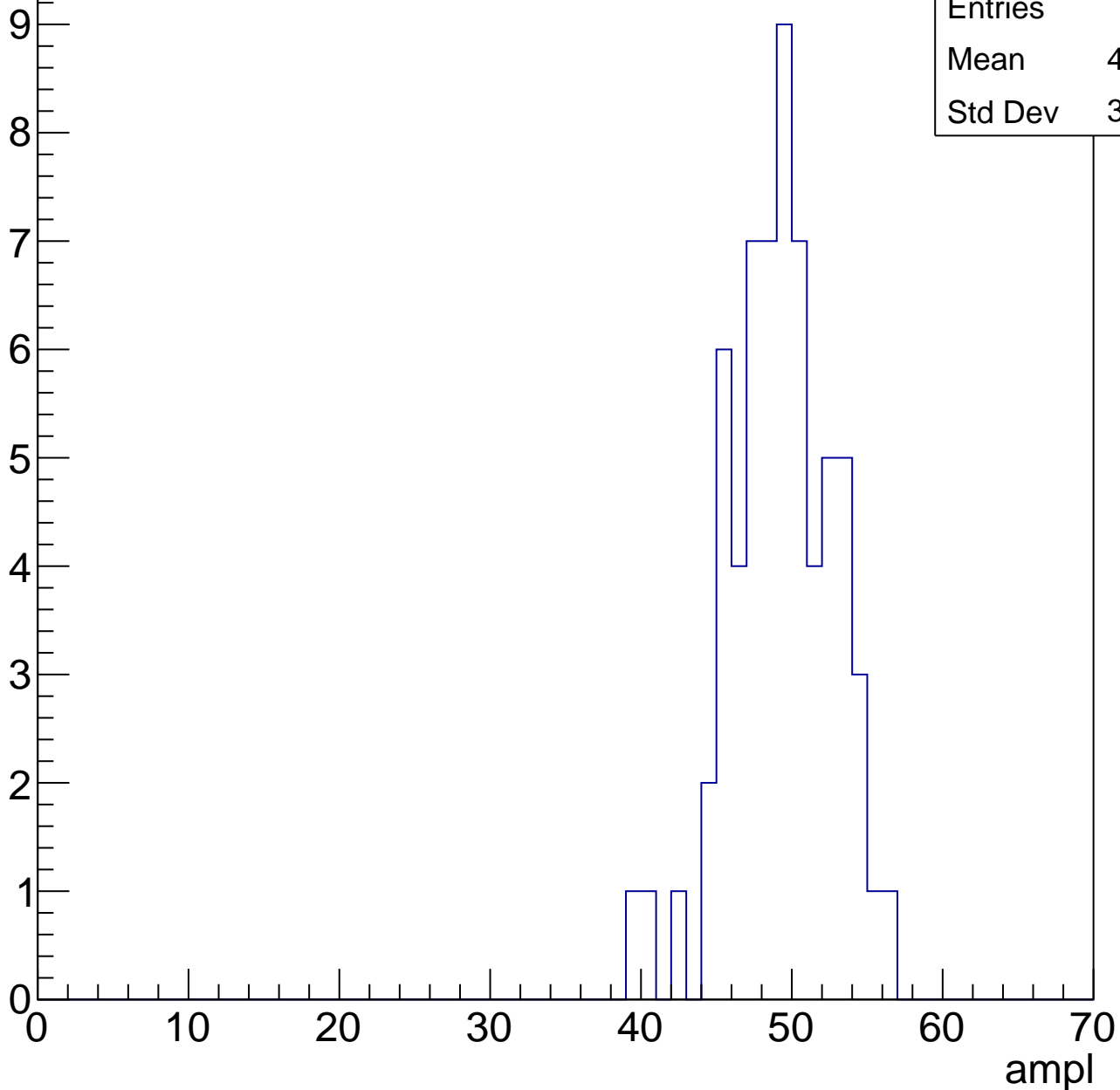


B1L100S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	48.77
Std Dev	3.427

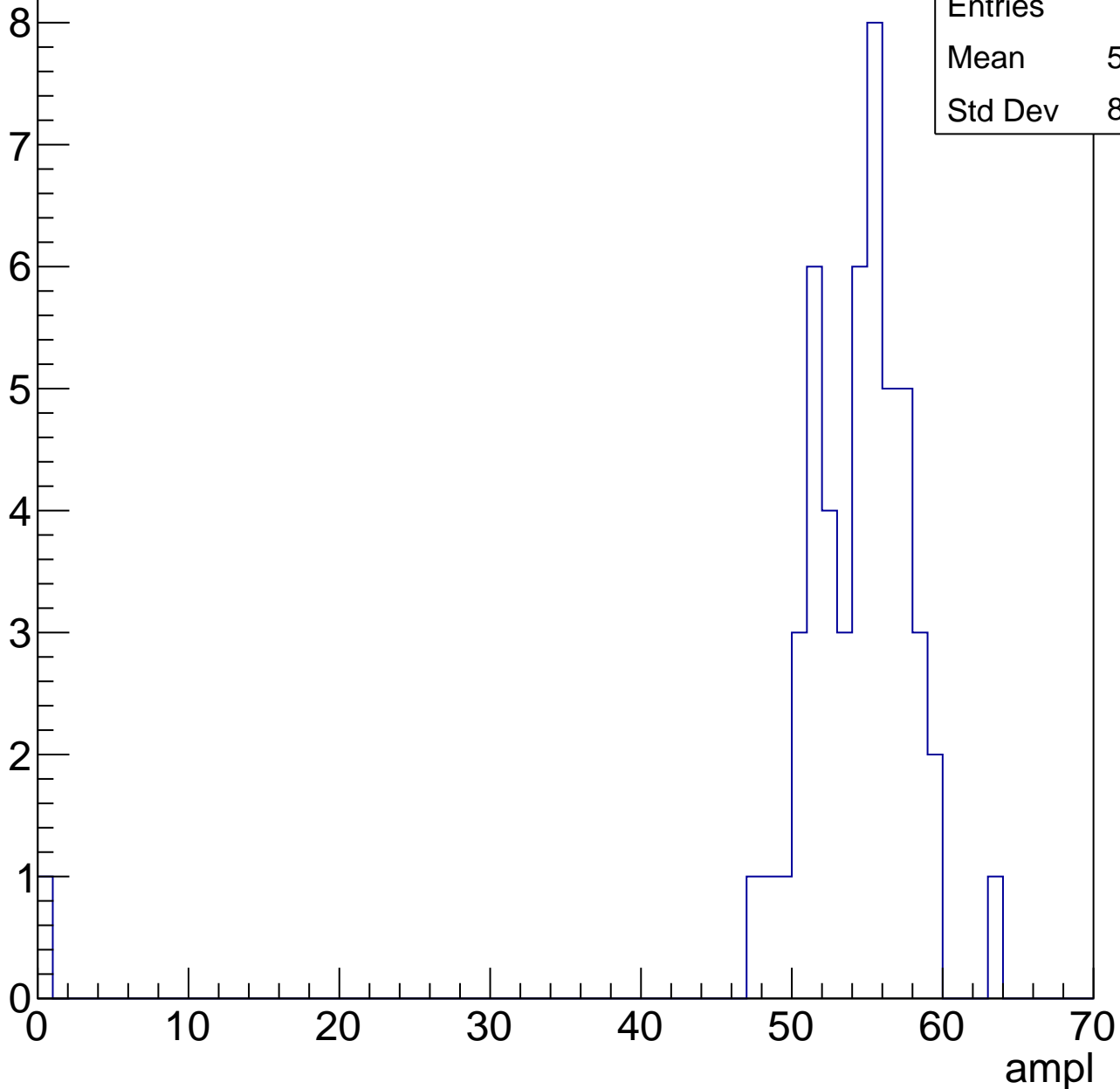


B1L100S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

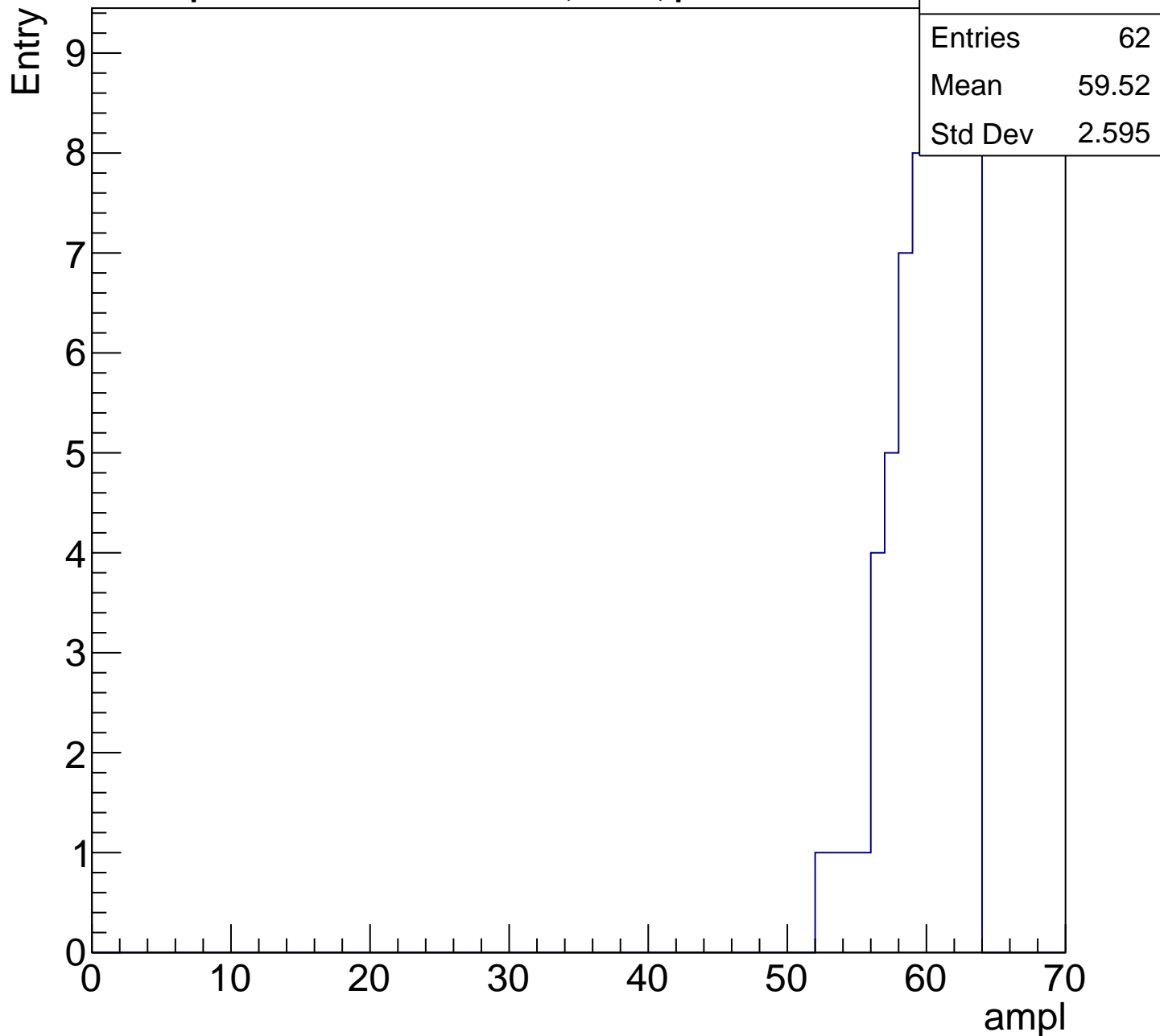
Entry

Entries	50
Mean	53.02
Std Dev	8.187



B1L100S, U14-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch36, adc0

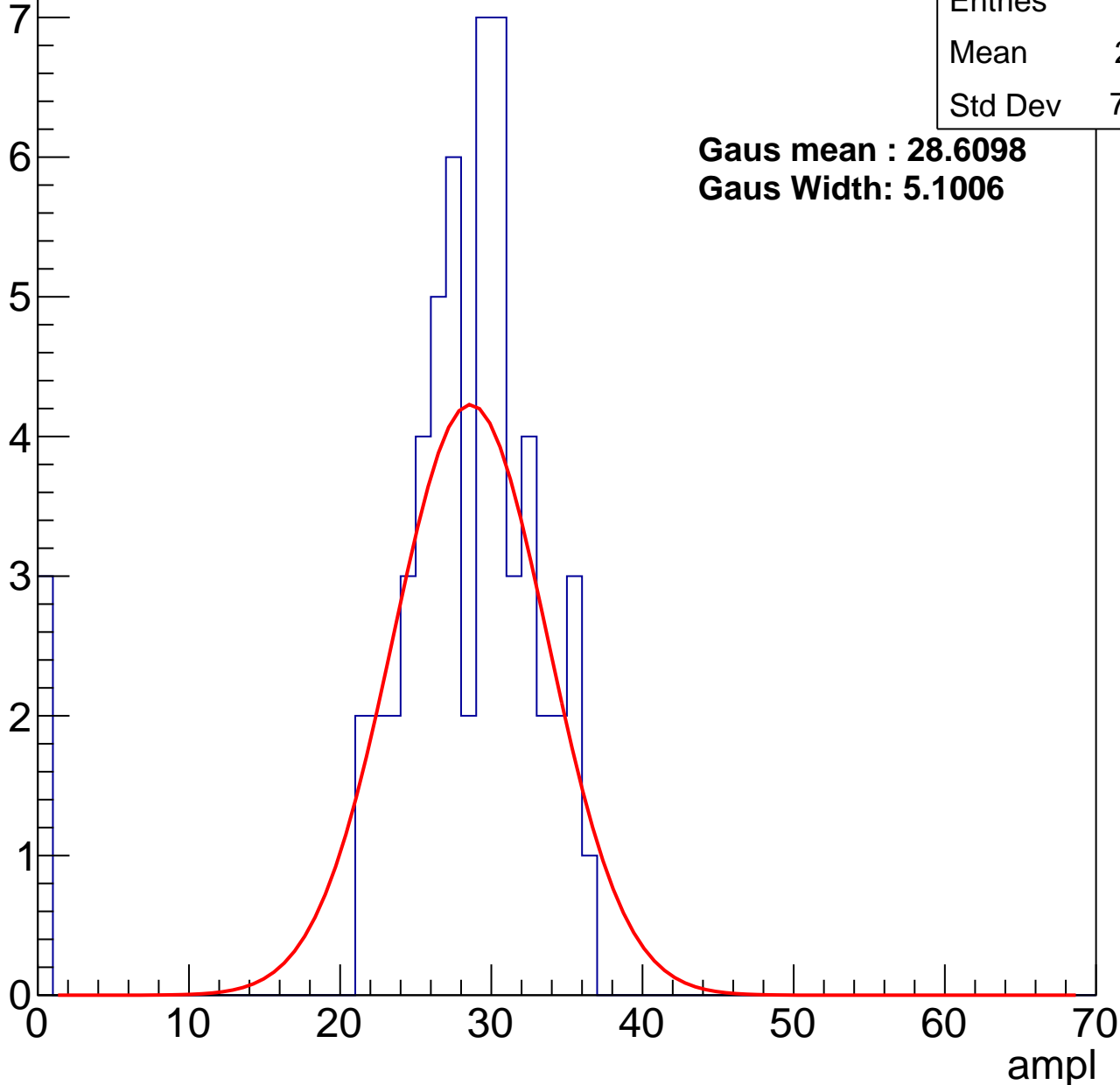
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	26.91
Std Dev	7.278

Gaus mean : 28.6098

Gaus Width: 5.1006



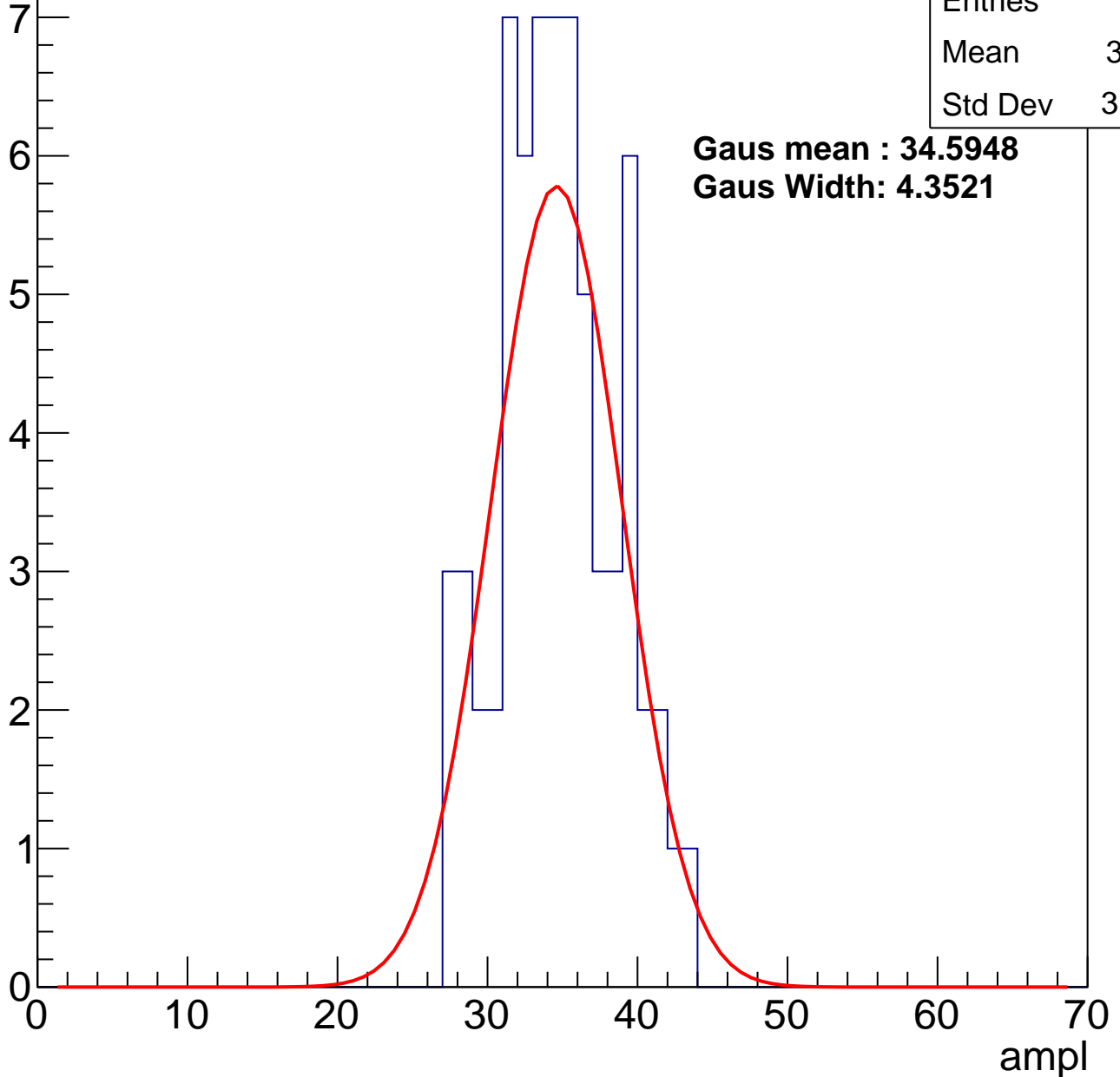
B1L100S, U14-ch36, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	34.21
Std Dev	3.854

Gaus mean : 34.5948
Gaus Width: 4.3521



B1L100S, U14-ch36, adc2

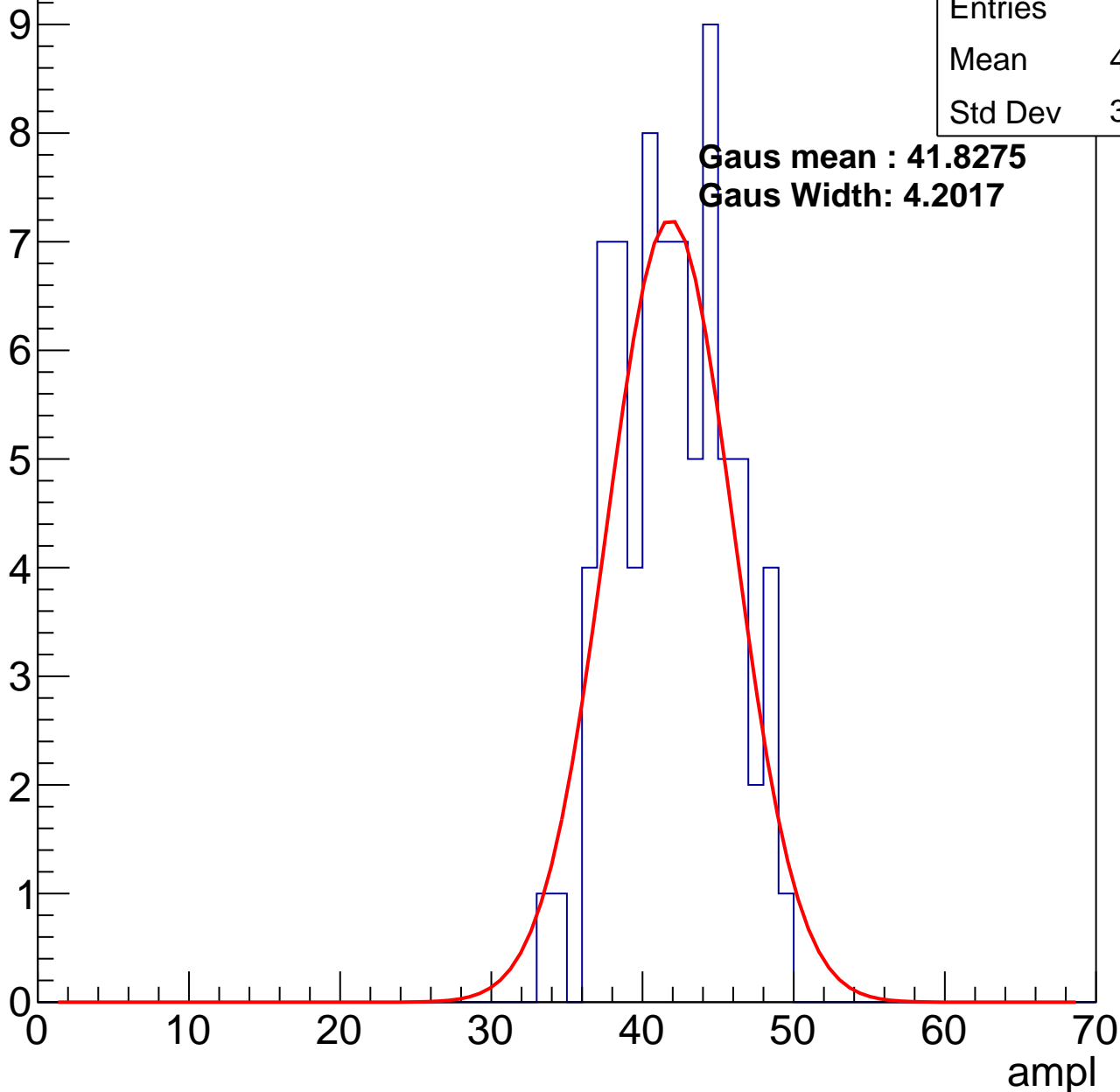
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	41.48
Std Dev	3.674

Gaus mean : 41.8275

Gaus Width: 4.2017

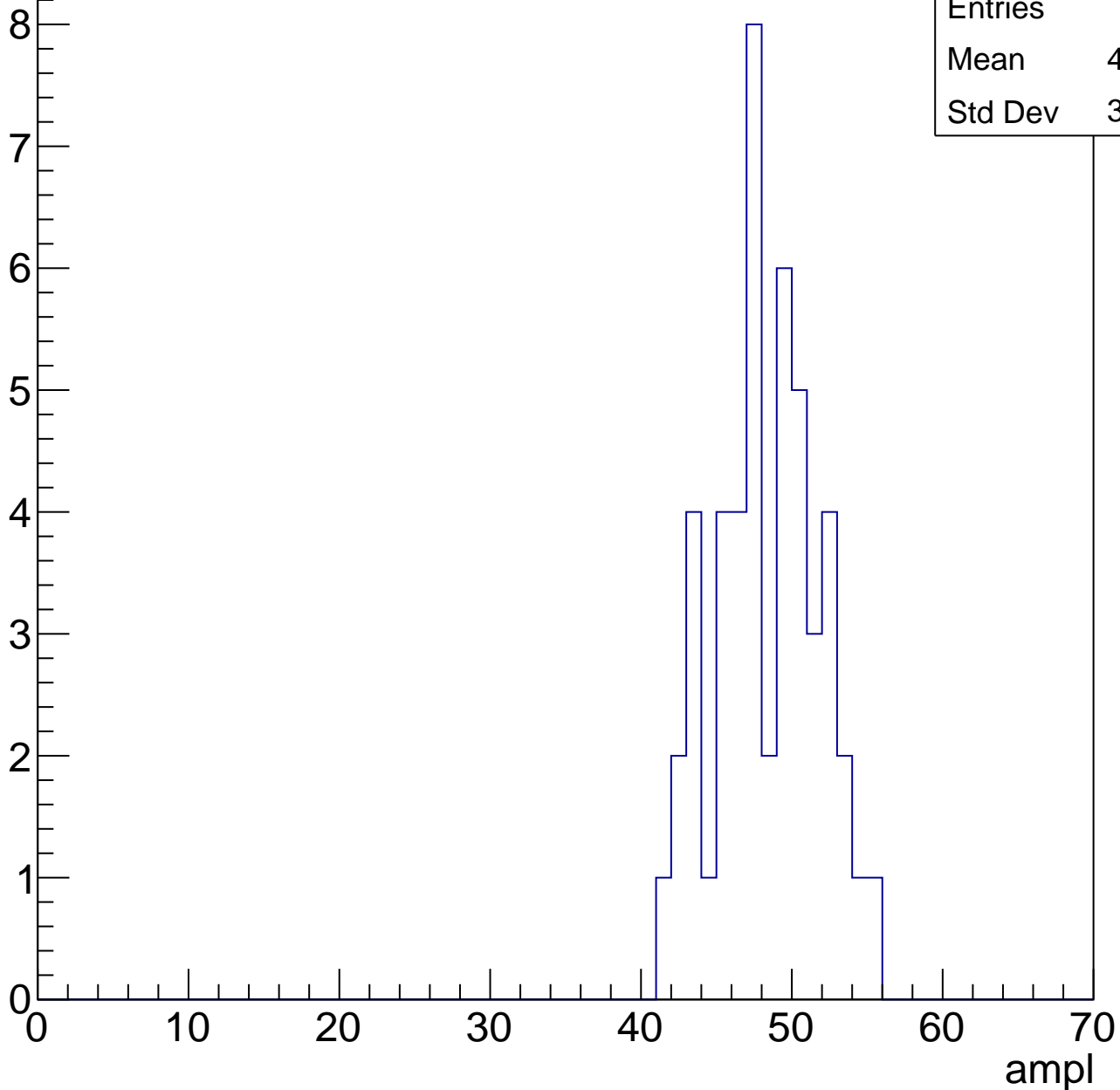


B1L100S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	47.85
Std Dev	3.379

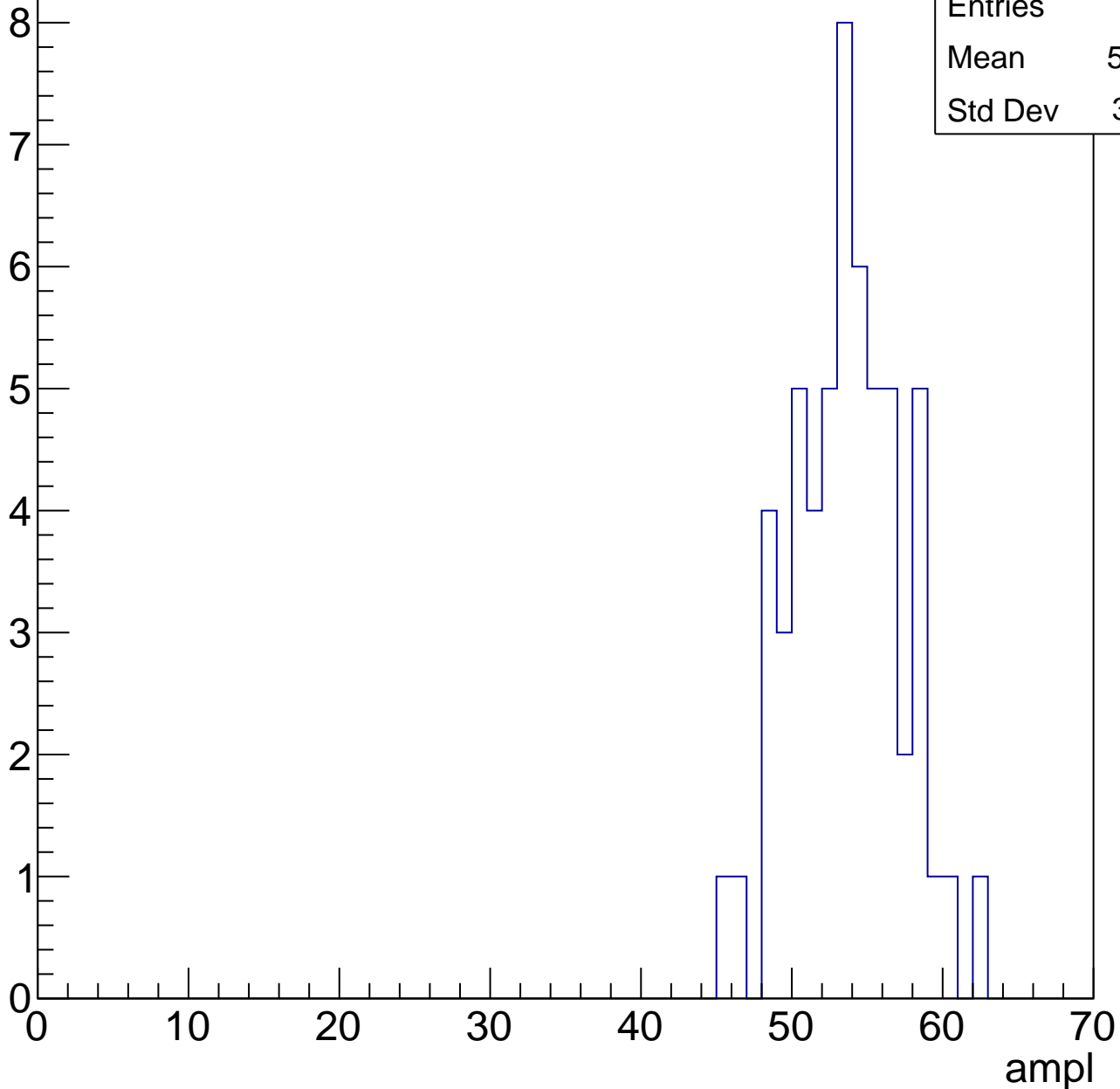


B1L100S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

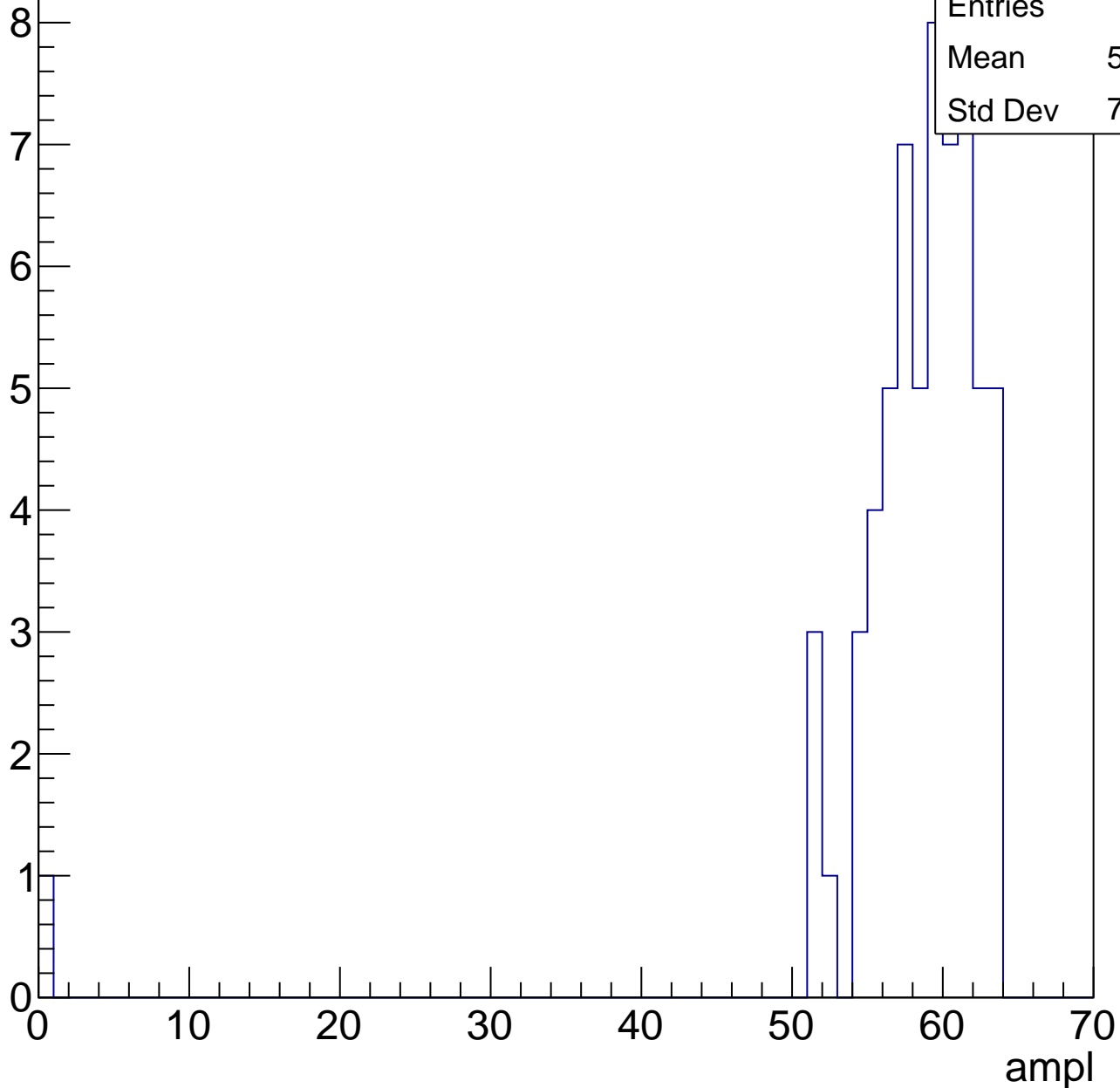
Entries	57
Mean	53.19
Std Dev	3.551



B1L100S, U14-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

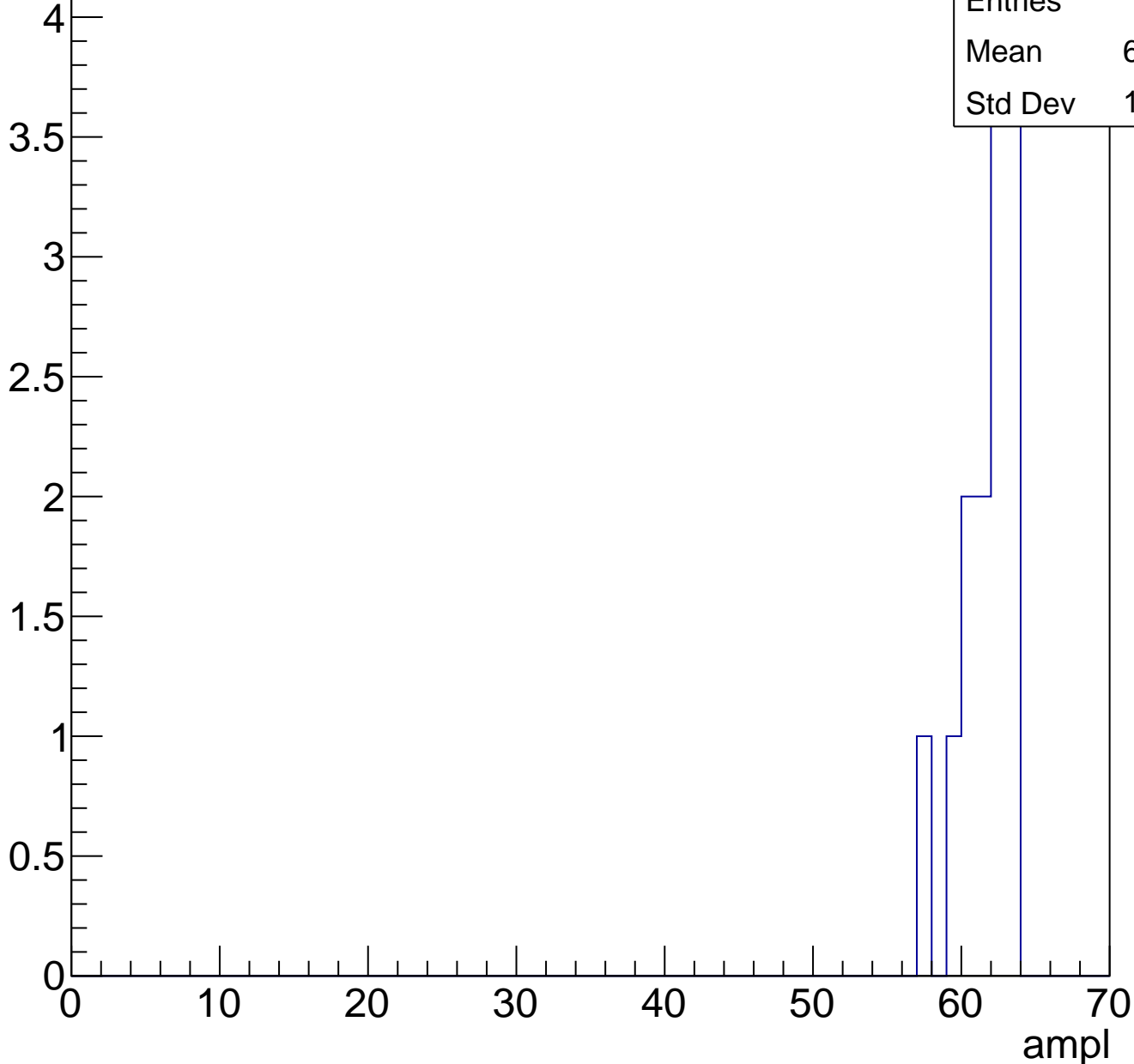
Entry



B1L100S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch37, adc0

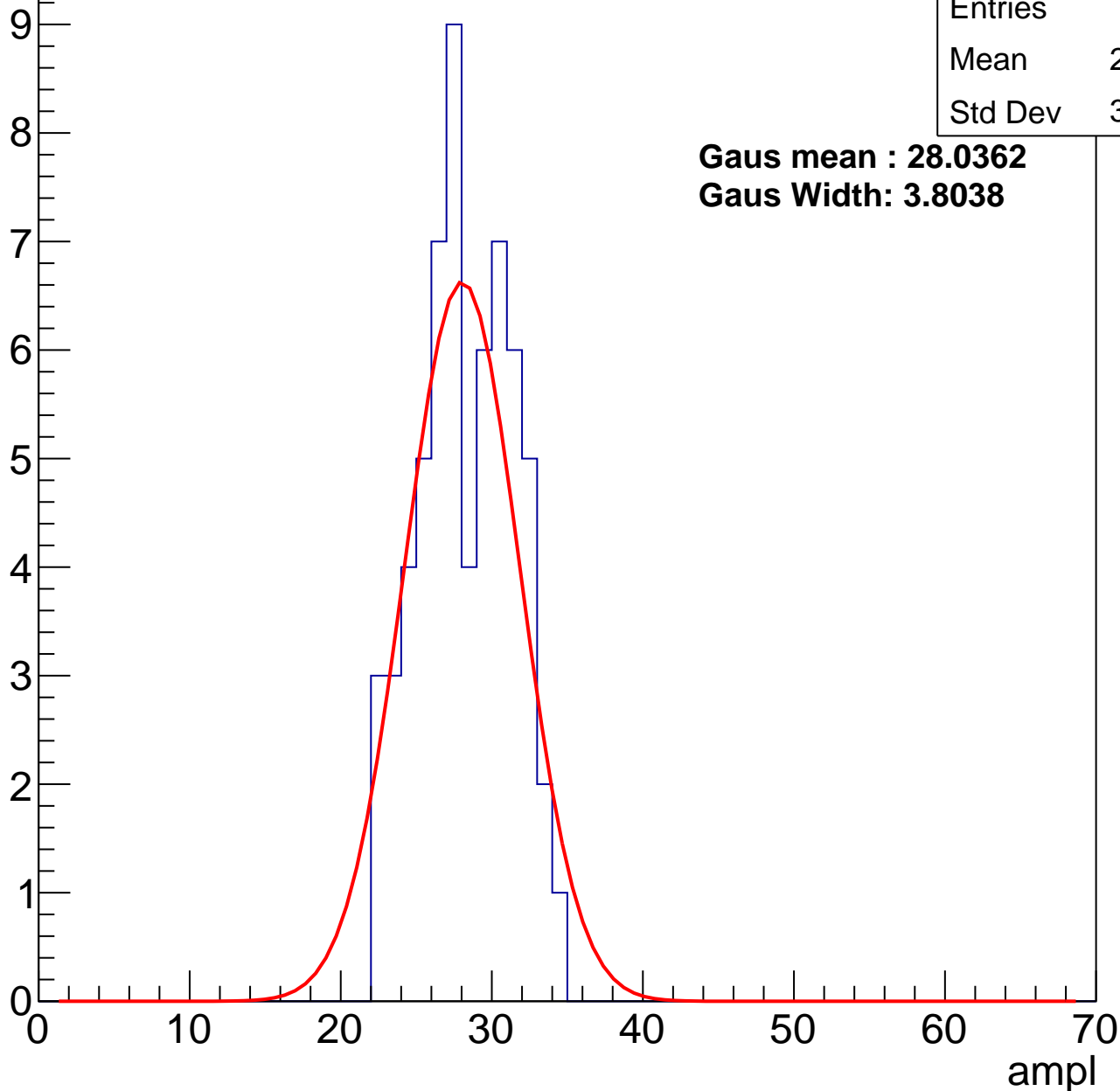
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.79
Std Dev	3.059

Gaus mean : 28.0362

Gaus Width: 3.8038



B1L100S, U14-ch37, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	34.66
Std Dev	3.67

Gaus mean : 34.6761

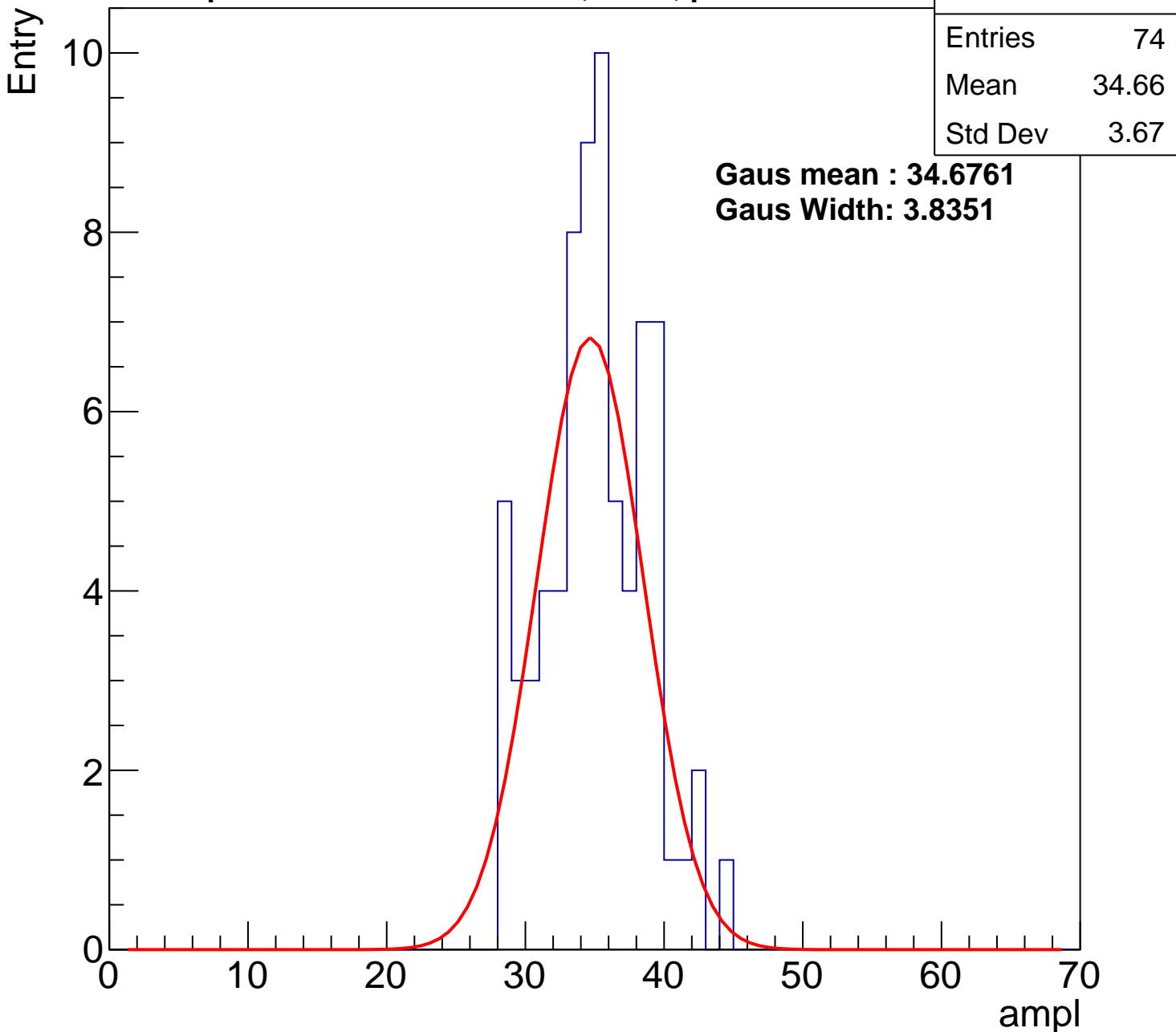
Gaus Width: 3.8351

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch37, adc2

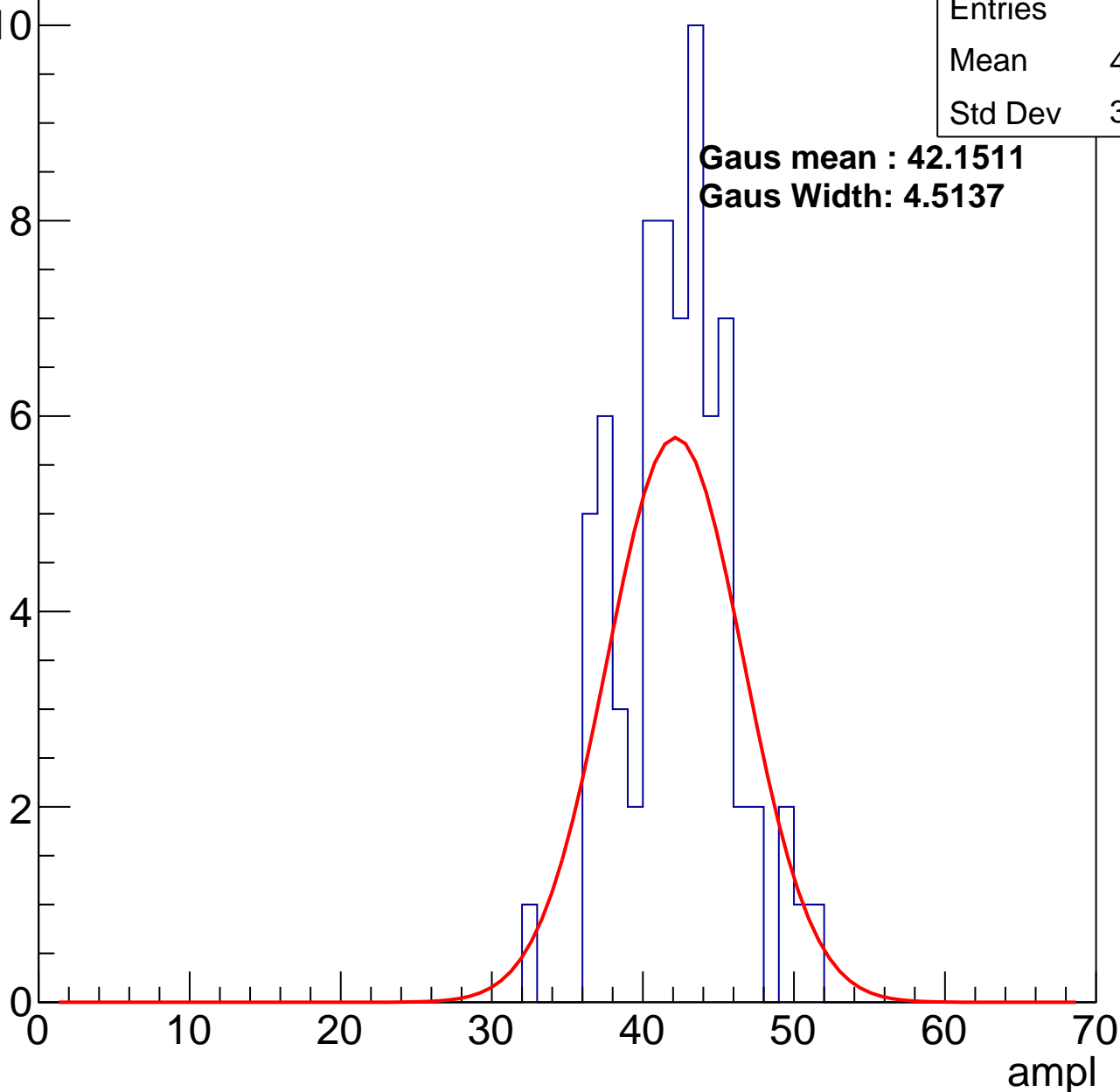
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	41.72
Std Dev	3.662

Gaus mean : 42.1511

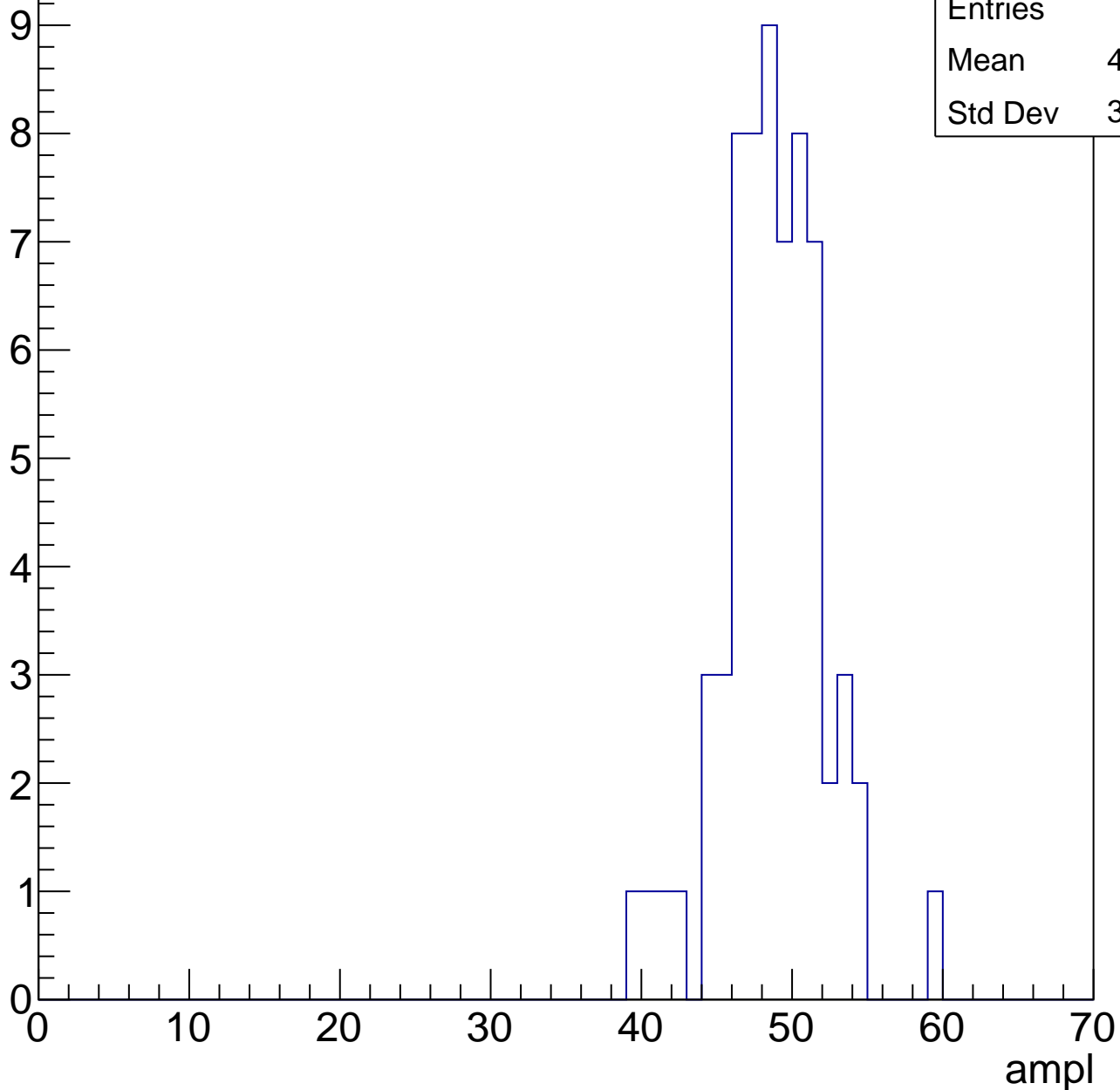
Gaus Width: 4.5137



B1L100S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

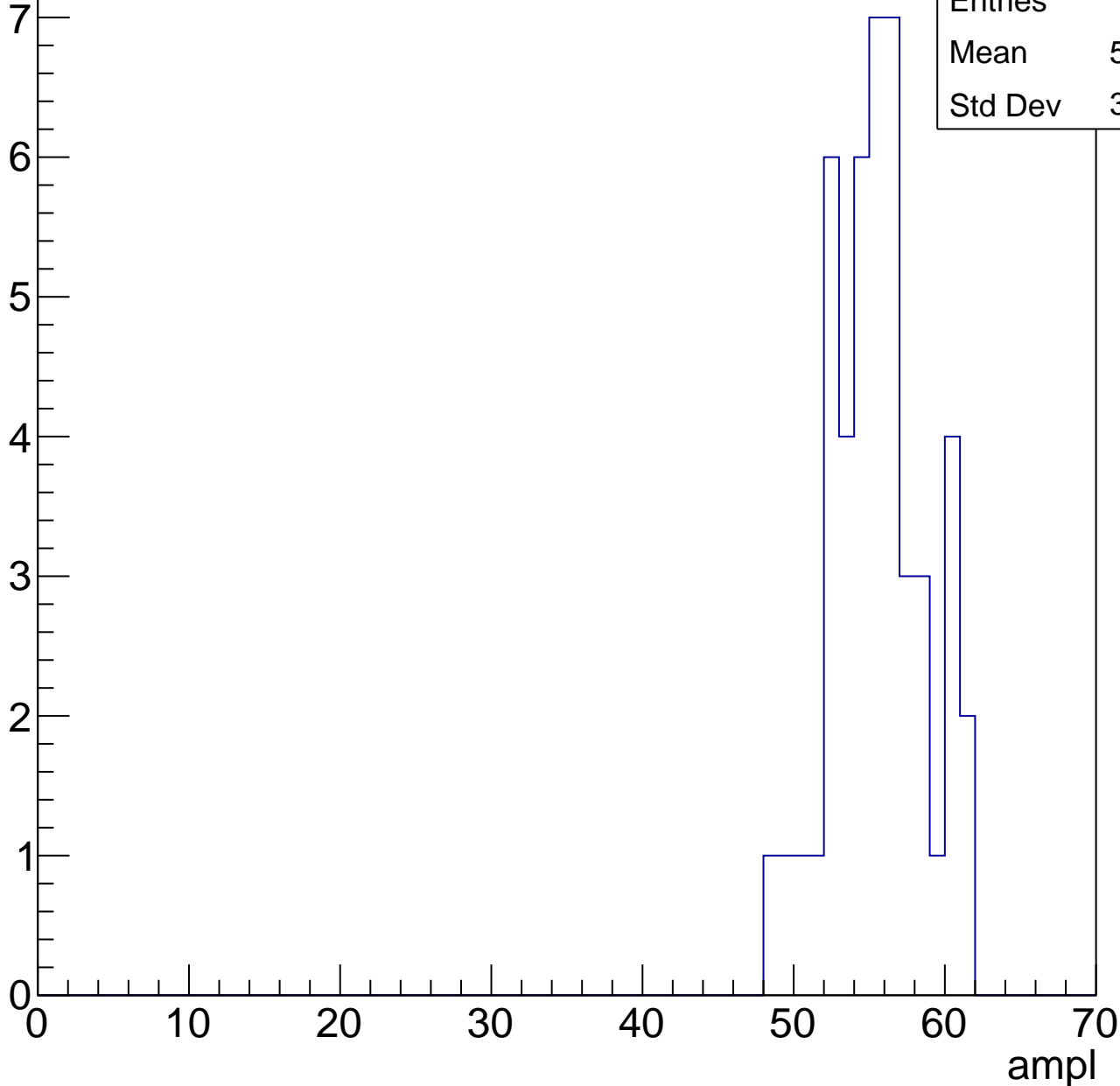


B1L100S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	55.09
Std Dev	3.045

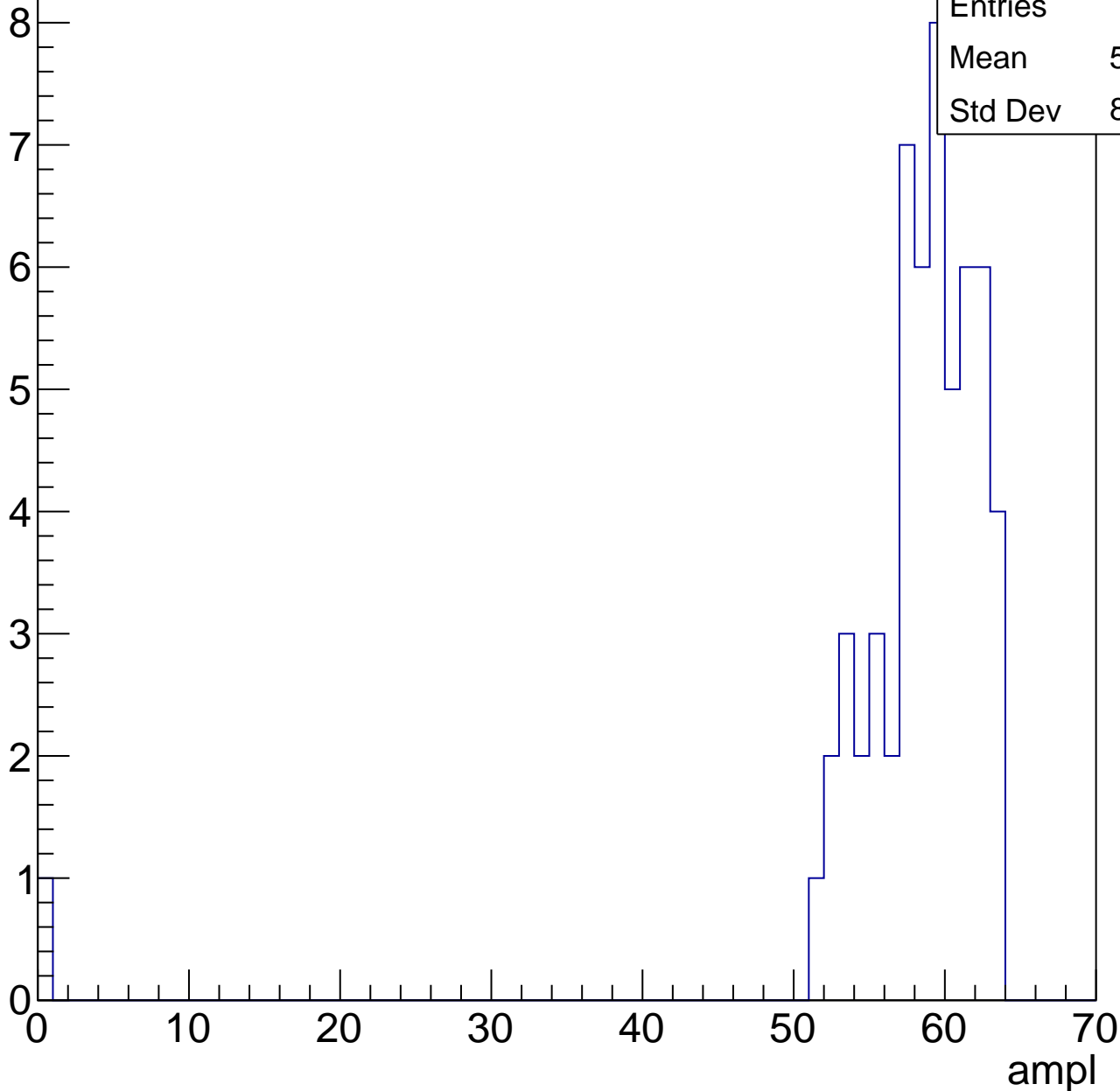


B1L100S, U14-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.29
Std Dev	8.327

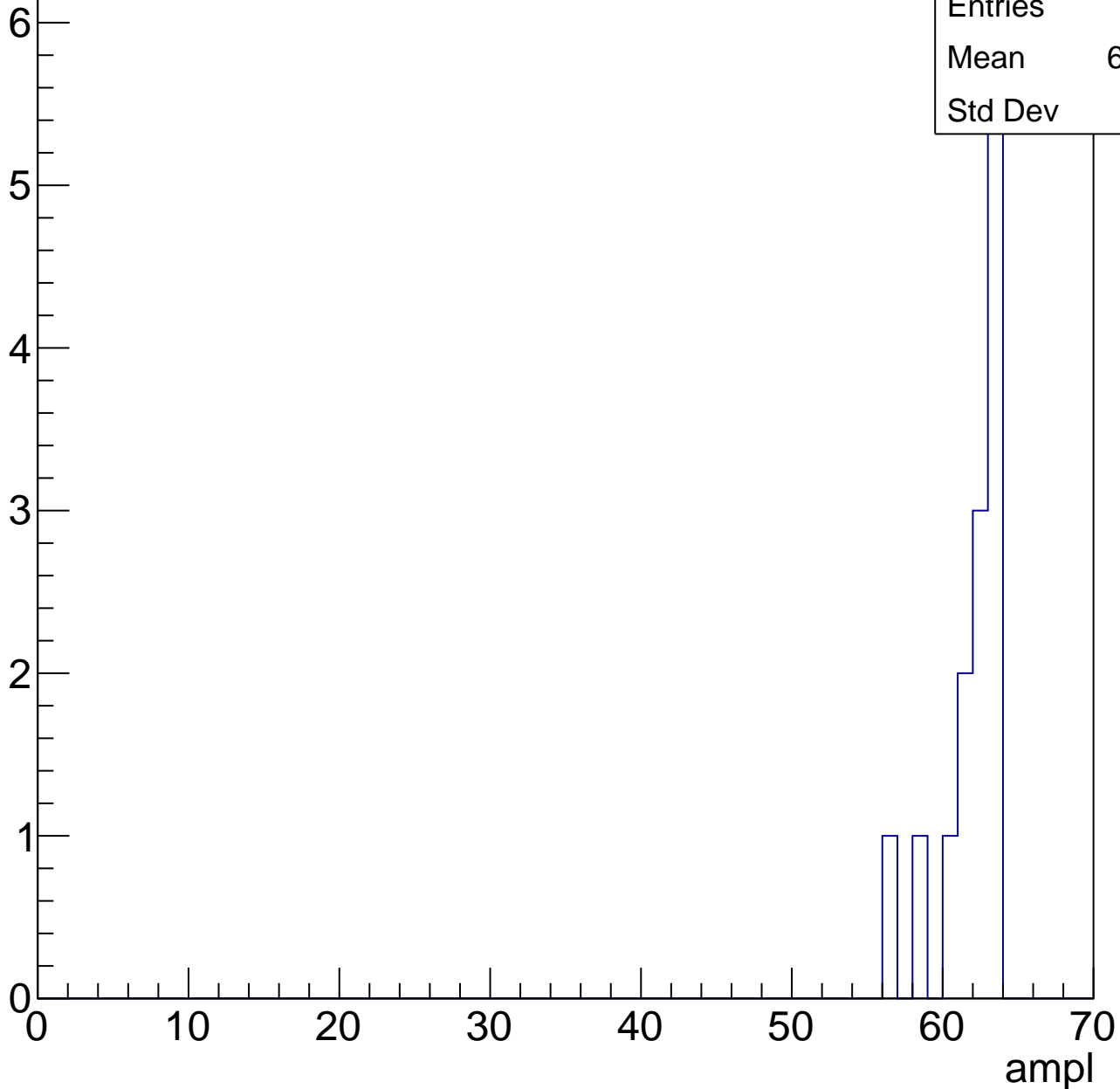


B1L100S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

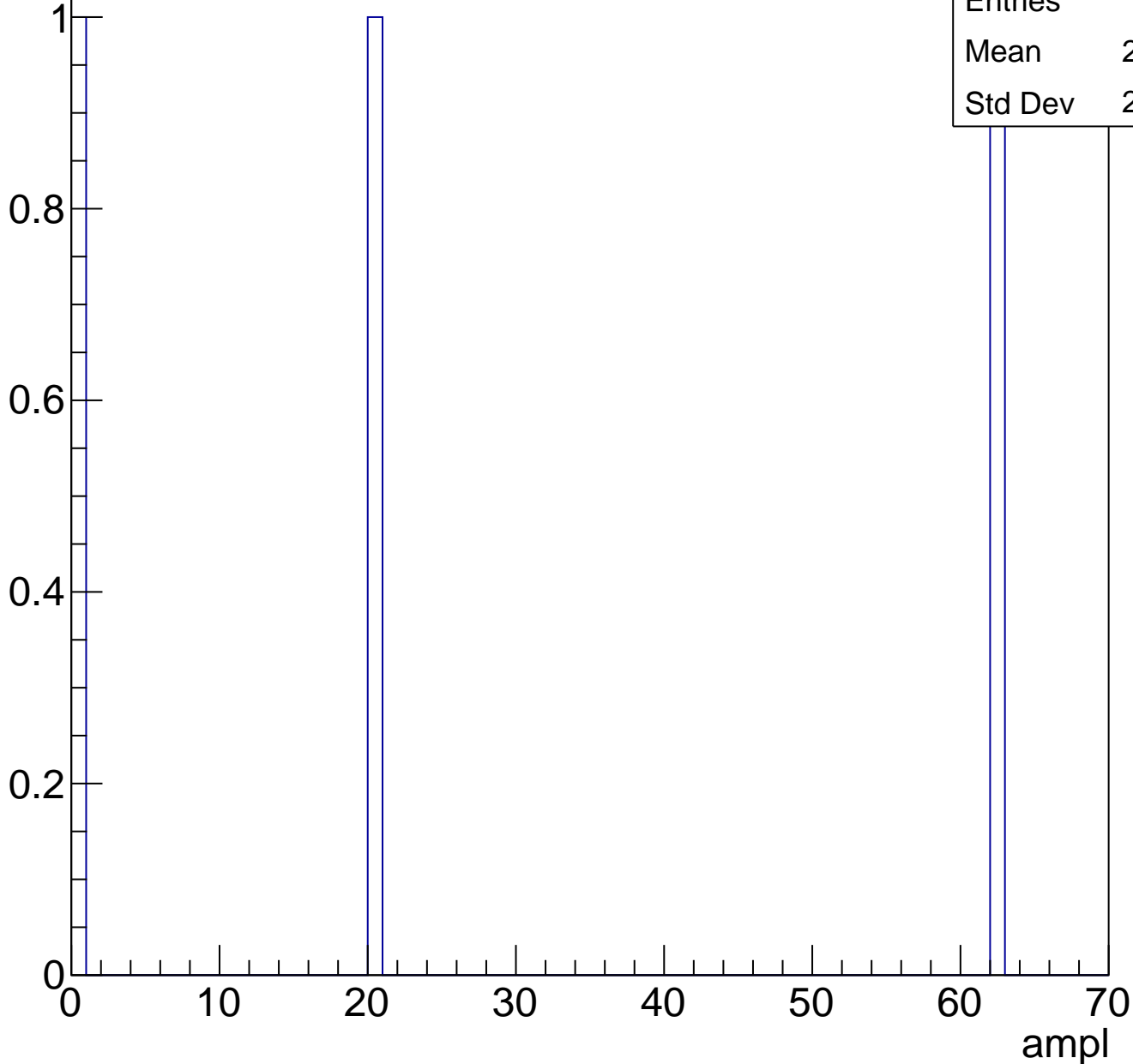
Entries	14
Mean	61.43
Std Dev	2.06



B1L100S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B1L100S, U14-ch38, adc0

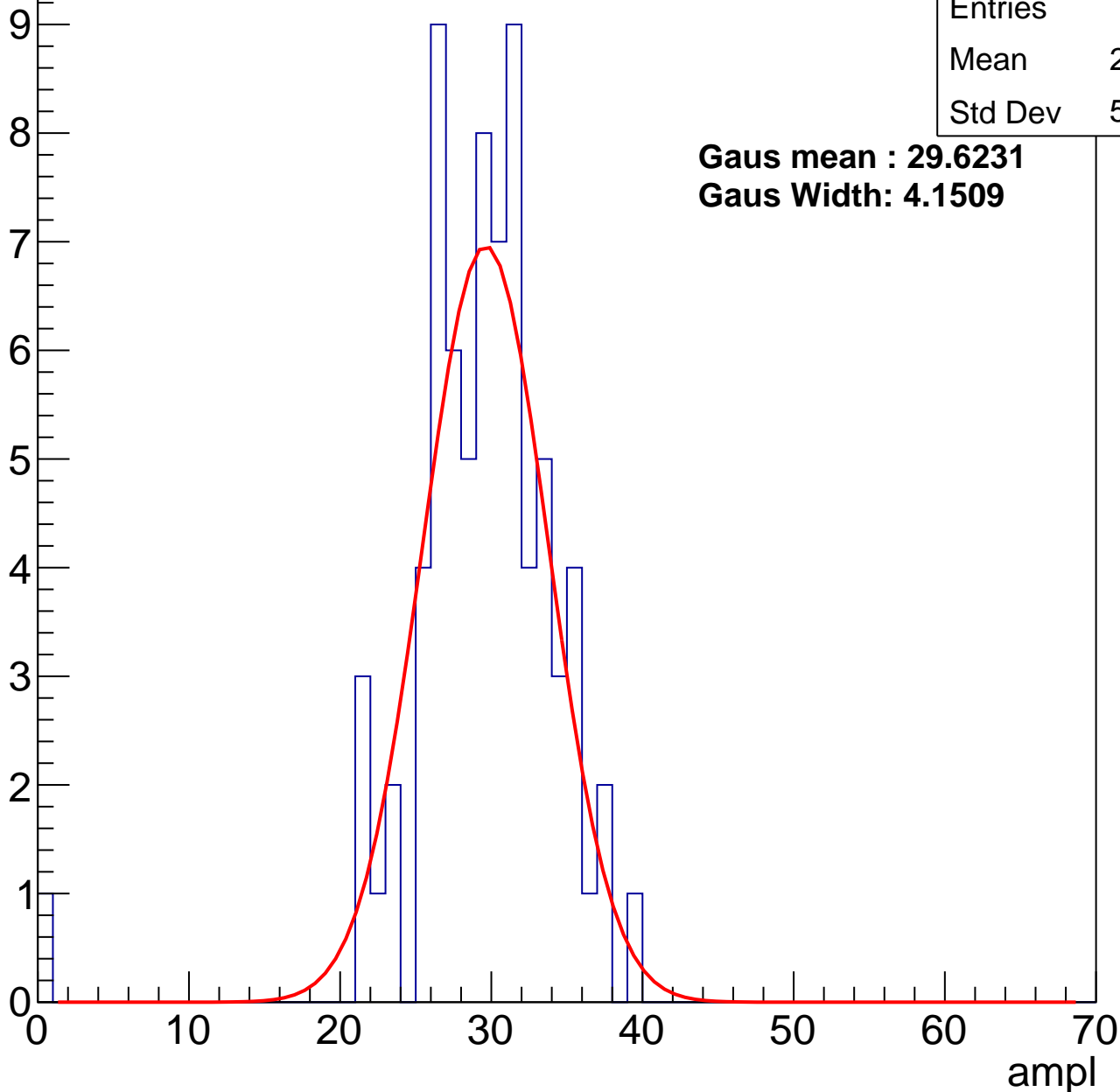
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	28.96
Std Dev	5.142

Gaus mean : 29.6231

Gaus Width: 4.1509



B1L100S, U14-ch38, adc1

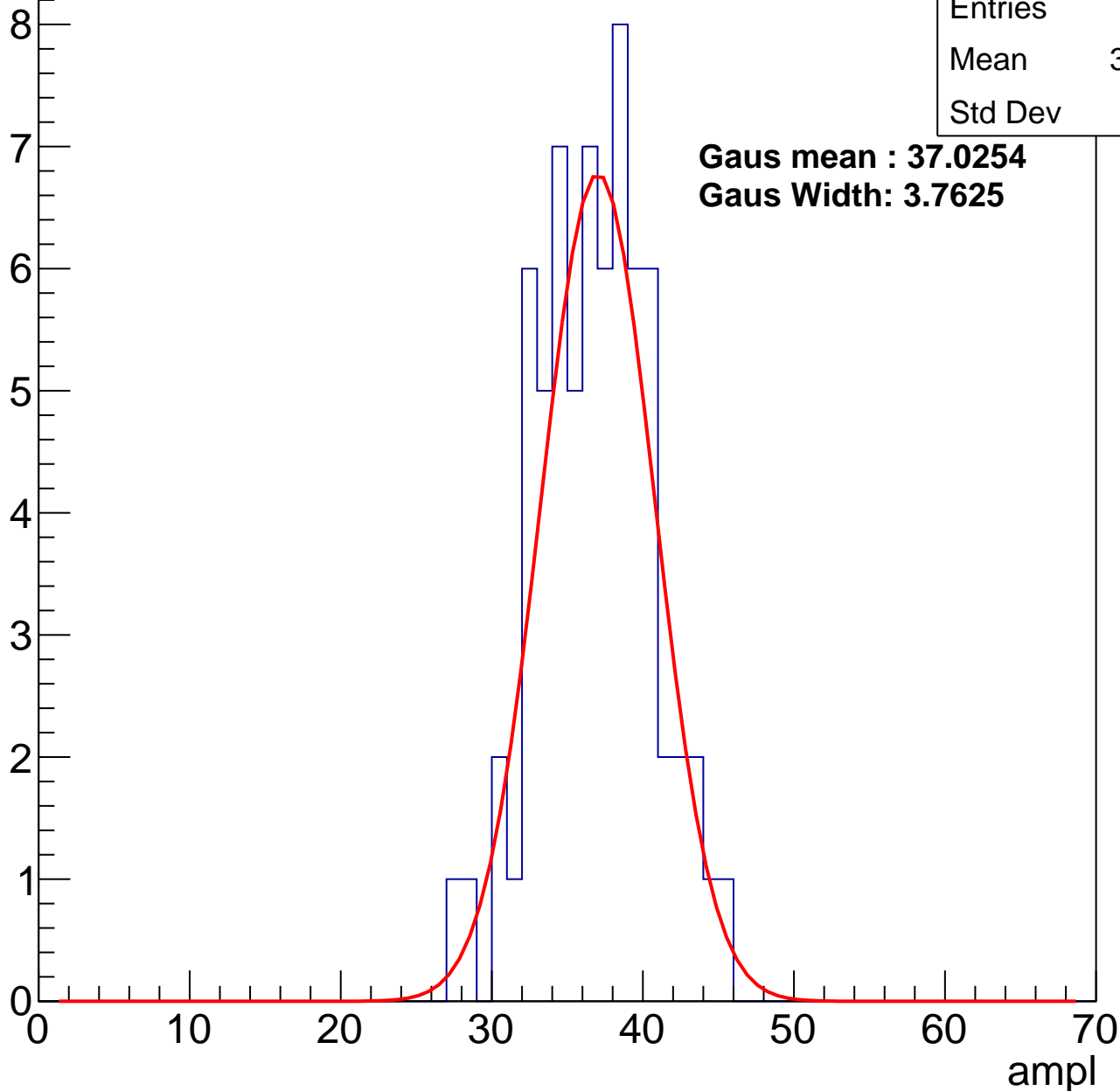
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.36
Std Dev	3.73

Gaus mean : 37.0254

Gaus Width: 3.7625



B1L100S, U14-ch38, adc2

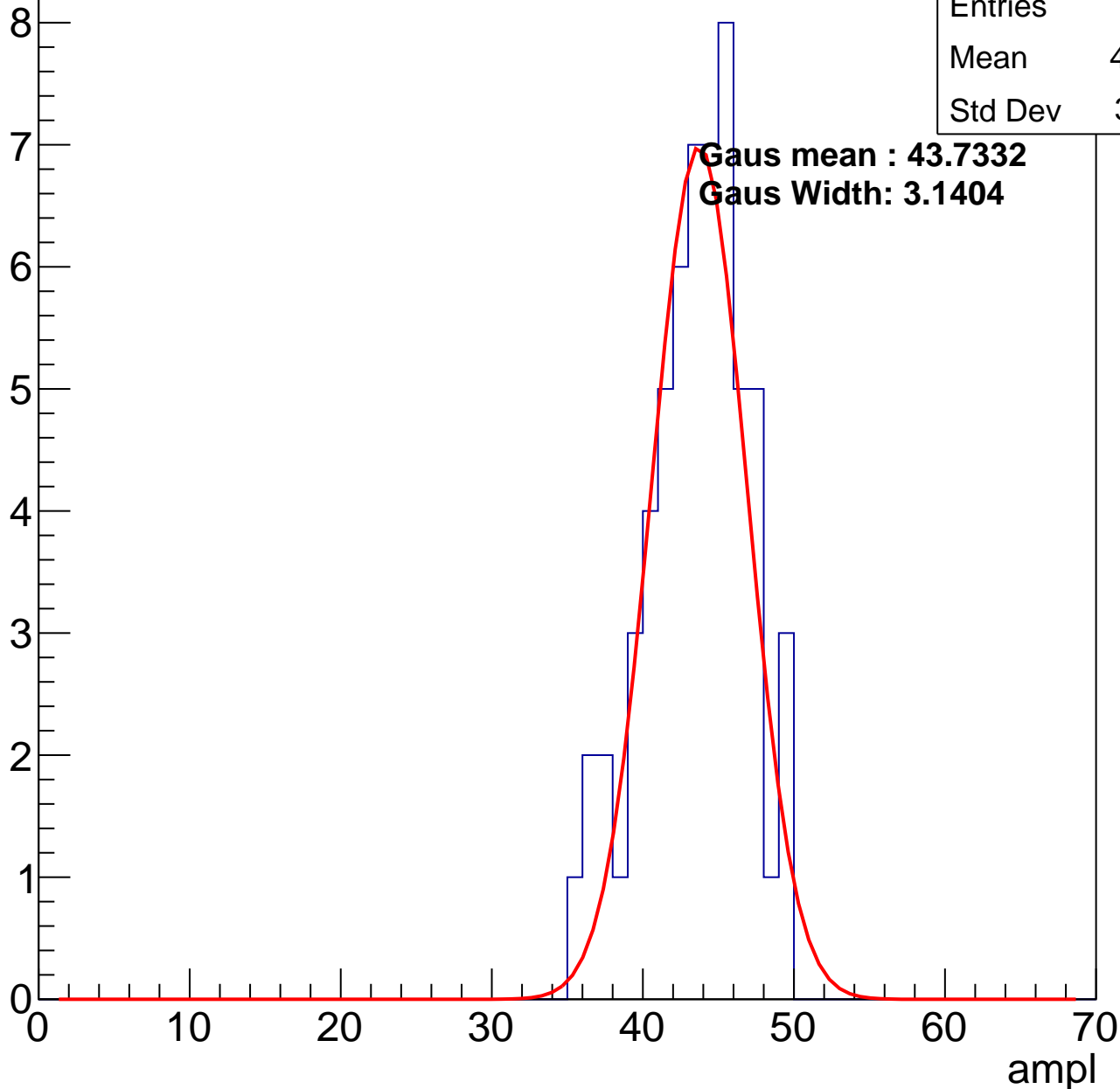
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.03
Std Dev	3.331

Gaus mean : 43.7332

Gaus Width: 3.1404

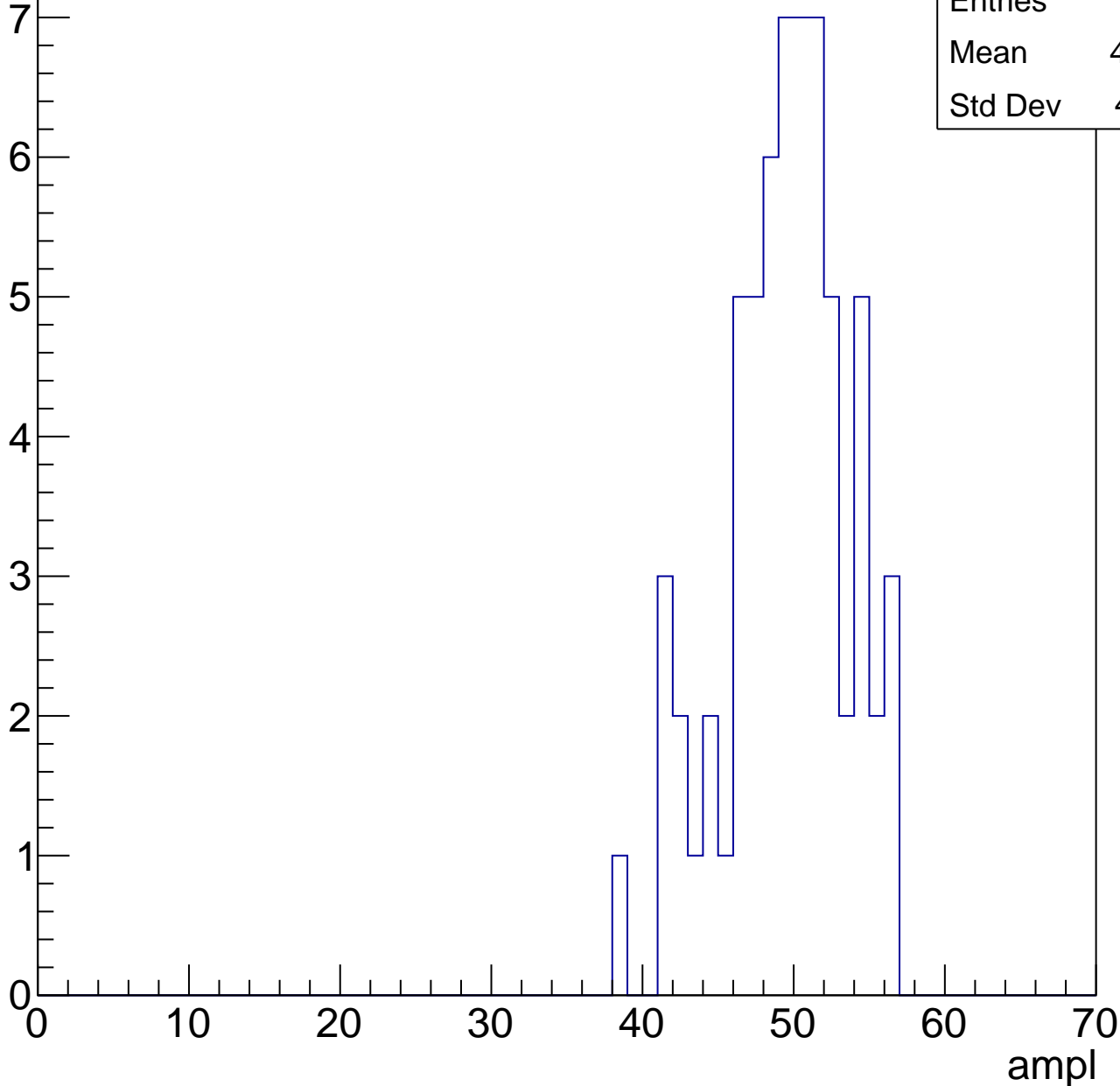


B1L100S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

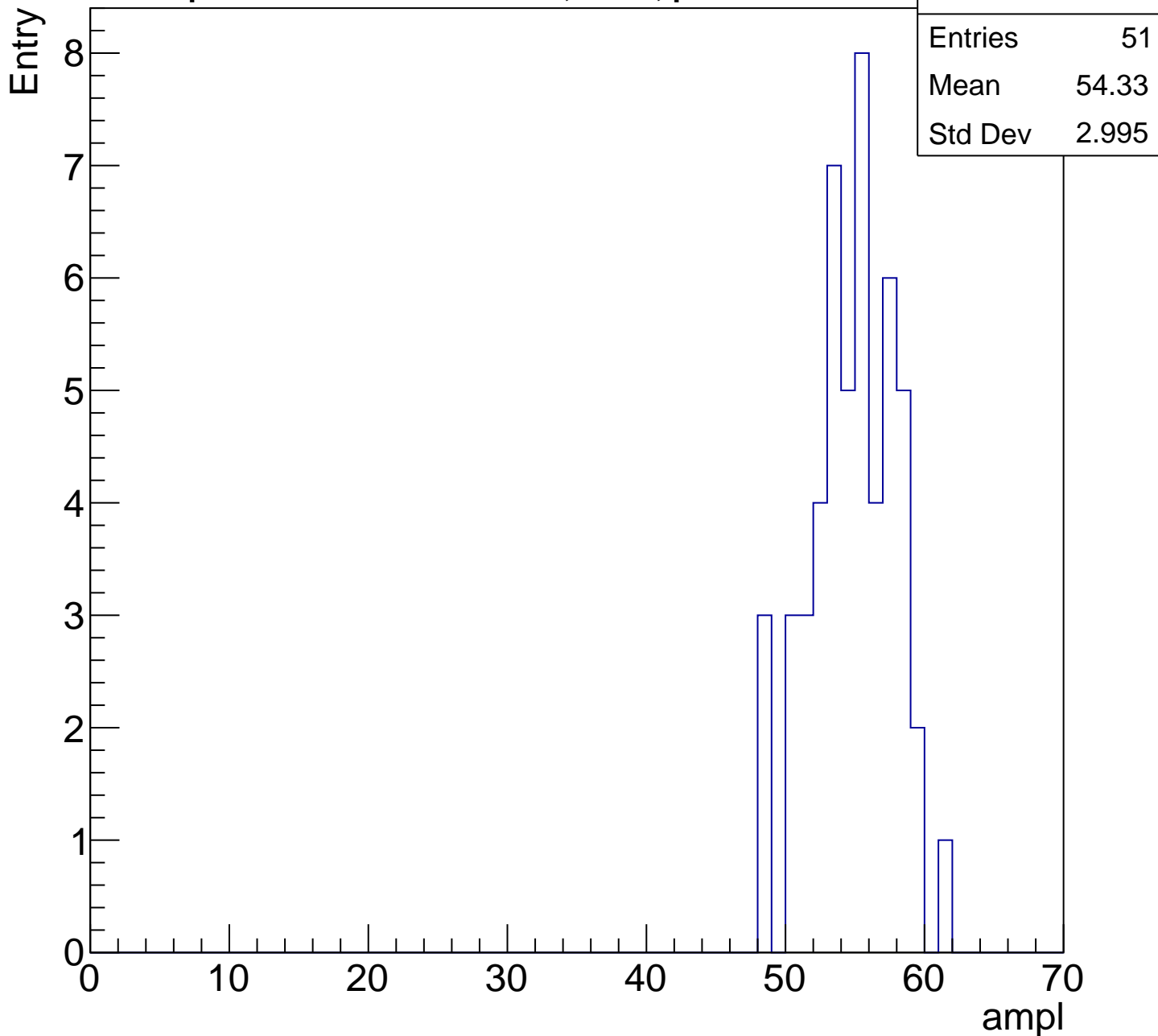
Entry

Entries	64
Mean	49.03
Std Dev	4.031



B1L100S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.63
Std Dev	8.661

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

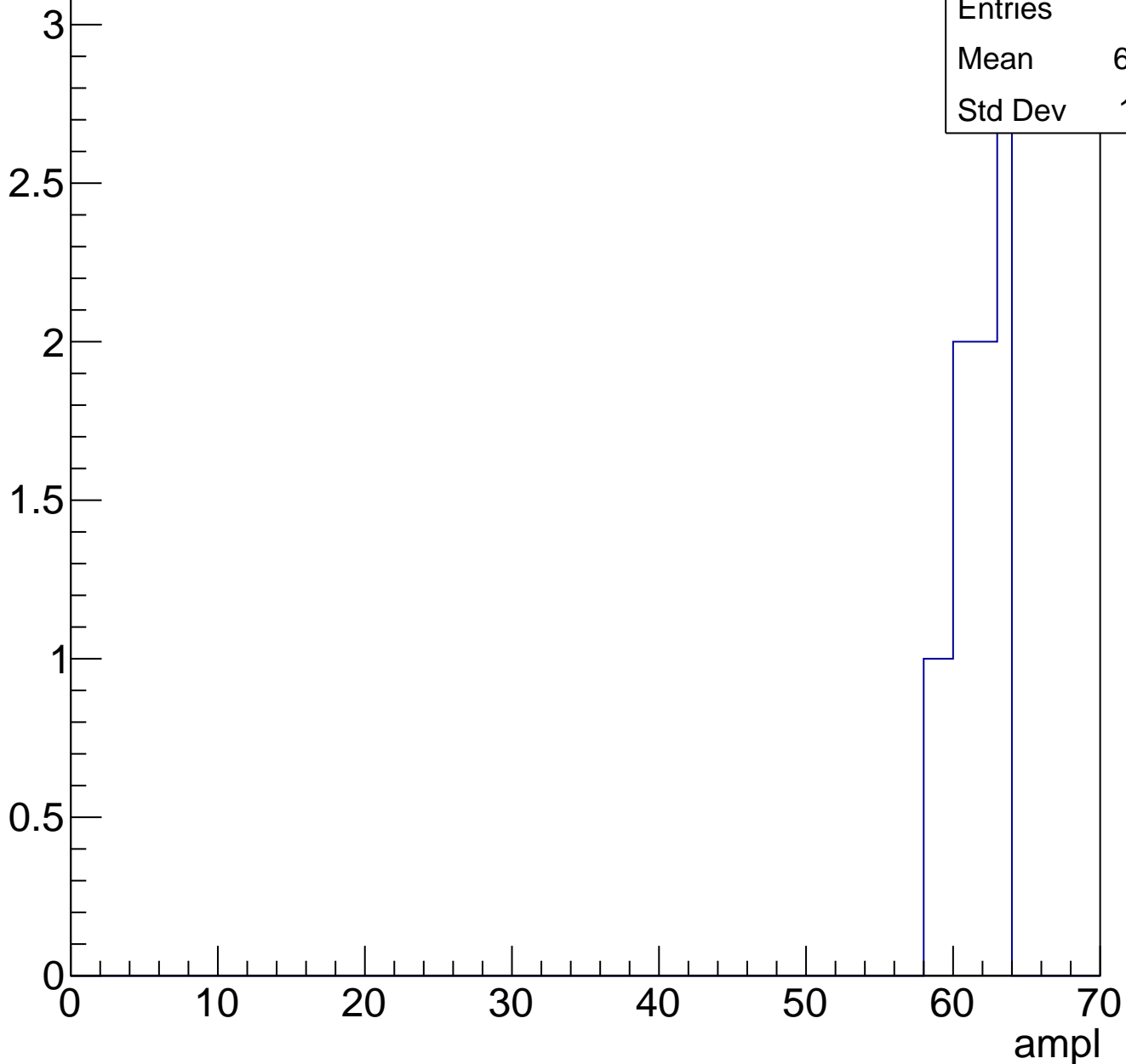
7

8

B1L100S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch39, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	27.38
Std Dev	4.863

Gaus mean : 29.2120

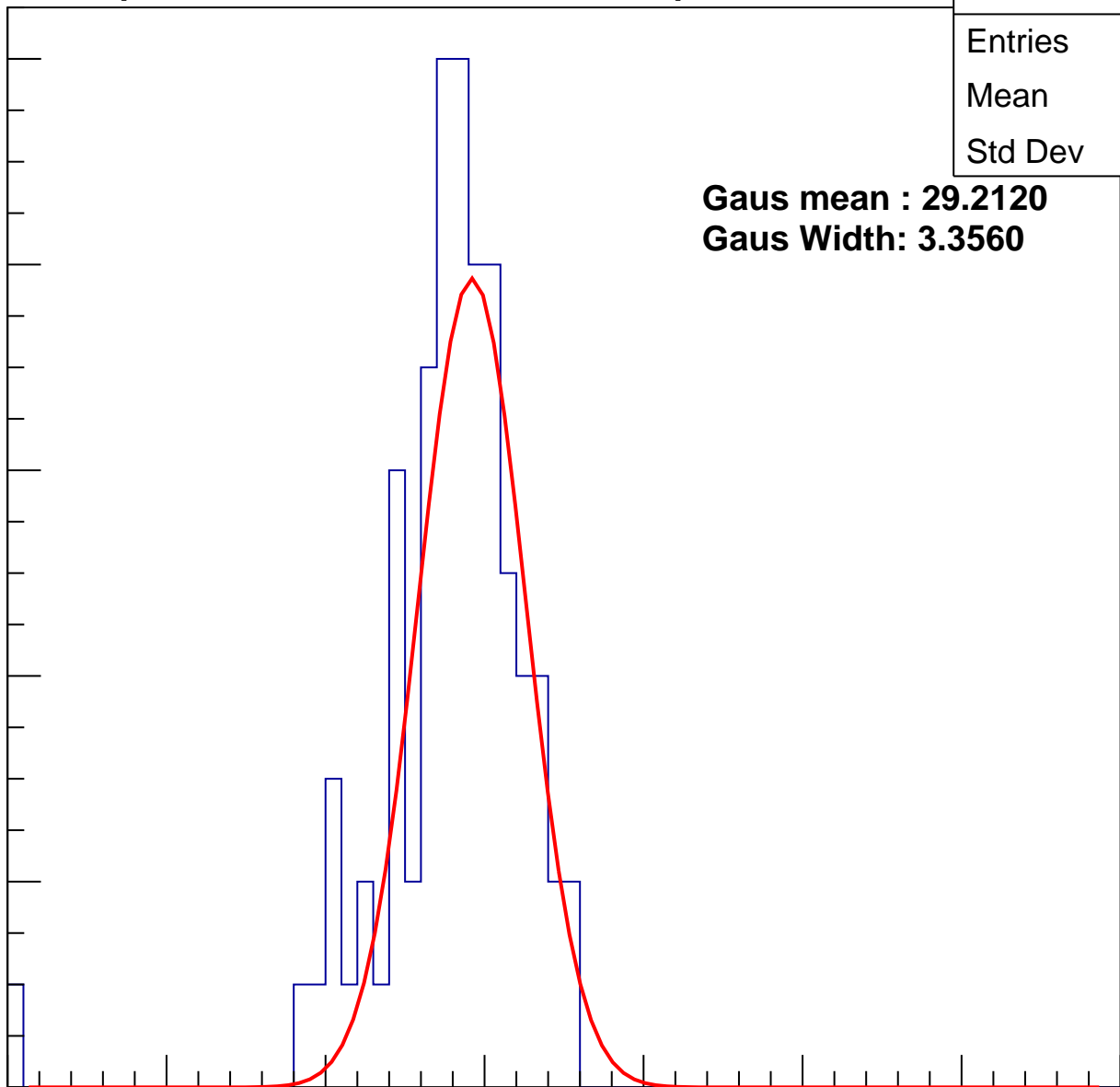
Gaus Width: 3.3560

Entry

10
8
6
4
2
0

ampl

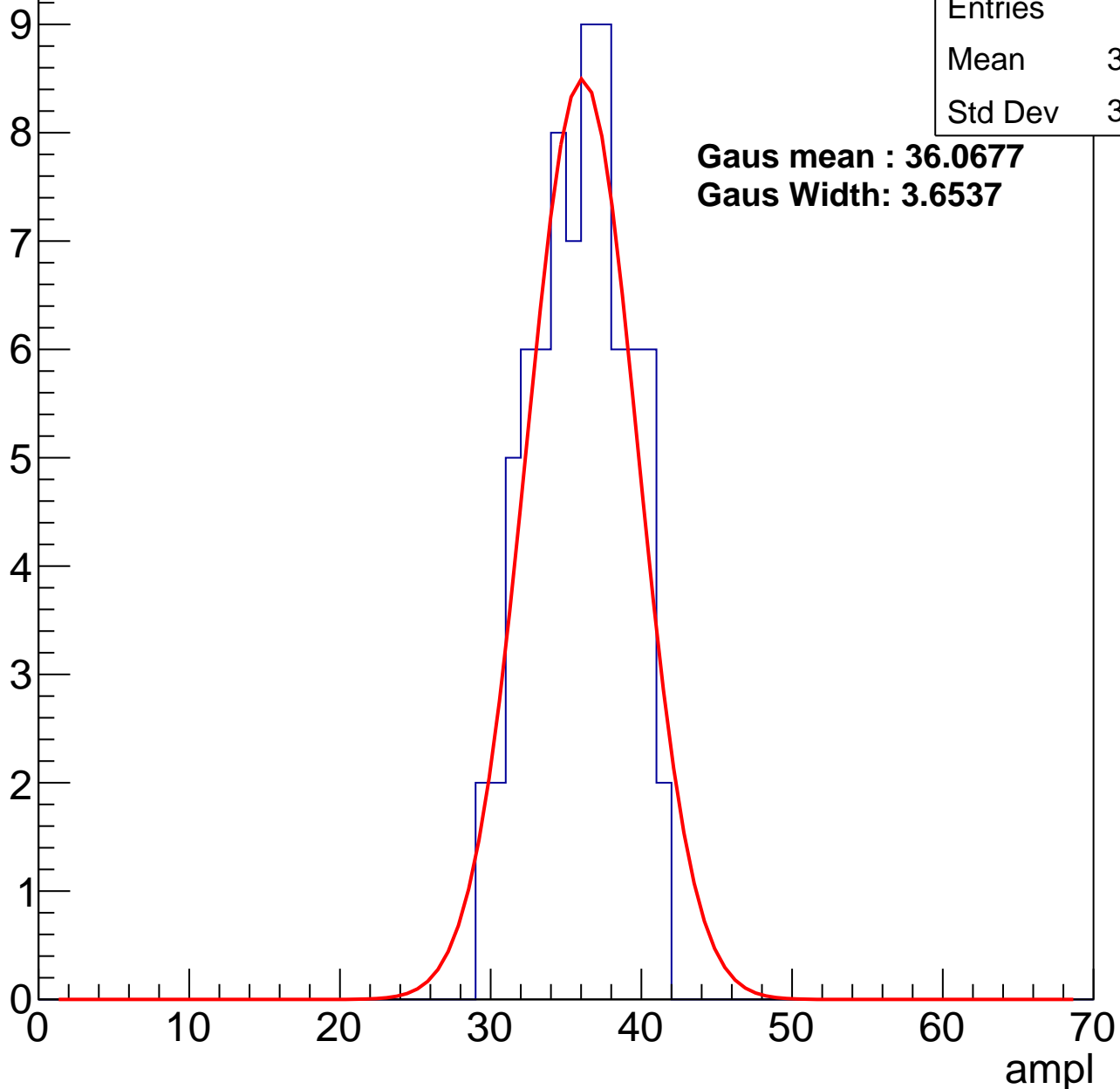
0 10 20 30 40 50 60 70



B1L100S, U14-ch39, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	74
Mean	35.42
Std Dev	3.058

Gaus mean : 36.0677
Gaus Width: 3.6537

B1L100S, U14-ch39, adc2

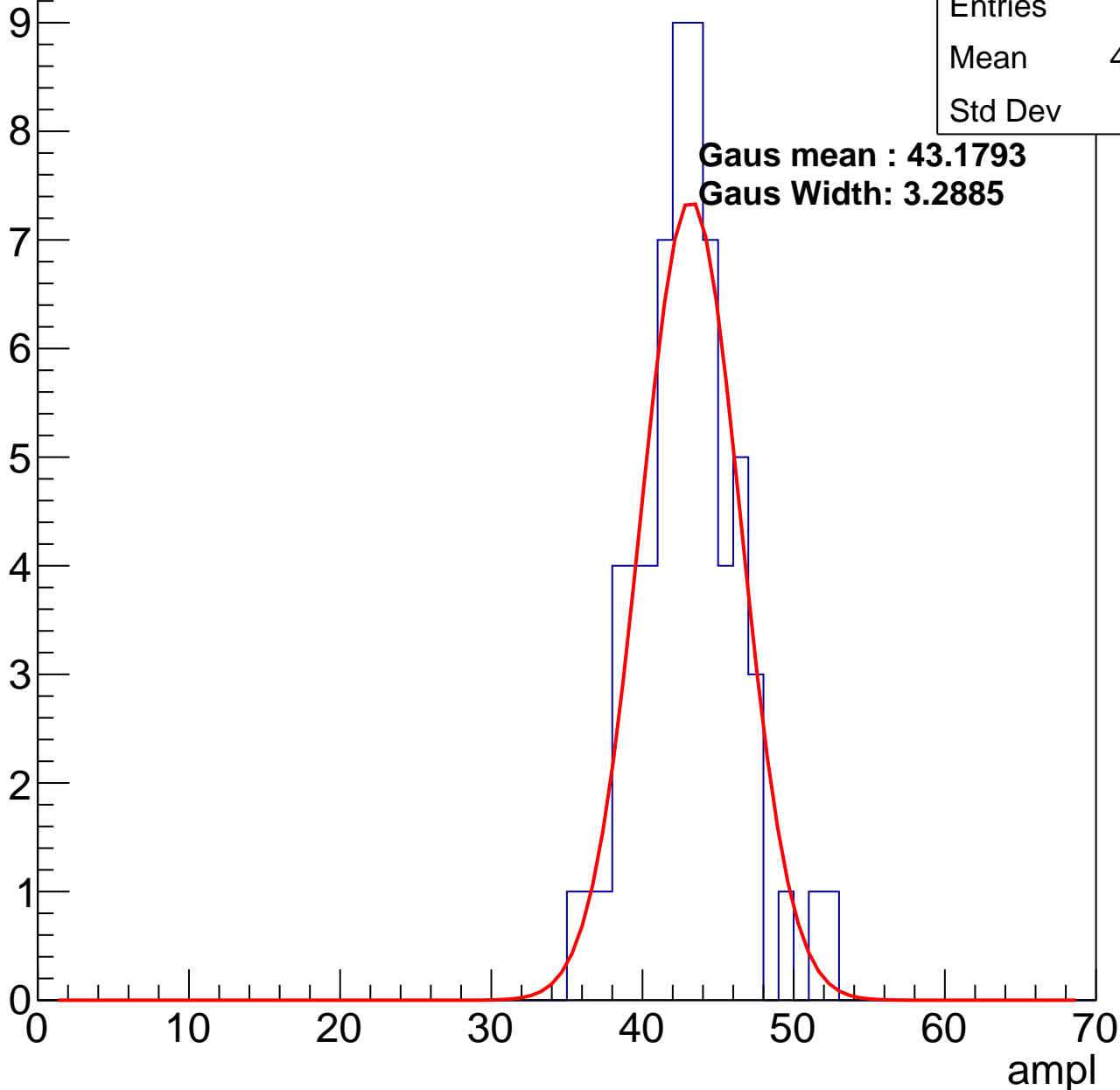
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.56
Std Dev	3.29

Gaus mean : 43.1793

Gaus Width: 3.2885

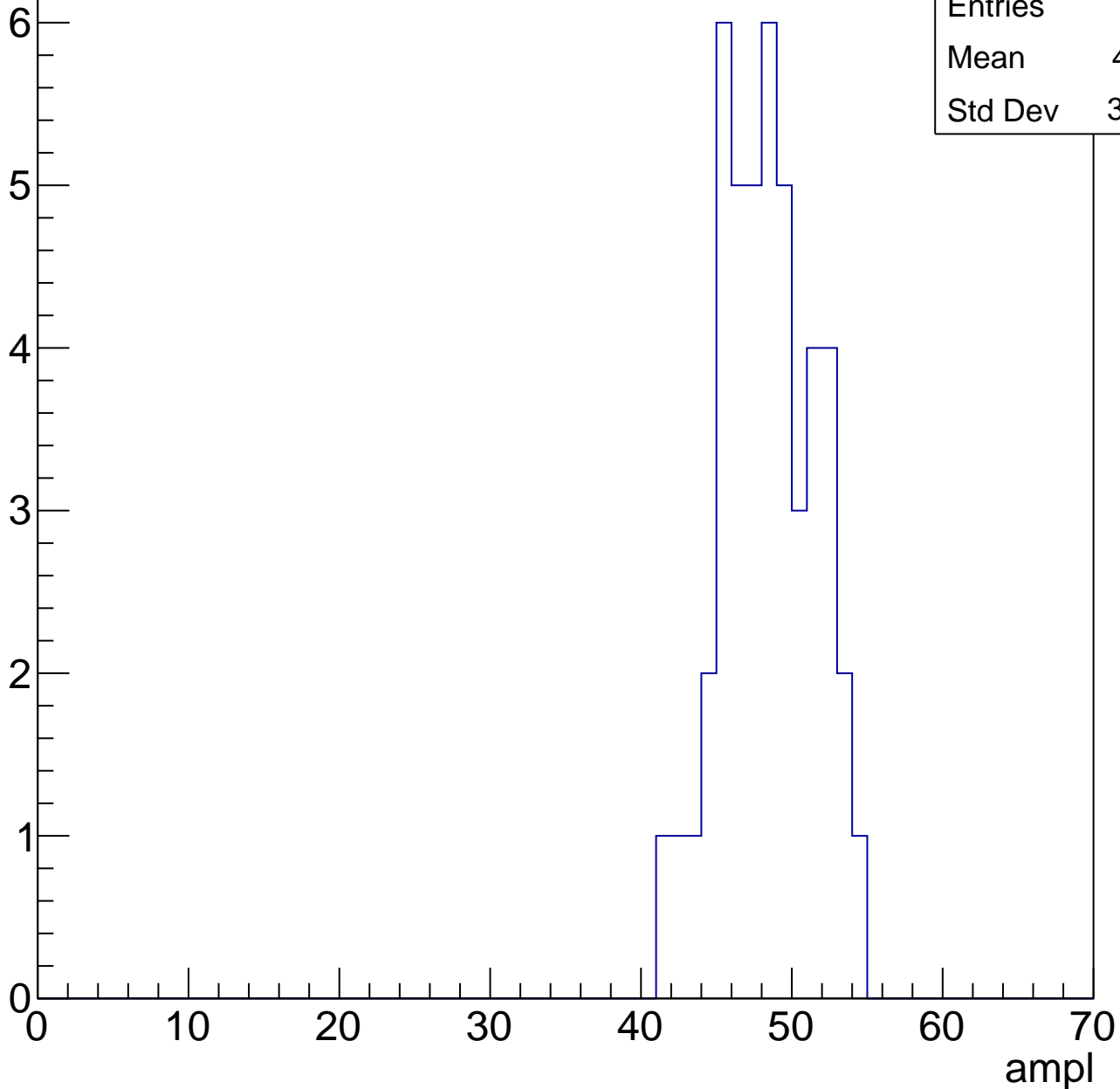


B1L100S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	47.91
Std Dev	3.035

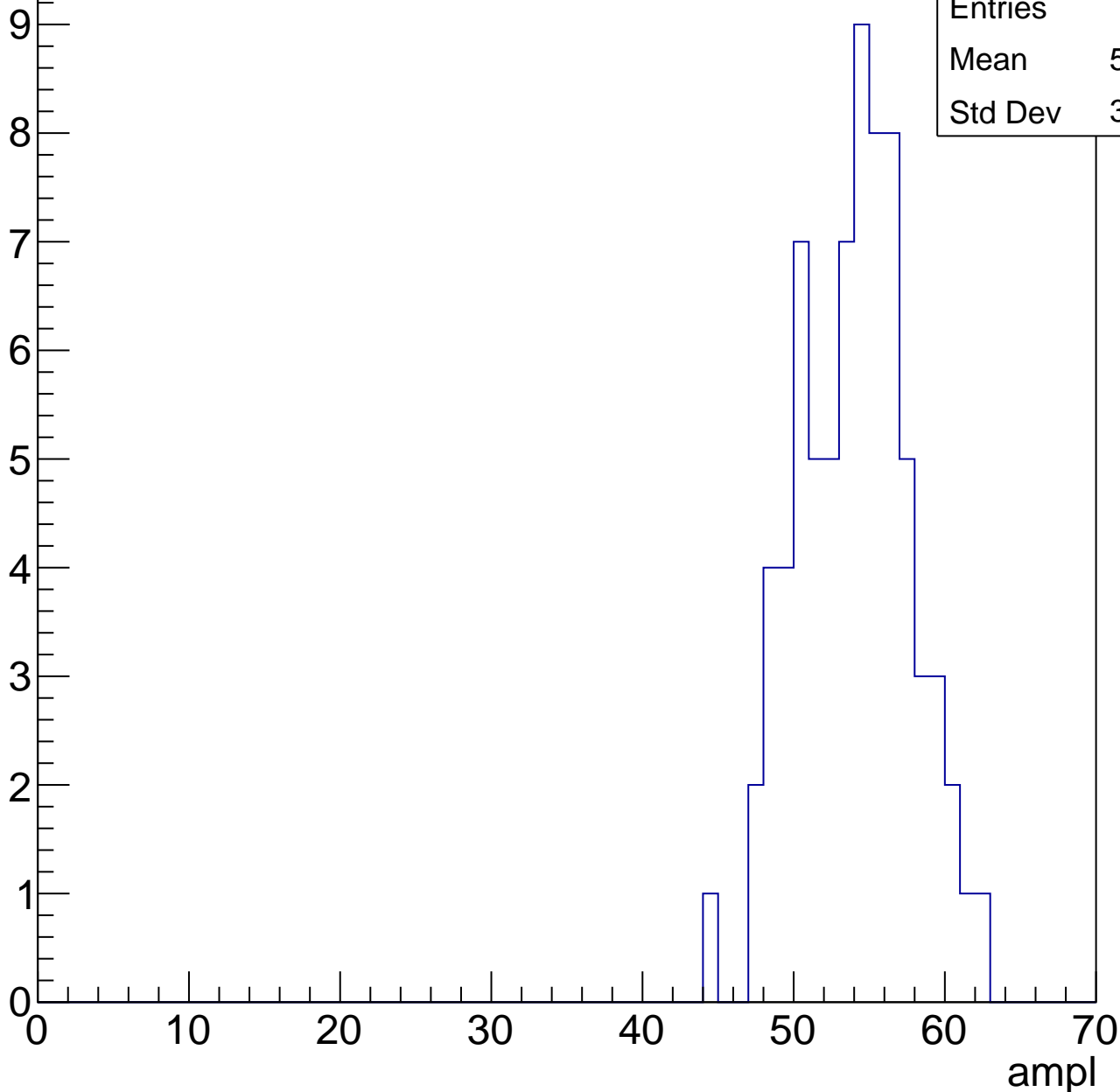


B1L100S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	53.53
Std Dev	3.656

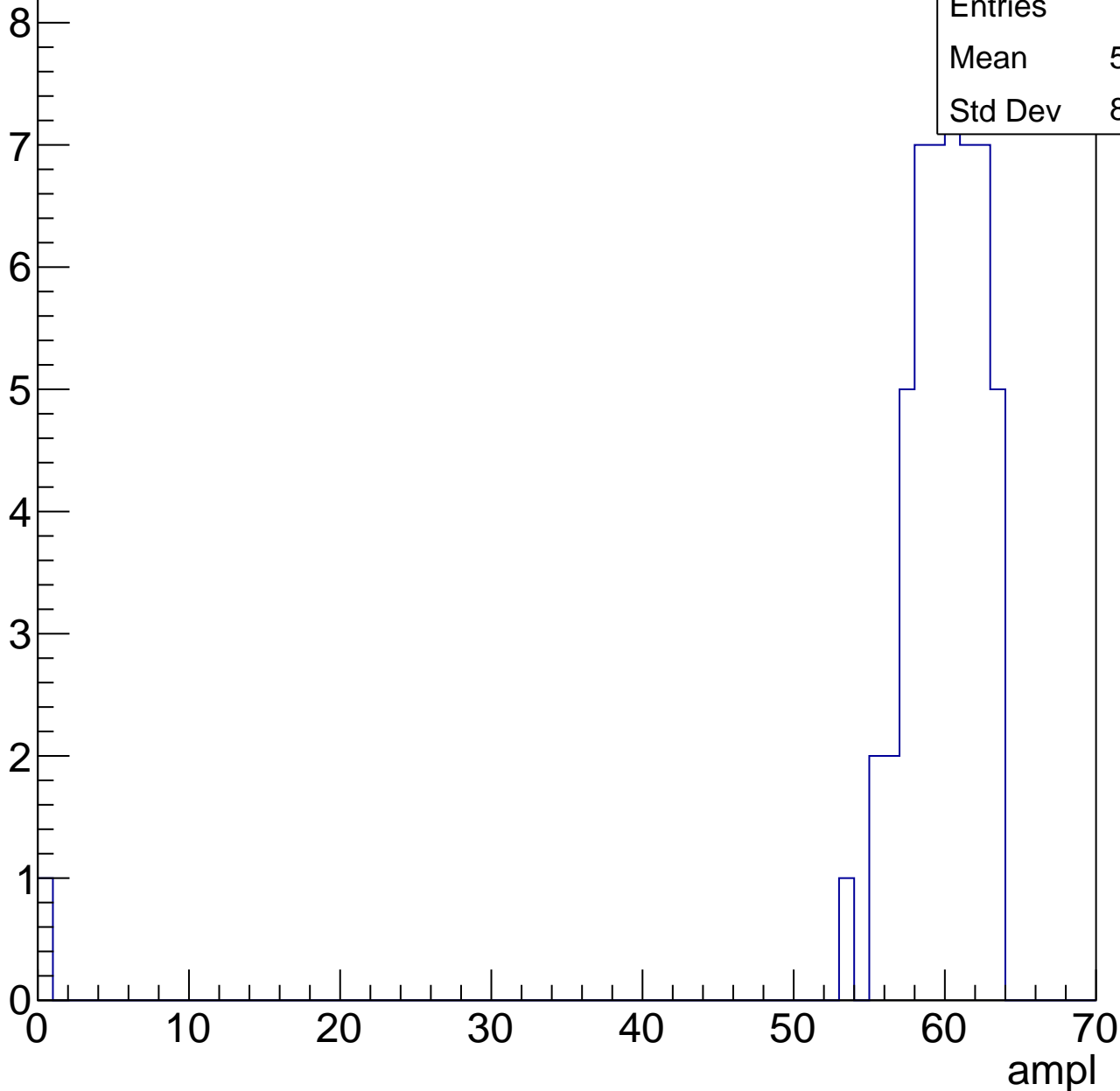


B1L100S, U14-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

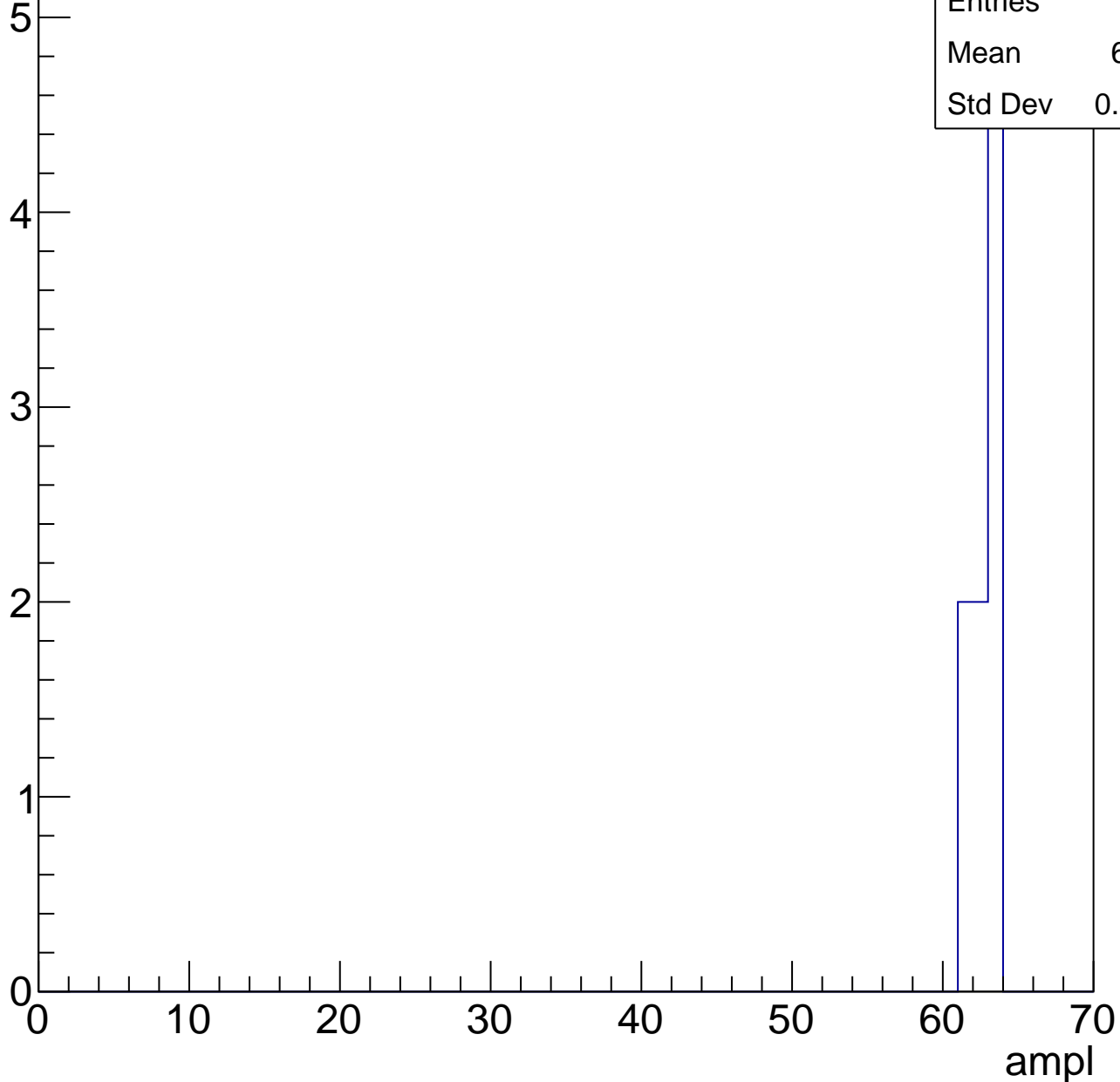
Entries	52
Mean	58.37
Std Dev	8.494



B1L100S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch40, adc0

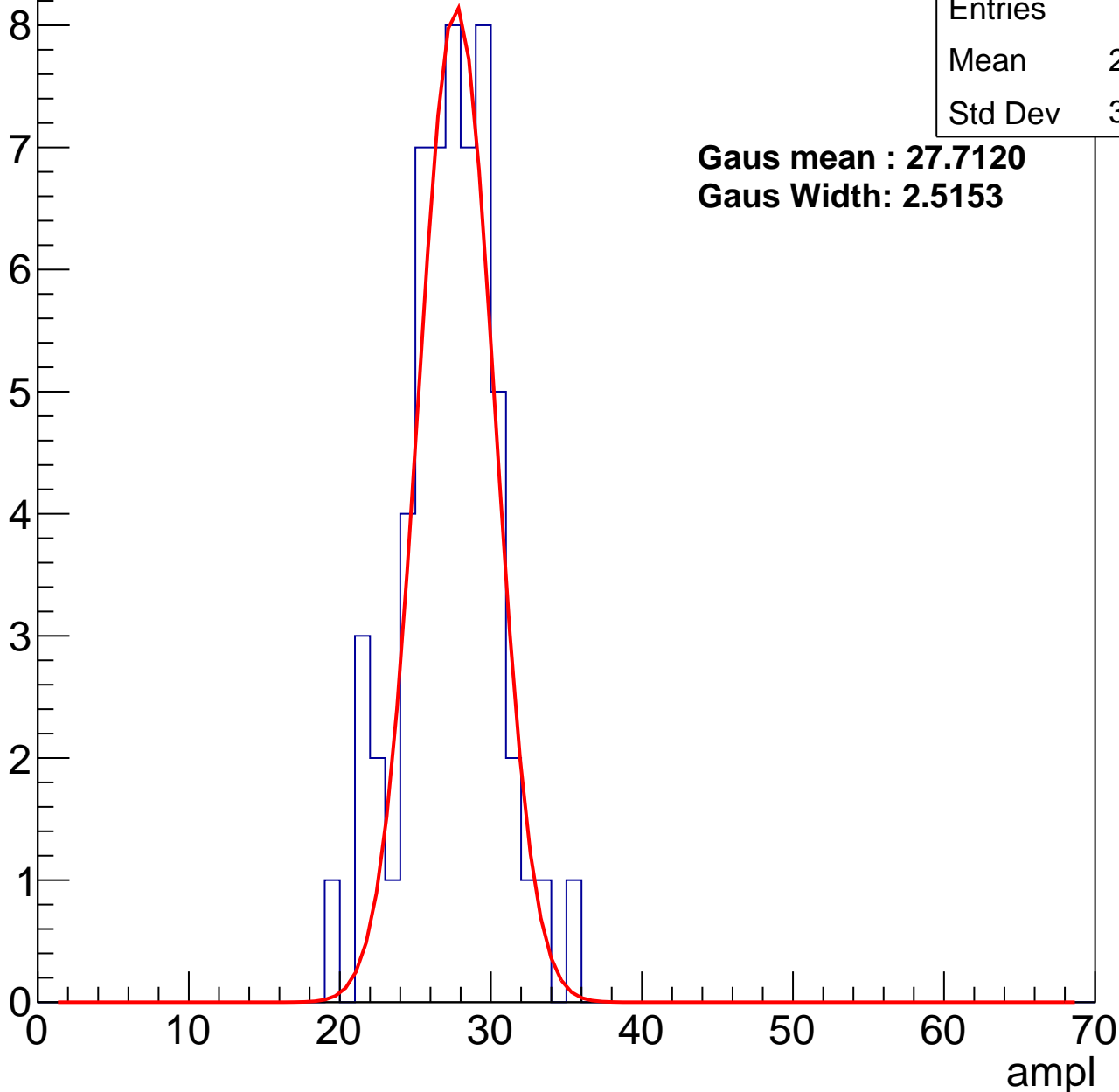
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	26.86
Std Dev	3.076

Gaus mean : 27.7120

Gaus Width: 2.5153



B1L100S, U14-ch40, adc1

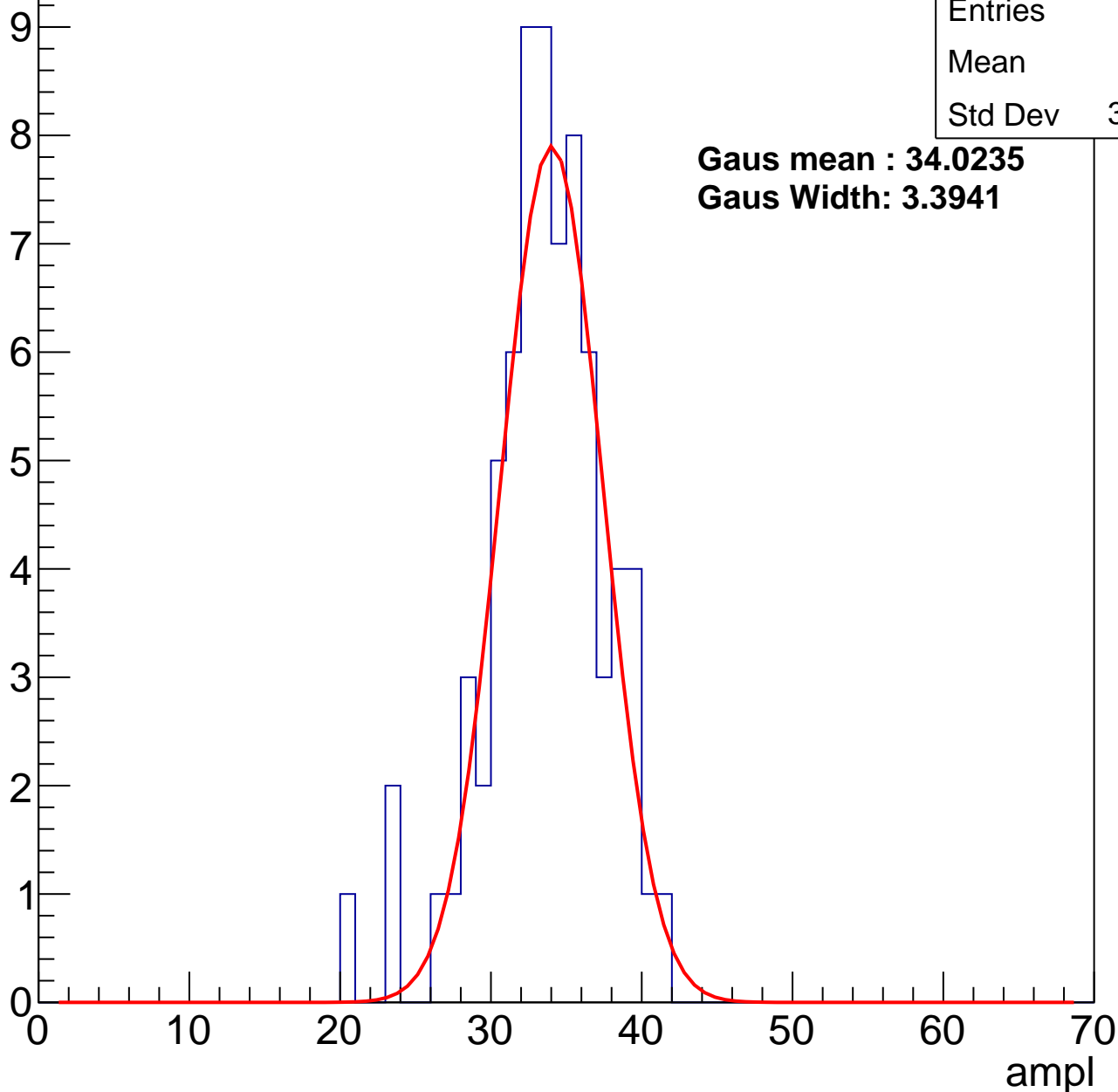
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	33.1
Std Dev	3.945

Gaus mean : 34.0235

Gaus Width: 3.3941



B1L100S, U14-ch40, adc2

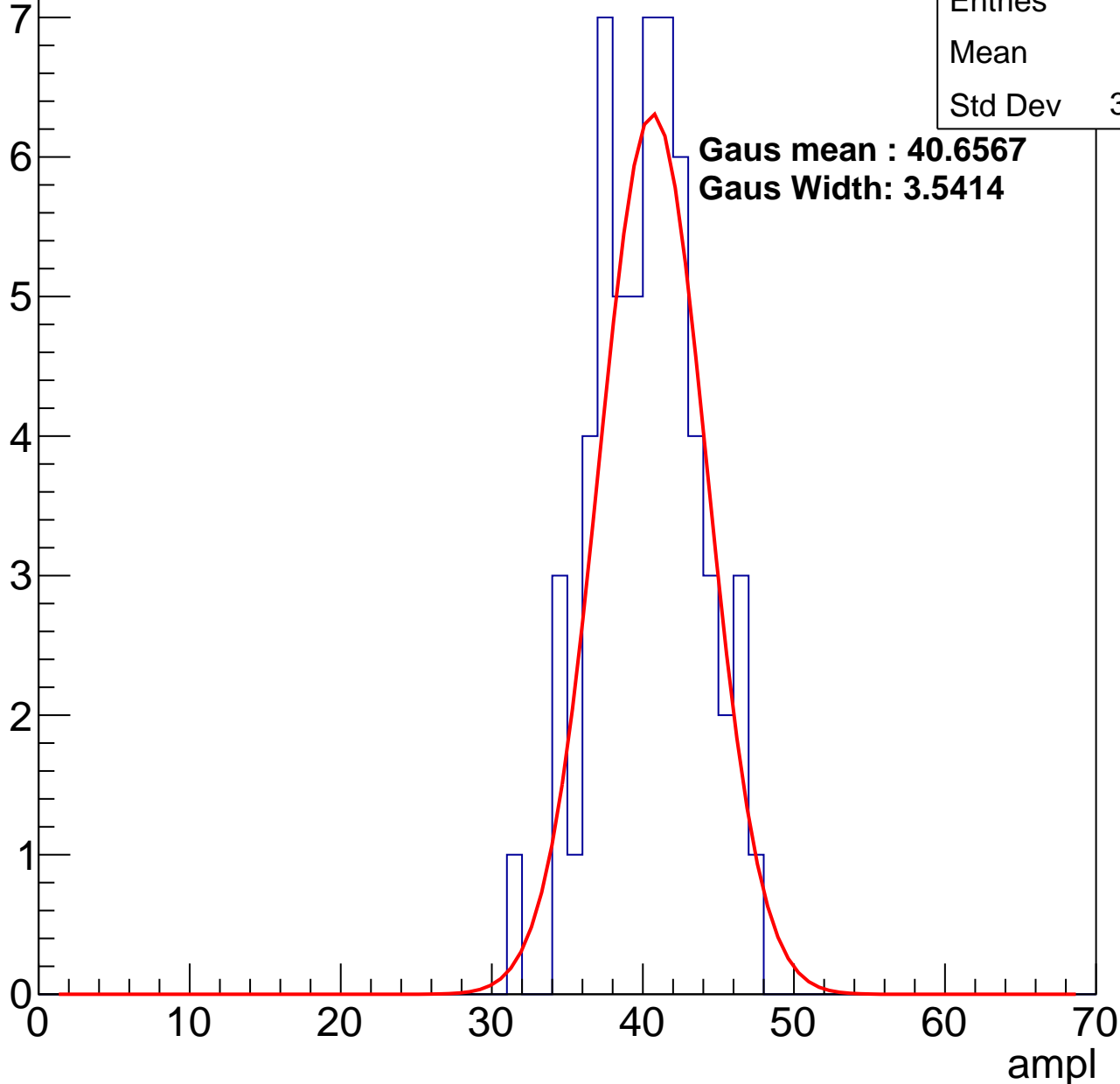
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	39.9
Std Dev	3.413

Gaus mean : 40.6567

Gaus Width: 3.5414

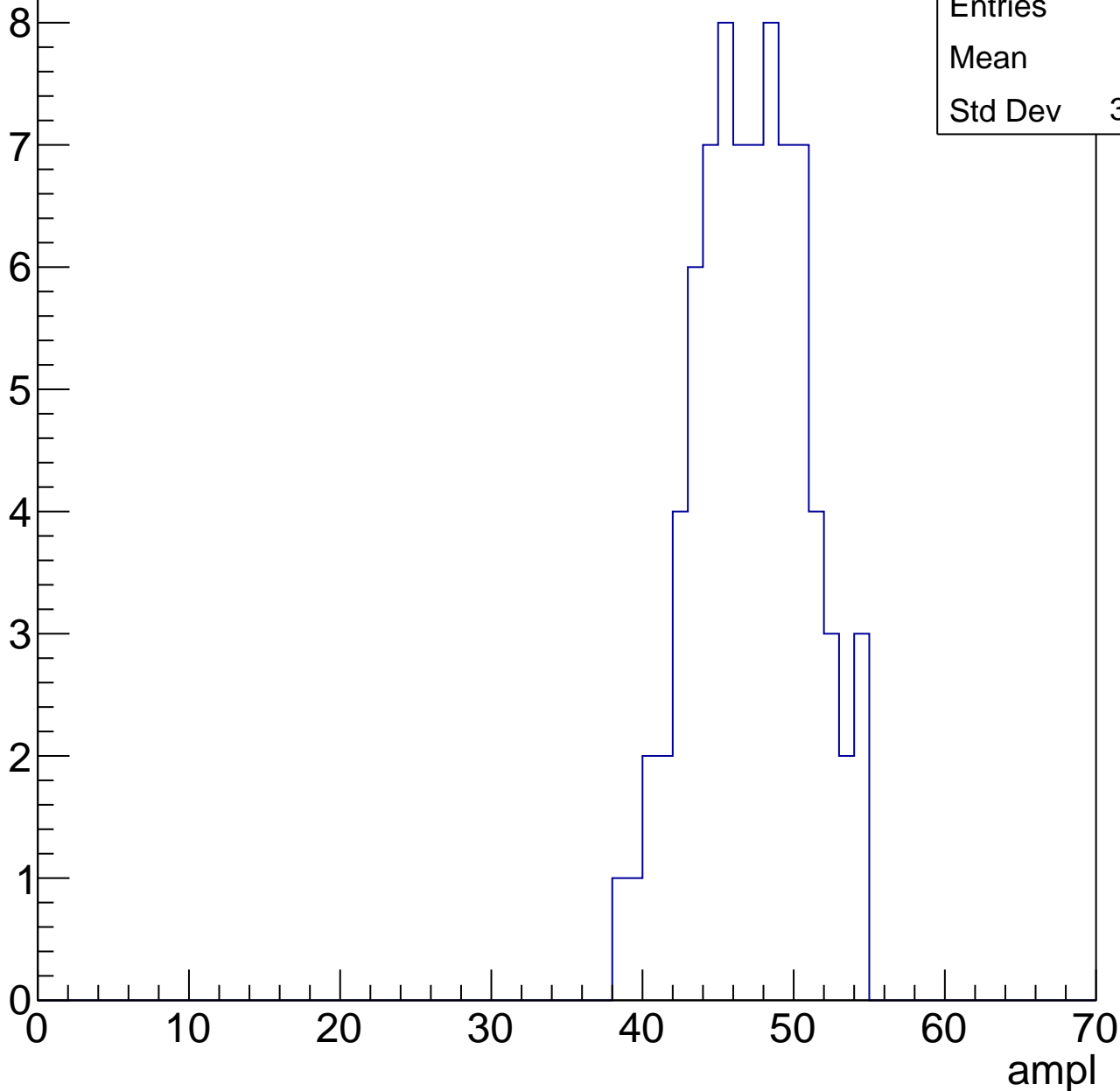


B1L100S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	46.7
Std Dev	3.668

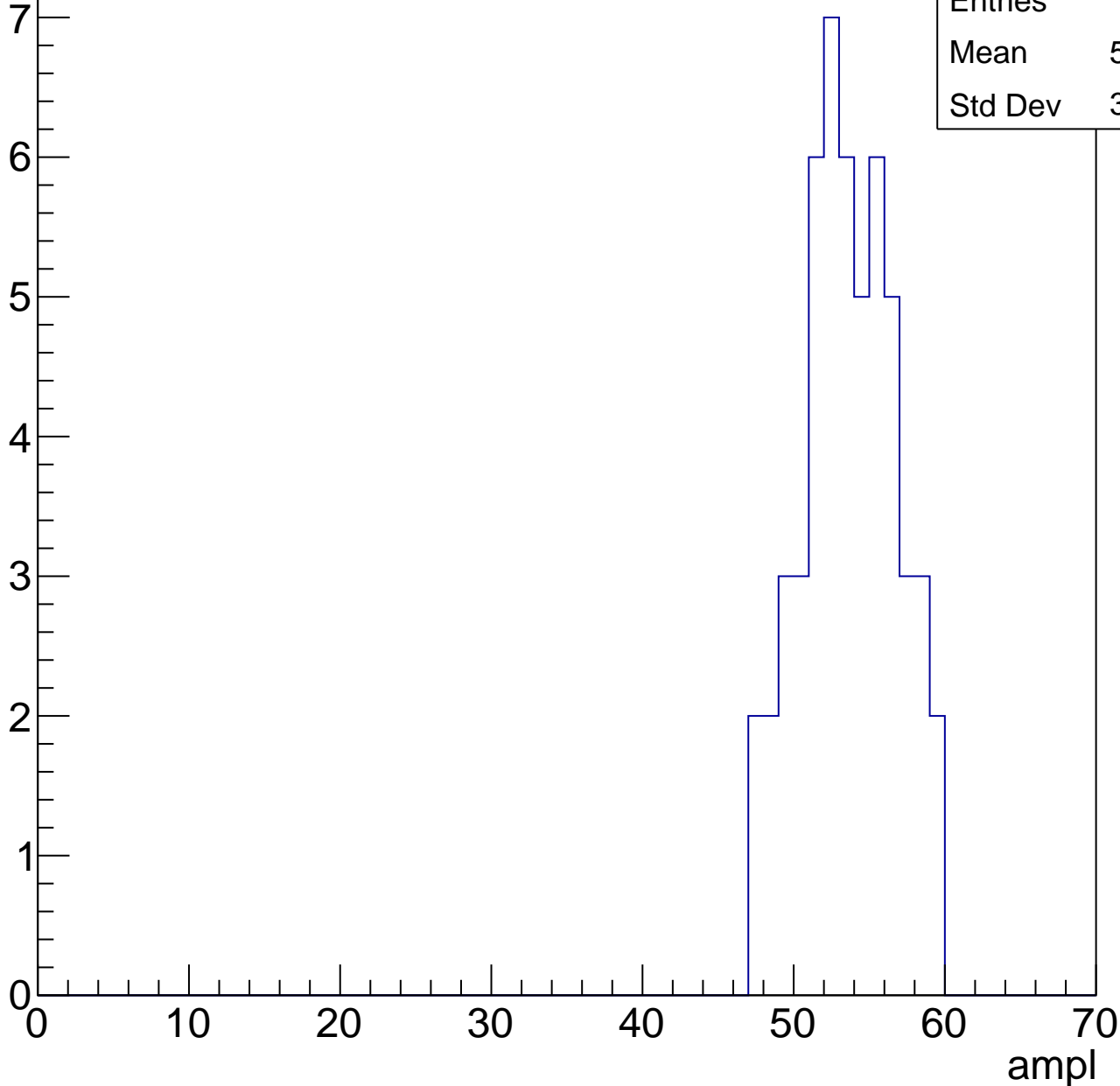


B1L100S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	53.17
Std Dev	3.058



B1L100S, U14-ch40, adc5

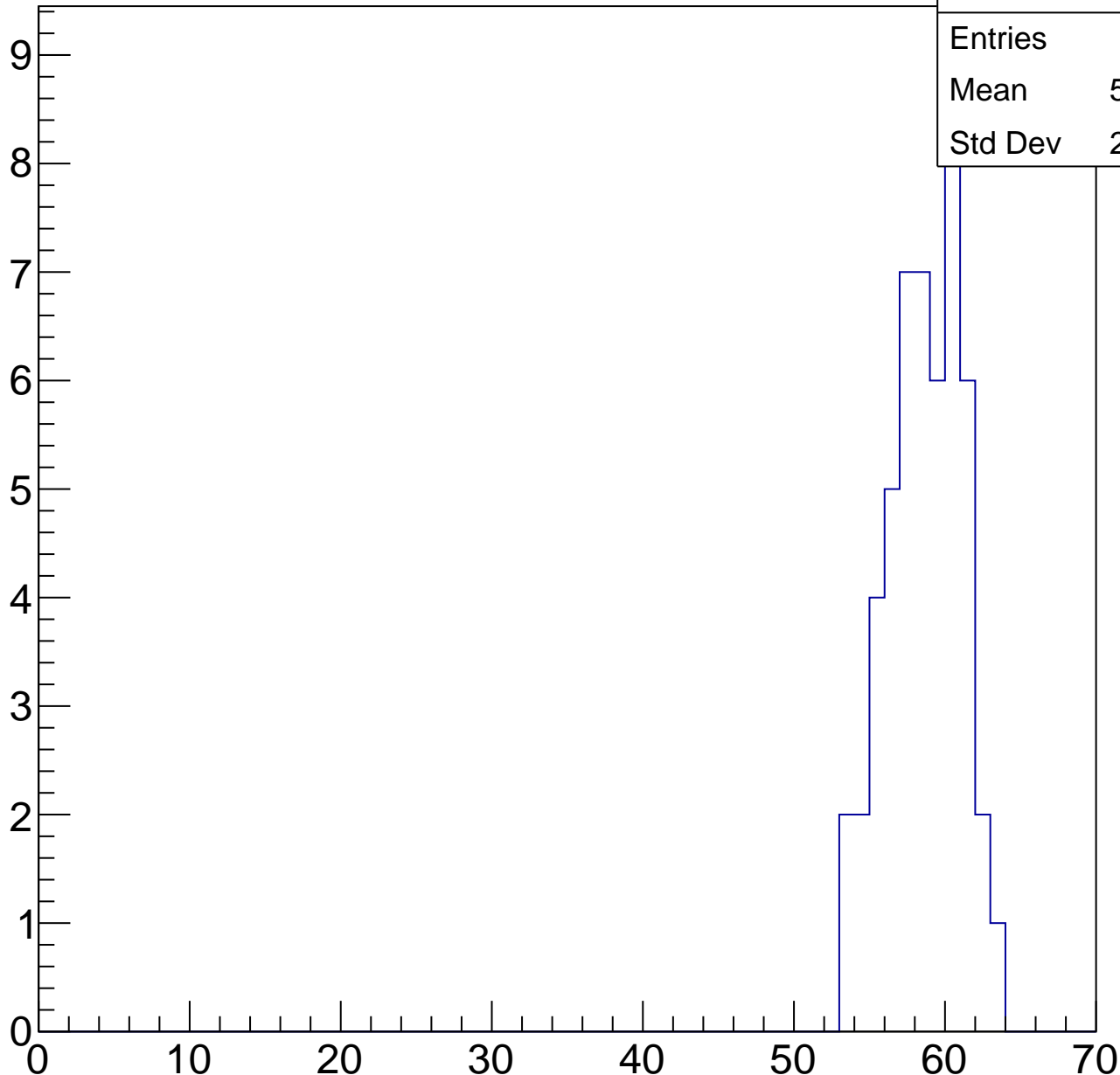
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.16
Std Dev	2.412

ampl

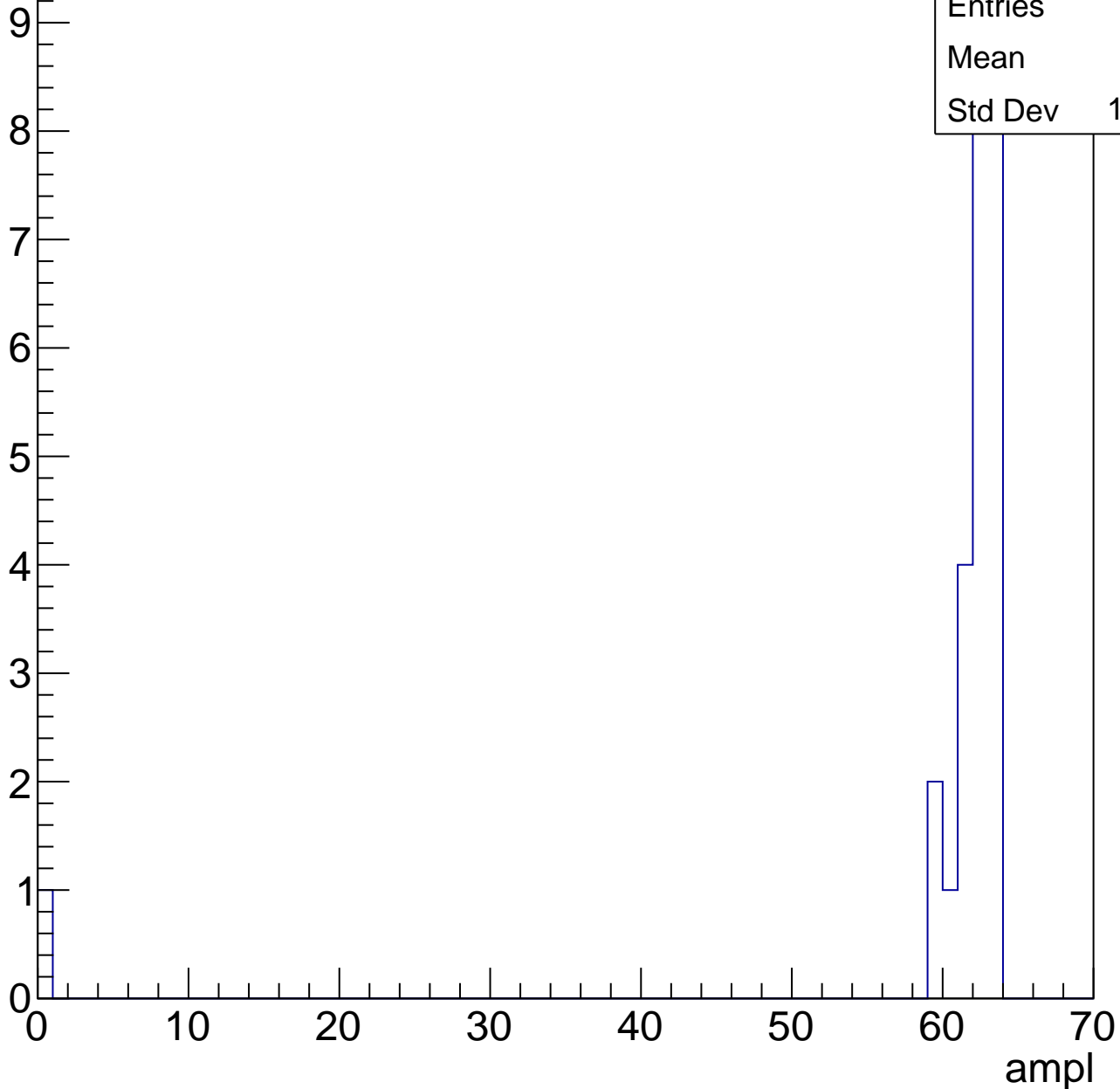


B1L100S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	59.4
Std Dev	12.18



B1L100S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch41, adc0

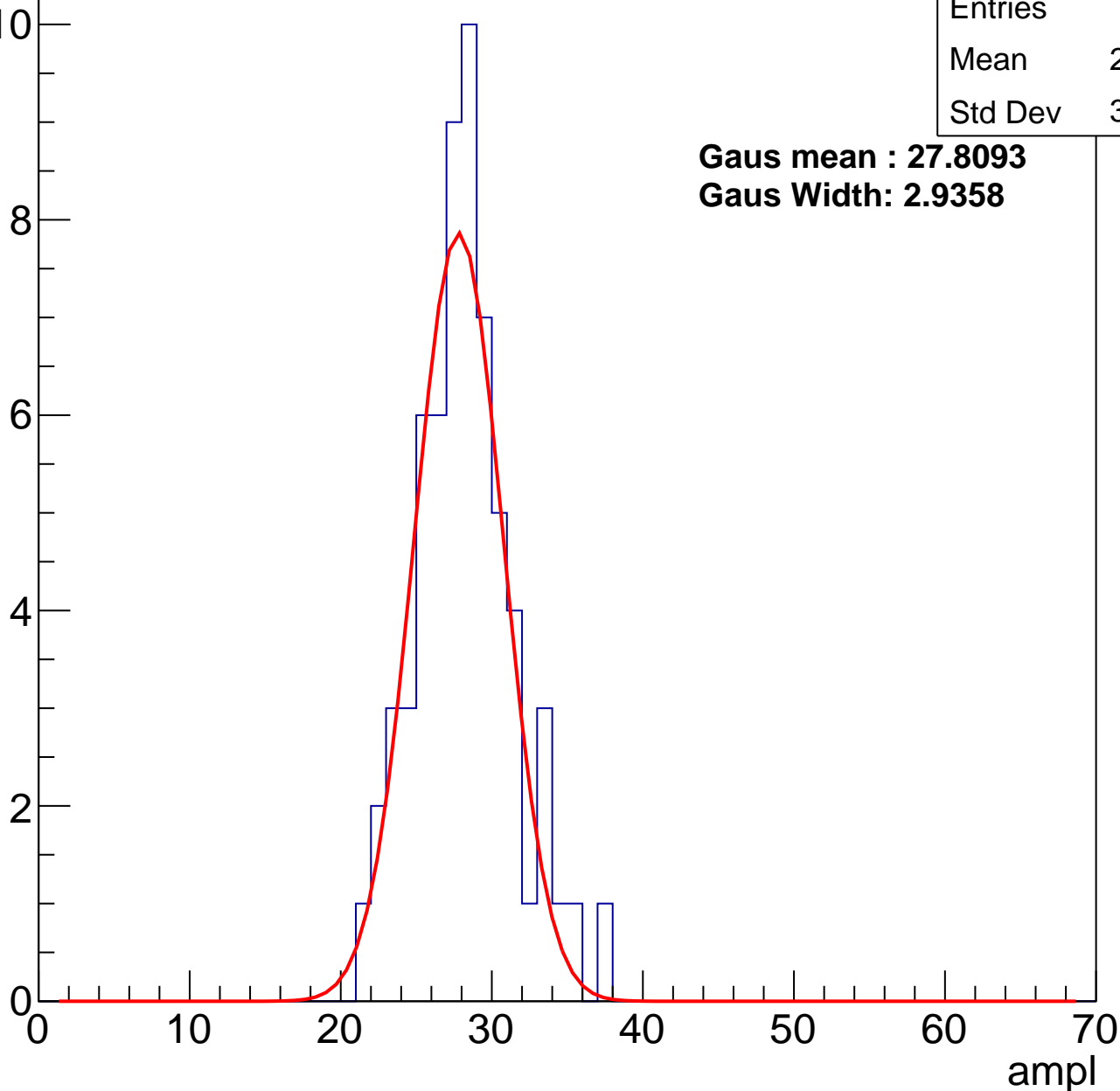
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.76
Std Dev	3.206

Gaus mean : 27.8093

Gaus Width: 2.9358



B1L100S, U14-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	34.72
Std Dev	3.407

Gaus mean : 34.9216

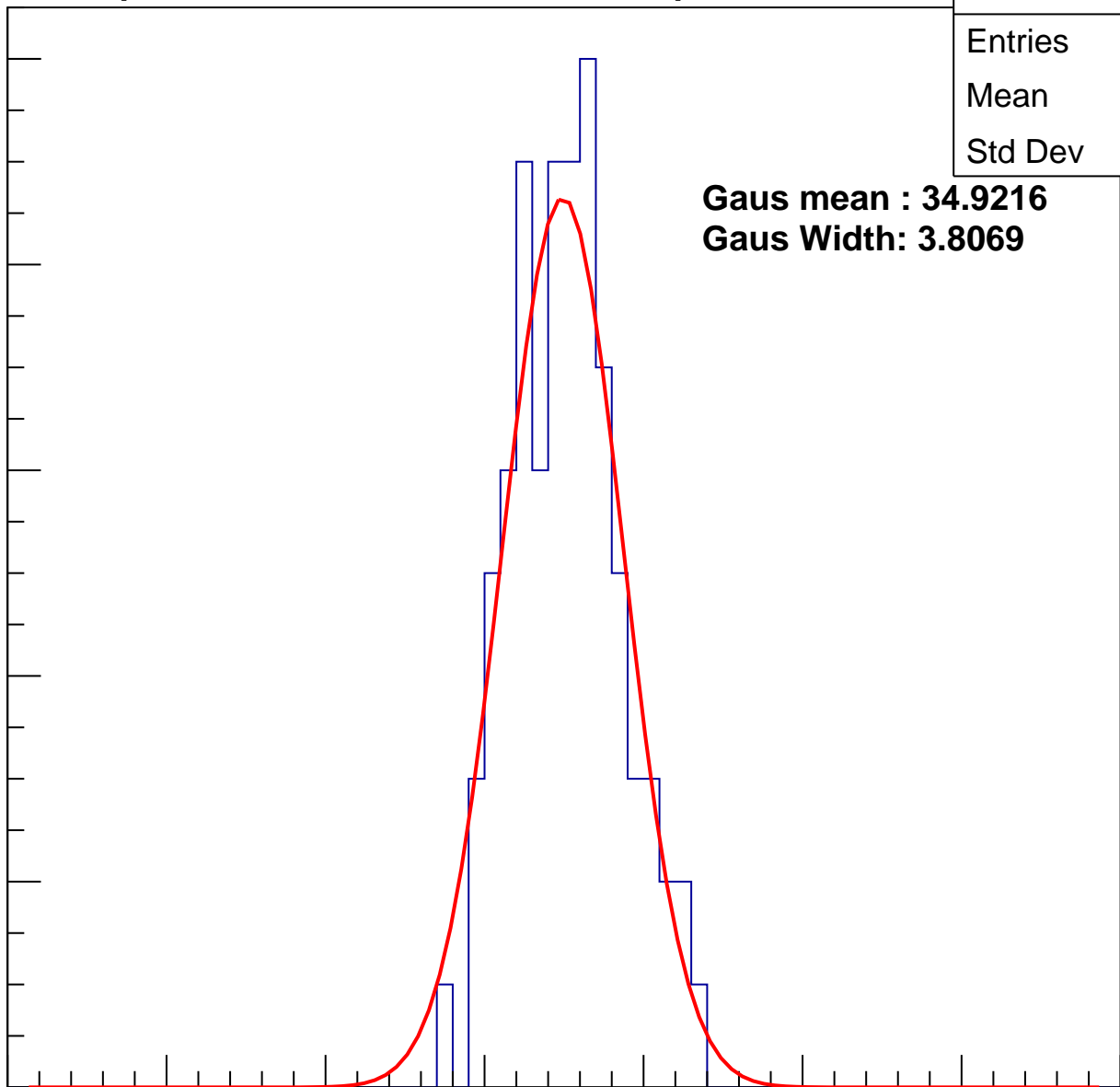
Gaus Width: 3.8069

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch41, adc2

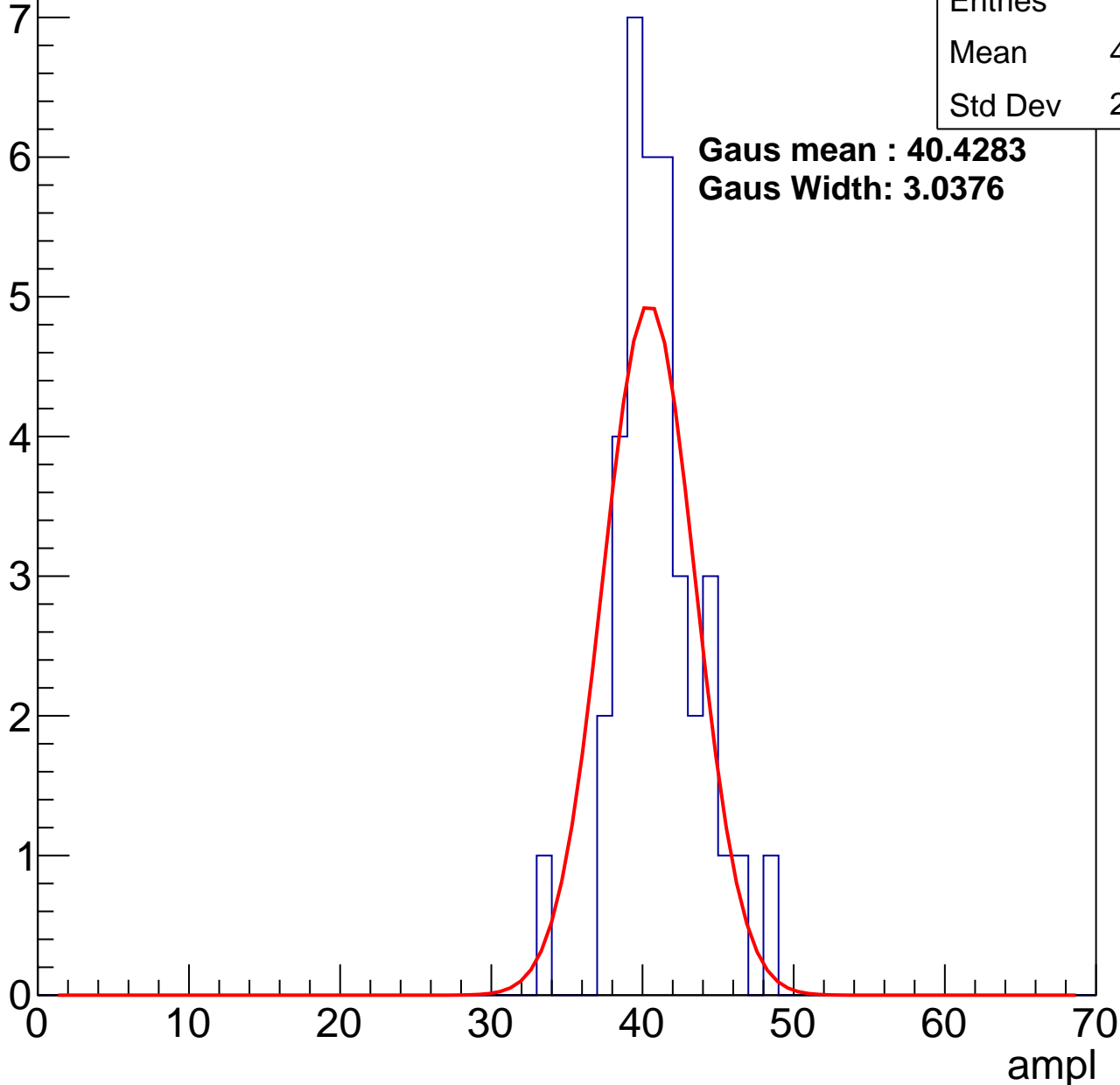
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	40.57
Std Dev	2.785

Gaus mean : 40.4283

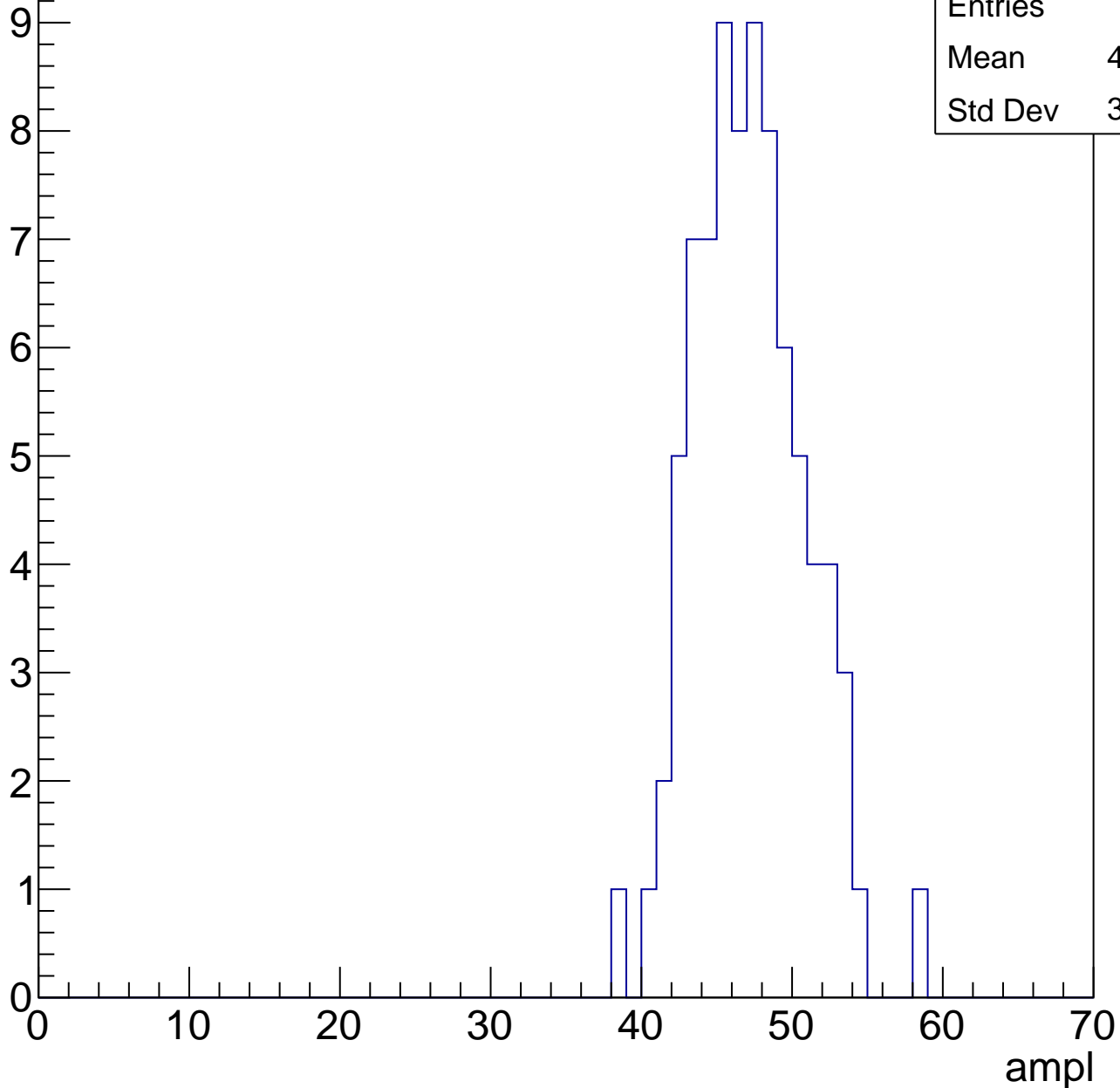
Gaus Width: 3.0376



B1L100S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

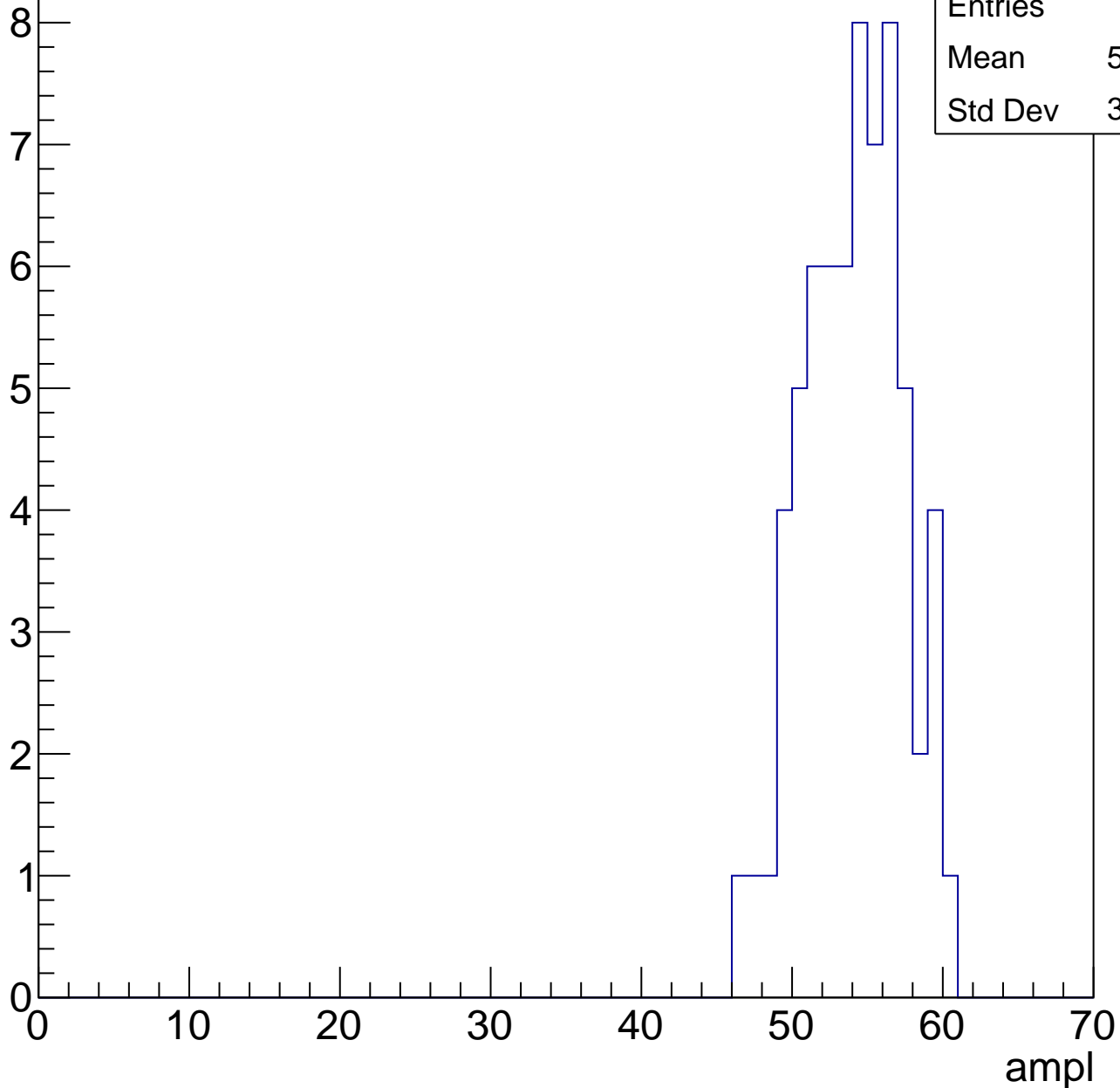


Entries	81
Mean	46.74
Std Dev	3.617

B1L100S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 50

Mean 58.04

Std Dev 8.66

0

0

10

20

30

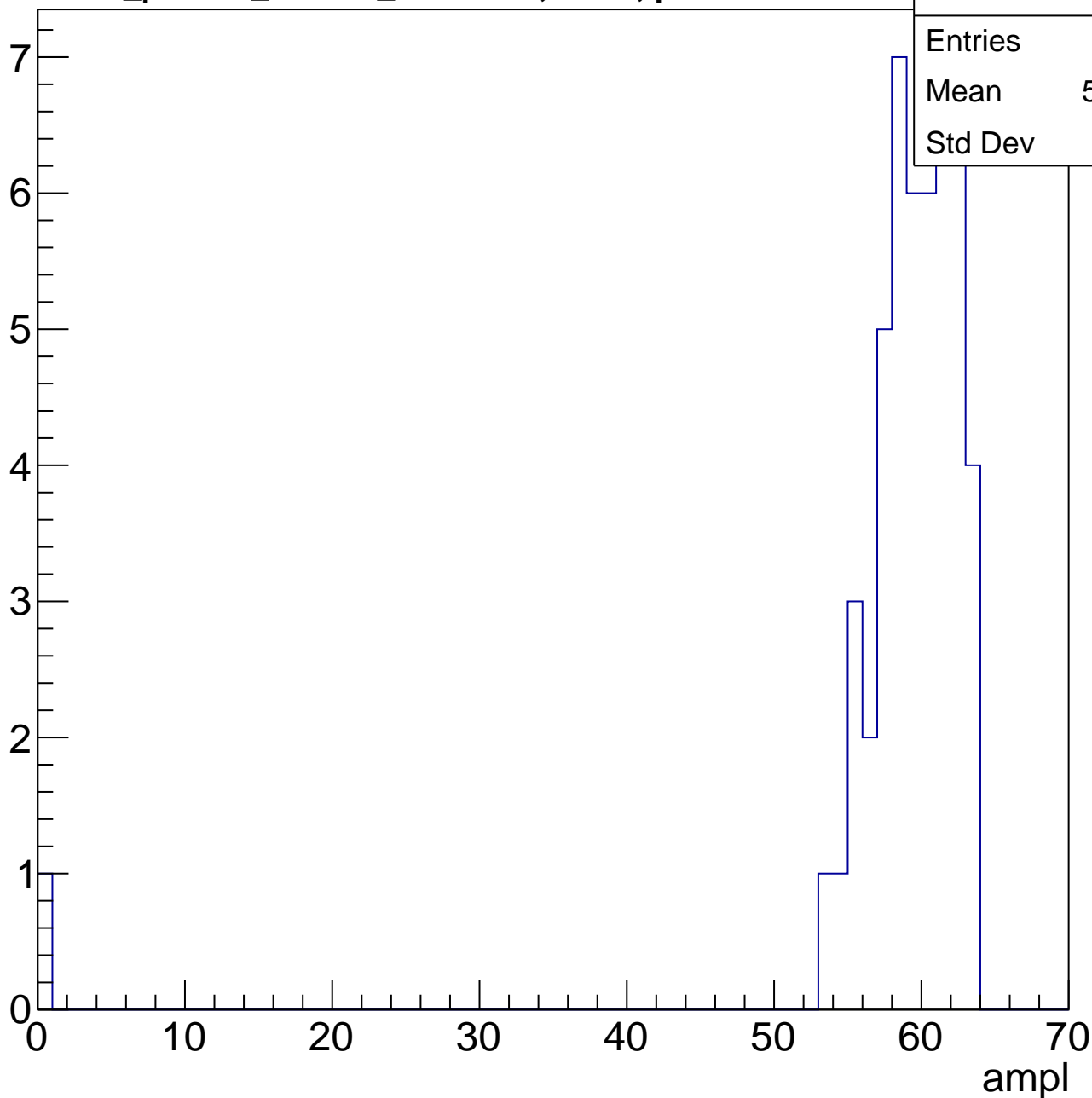
40

50

60

70

ampl

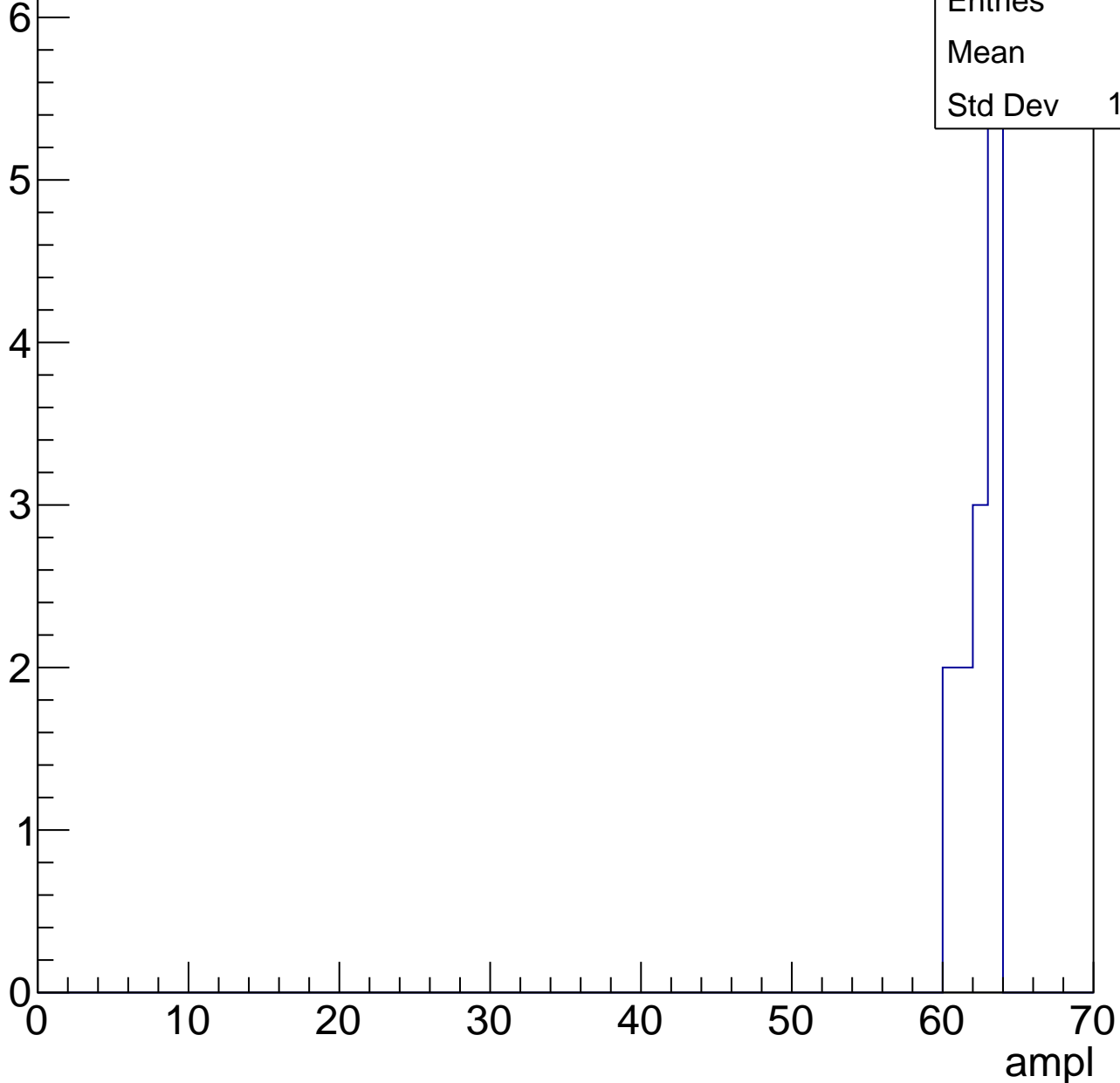


B1L100S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

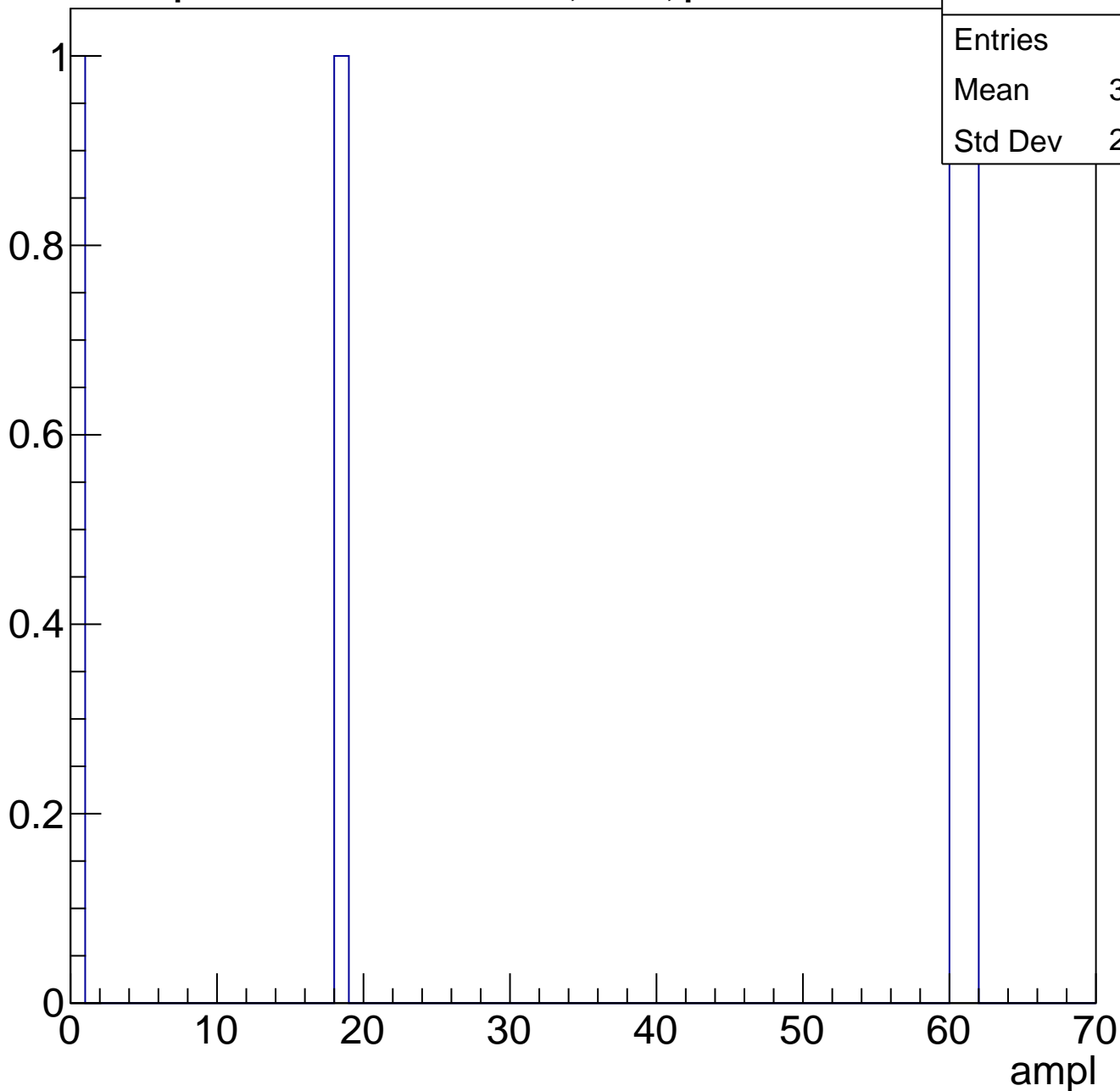
Entries	13
Mean	62
Std Dev	1.109



B1L100S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch42, adc0

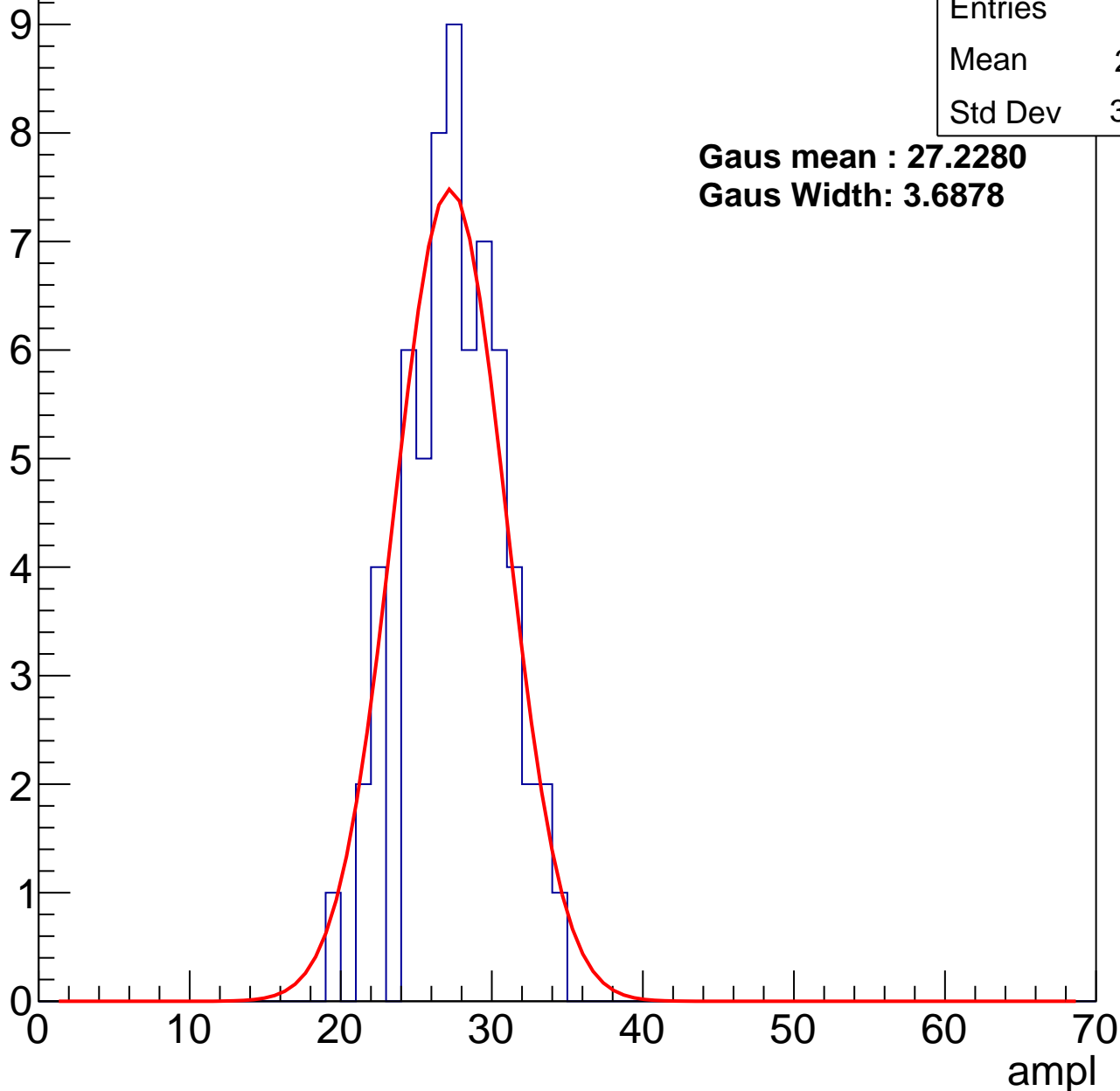
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.11
Std Dev	3.188

Gaus mean : 27.2280

Gaus Width: 3.6878



B1L100S, U14-ch42, adc1

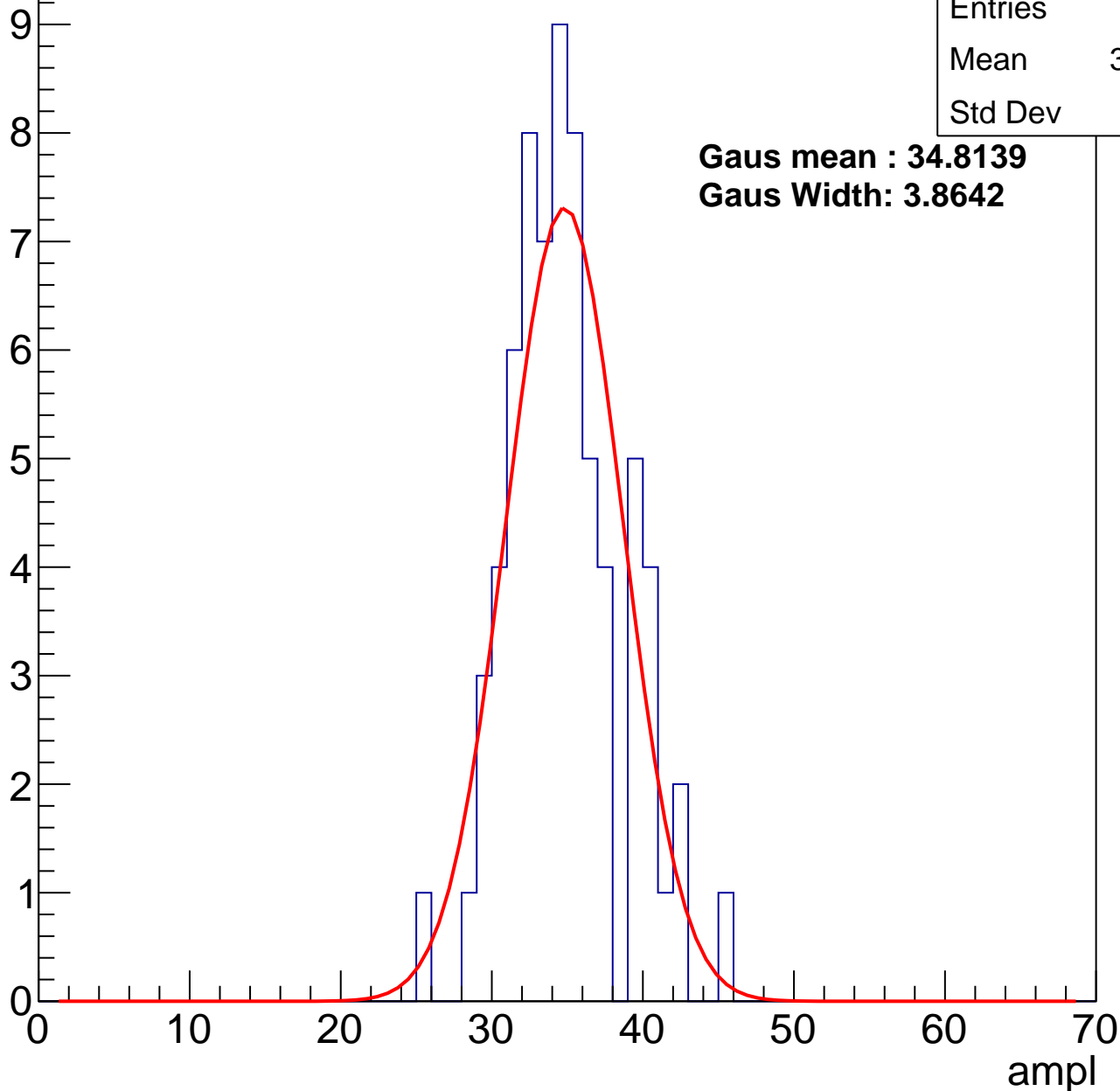
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.38
Std Dev	3.75

Gaus mean : 34.8139

Gaus Width: 3.8642



B1L100S, U14-ch42, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	40.28
Std Dev	3.281

Gaus mean : 40.4148

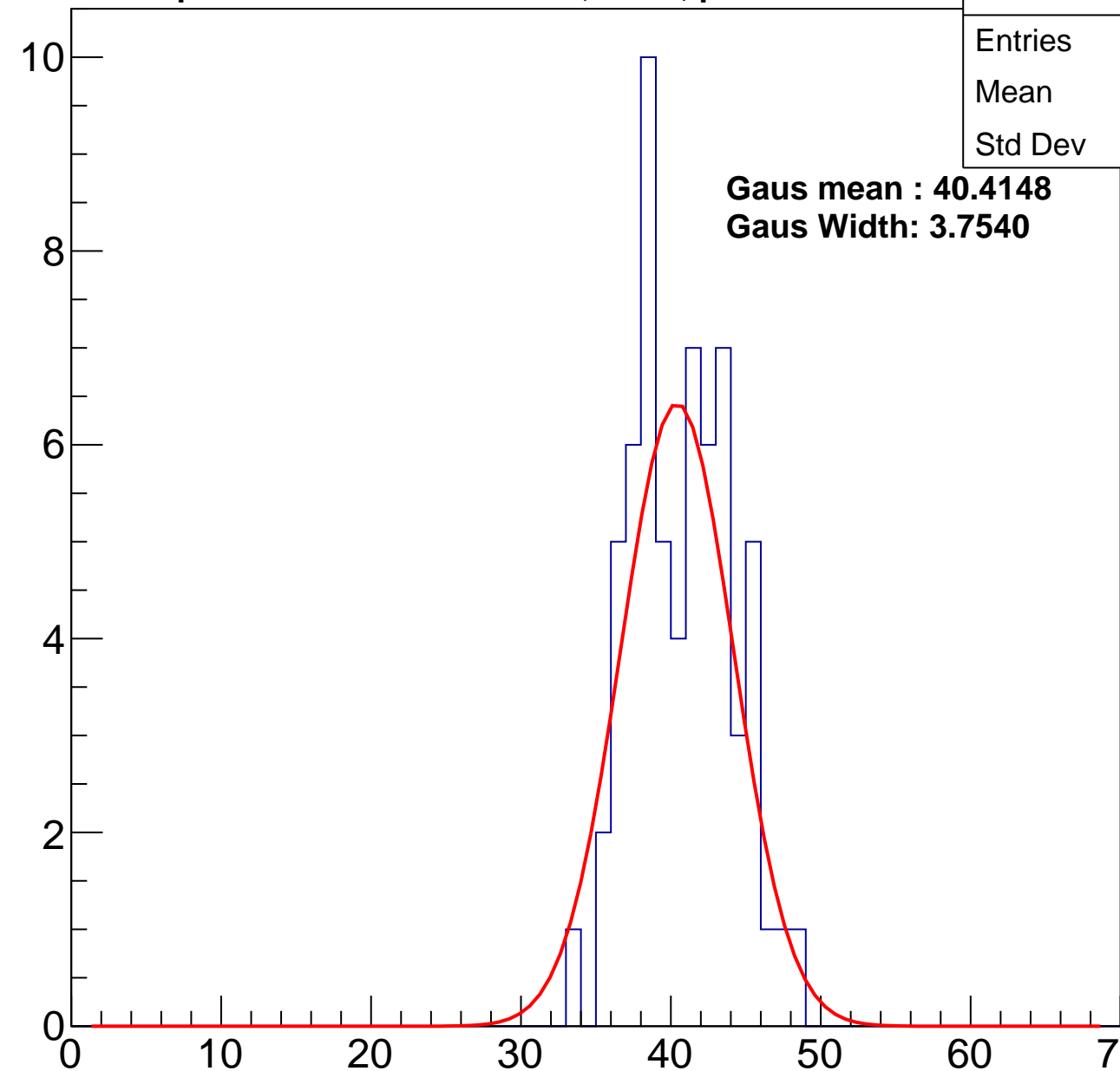
Gaus Width: 3.7540

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

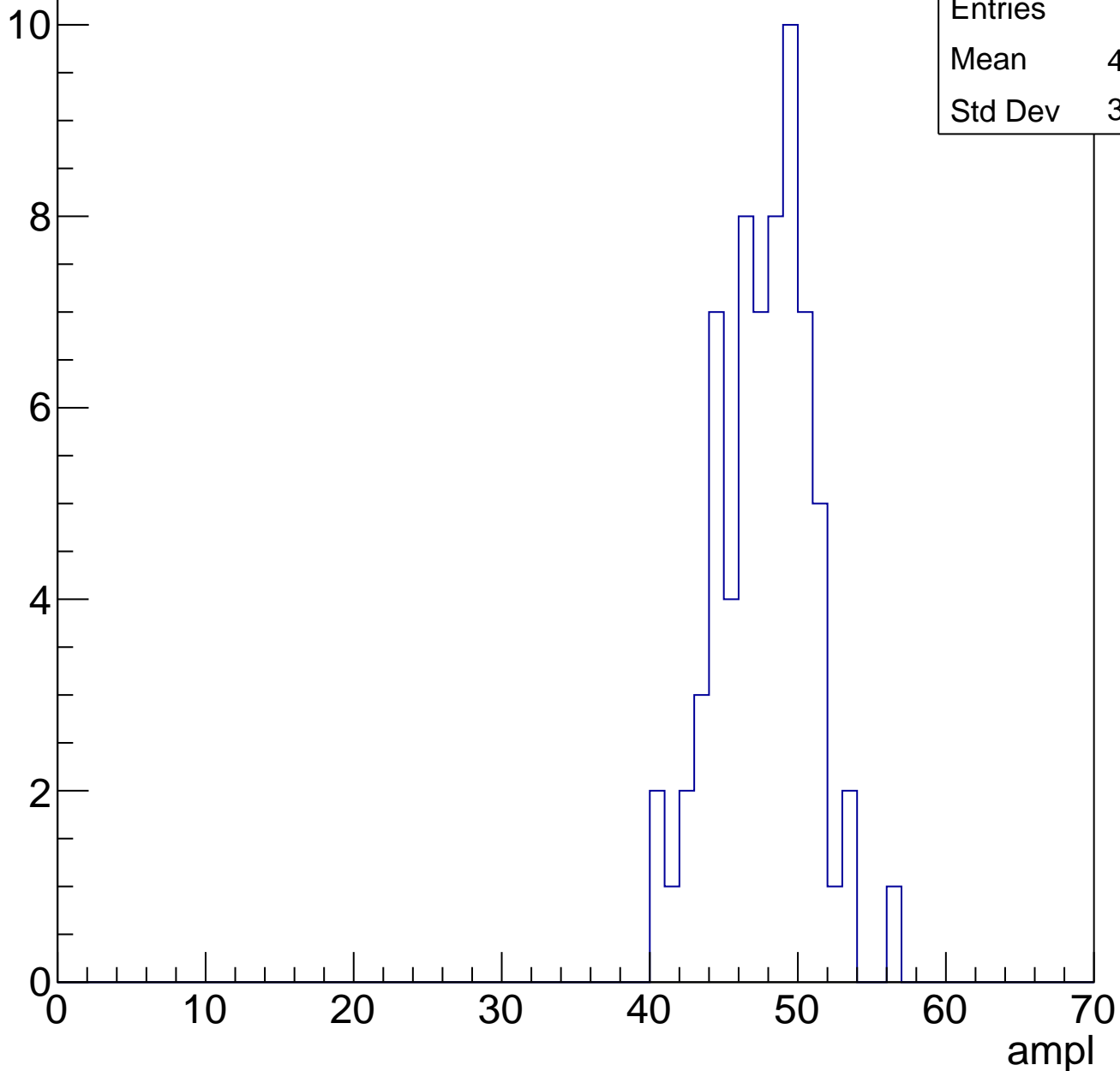


B1L100S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

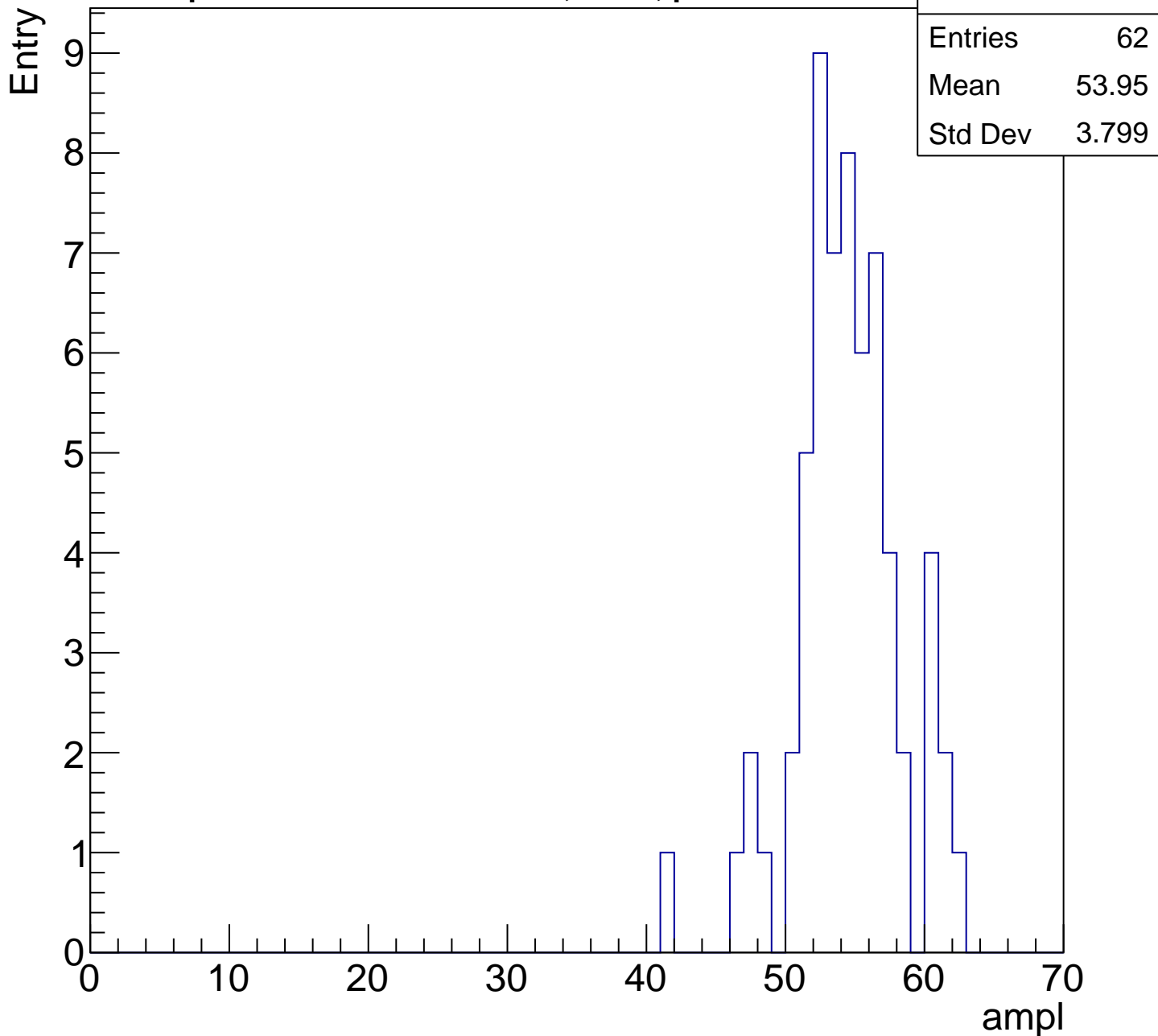
Entry

Entries	68
Mean	47.24
Std Dev	3.172



B1L100S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

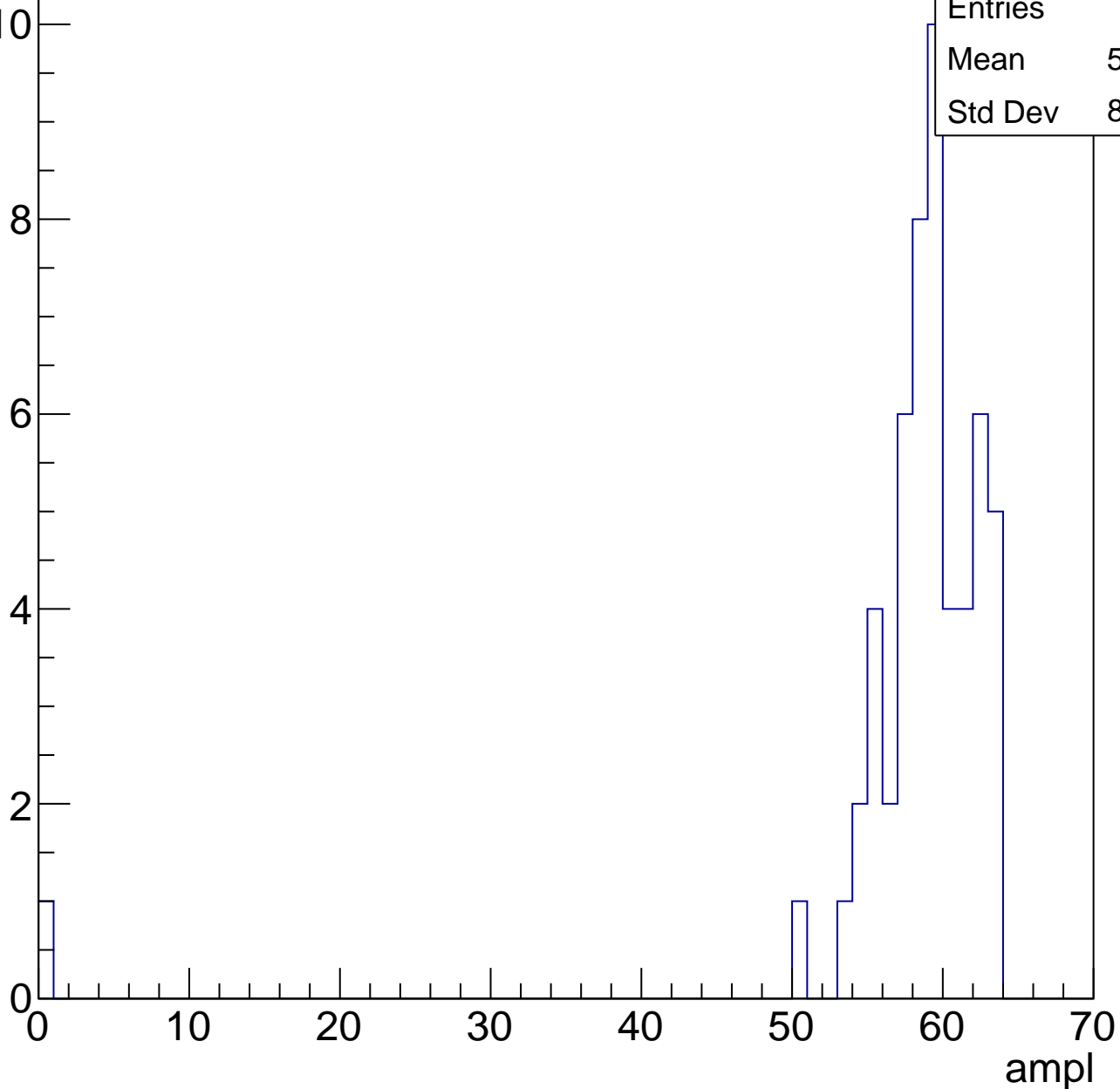


B1L100S, U14-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.59
Std Dev	8.399



B1L100S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.8

Std Dev

0.9798

B1L100S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

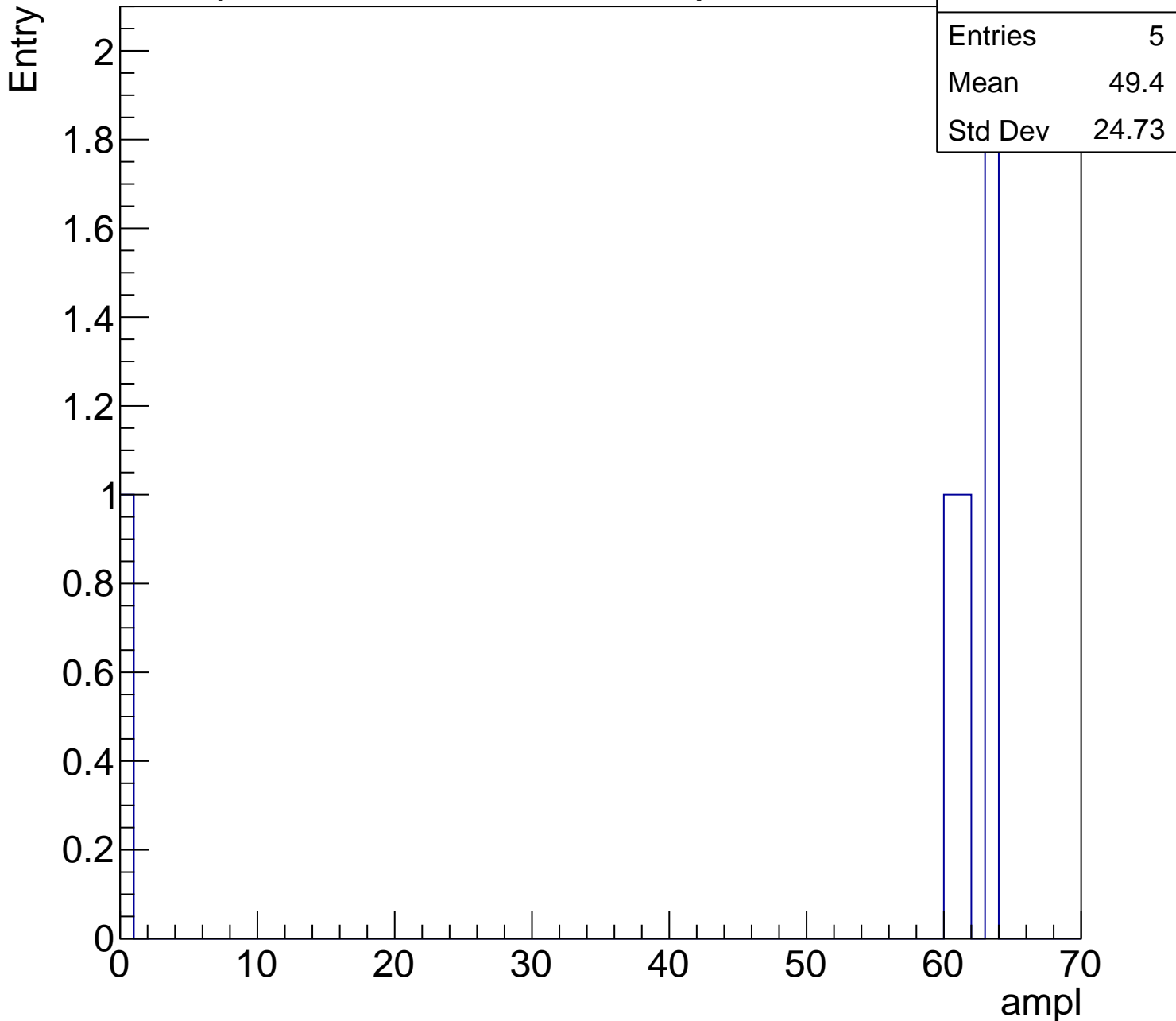
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.73

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch43, adc0

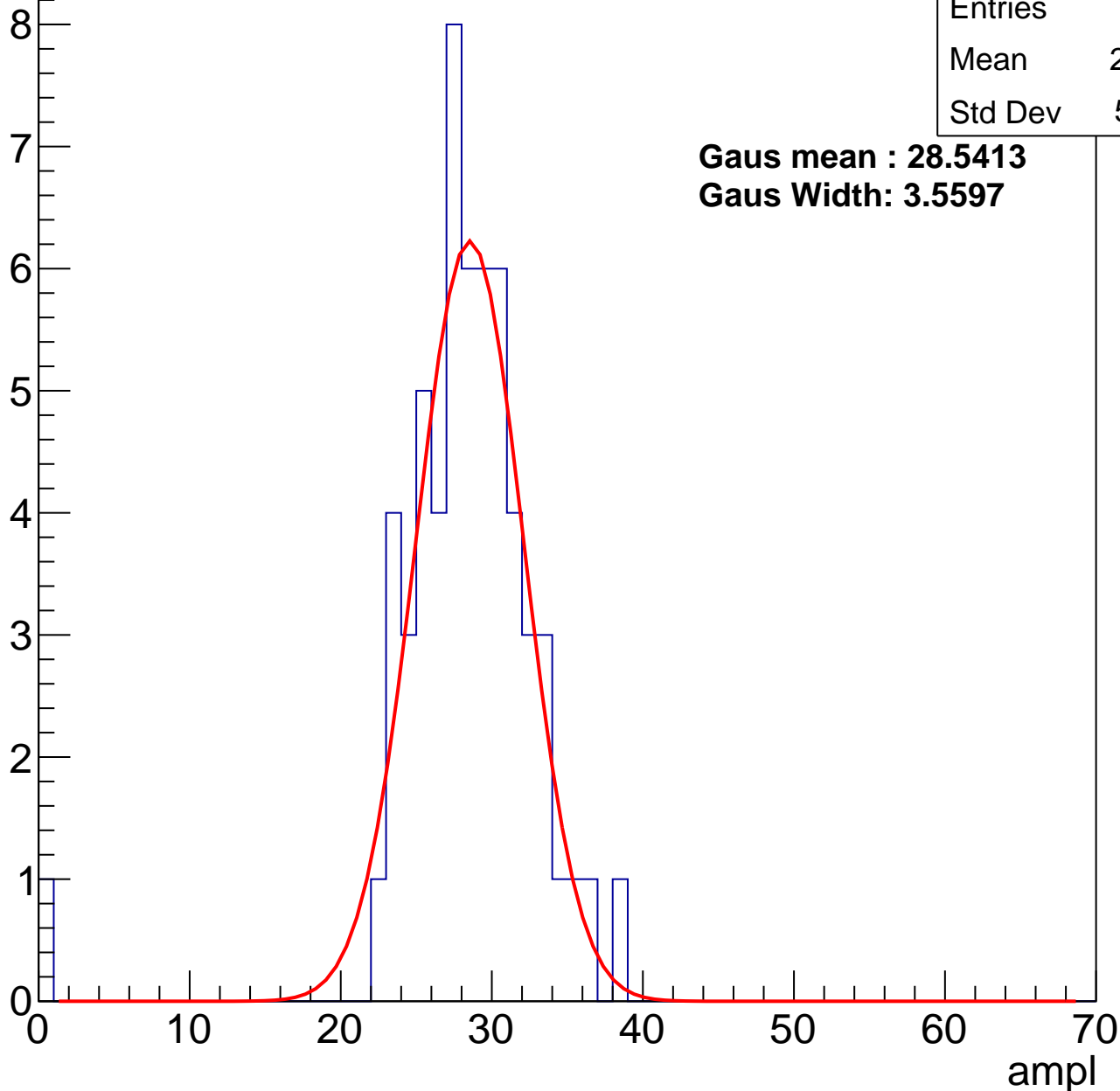
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	27.84
Std Dev	5.041

Gaus mean : 28.5413

Gaus Width: 3.5597



B1L100S, U14-ch43, adc1

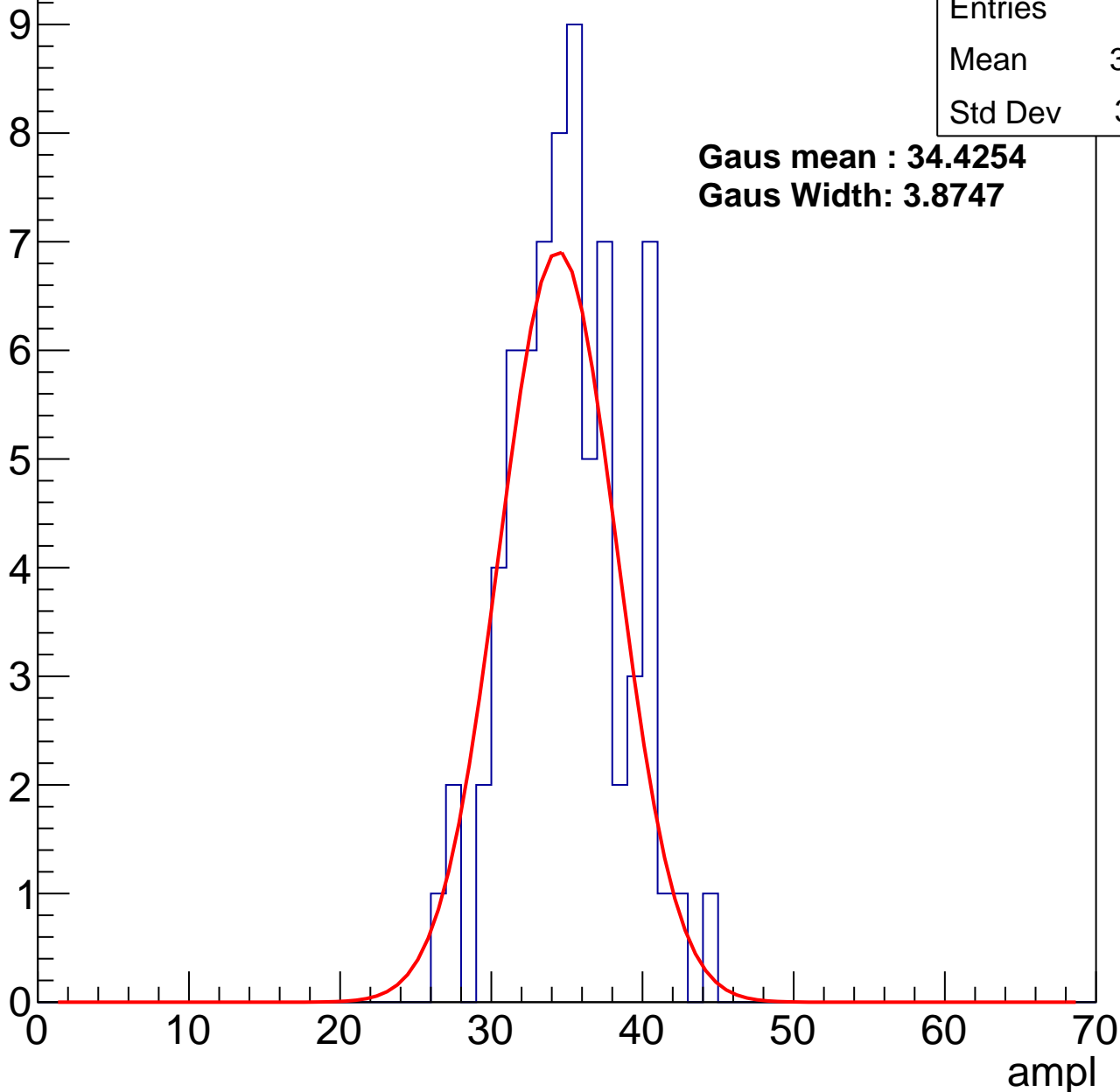
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	34.62
Std Dev	3.721

Gaus mean : 34.4254

Gaus Width: 3.8747



B1L100S, U14-ch43, adc2

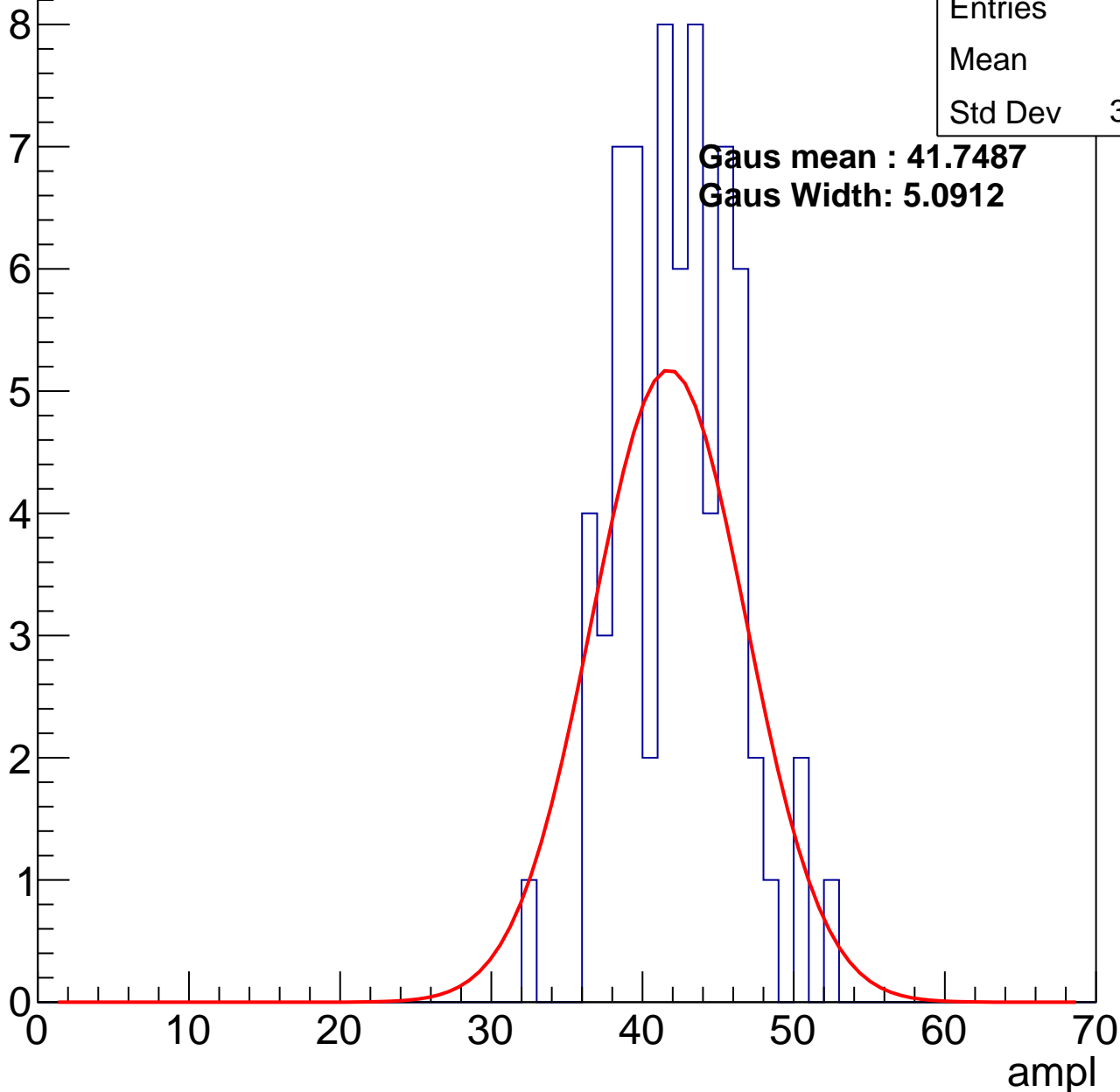
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.9
Std Dev	3.819

Gaus mean : 41.7487

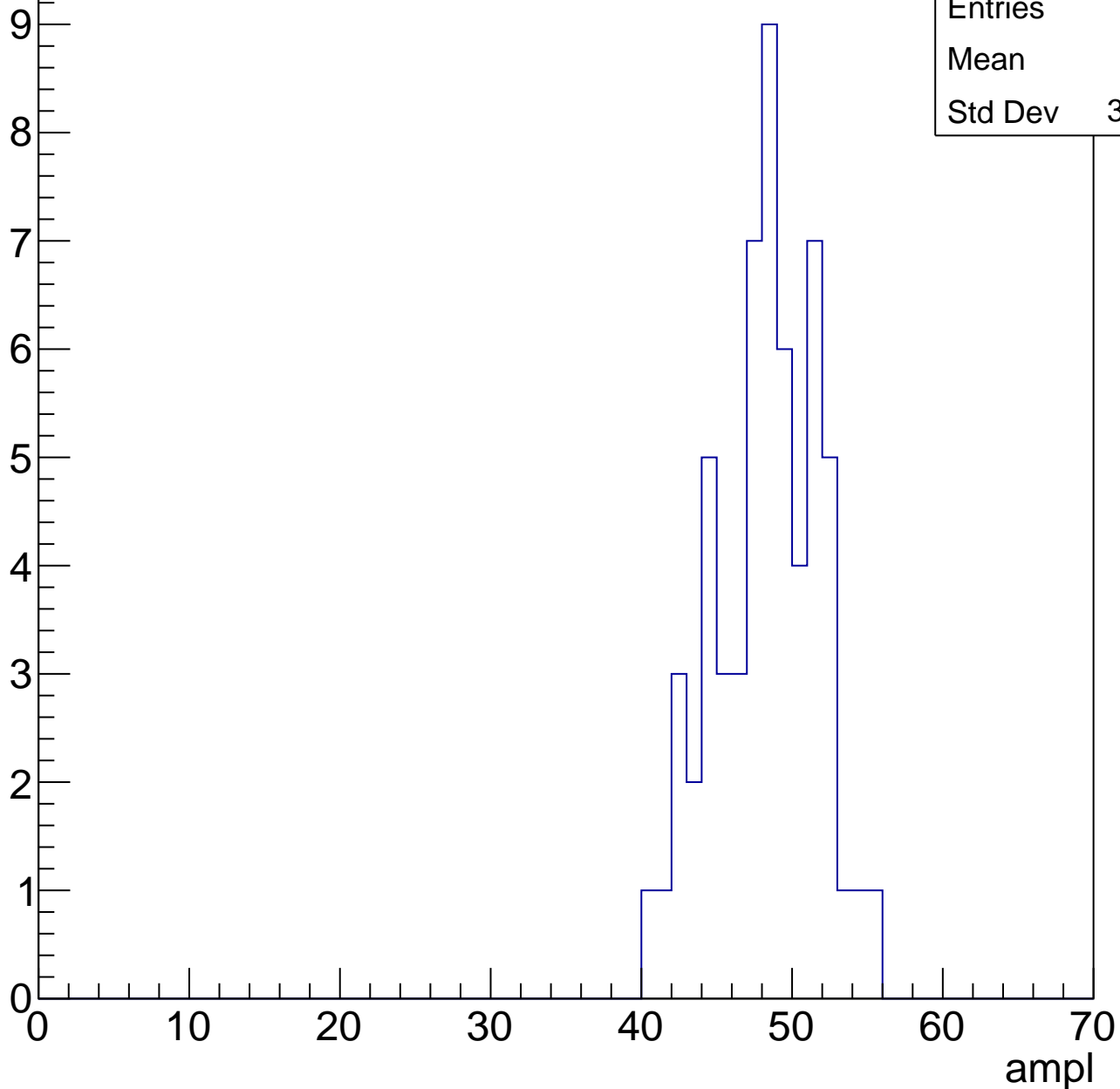
Gaus Width: 5.0912



B1L100S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

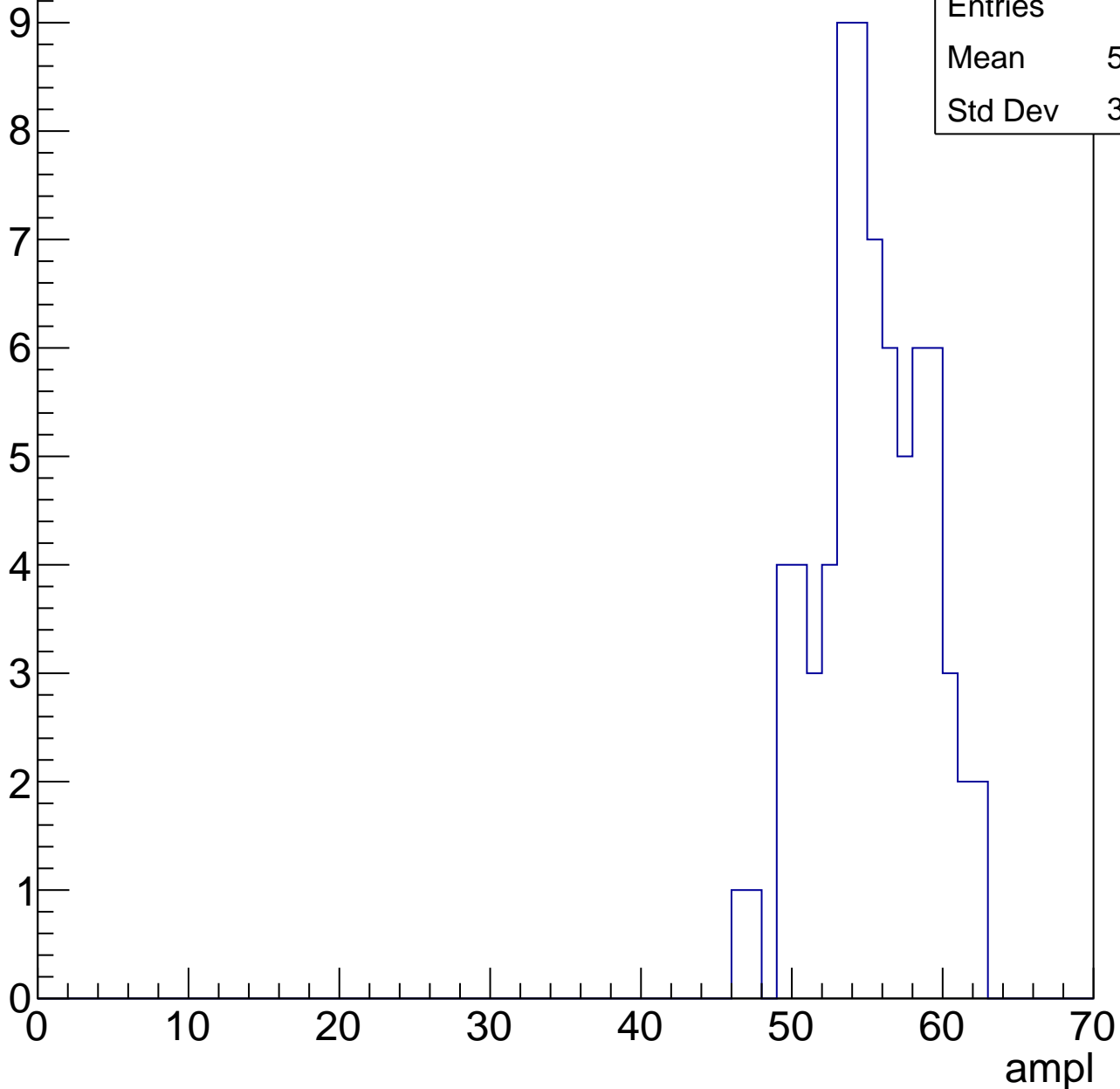


Entries	59
Mean	47.8
Std Dev	3.369

B1L100S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

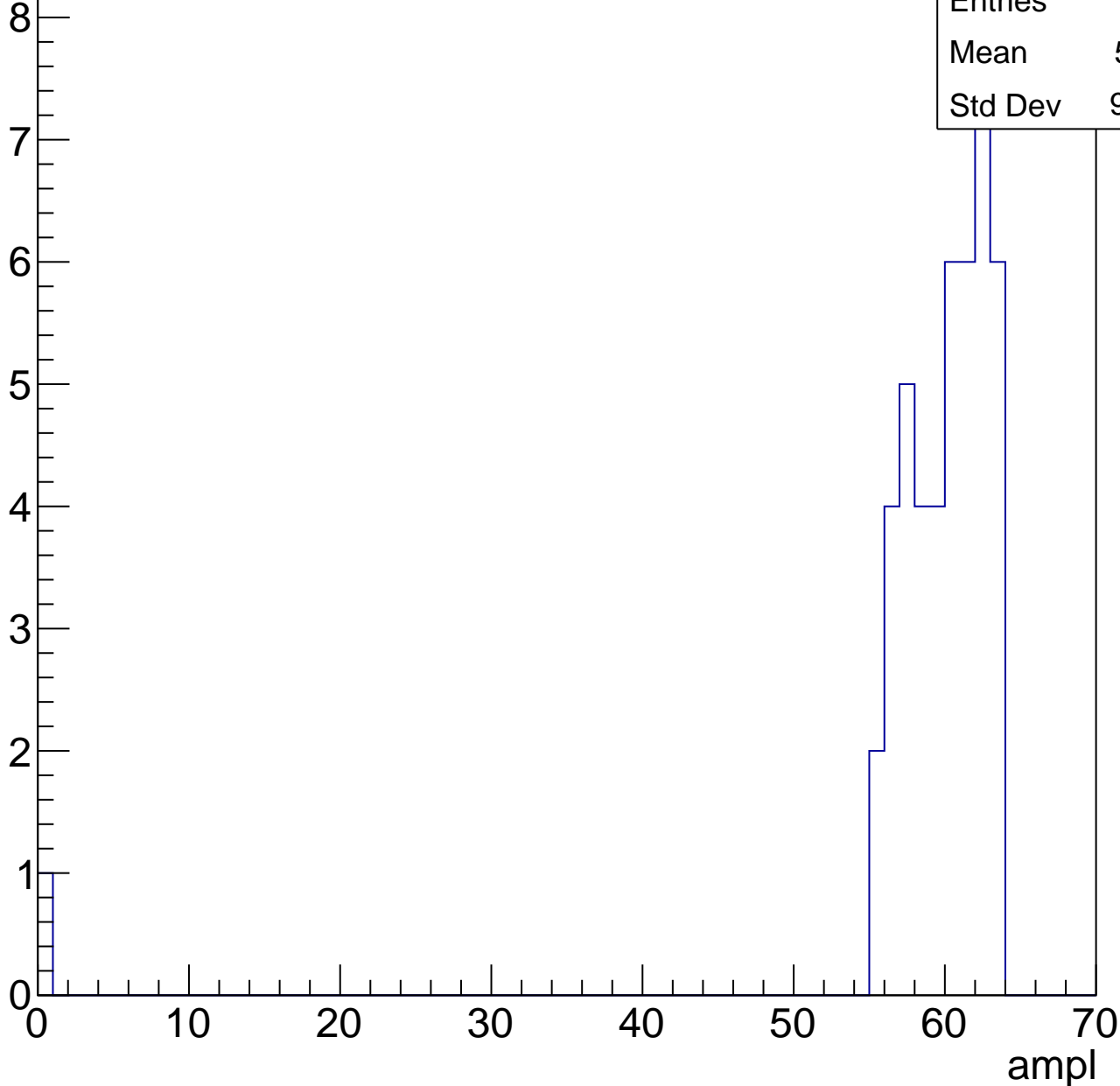


B1L100S, U14-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

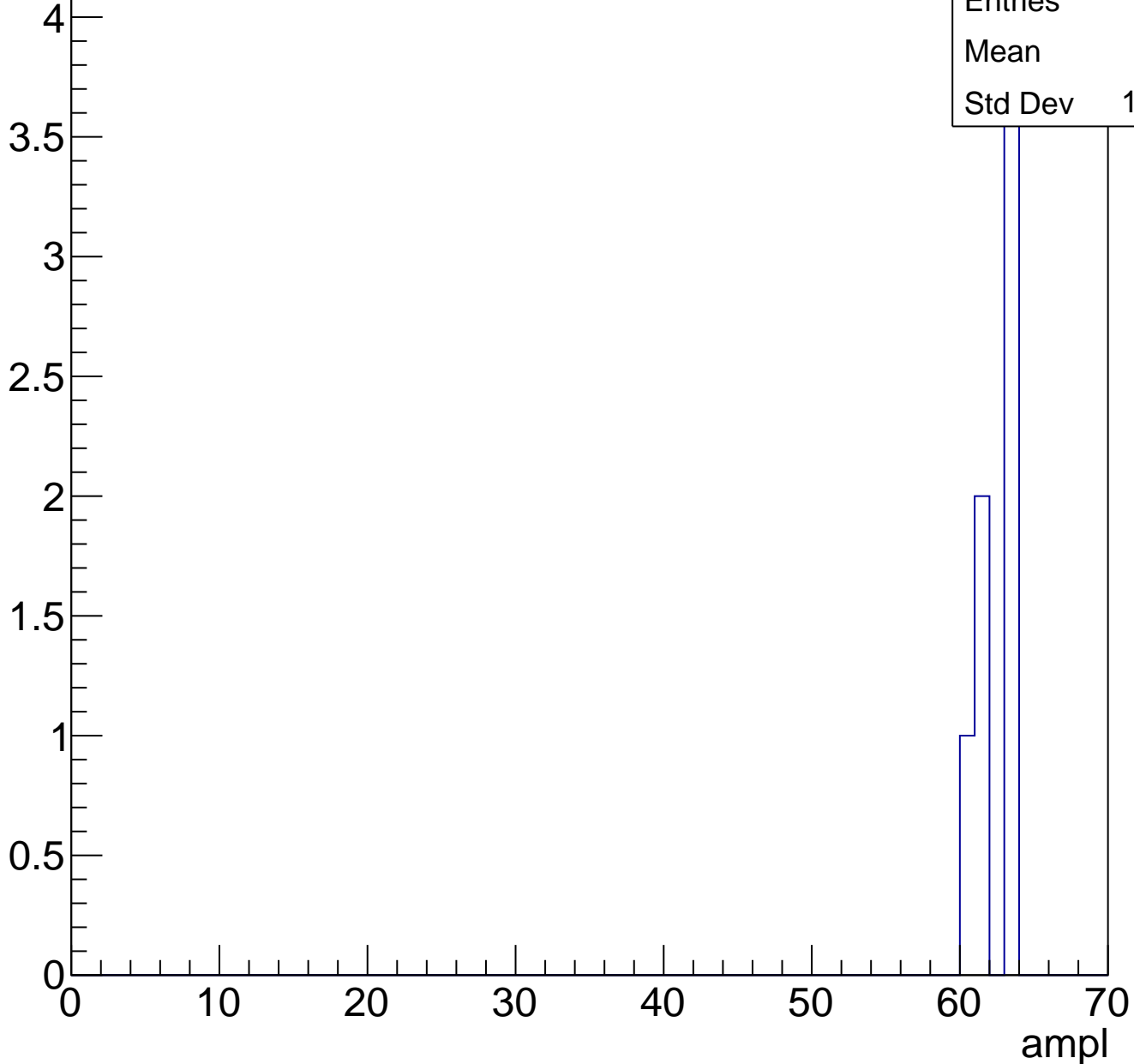
Entries	46
Mean	58.41
Std Dev	9.035



B1L100S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch44, adc0

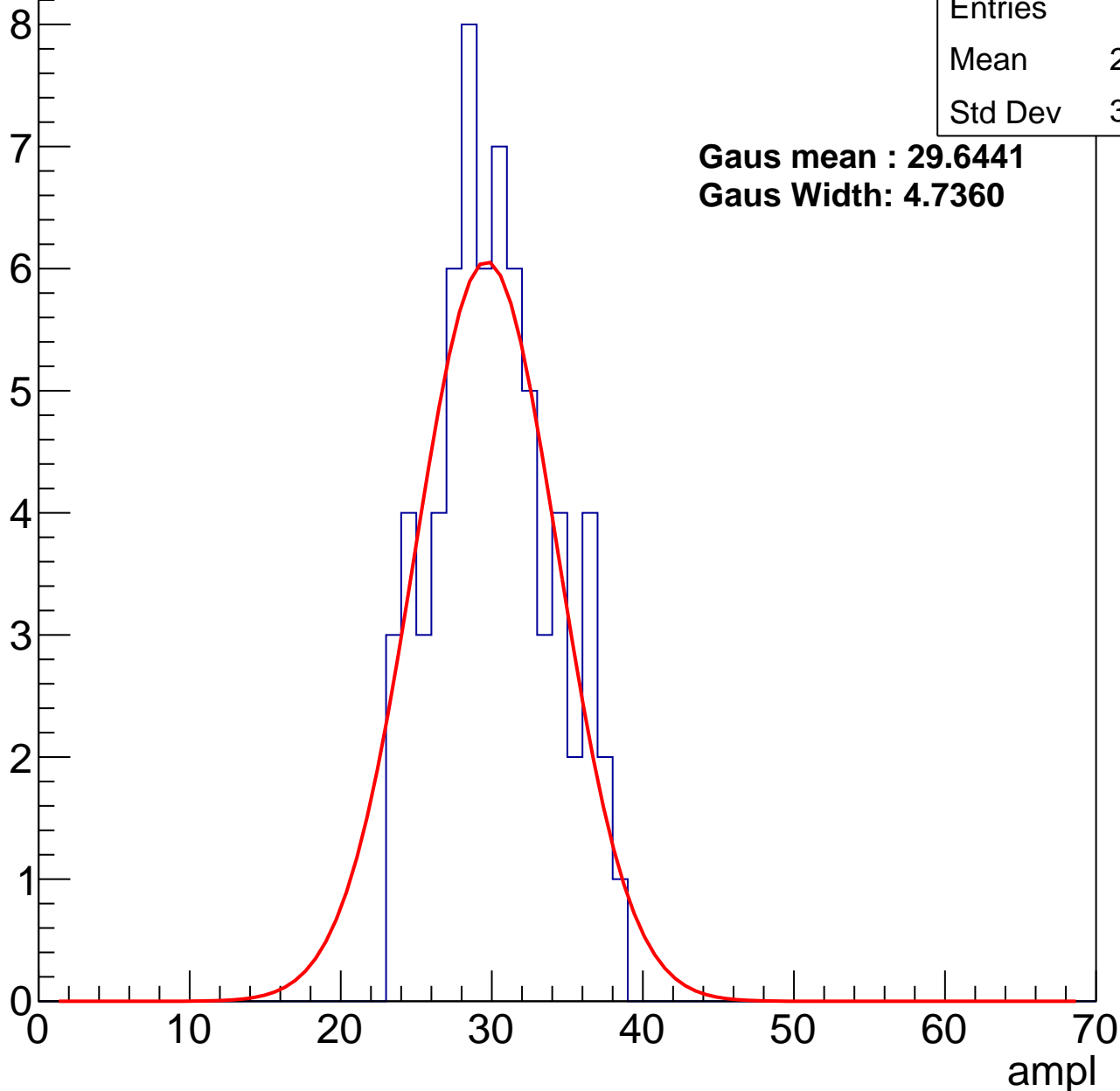
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.72
Std Dev	3.815

Gaus mean : 29.6441

Gaus Width: 4.7360



B1L100S, U14-ch44, adc1

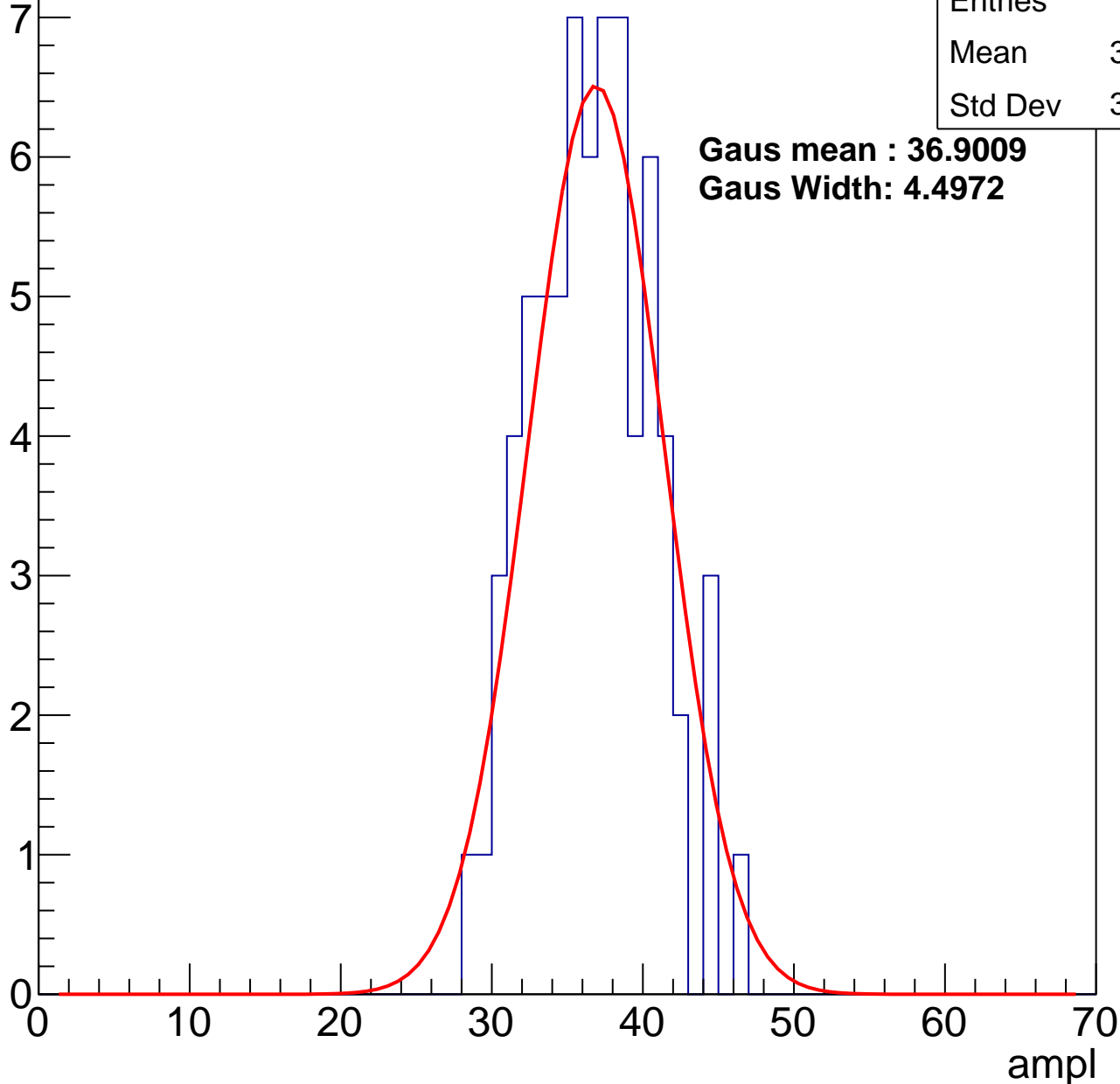
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.25
Std Dev	3.935

Gaus mean : 36.9009

Gaus Width: 4.4972



B1L100S, U14-ch44, adc2

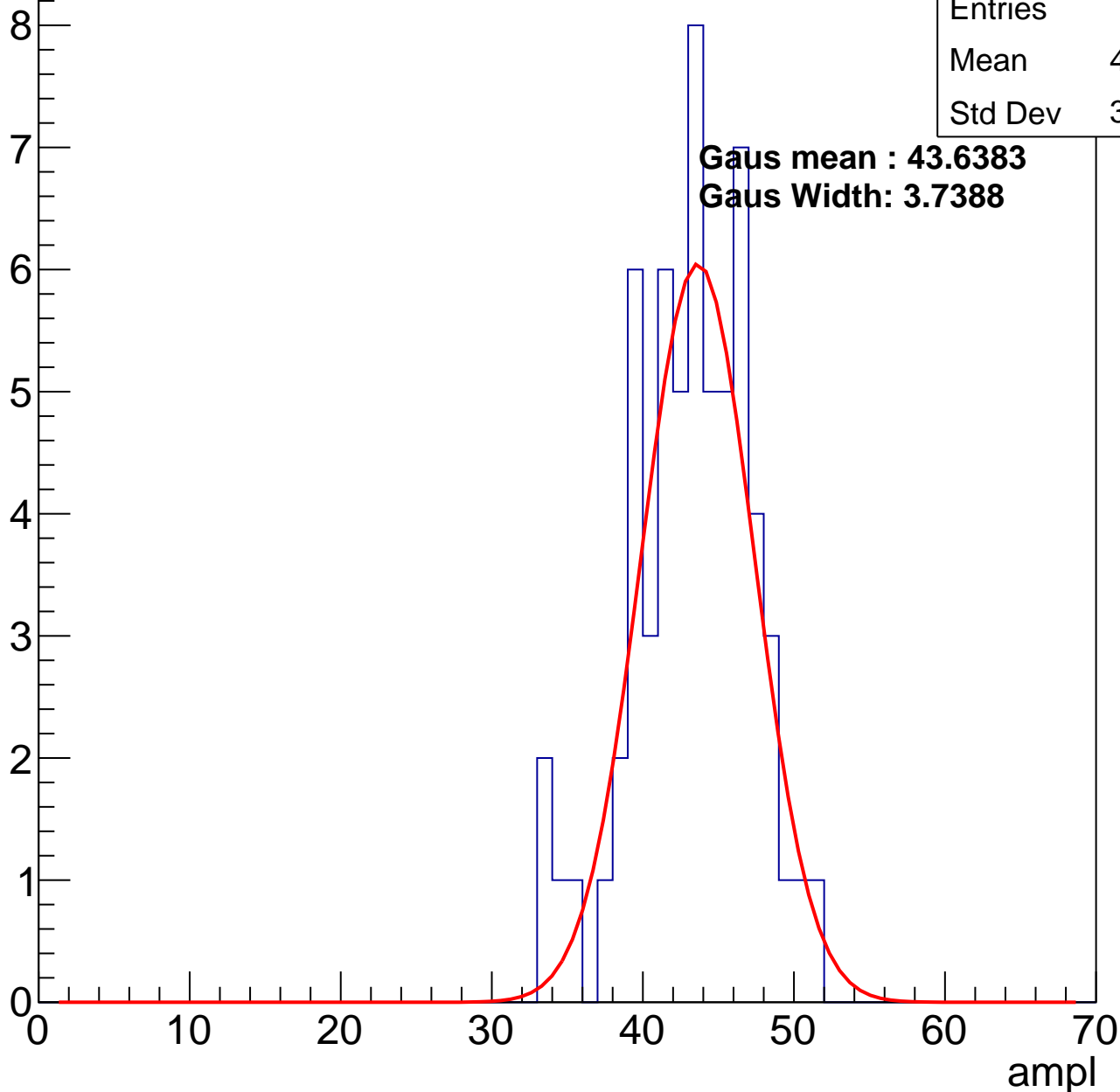
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.76
Std Dev	3.925

Gaus mean : 43.6383

Gaus Width: 3.7388

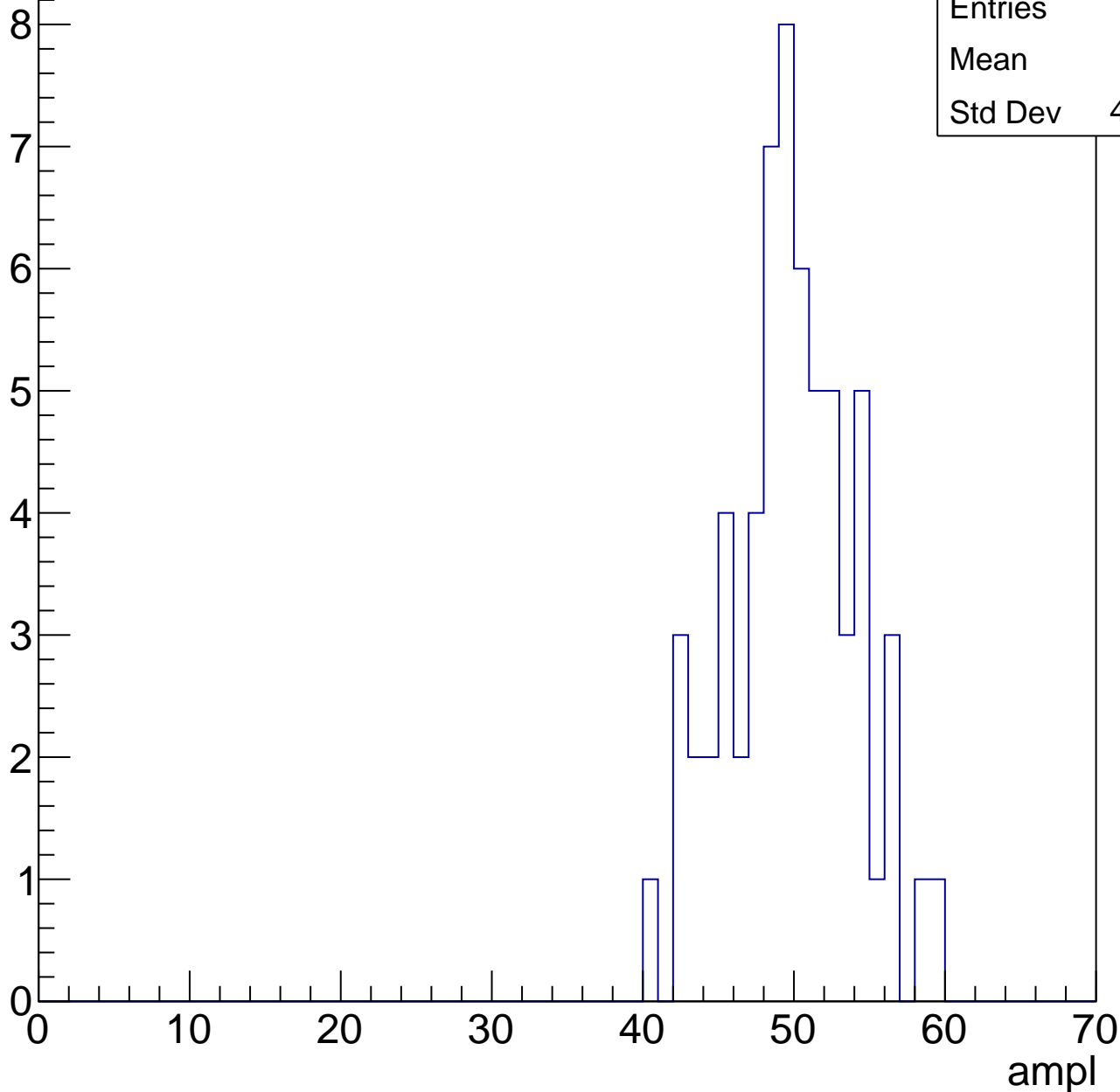


B1L100S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	49.4
Std Dev	4.088

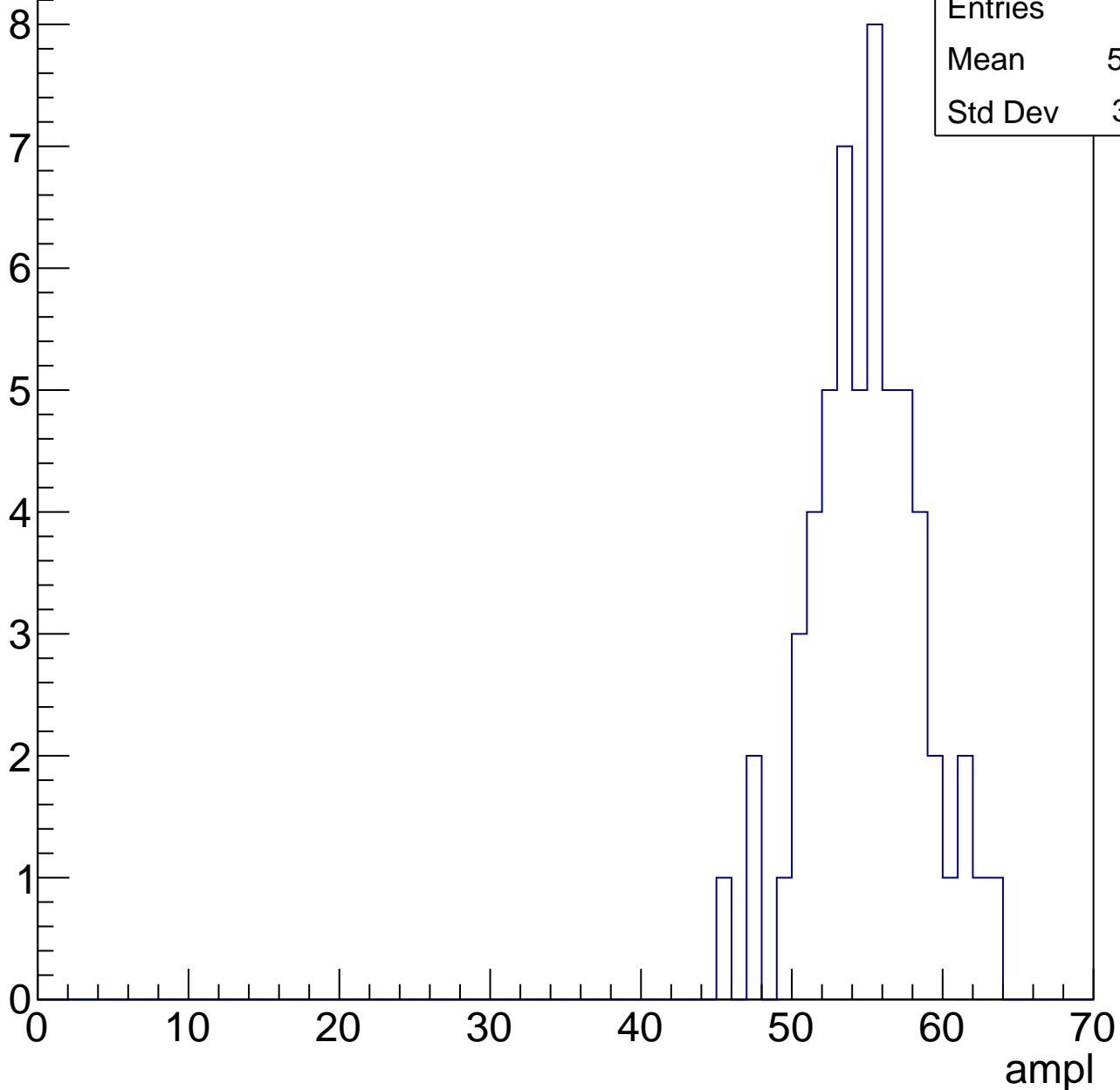


B1L100S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

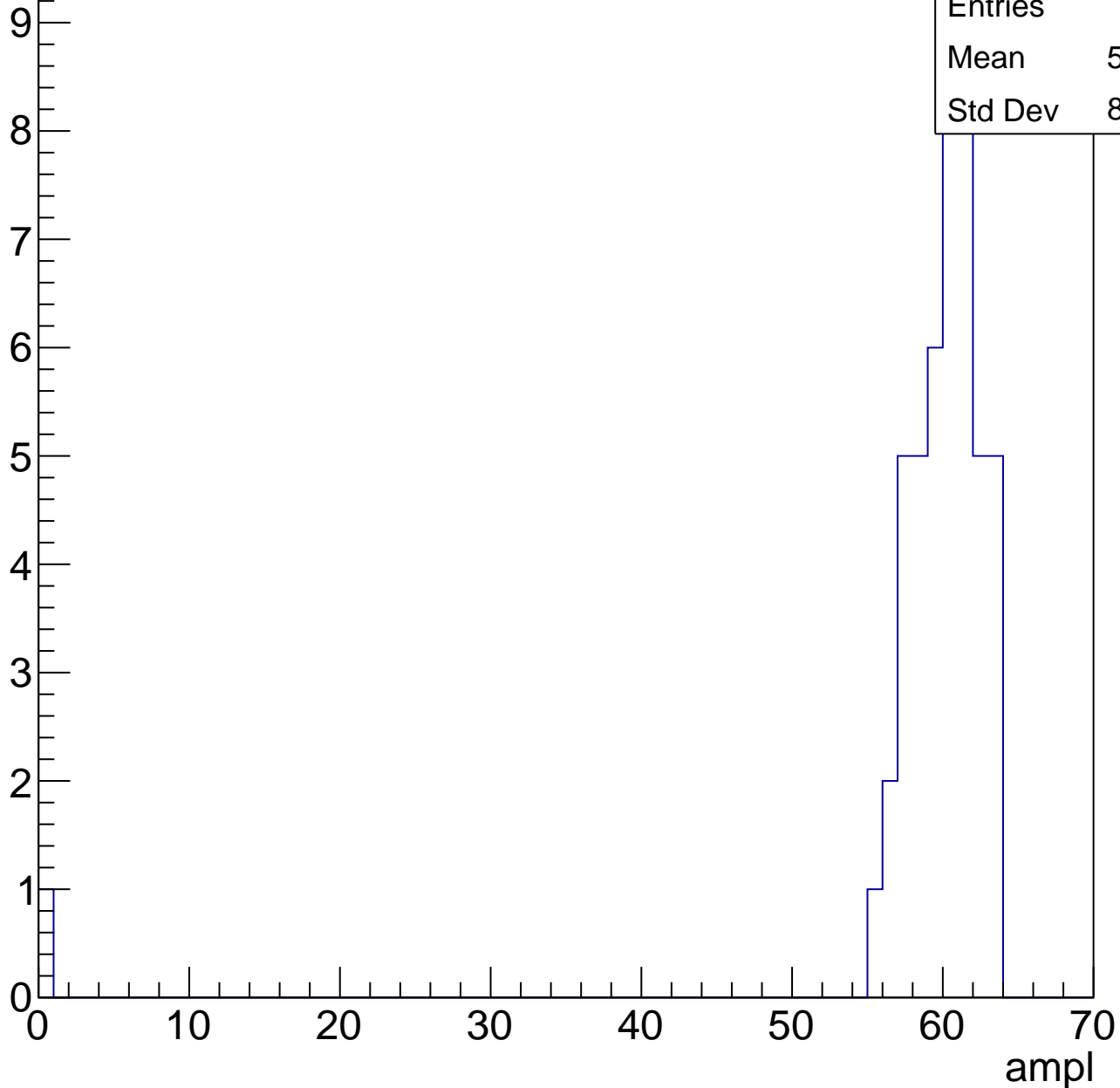
Entries	57
Mean	54.47
Std Dev	3.671



B1L100S, U14-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

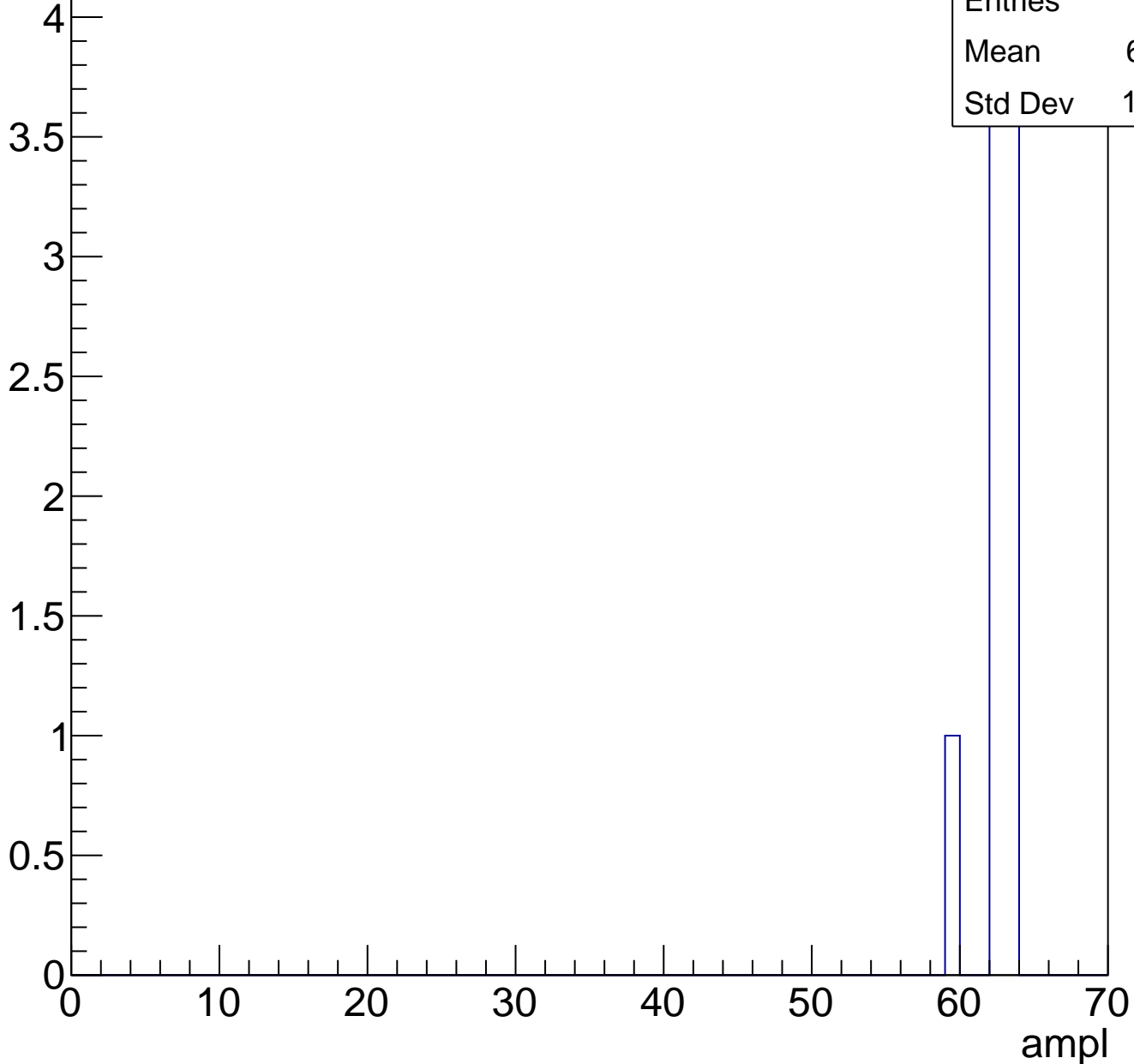


Entries	47
Mean	58.49
Std Dev	8.865

B1L100S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

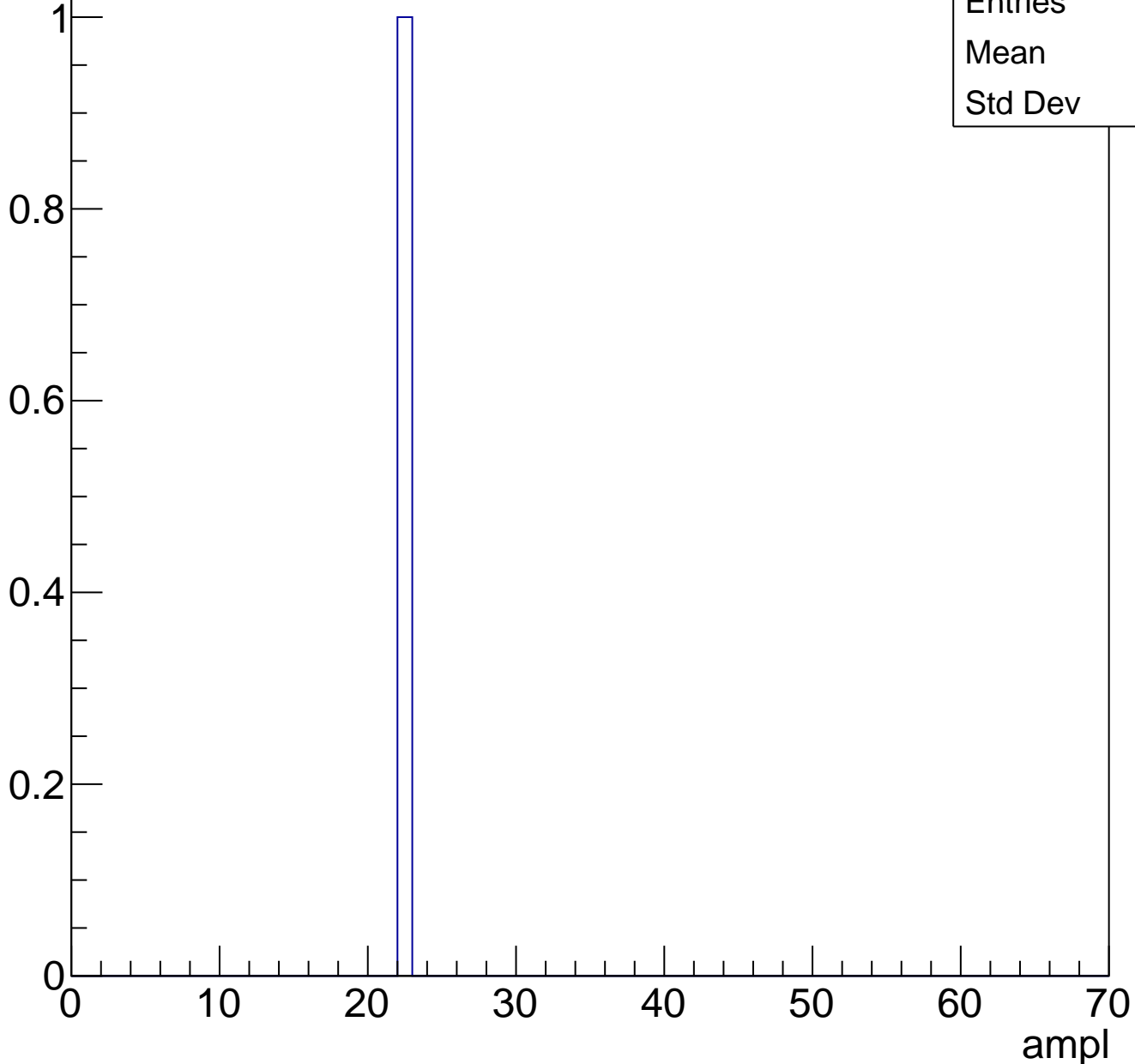
Entry



B1L100S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U14-ch45, adc0

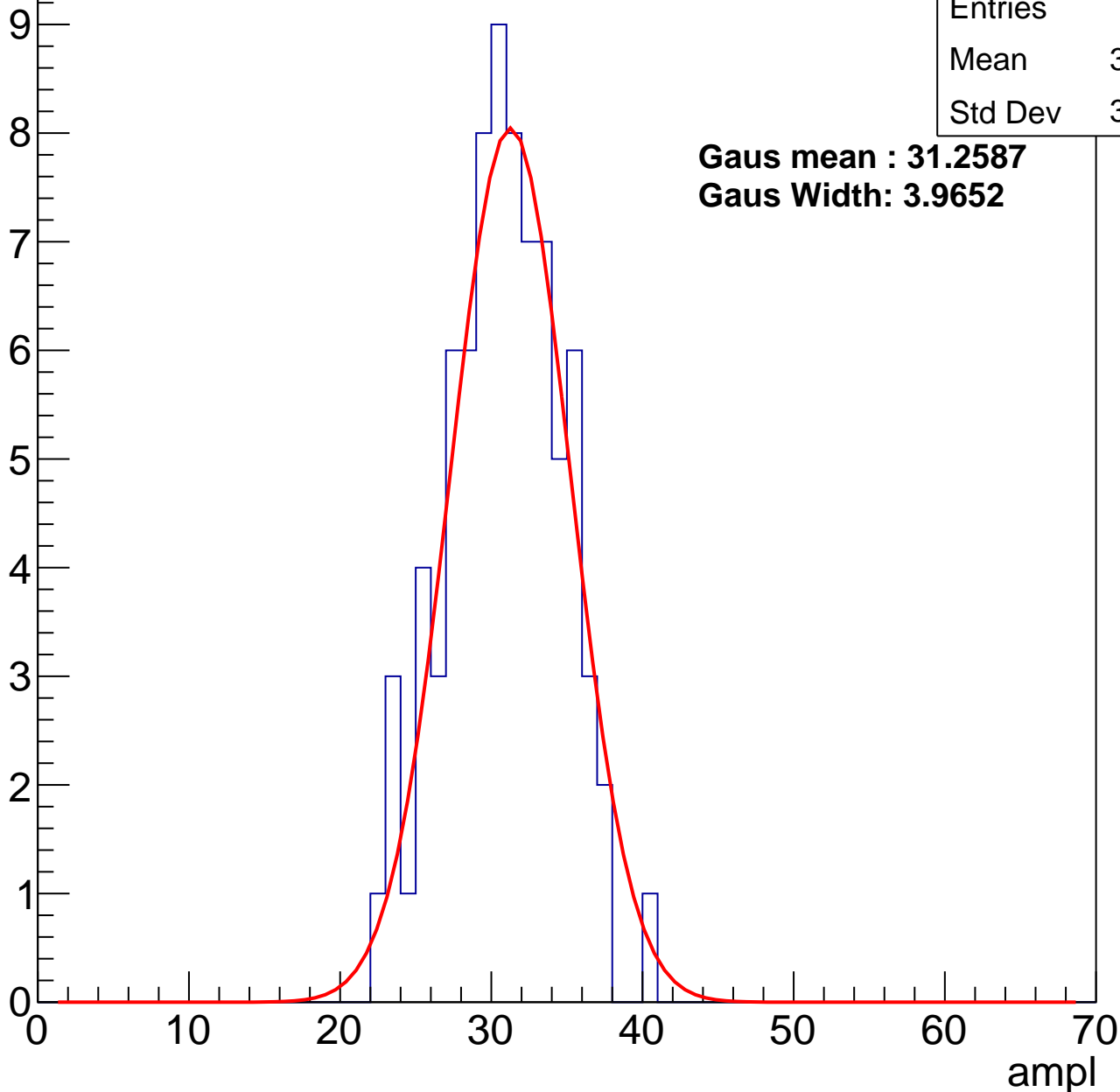
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	30.38
Std Dev	3.716

Gaus mean : 31.2587

Gaus Width: 3.9652



B1L100S, U14-ch45, adc1

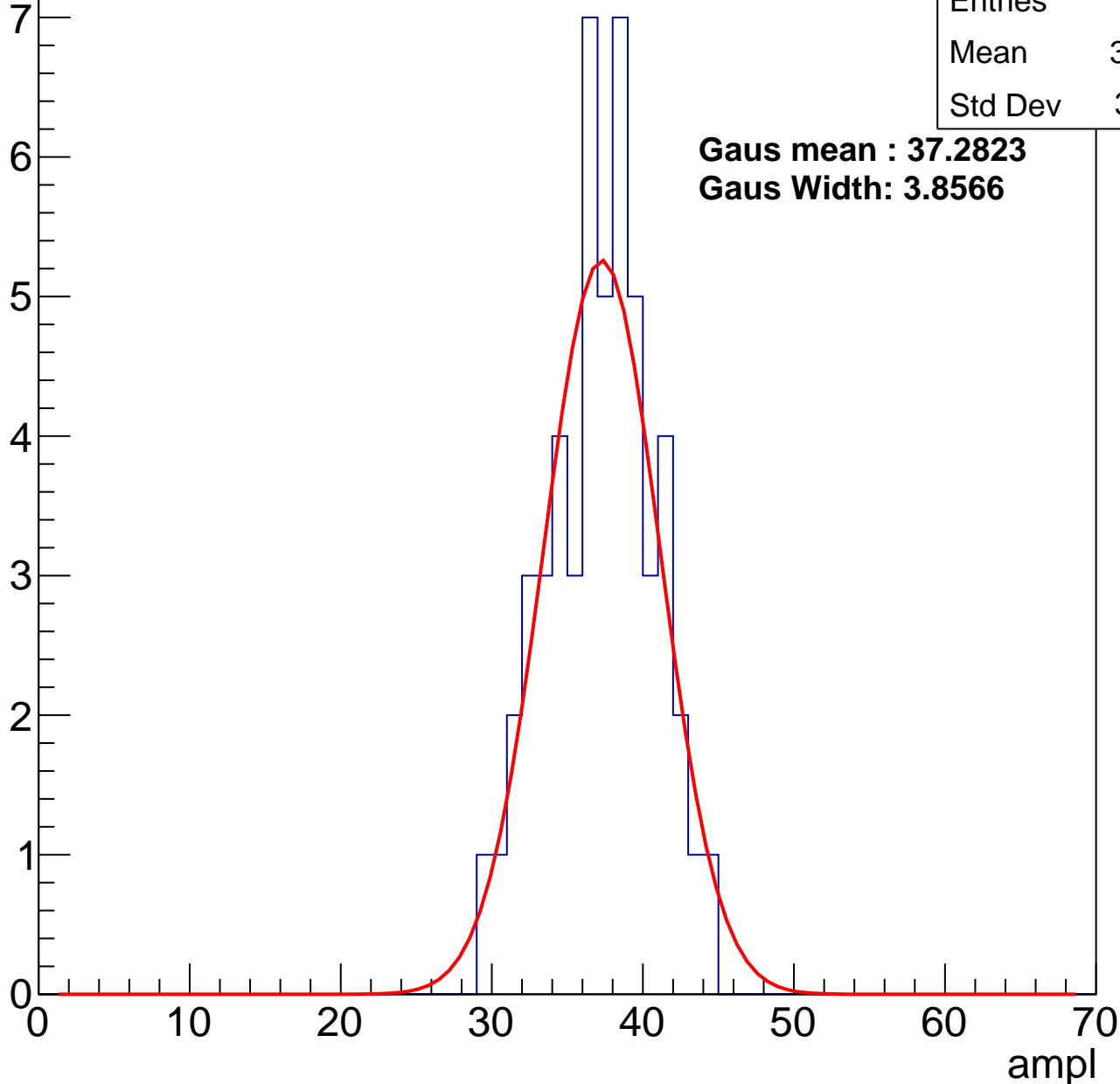
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	36.73
Std Dev	3.431

Gaus mean : 37.2823

Gaus Width: 3.8566



B1L100S, U14-ch45, adc2

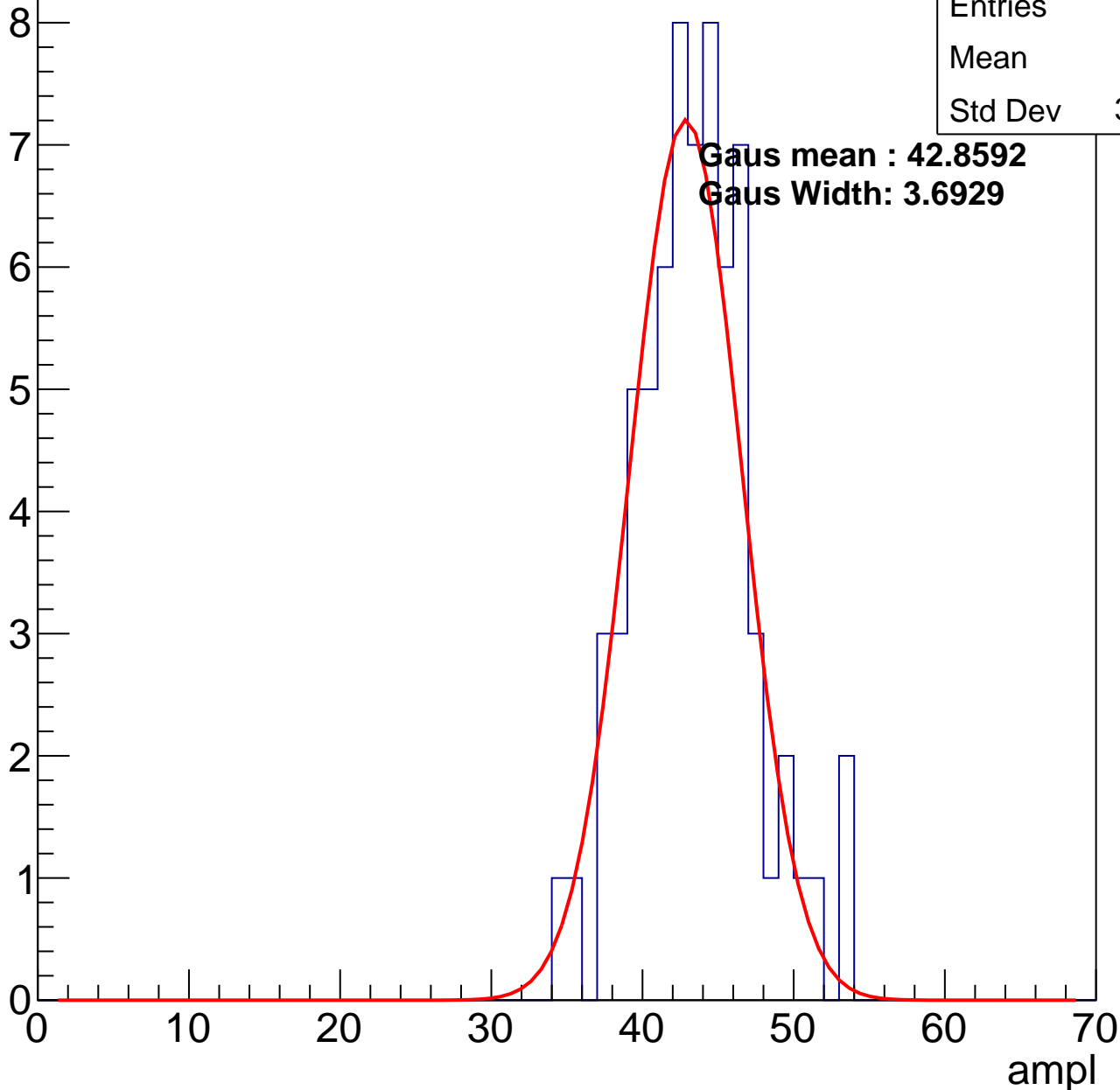
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43
Std Dev	3.851

Gaus mean : 42.8592

Gaus Width: 3.6929

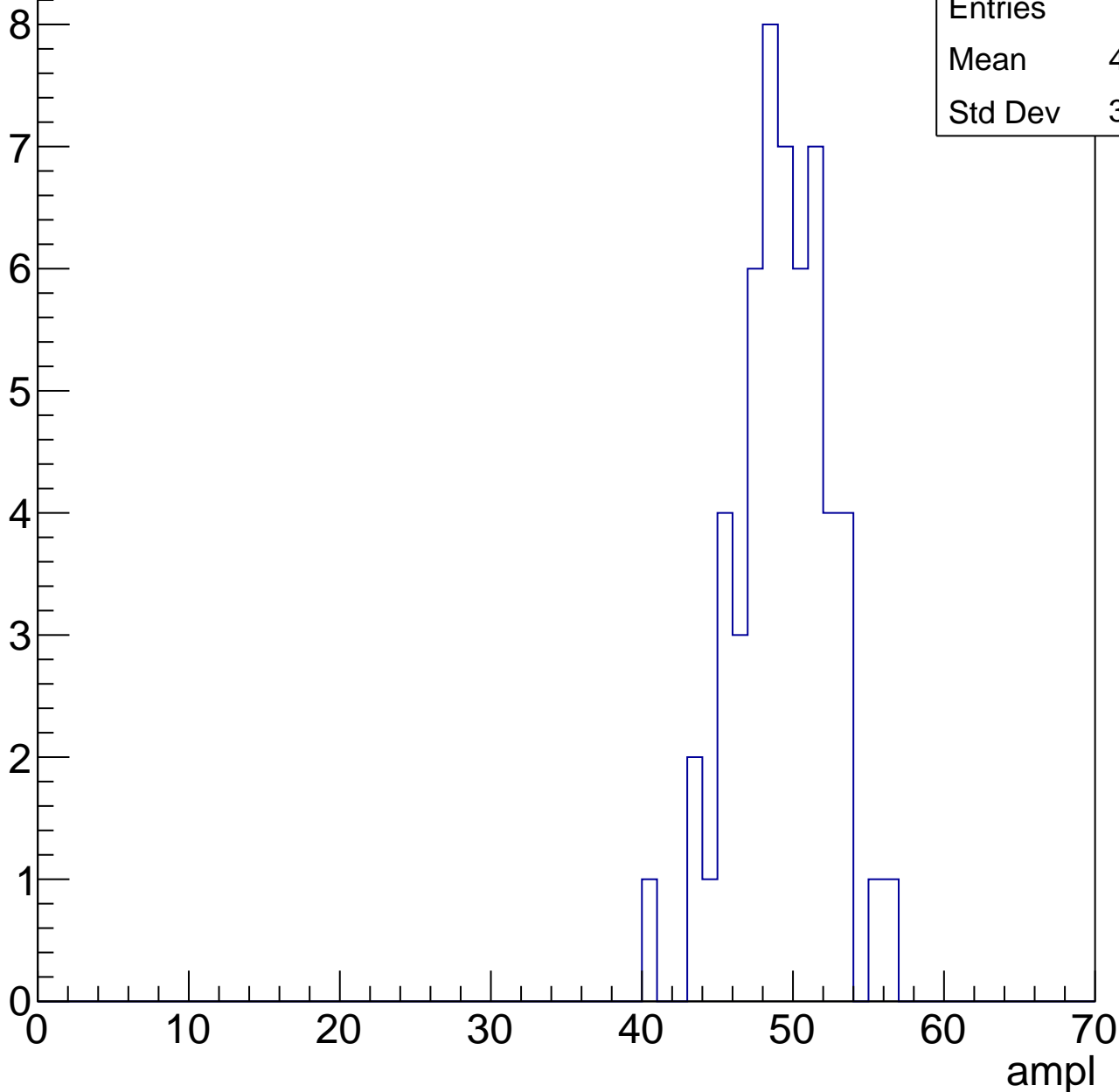


B1L100S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	48.82
Std Dev	3.069

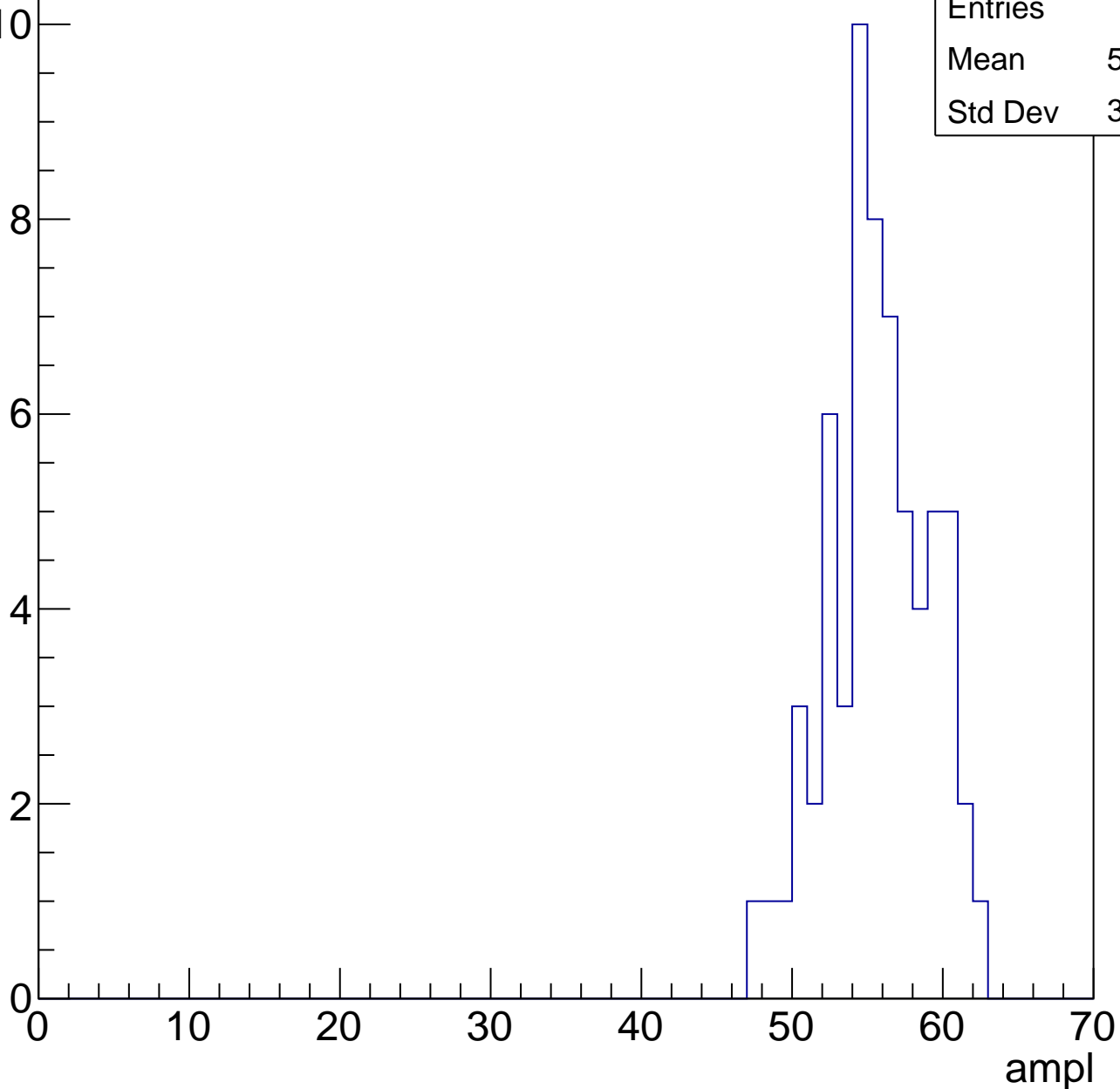


B1L100S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.23
Std Dev	3.348

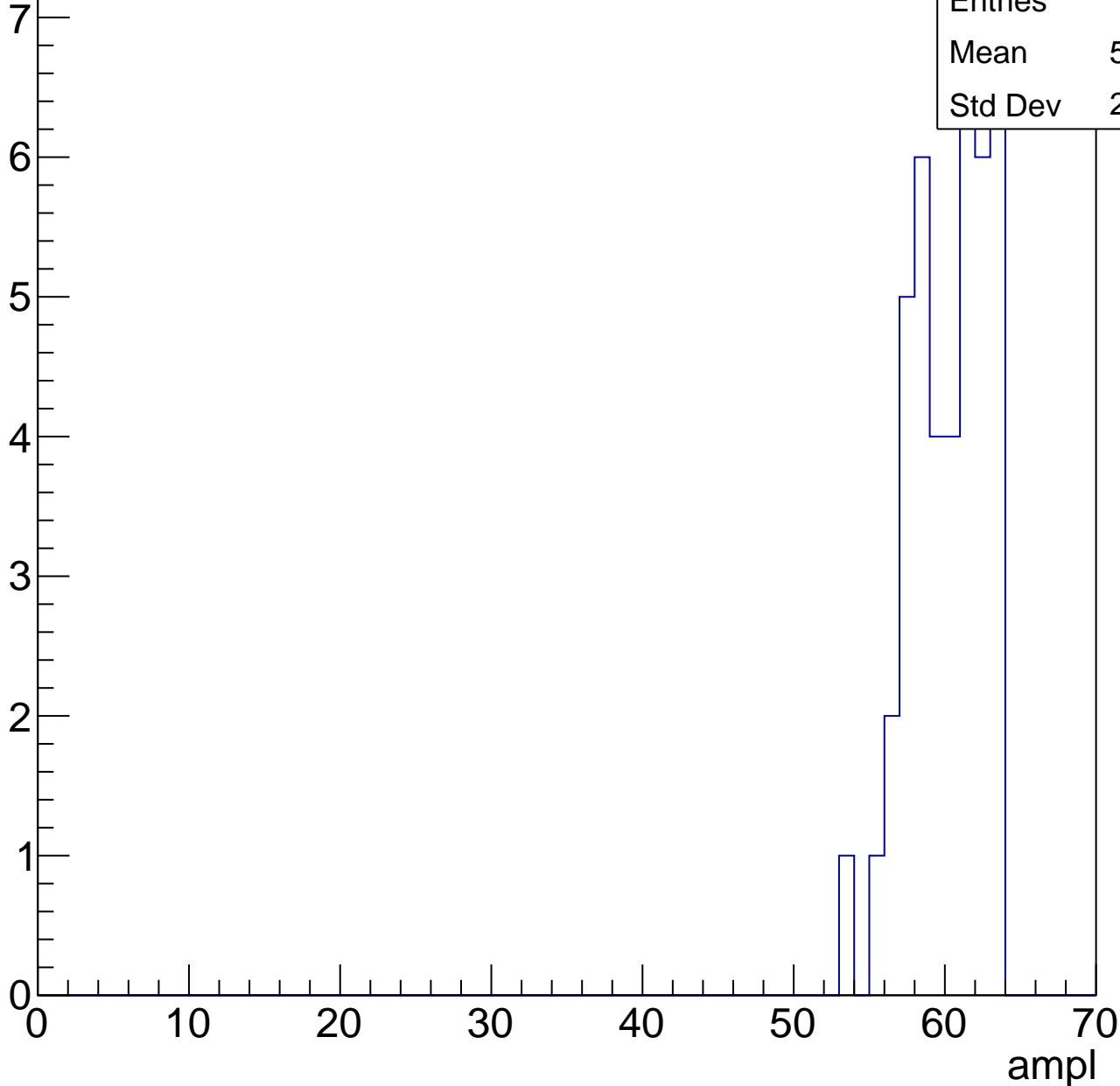


B1L100S, U14-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	59.74
Std Dev	2.507



B1L100S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

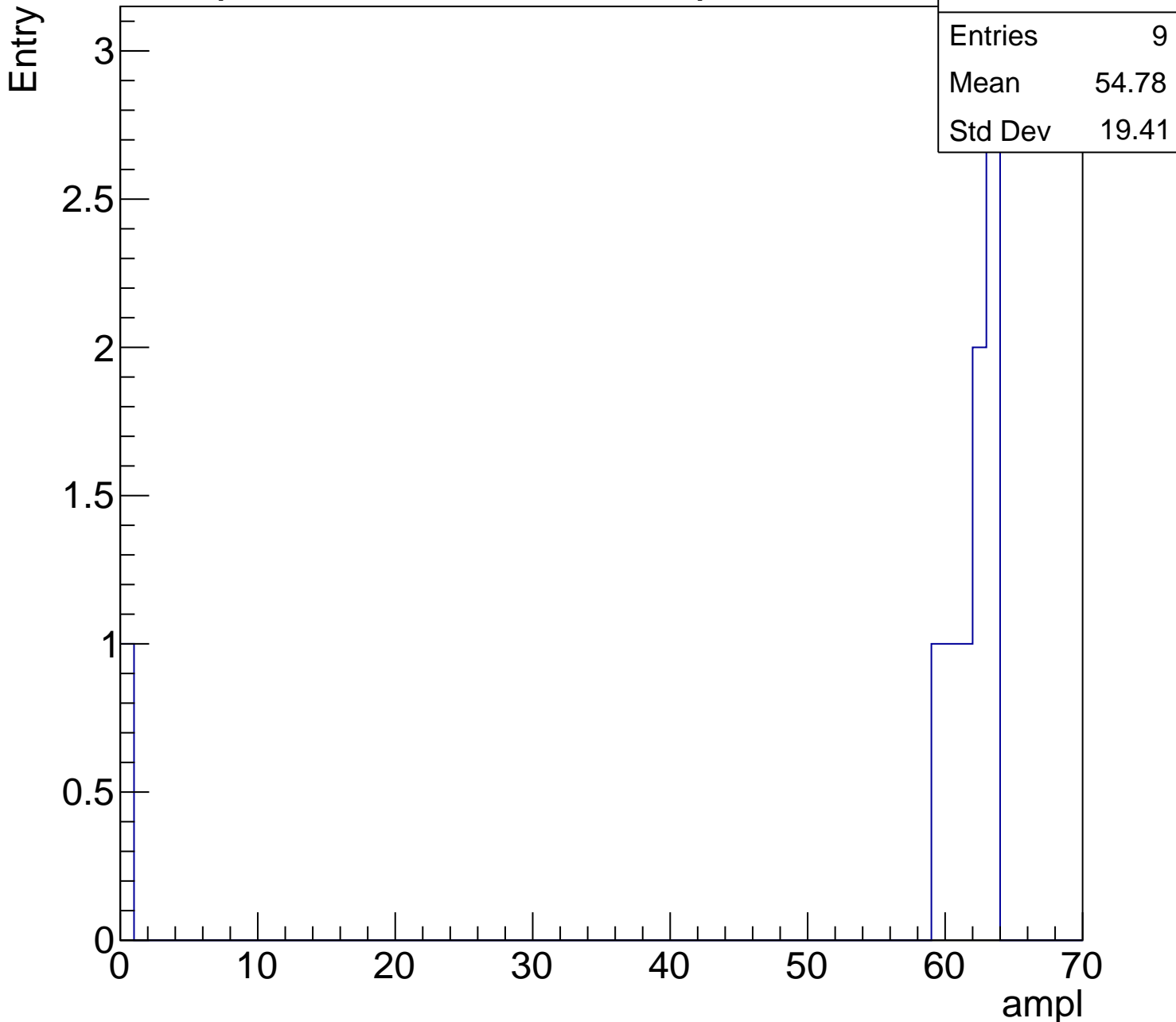
Mean

54.78

Std Dev

19.41

ampl



B1L100S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch46, adc0

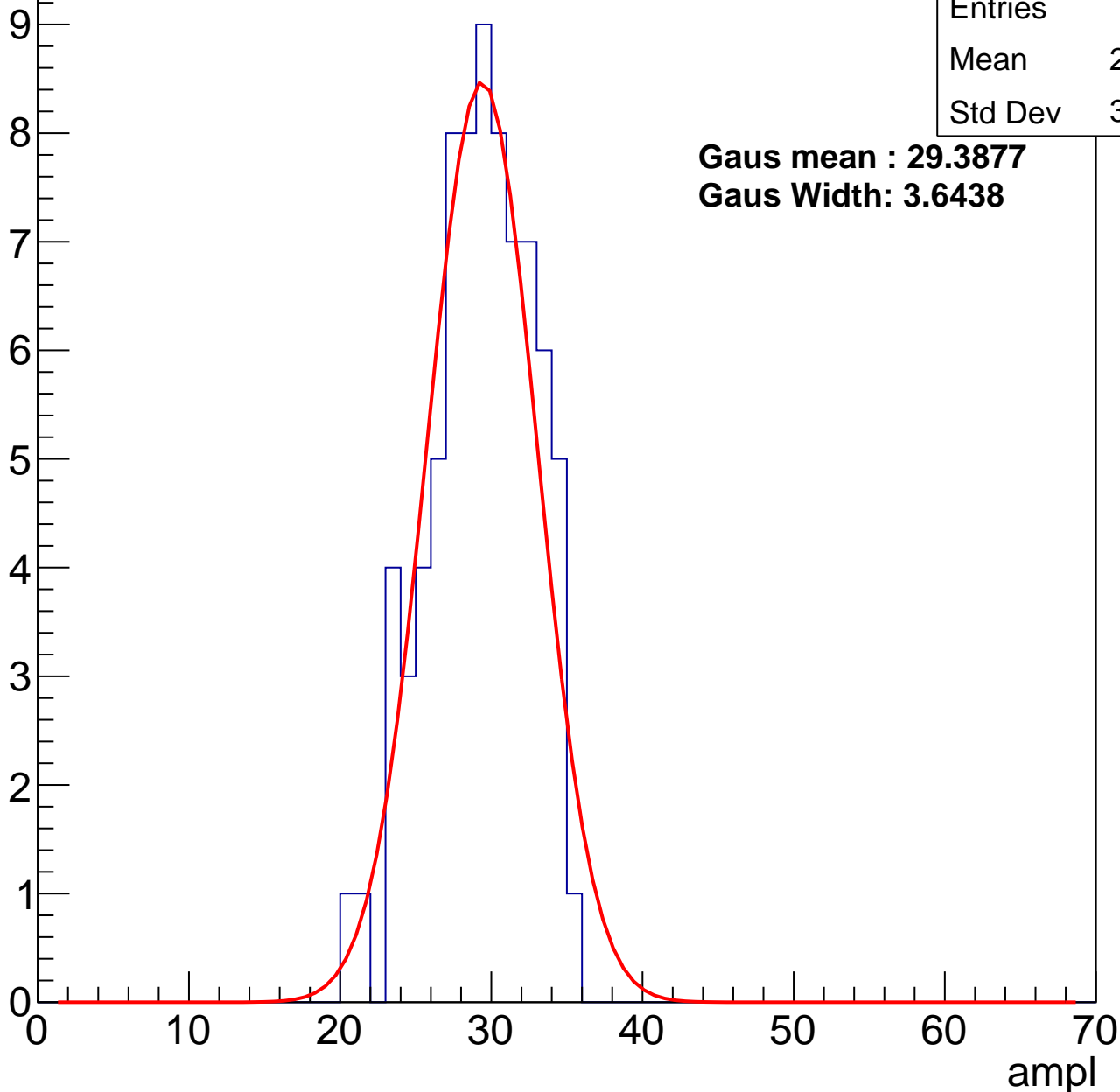
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	28.83
Std Dev	3.355

Gaus mean : 29.3877

Gaus Width: 3.6438



B1L100S, U14-ch46, adc1

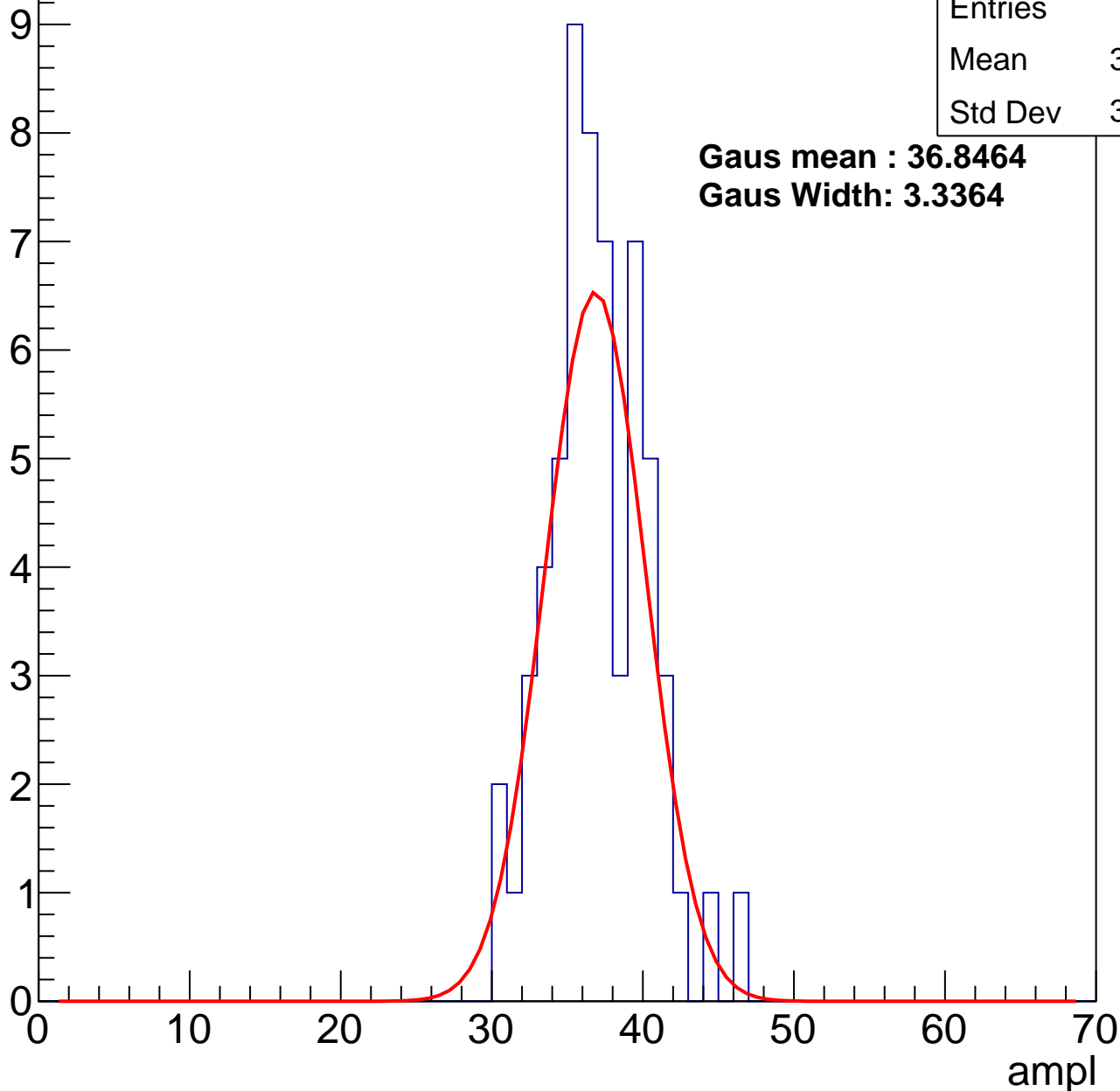
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	36.55
Std Dev	3.237

Gaus mean : 36.8464

Gaus Width: 3.3364



B1L100S, U14-ch46, adc2

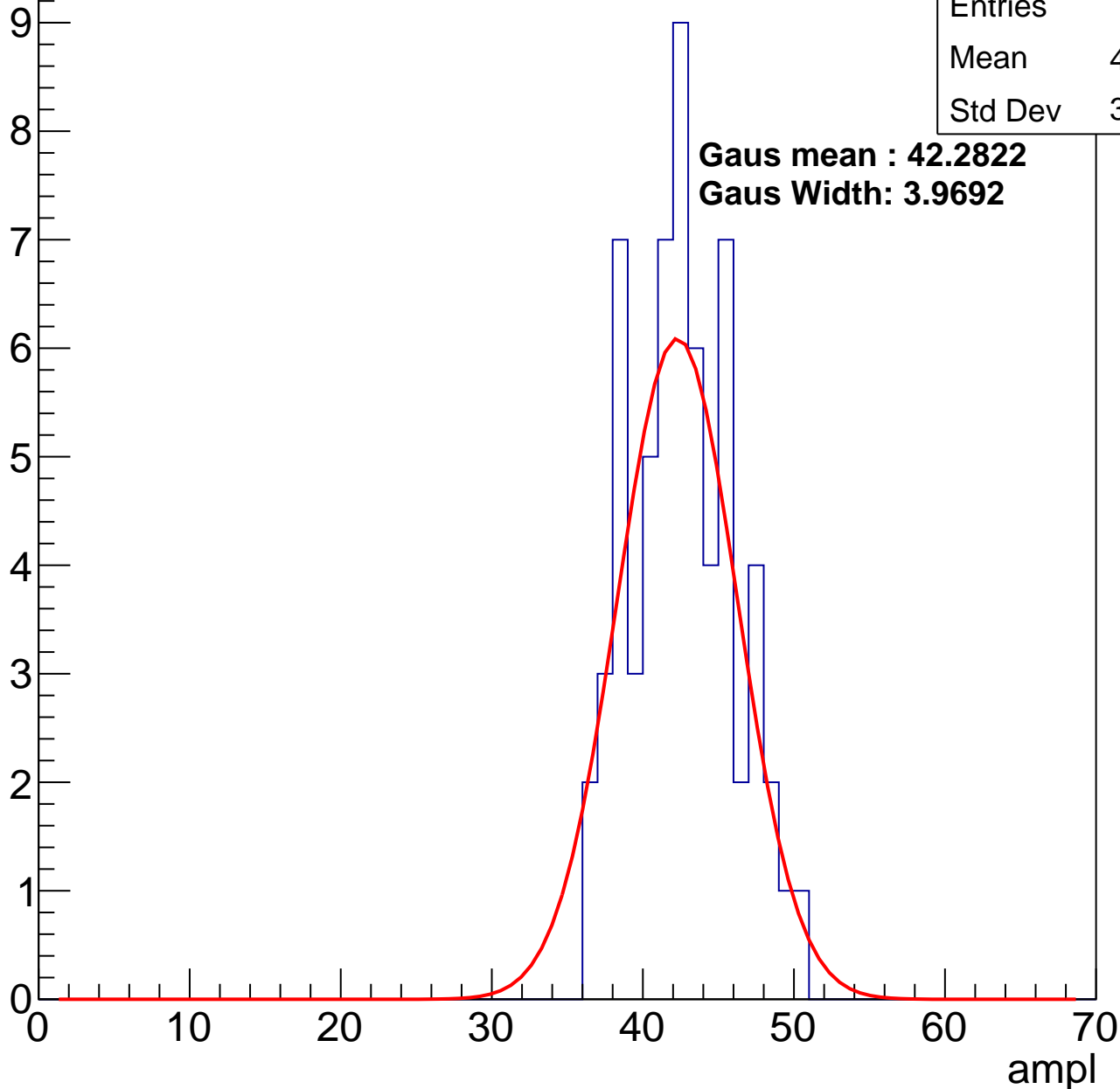
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.14
Std Dev	3.366

Gaus mean : 42.2822

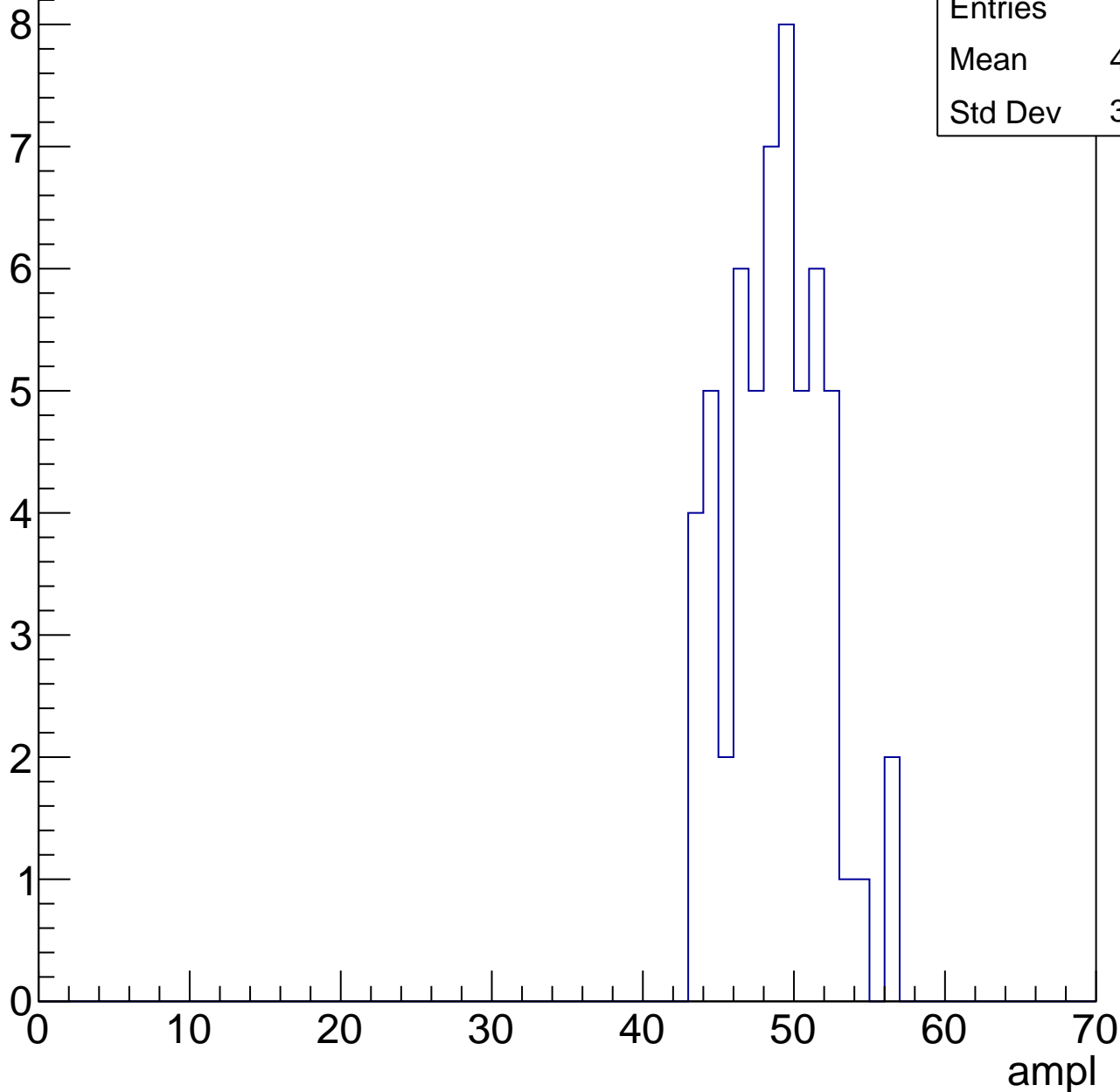
Gaus Width: 3.9692



B1L100S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

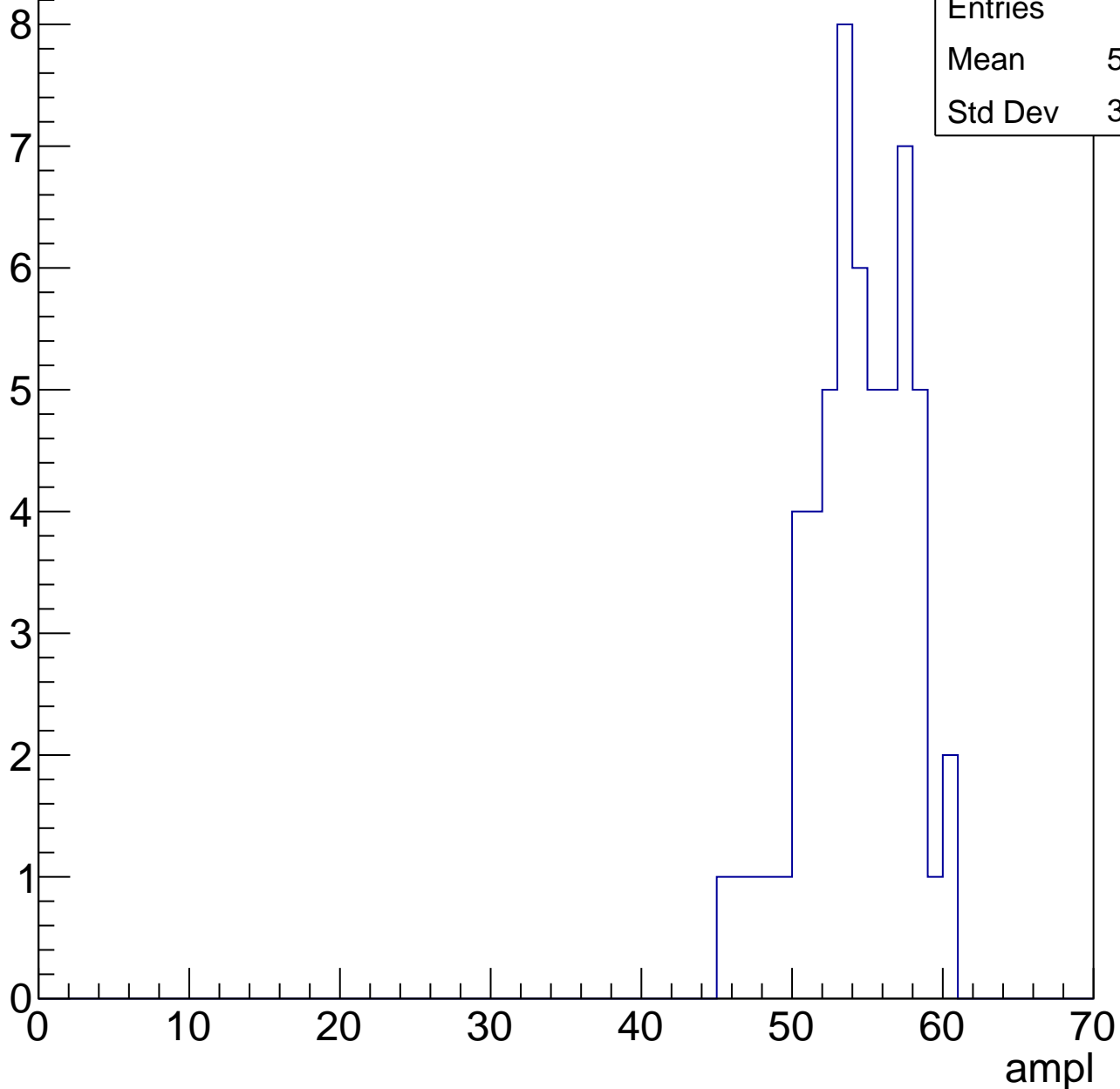


Entries	57
Mean	48.35
Std Dev	3.165

B1L100S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	57
Mean	53.86
Std Dev	3.369

B1L100S, U14-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	62
Mean	59.31
Std Dev	2.727

B1L100S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch47, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	27.82
Std Dev	3.33

Gaus mean : 28.2034

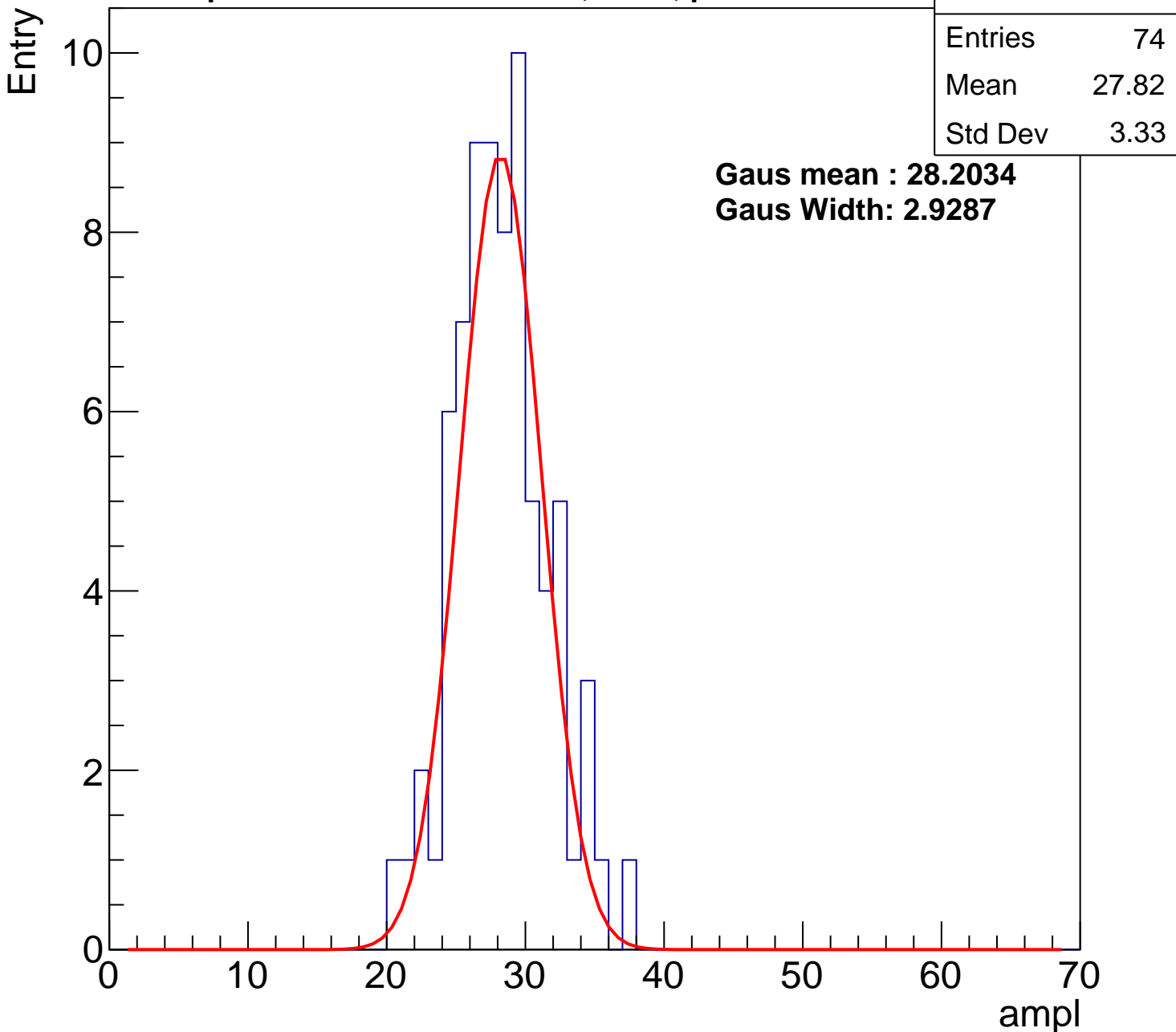
Gaus Width: 2.9287

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch47, adc1

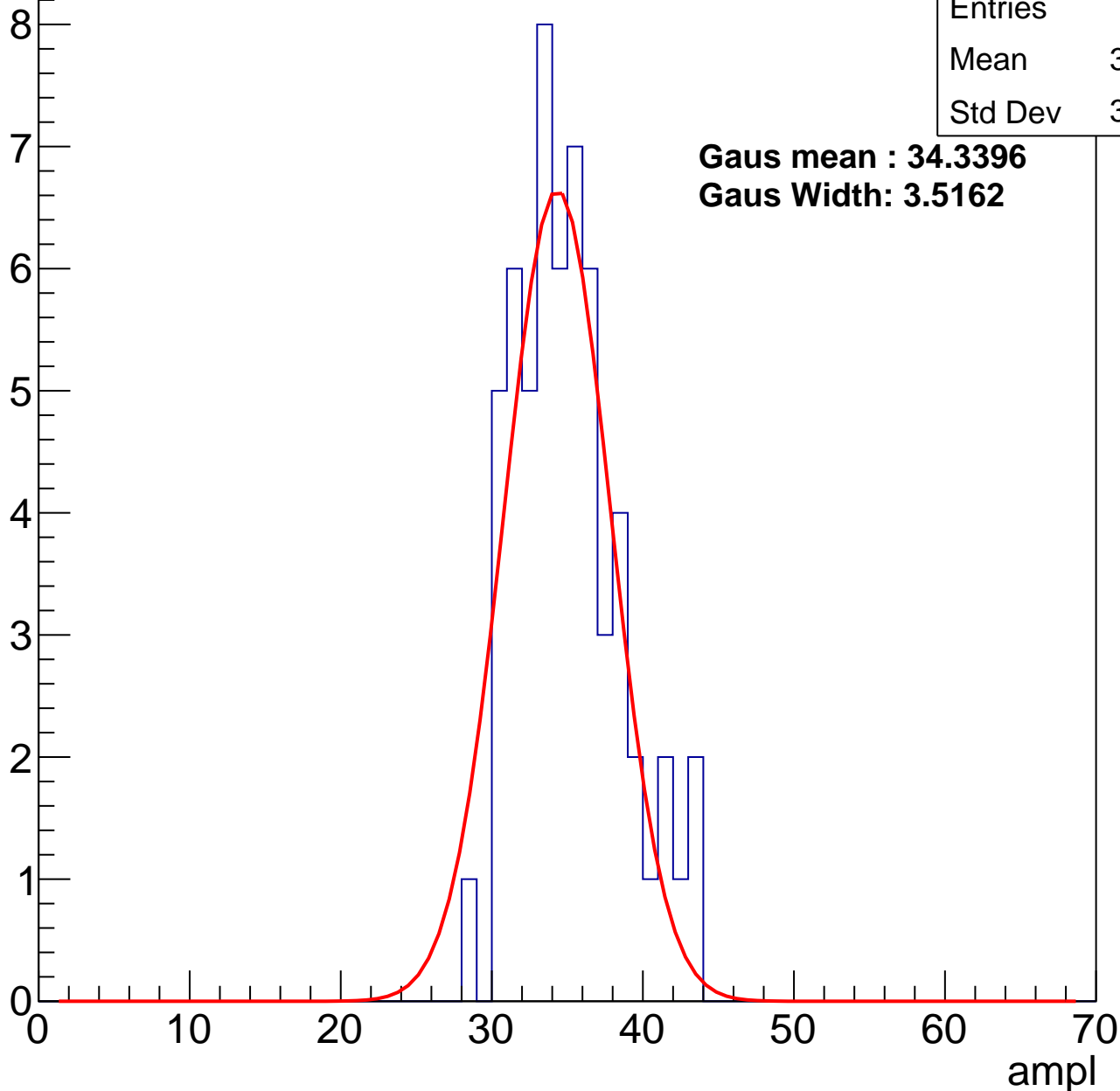
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.64
Std Dev	3.458

Gaus mean : 34.3396

Gaus Width: 3.5162



B1L100S, U14-ch47, adc2

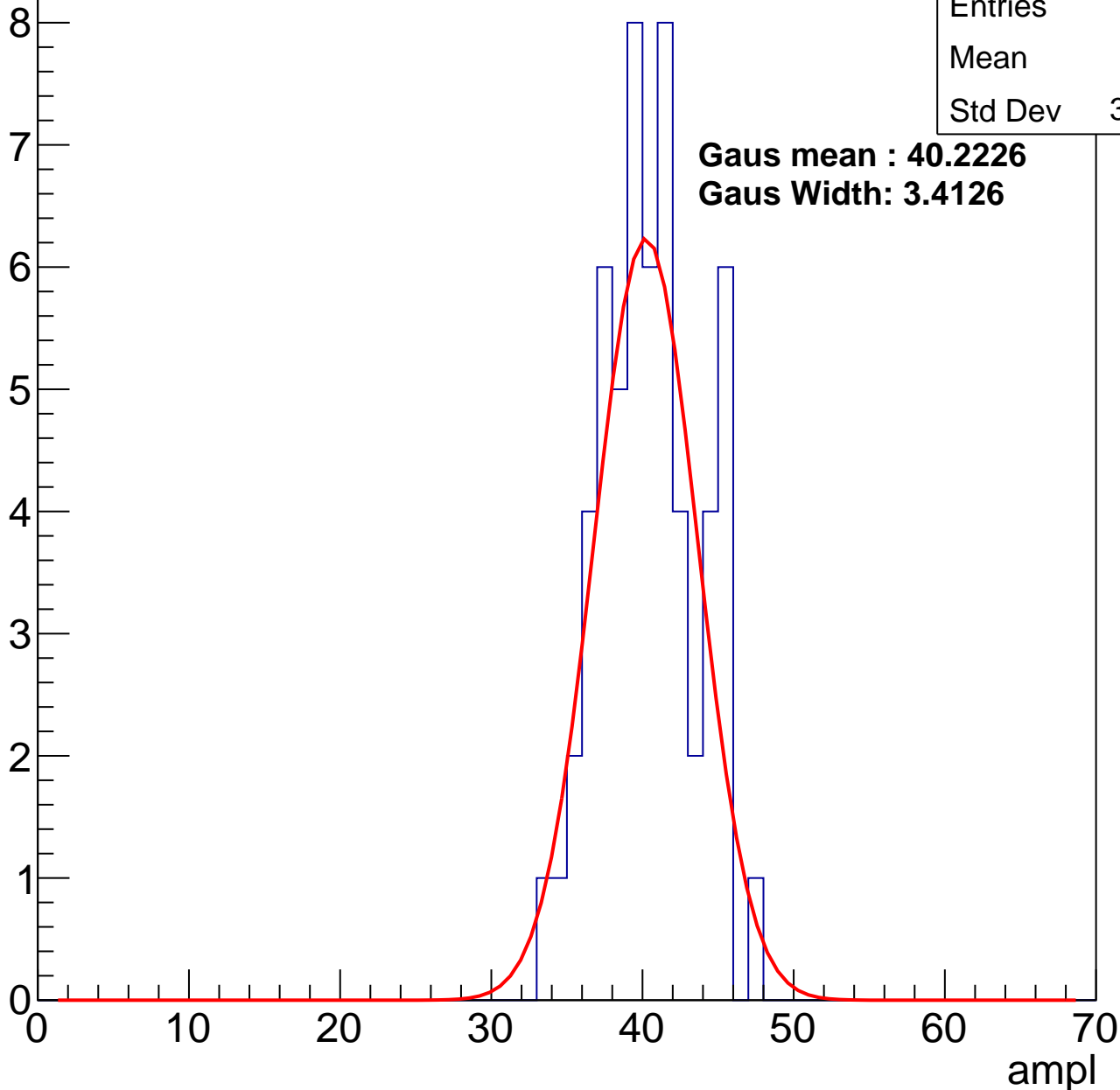
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	40
Std Dev	3.179

Gaus mean : 40.2226

Gaus Width: 3.4126



B1L100S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	47.23
Std Dev	3.63

Entry

10

8

6

4

2

0

0

10

20

30

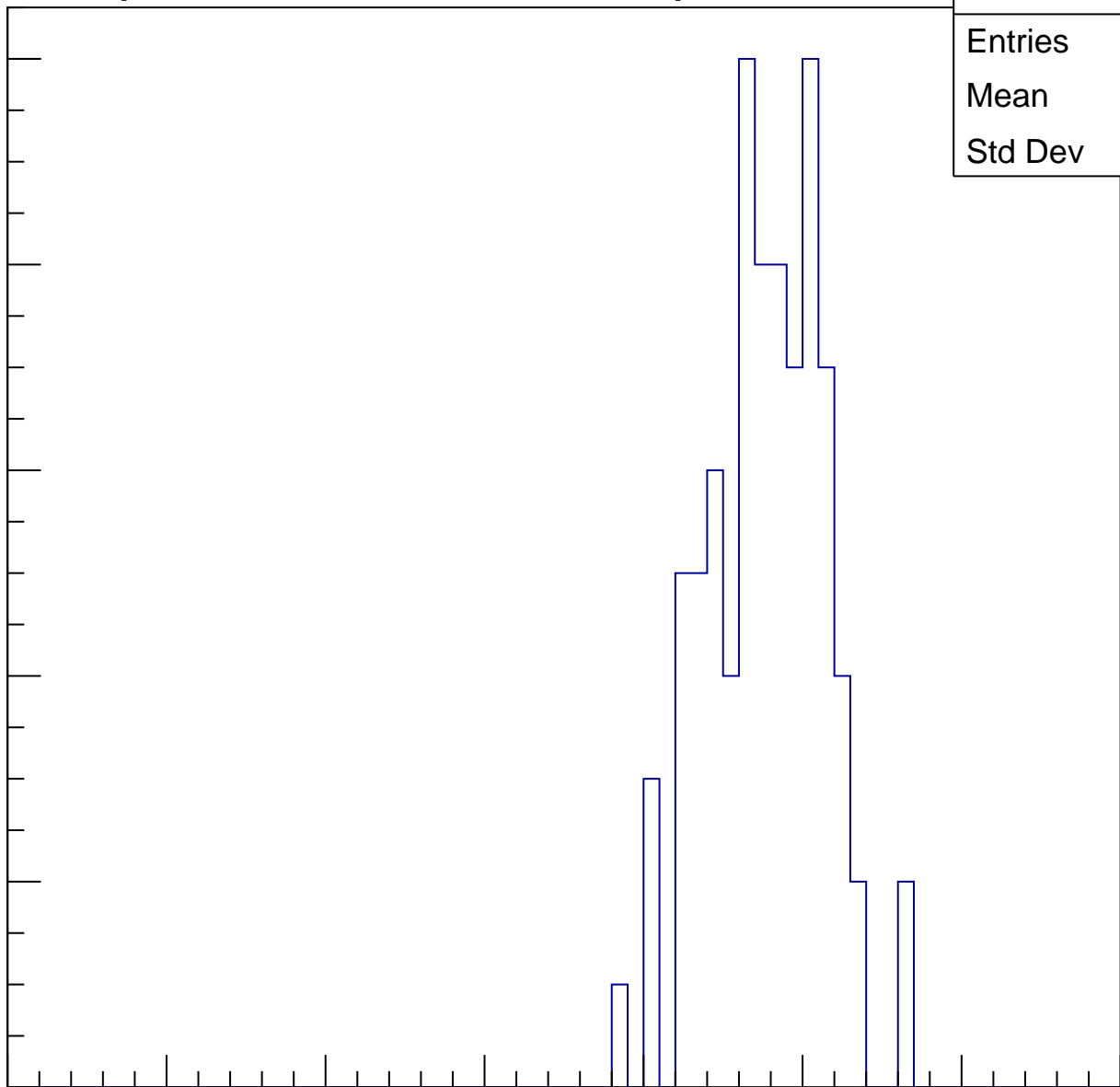
40

50

60

70

ampl



B1L100S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	50
Mean	53.42
Std Dev	3.105

Entry

10

8

6

4

2

0

0

10

20

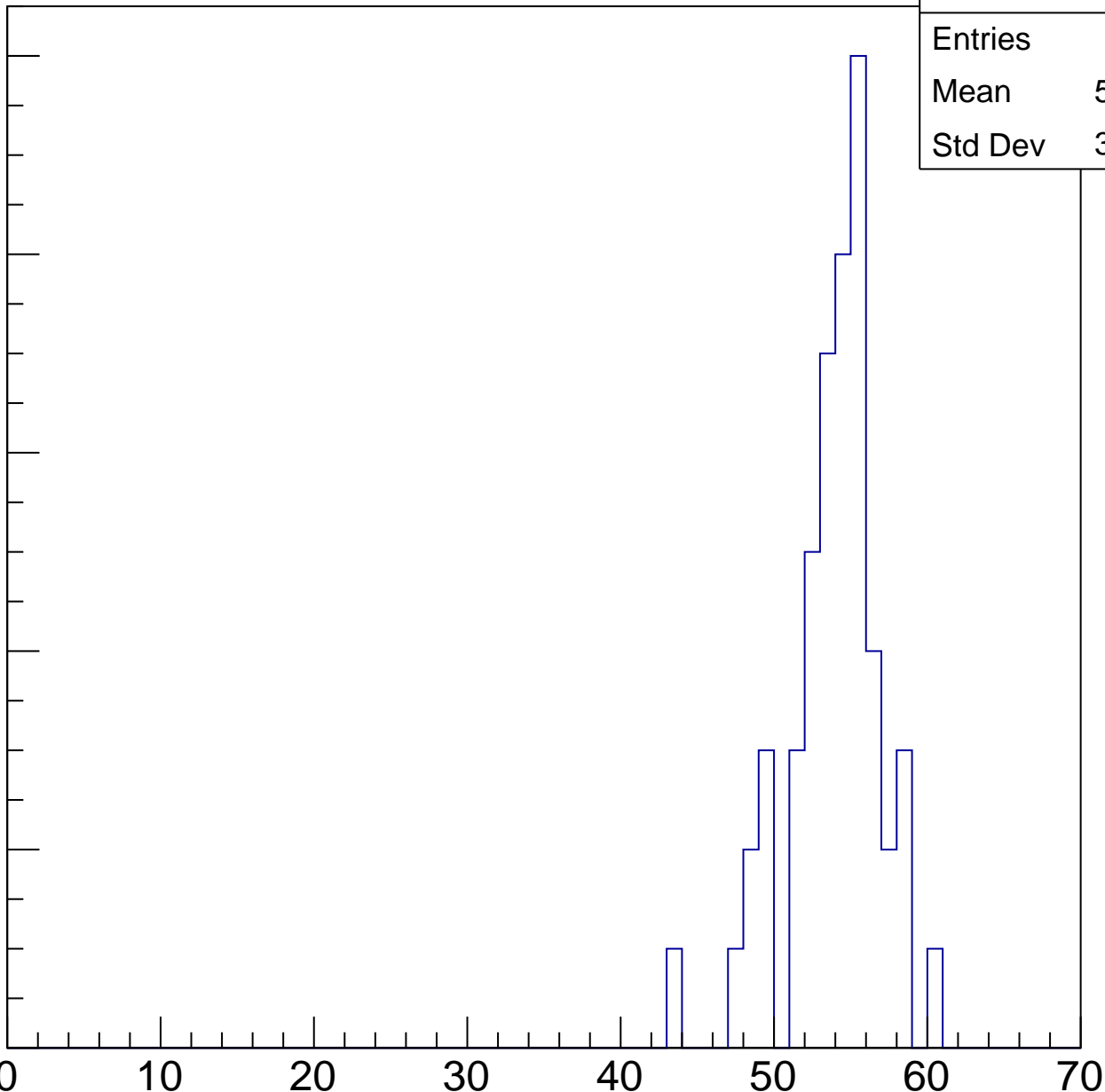
30

40

50

60

ampl

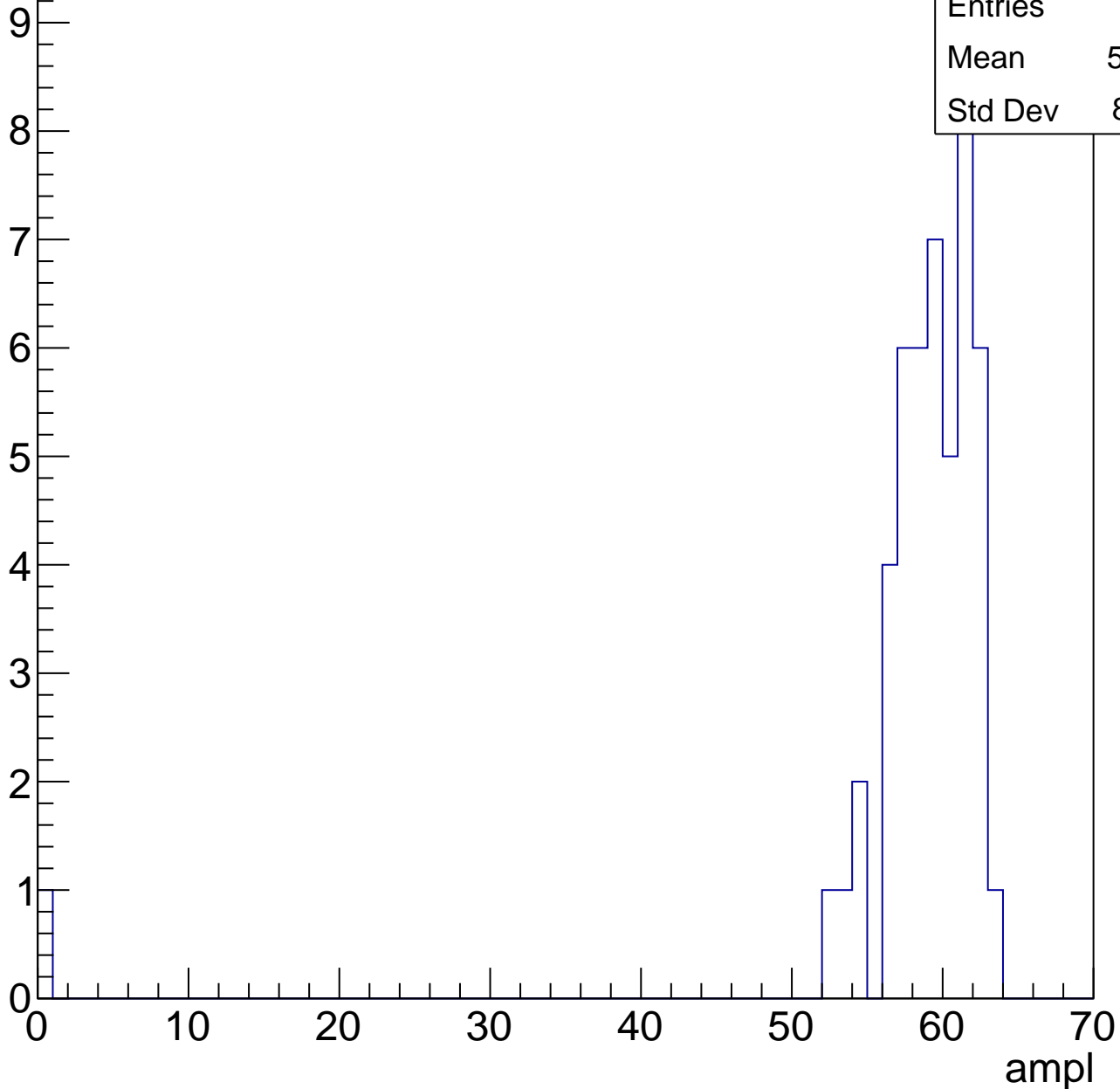


B1L100S, U14-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	57.63
Std Dev	8.691

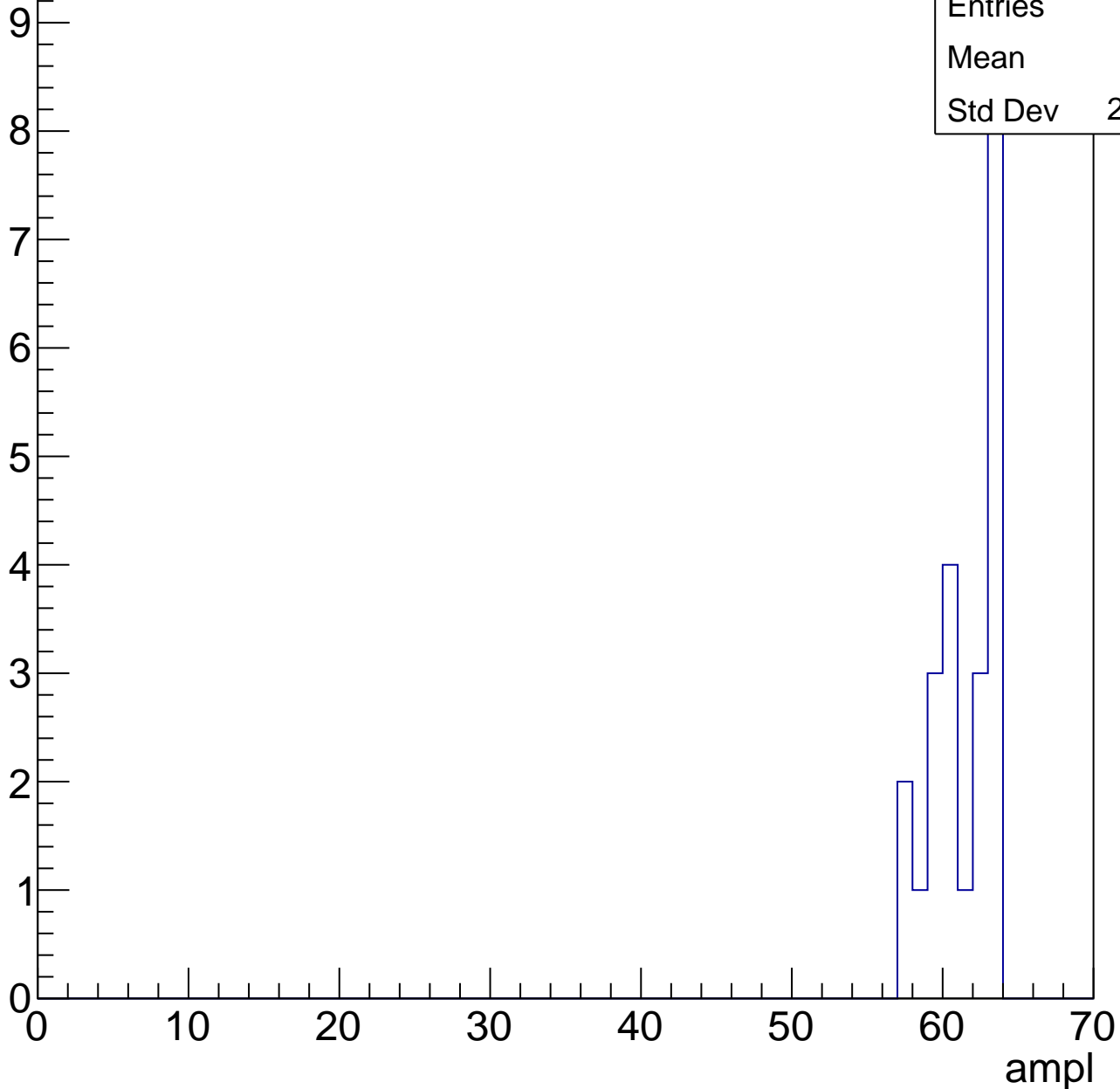


B1L100S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	61
Std Dev	2.043



B1L100S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch48, adc0

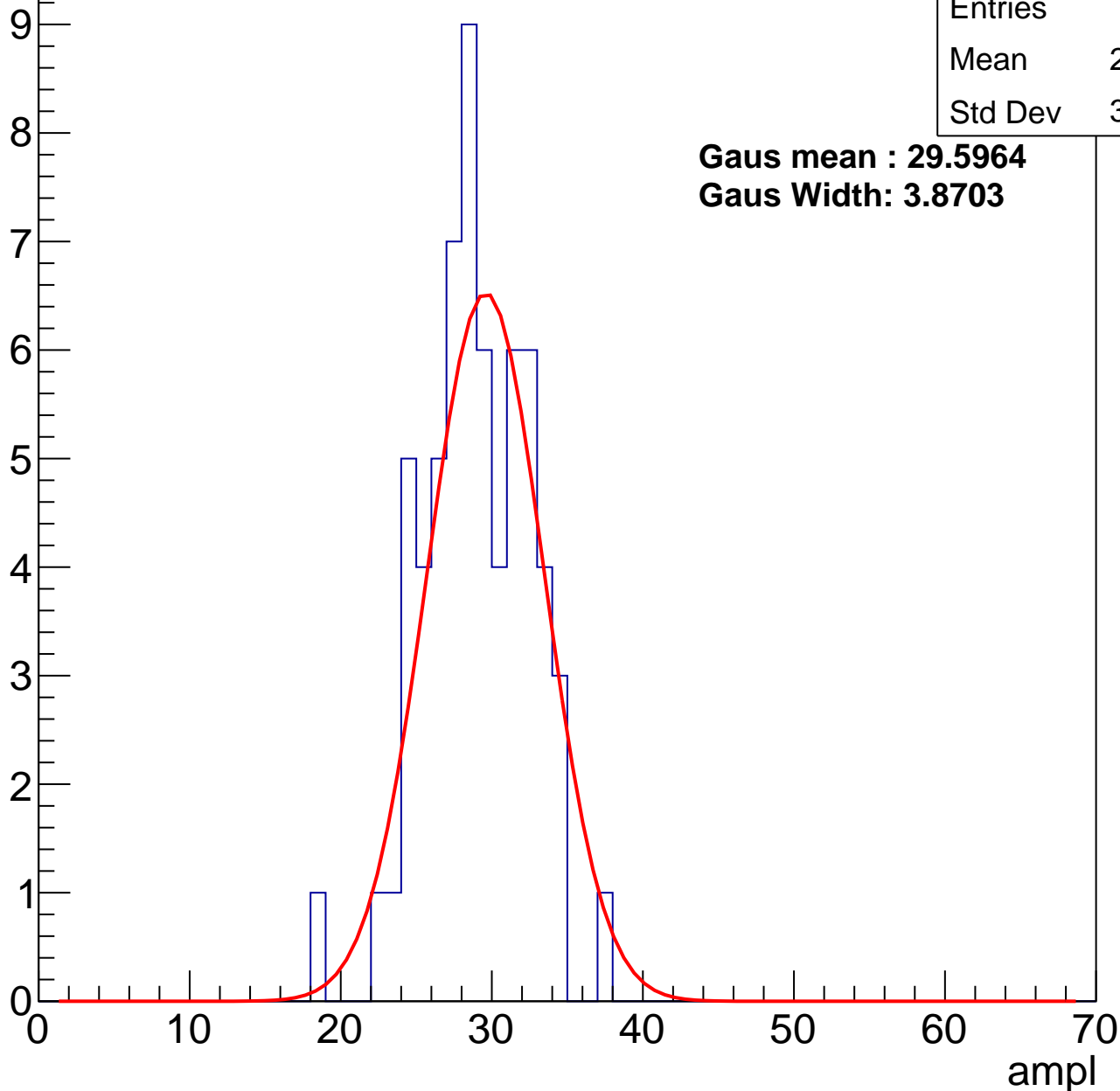
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.52
Std Dev	3.445

Gaus mean : 29.5964

Gaus Width: 3.8703



B1L100S, U14-ch48, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	35.69
Std Dev	3.487

Gaus mean : 36.0342

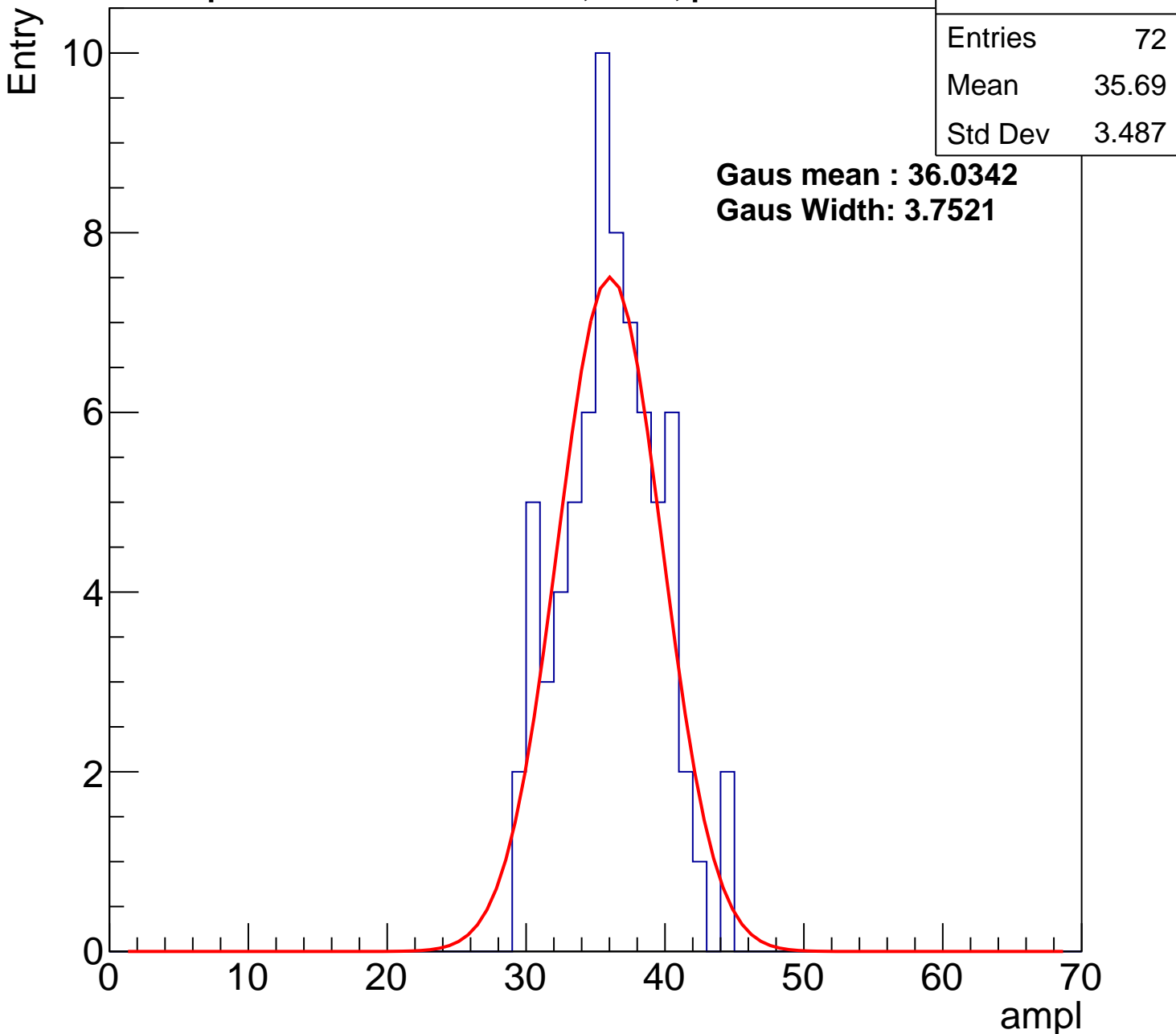
Gaus Width: 3.7521

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch48, adc2

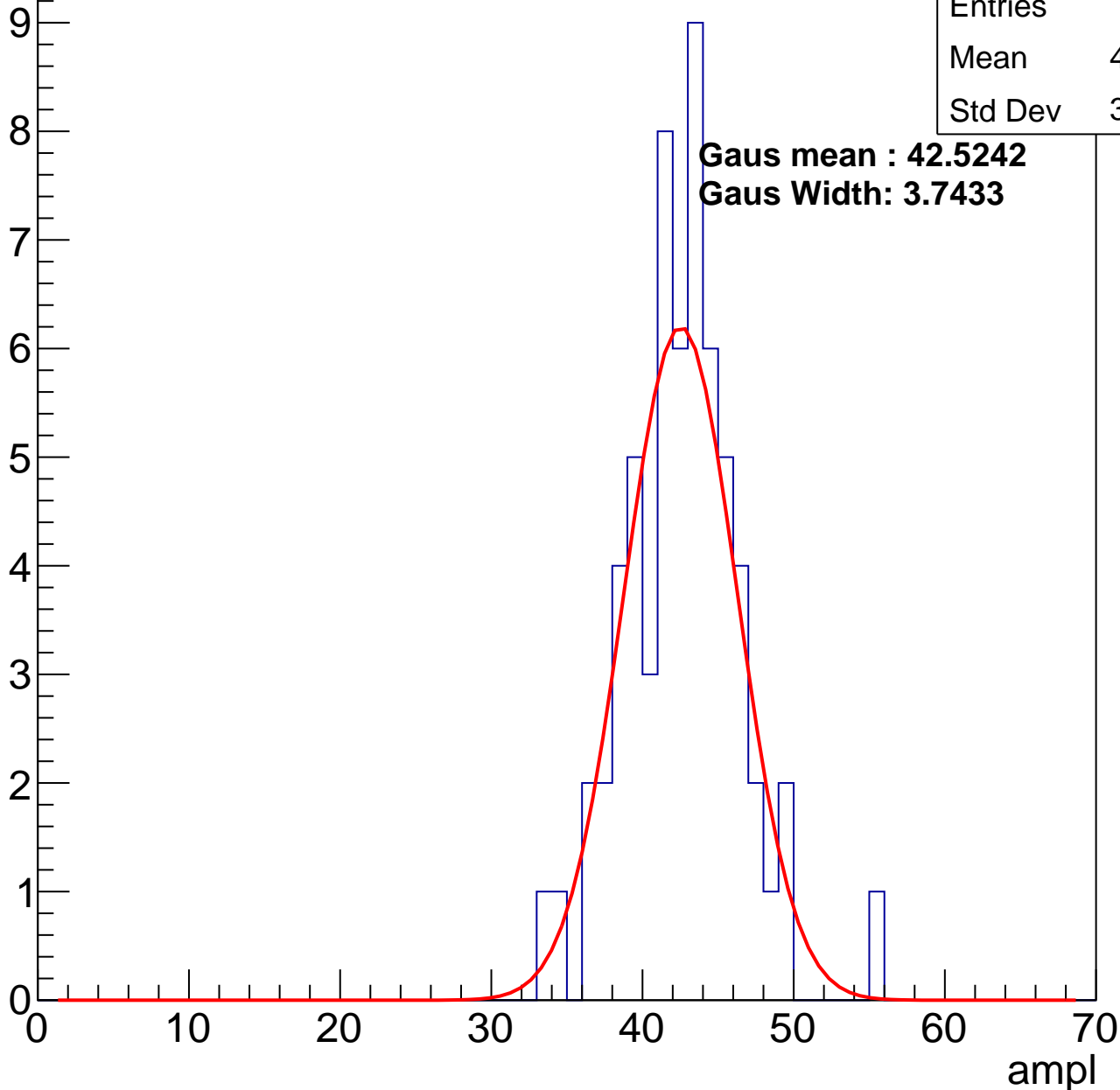
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.18
Std Dev	3.787

Gaus mean : 42.5242

Gaus Width: 3.7433

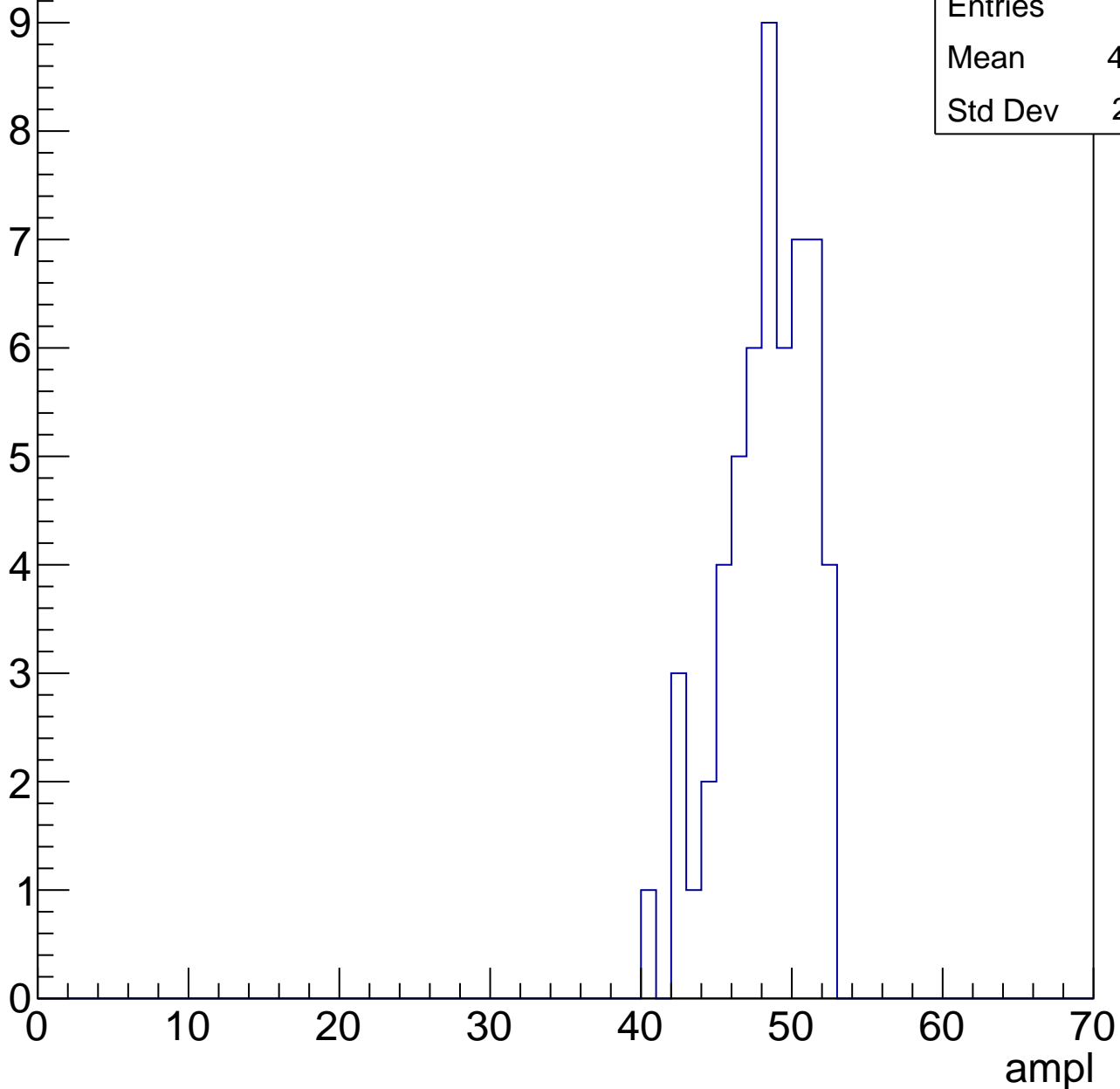


B1L100S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	47.82
Std Dev	2.861

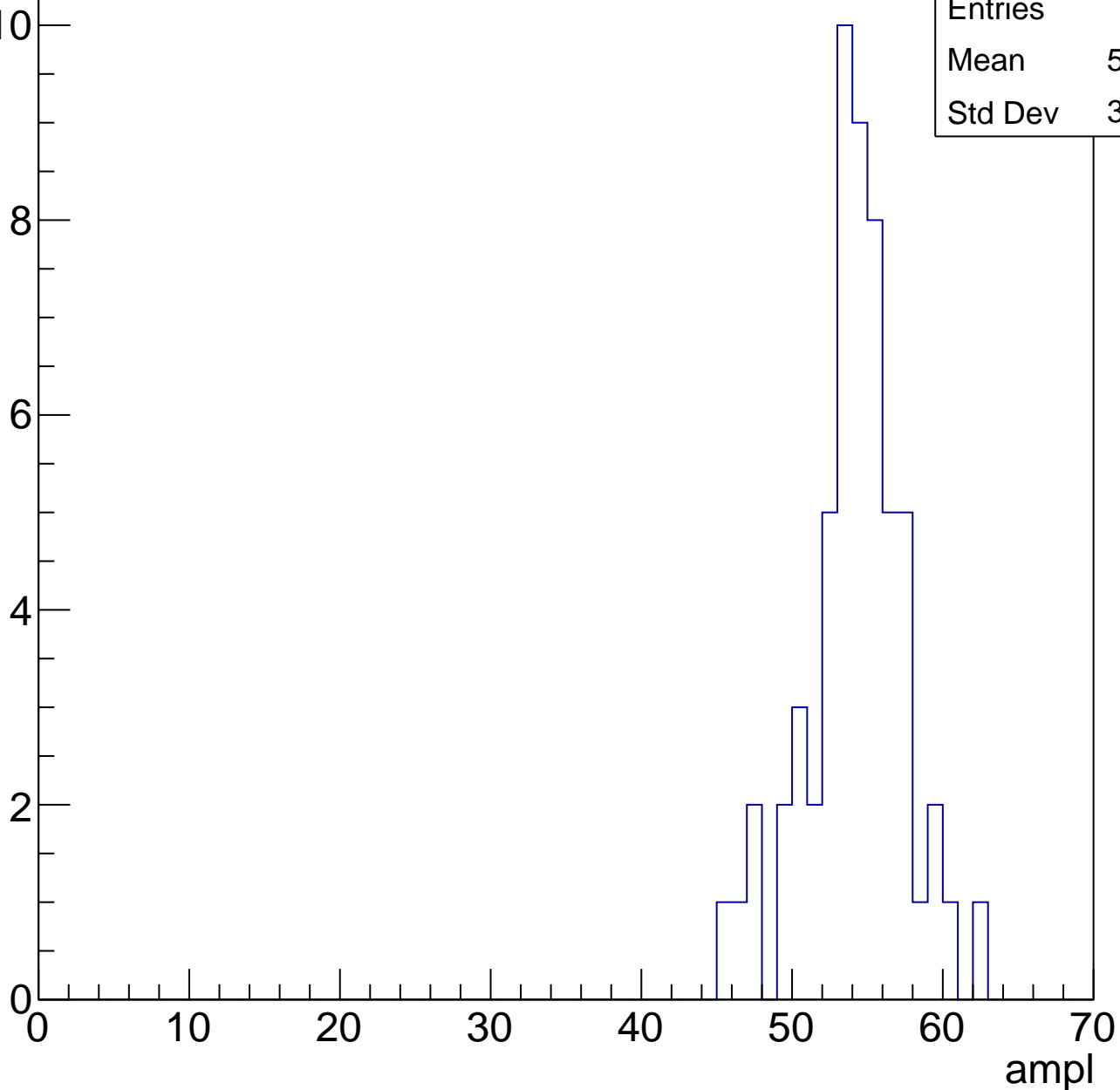


B1L100S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

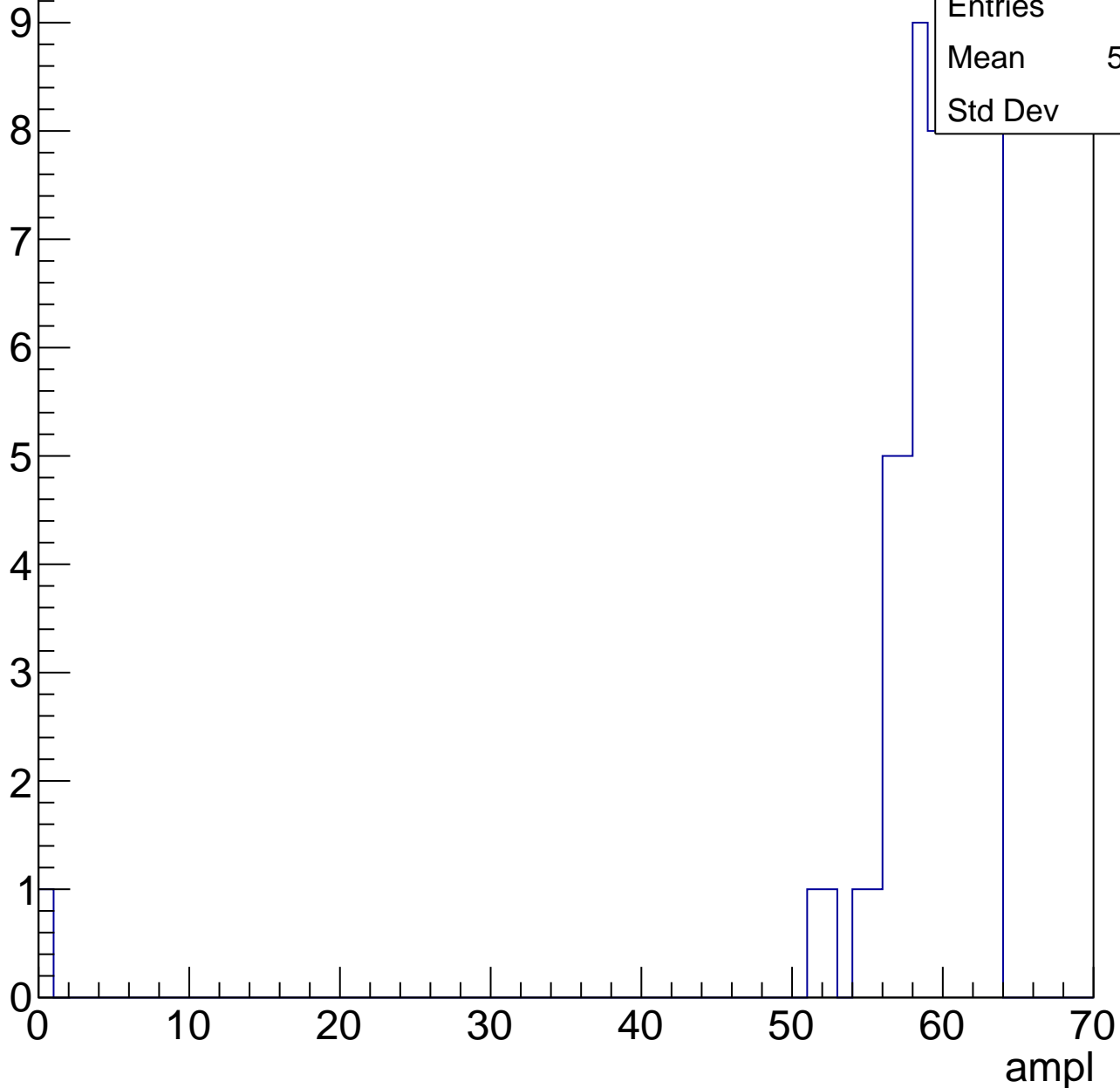
Entries	58
Mean	53.69
Std Dev	3.276



B1L100S, U14-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B1L100S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch49, adc0

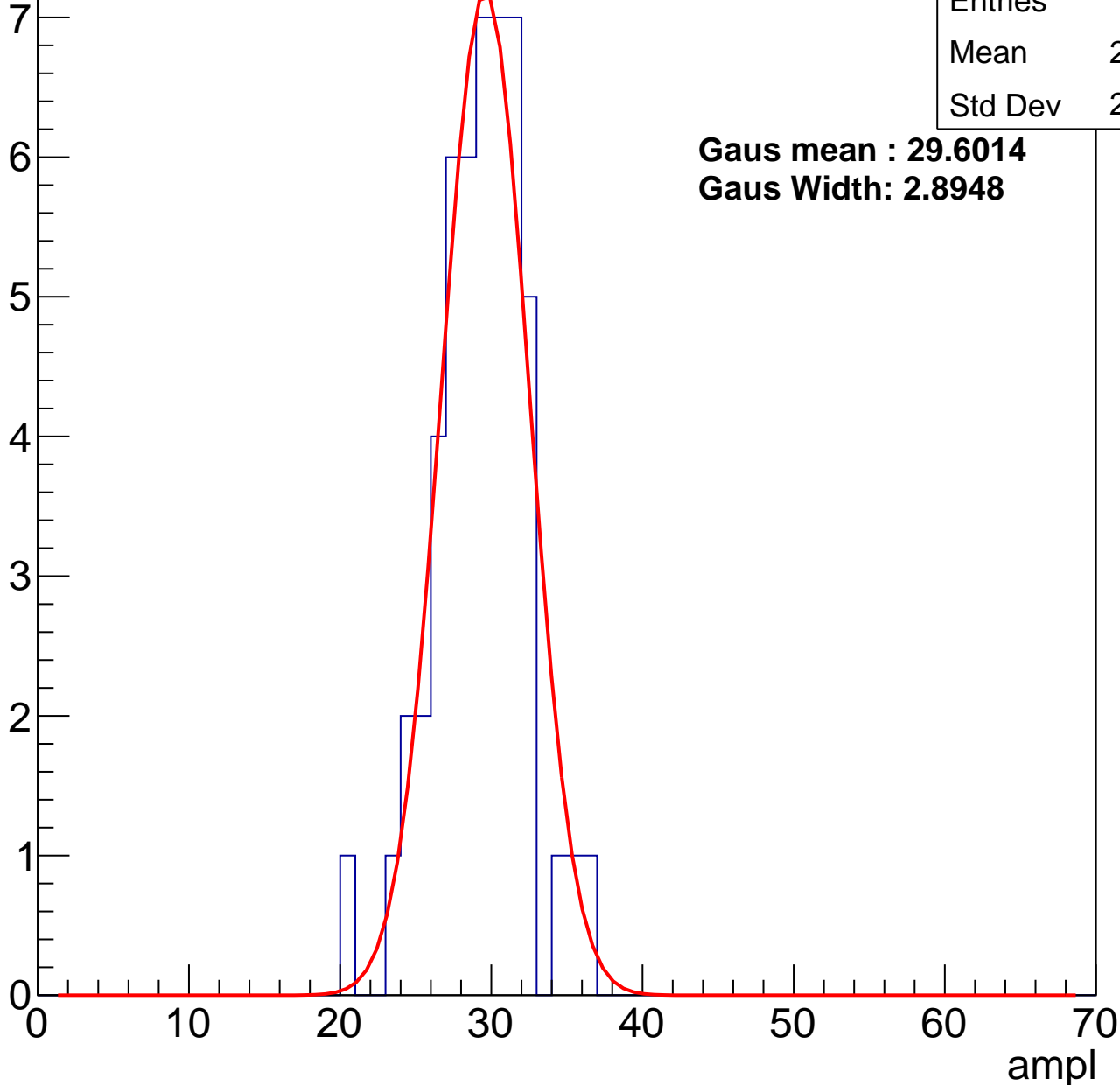
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.82
Std Dev	2.982

Gaus mean : 29.6014

Gaus Width: 2.8948



B1L100S, U14-ch49, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	35.97
Std Dev	3.393

Gaus mean : 36.6588

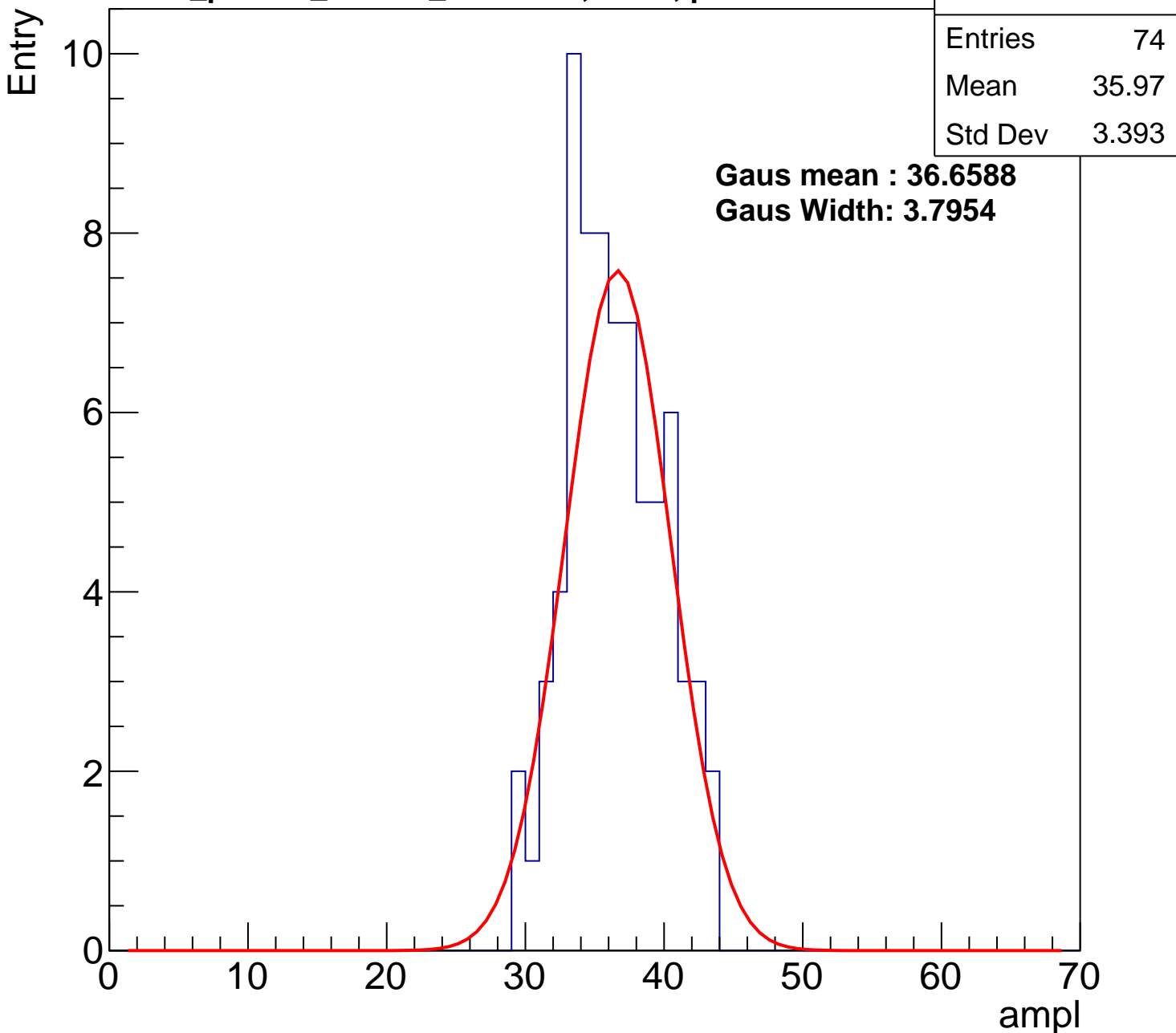
Gaus Width: 3.7954

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch49, adc2

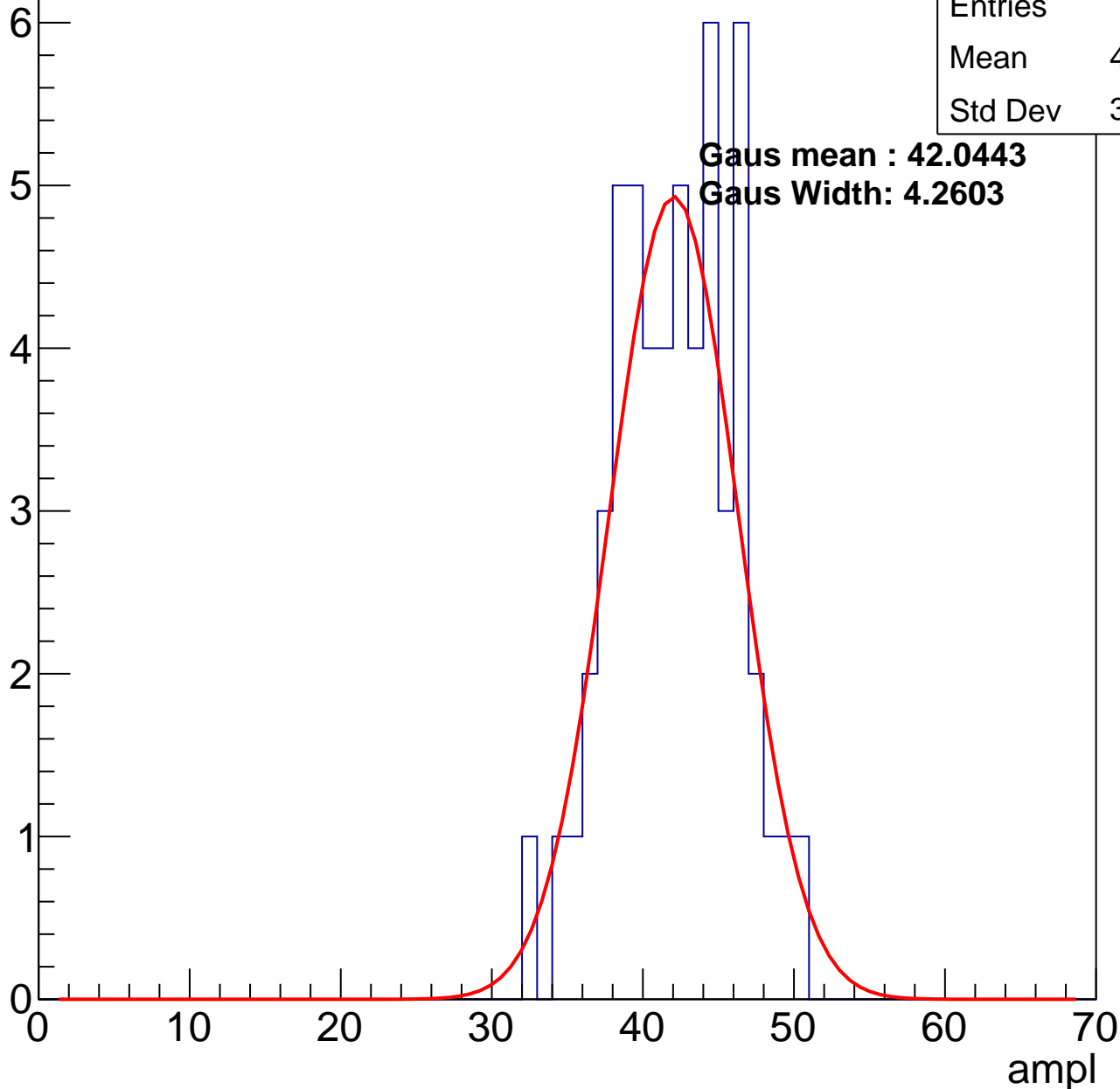
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	41.65
Std Dev	3.928

Gaus mean : 42.0443

Gaus Width: 4.2603

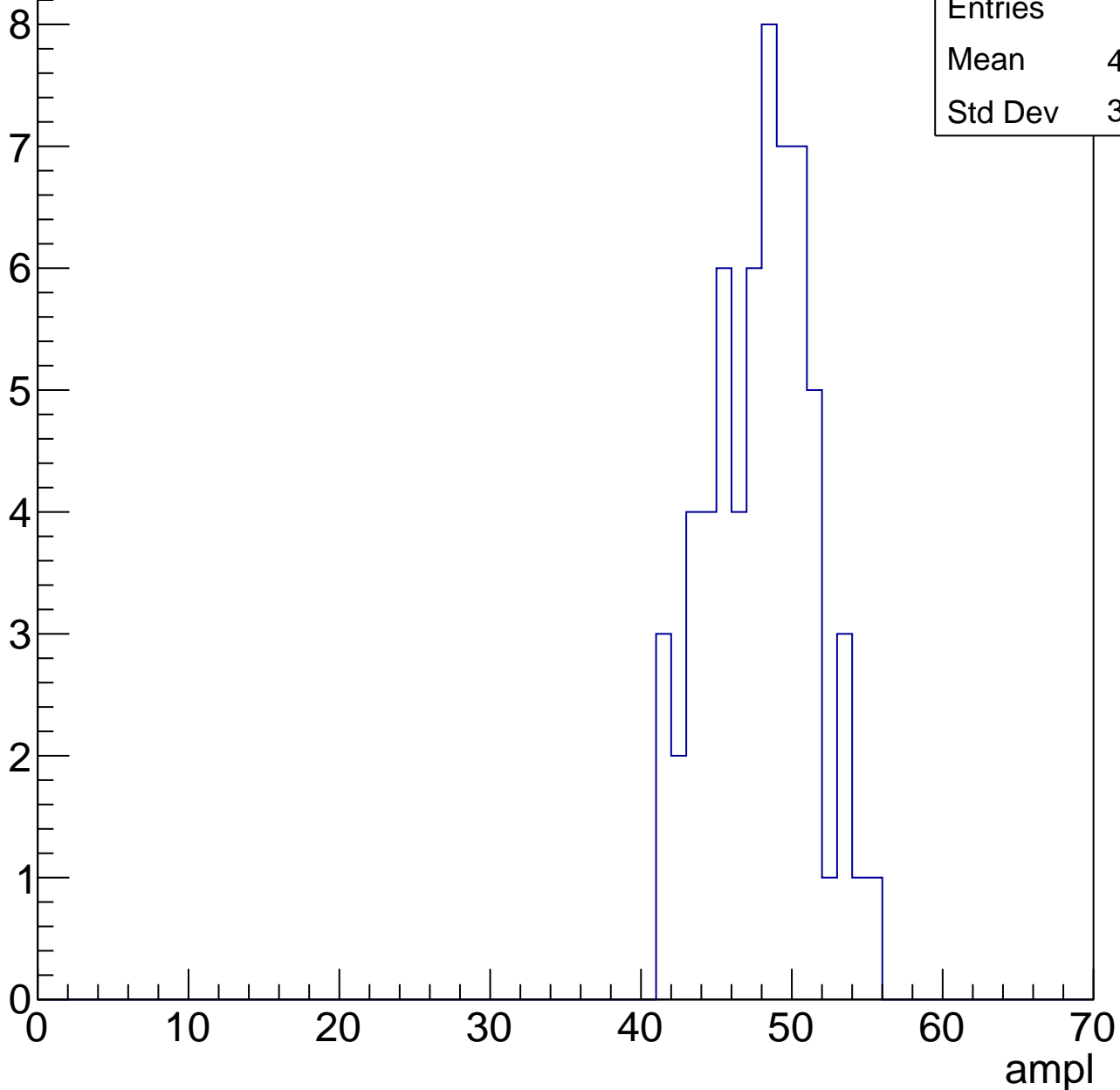


B1L100S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	47.47
Std Dev	3.354

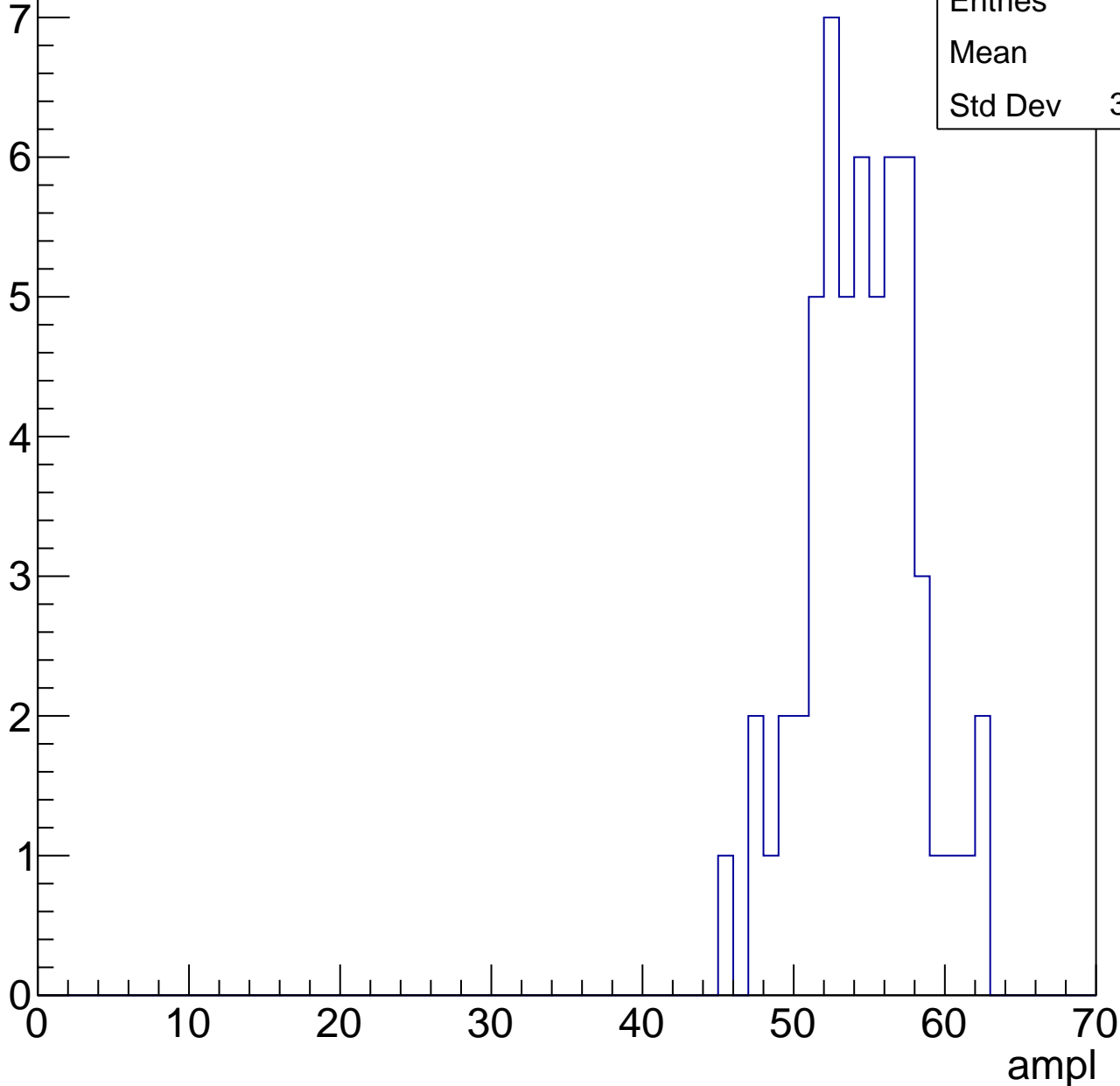


B1L100S, U14-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	54
Std Dev	3.645



B1L100S, U14-ch49, adc5

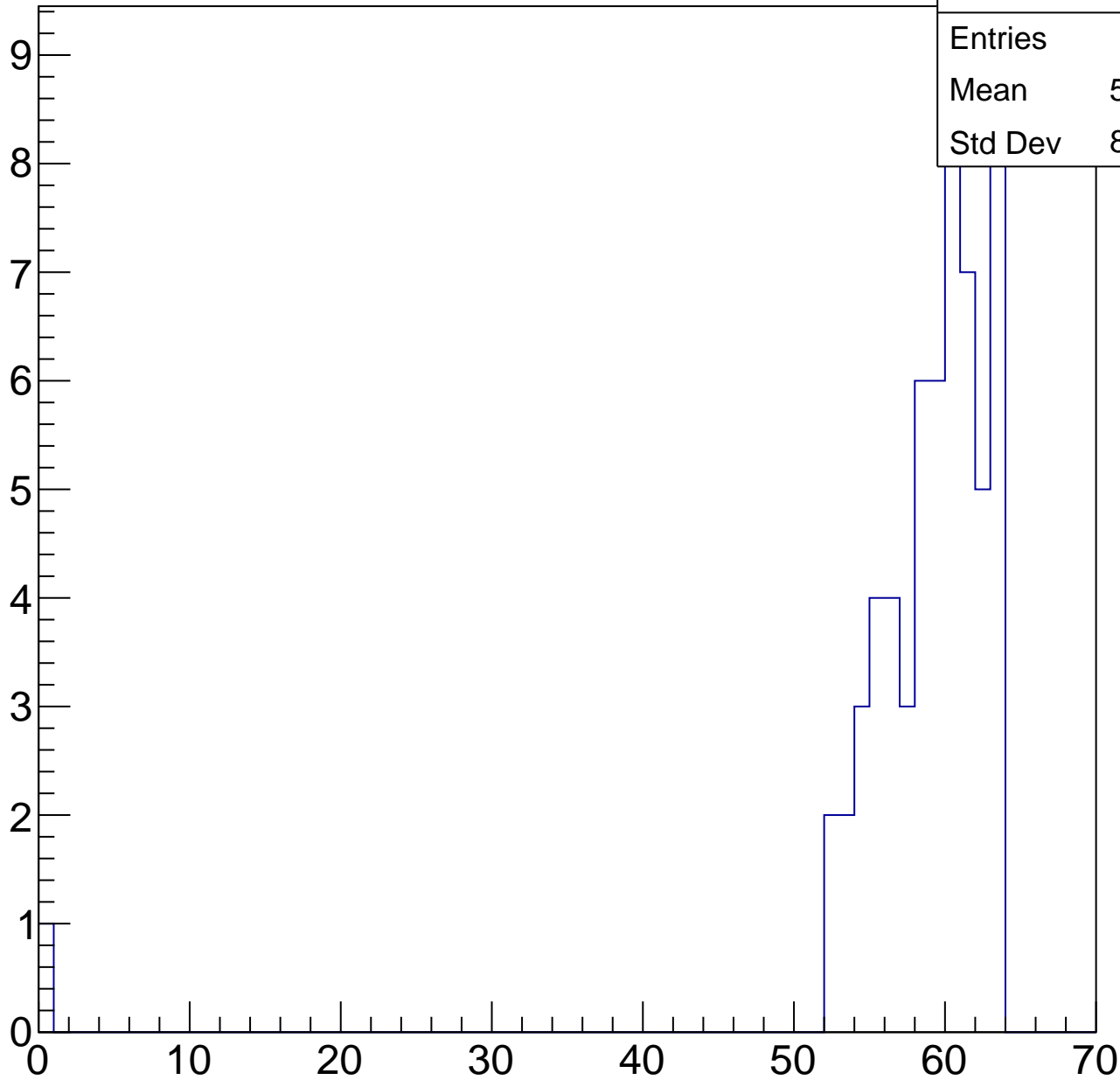
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	57.83
Std Dev	8.137

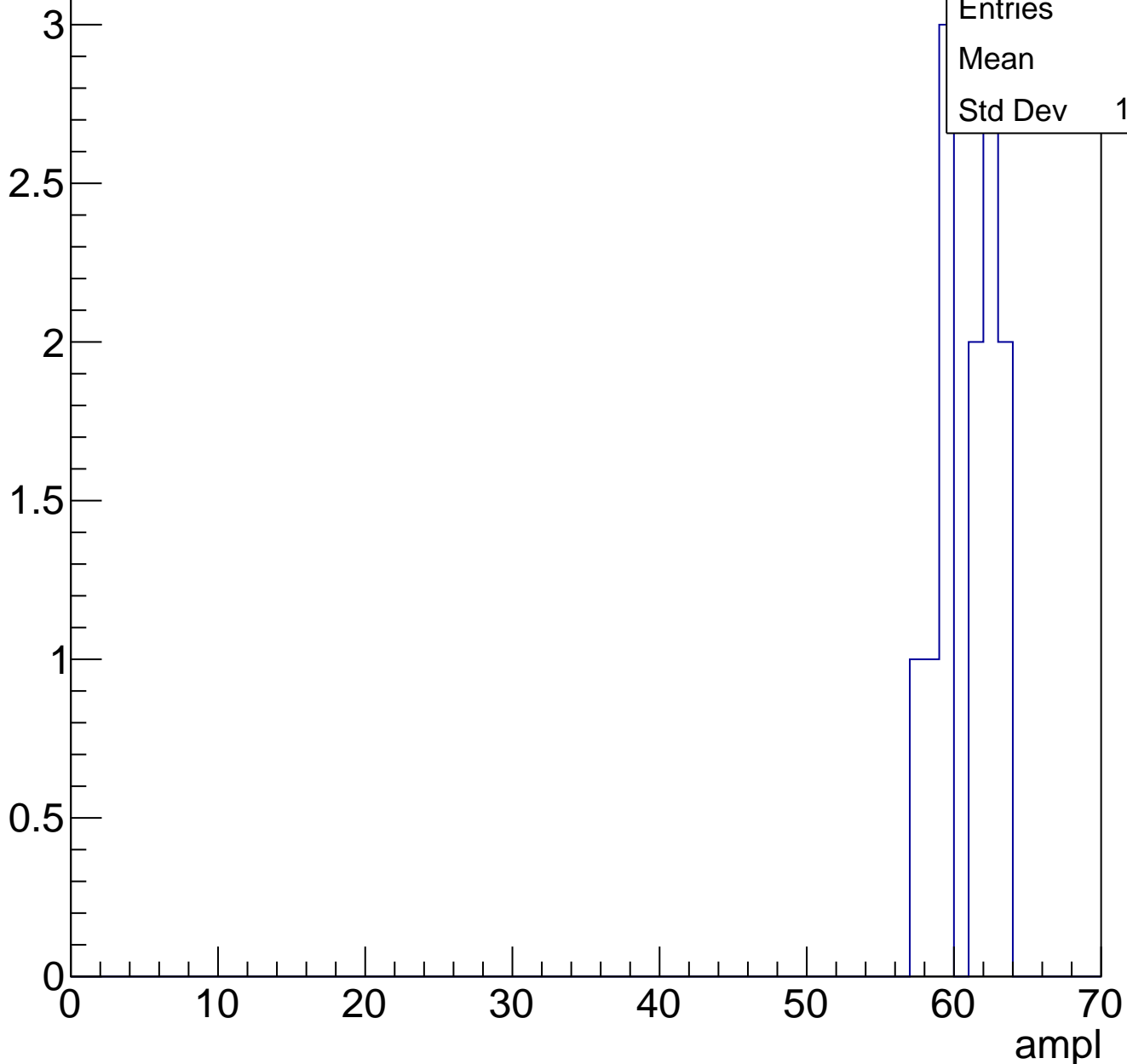
ampl



B1L100S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

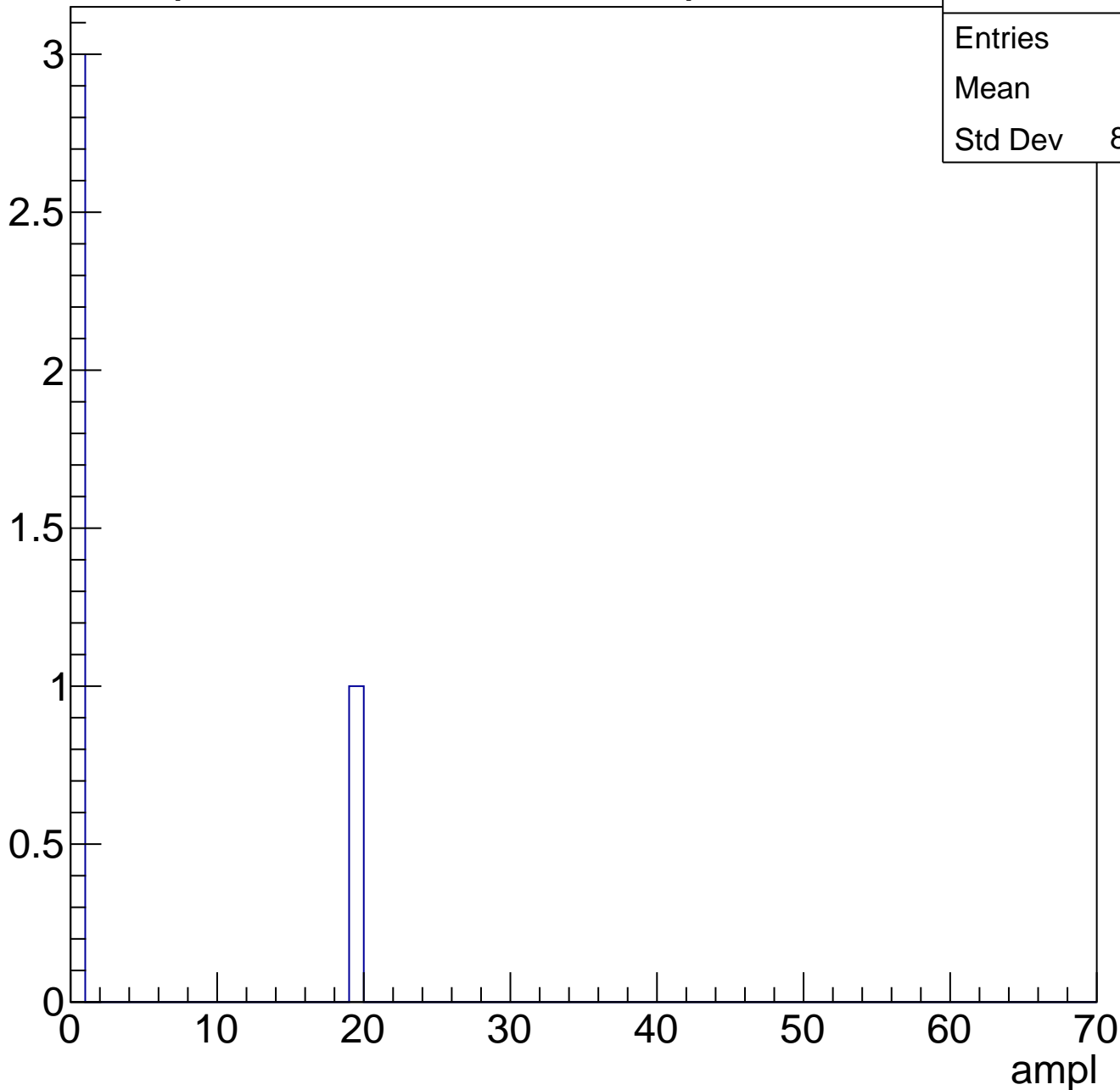
Entry



B1L100S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	4.75
Std Dev	8.227

B1L100S, U14-ch50, adc0

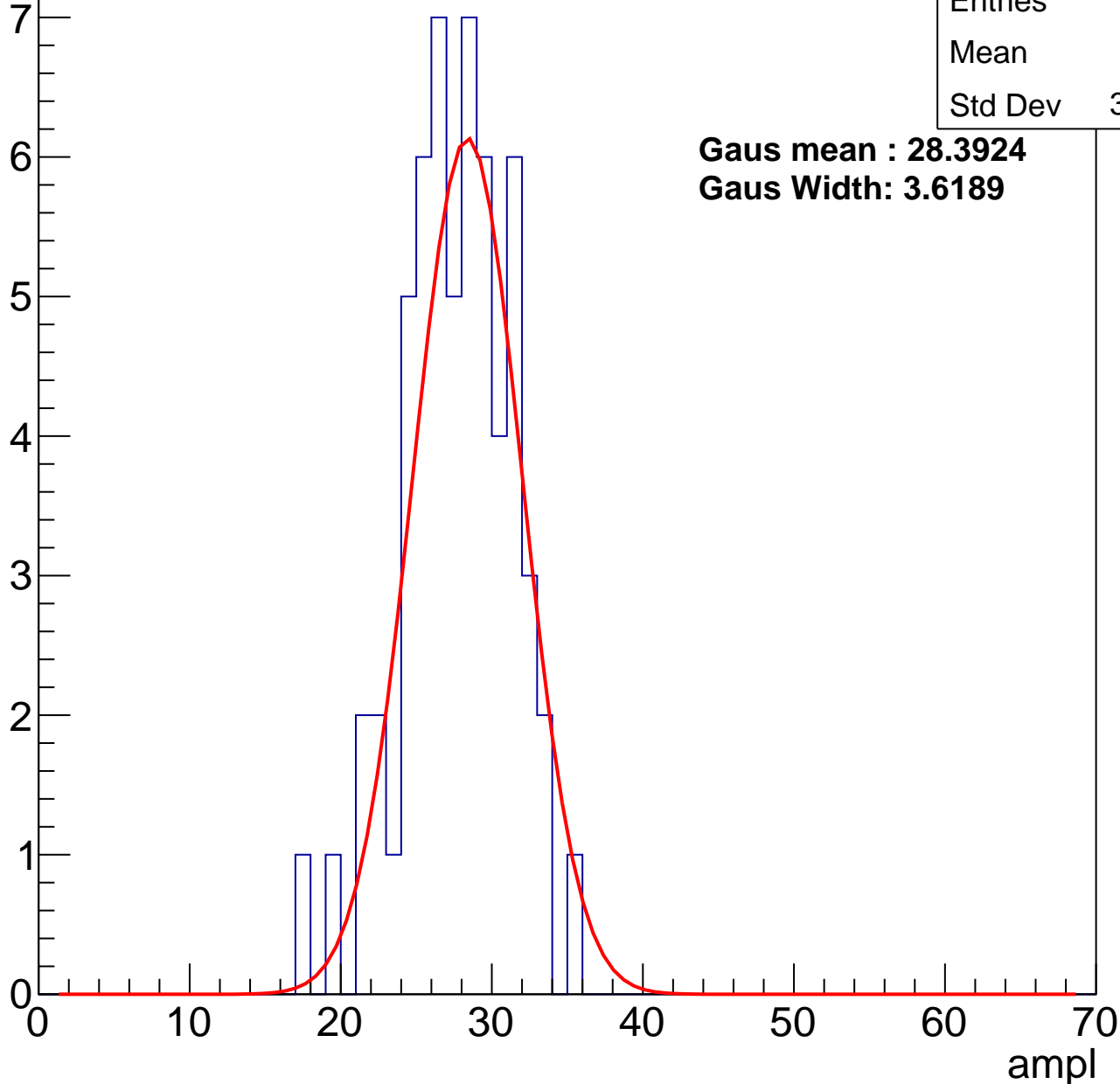
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	27.2
Std Dev	3.564

Gaus mean : 28.3924

Gaus Width: 3.6189



B1L100S, U14-ch50, adc1

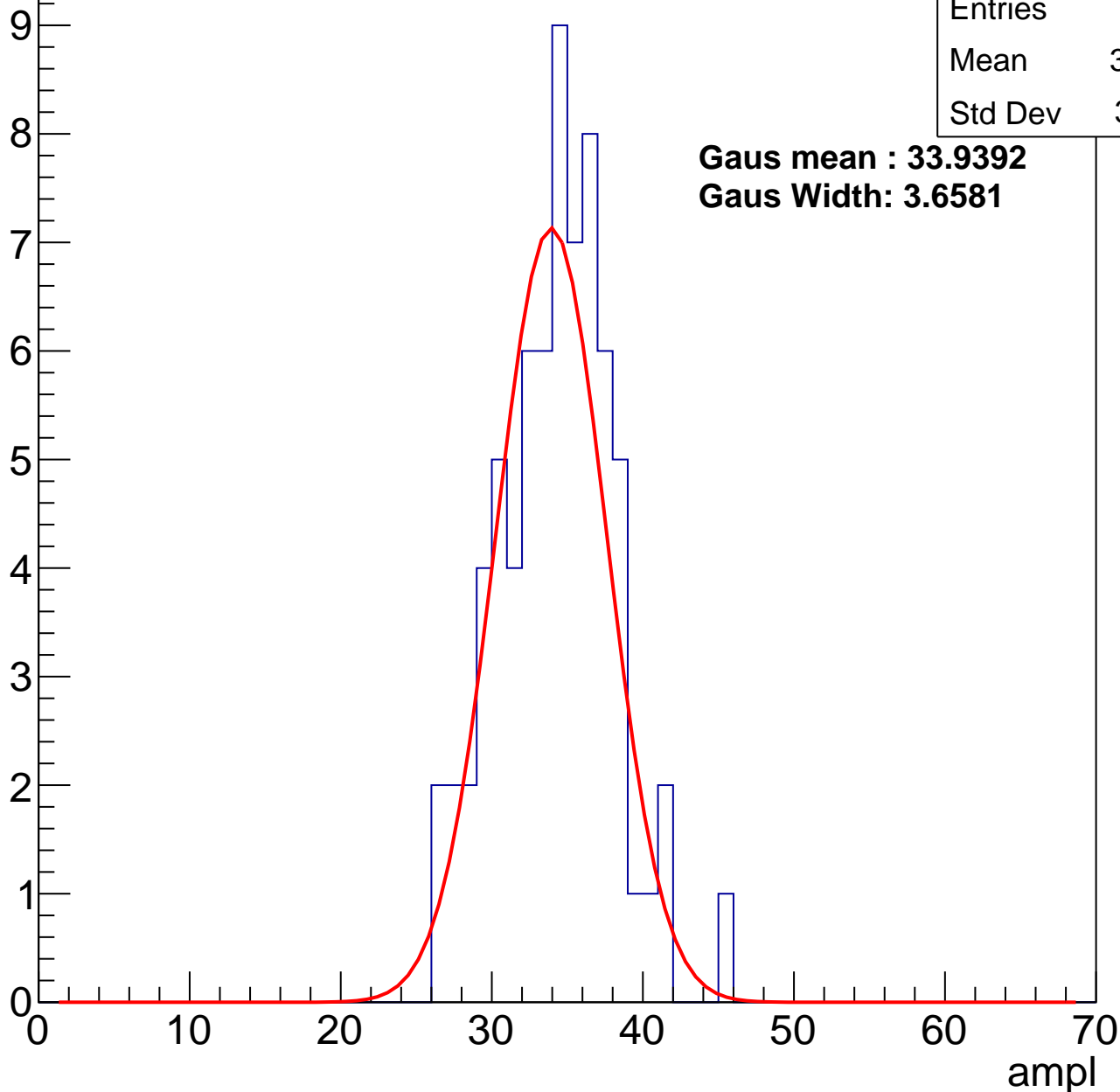
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	33.79
Std Dev	3.741

Gaus mean : 33.9392

Gaus Width: 3.6581



B1L100S, U14-ch50, adc2

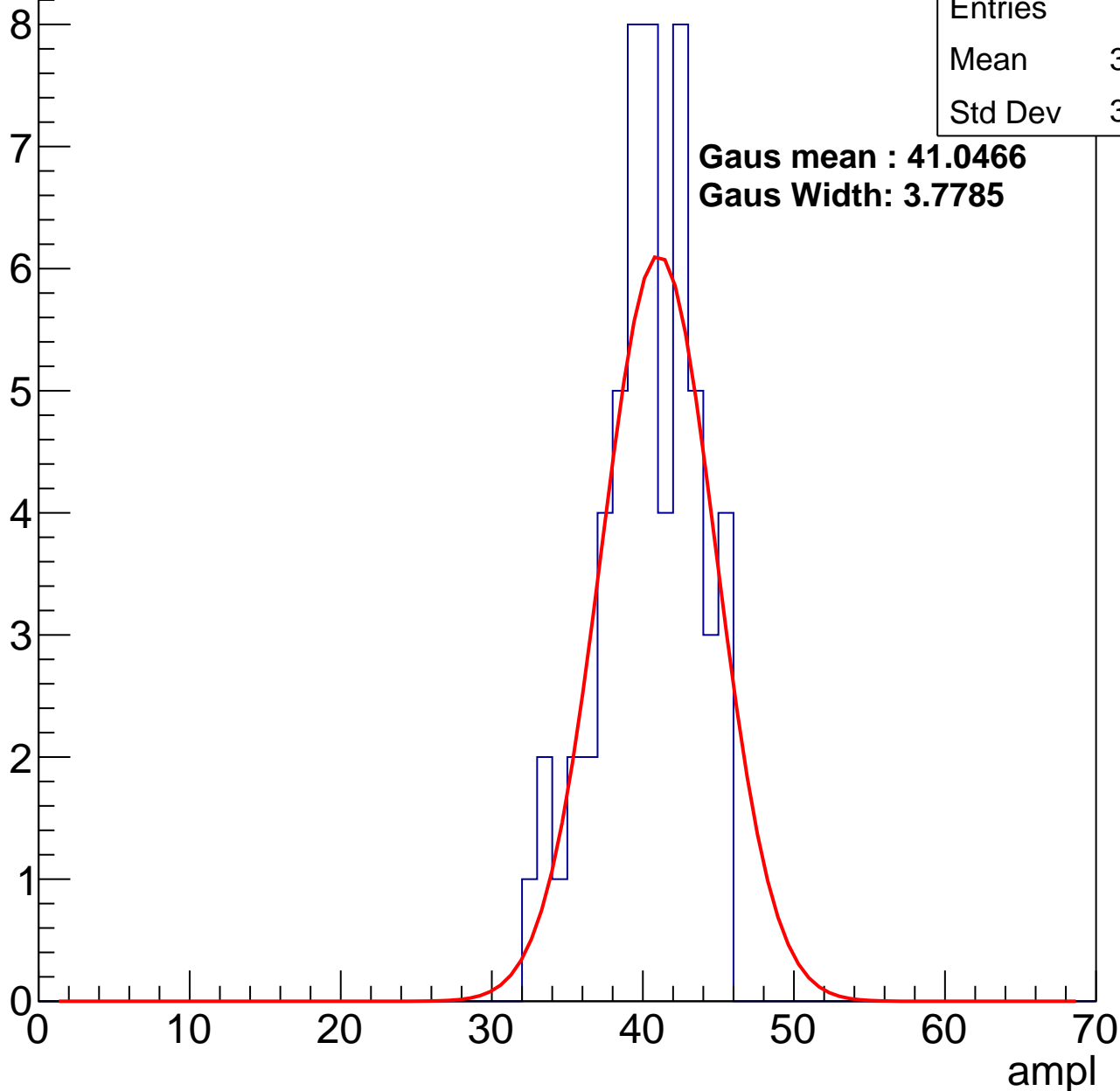
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	39.84
Std Dev	3.167

Gaus mean : 41.0466

Gaus Width: 3.7785

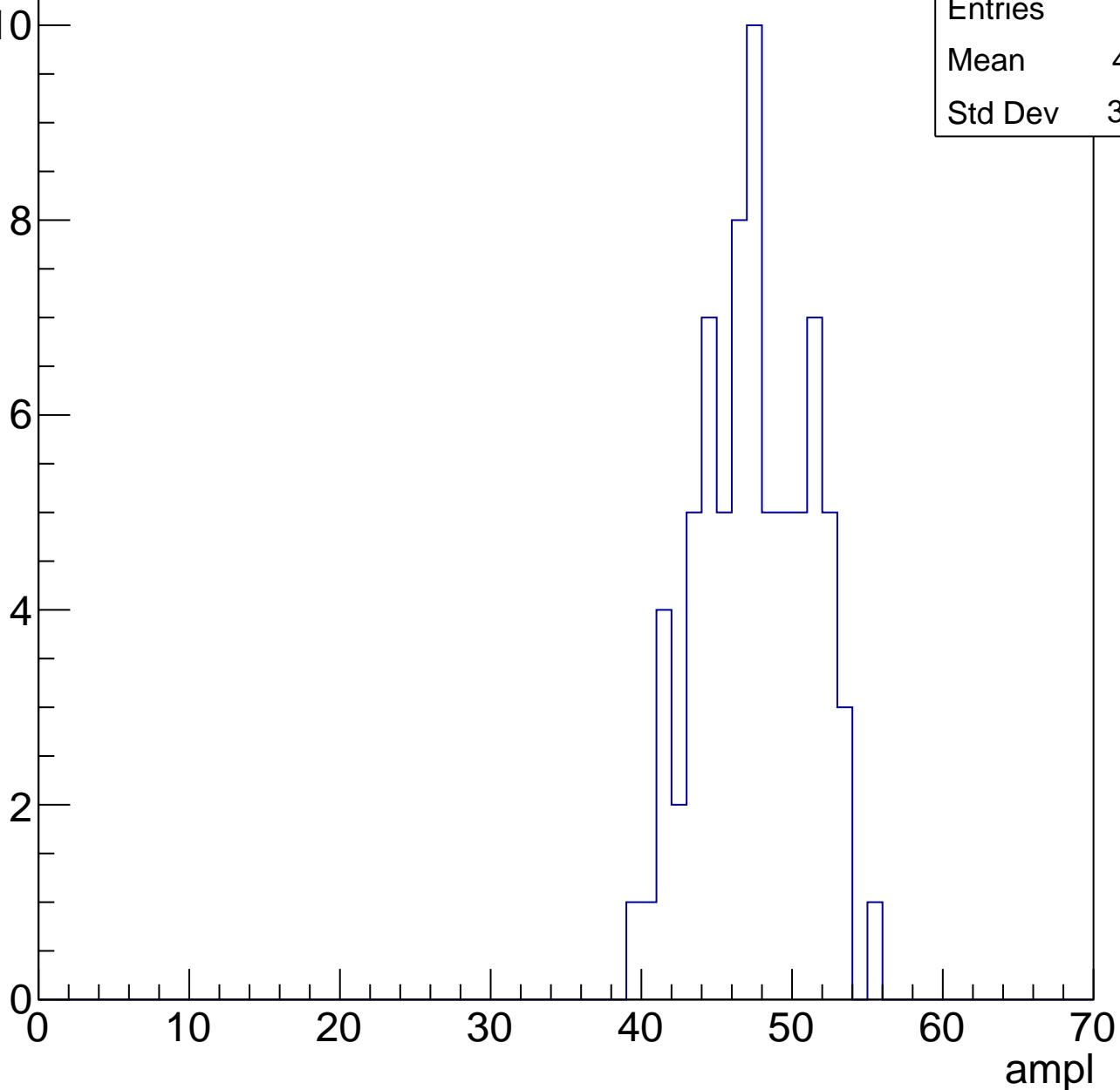


B1L100S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	47.01
Std Dev	3.596

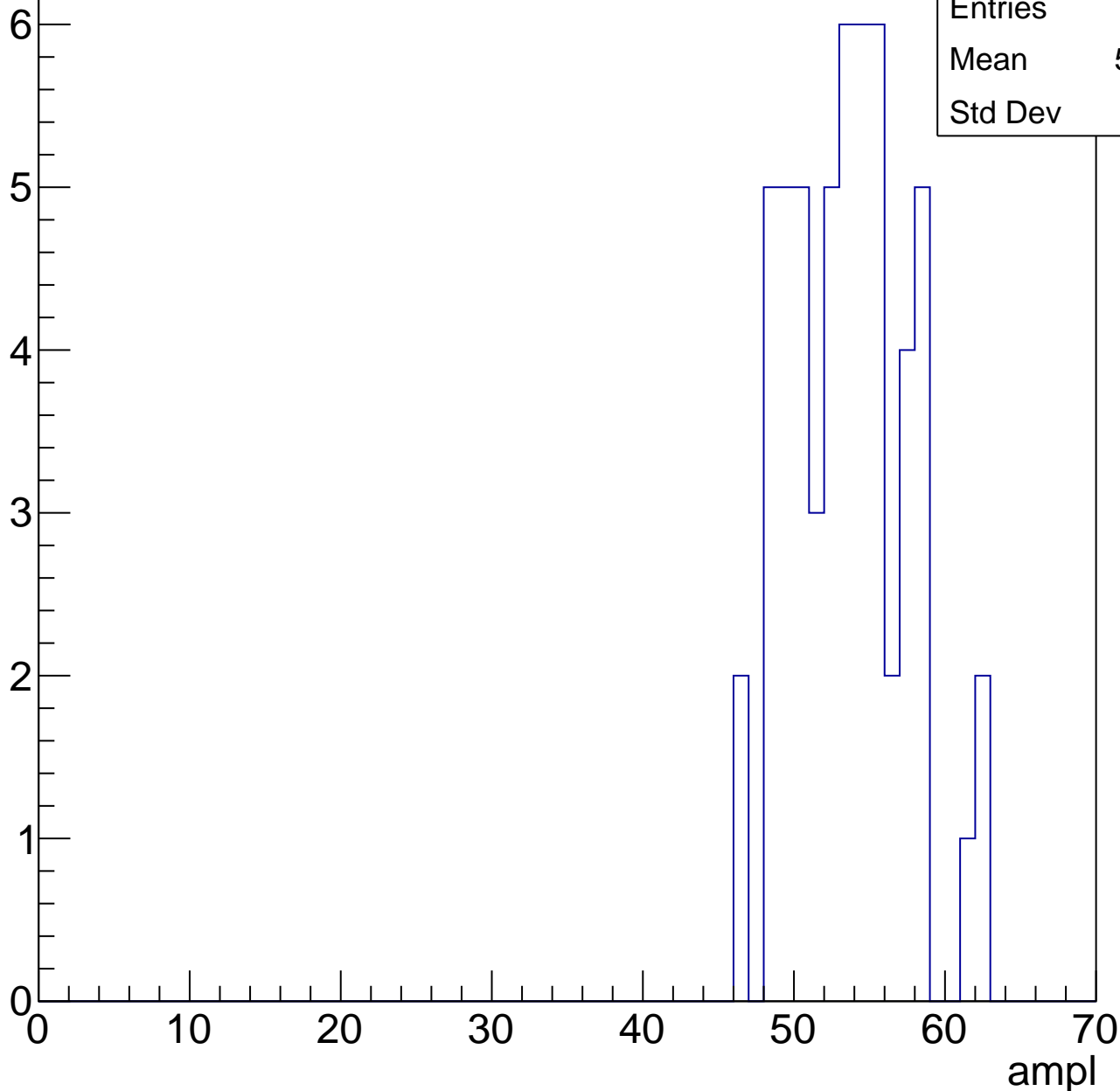


B1L100S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	53.11
Std Dev	3.81

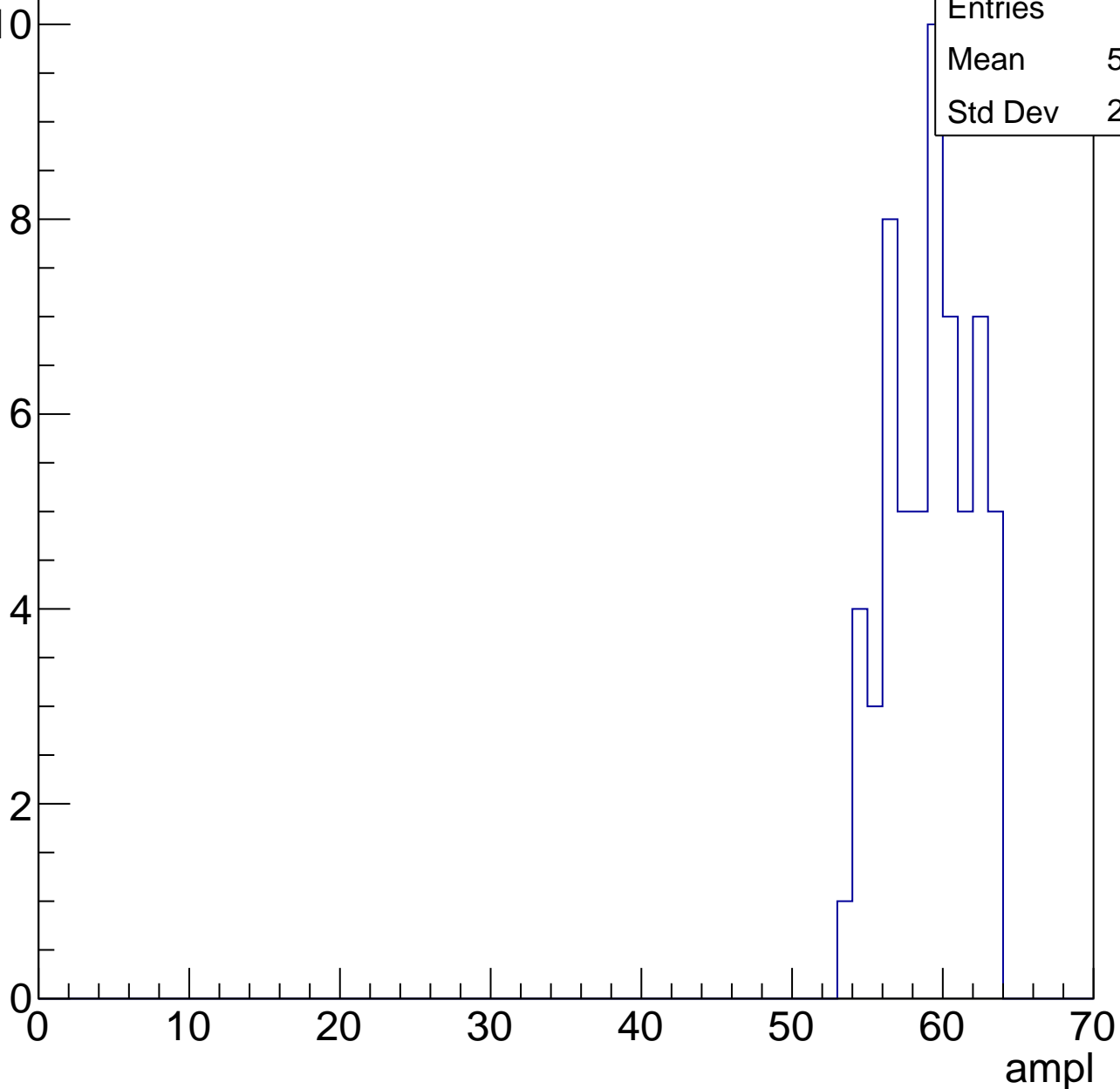


B1L100S, U14-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

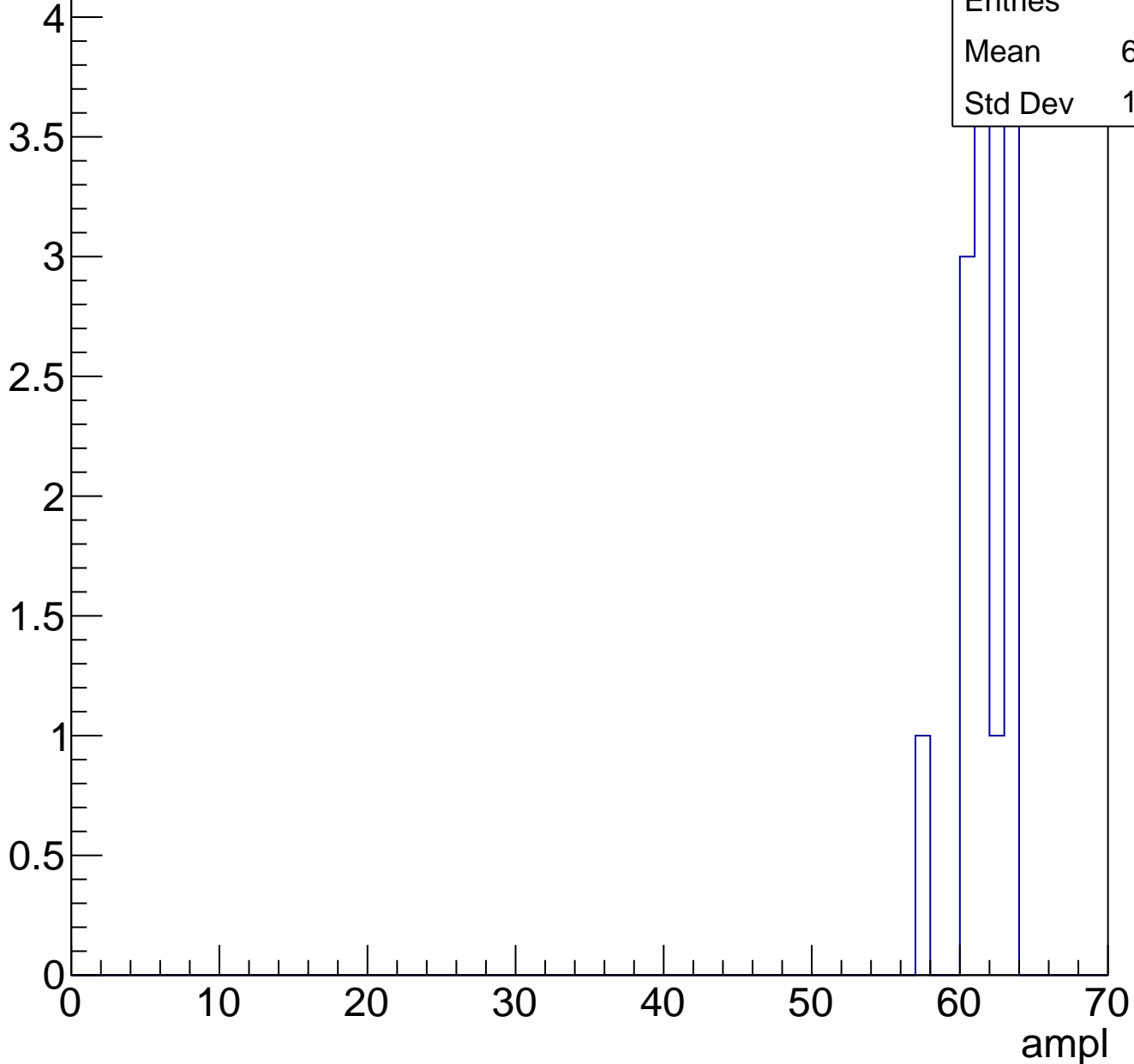
Entries	60
Mean	58.68
Std Dev	2.723



B1L100S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

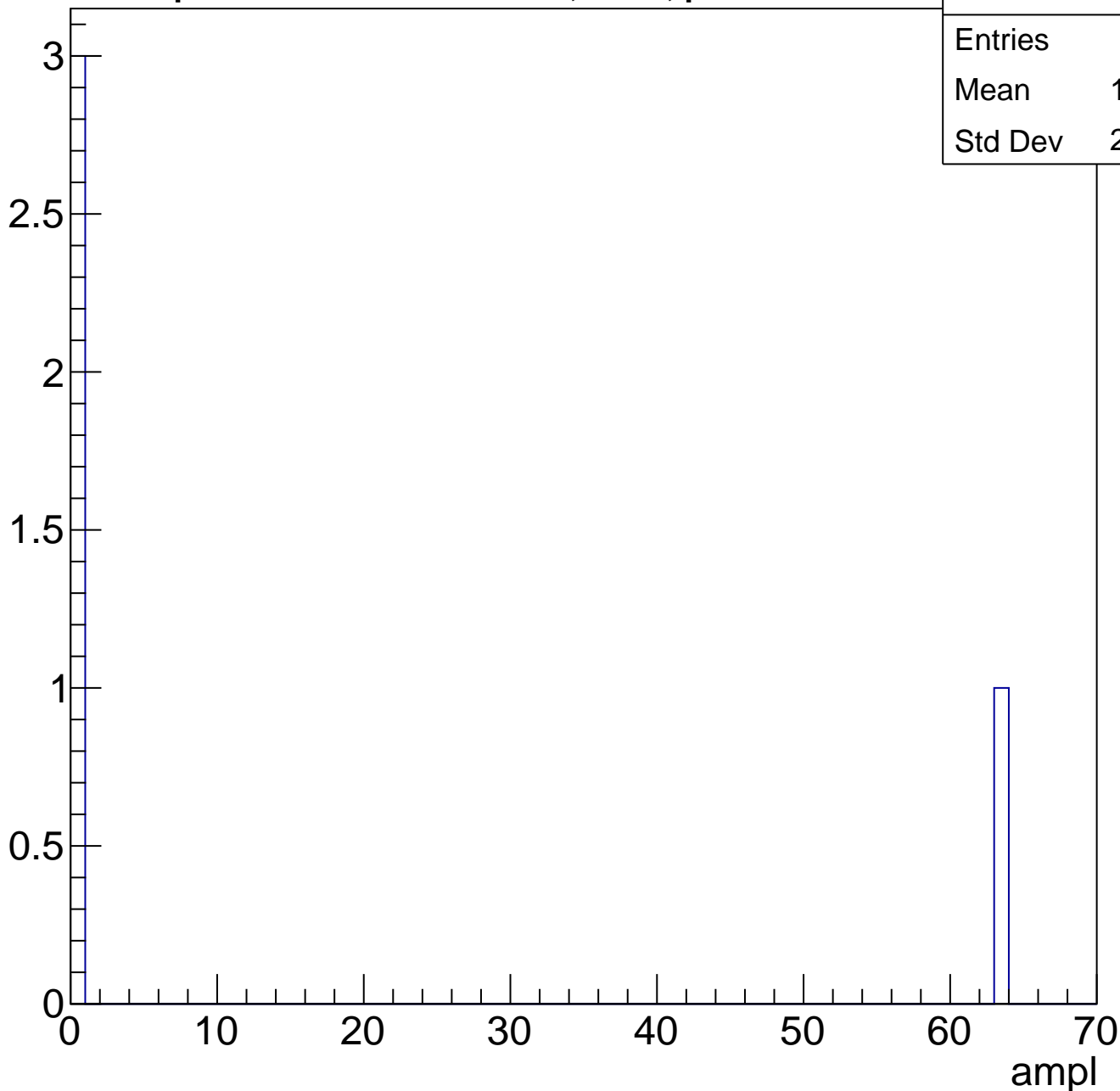
Entry



B1L100S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

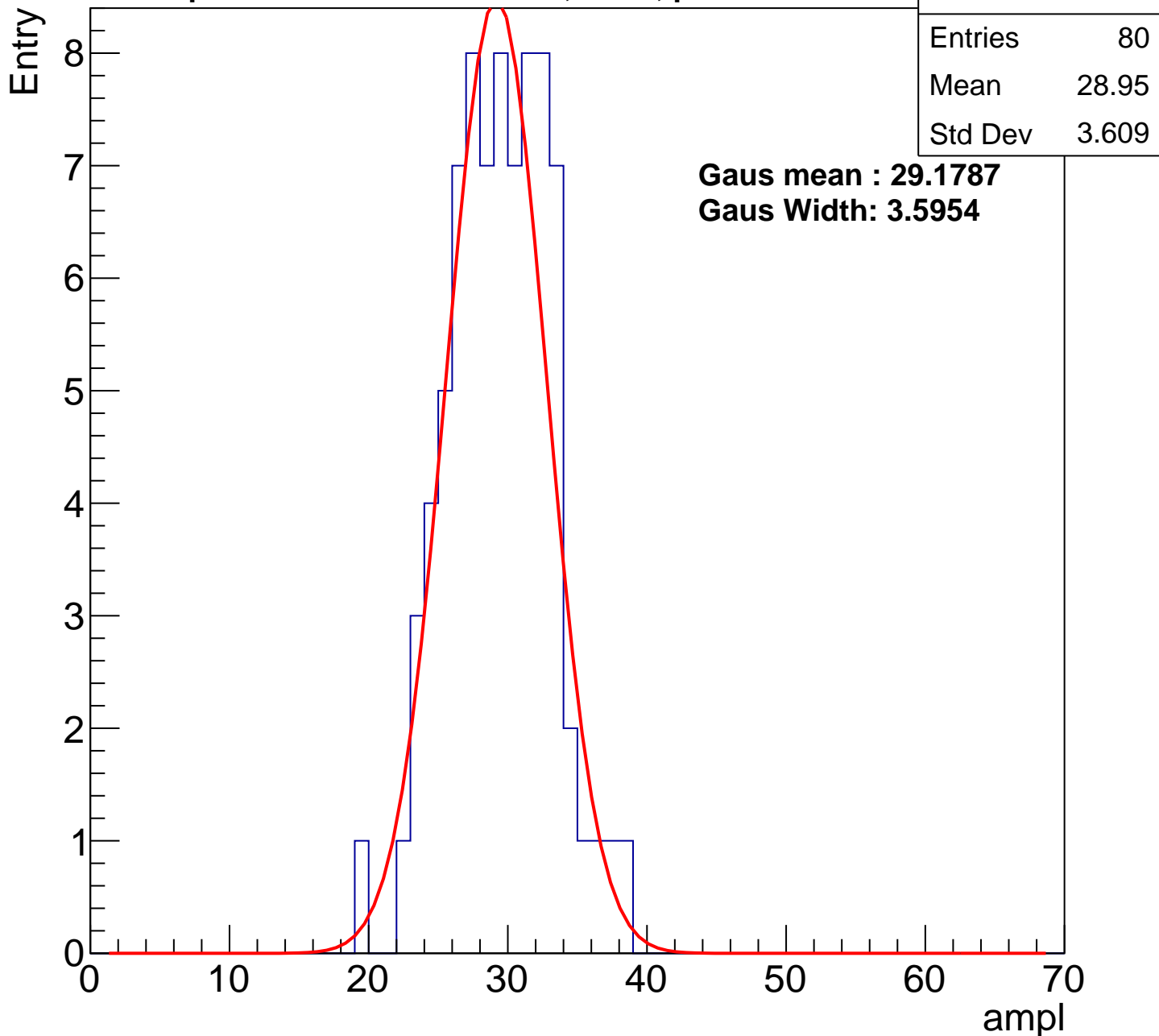
Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L100S, U14-ch51, adc0

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch51, adc1

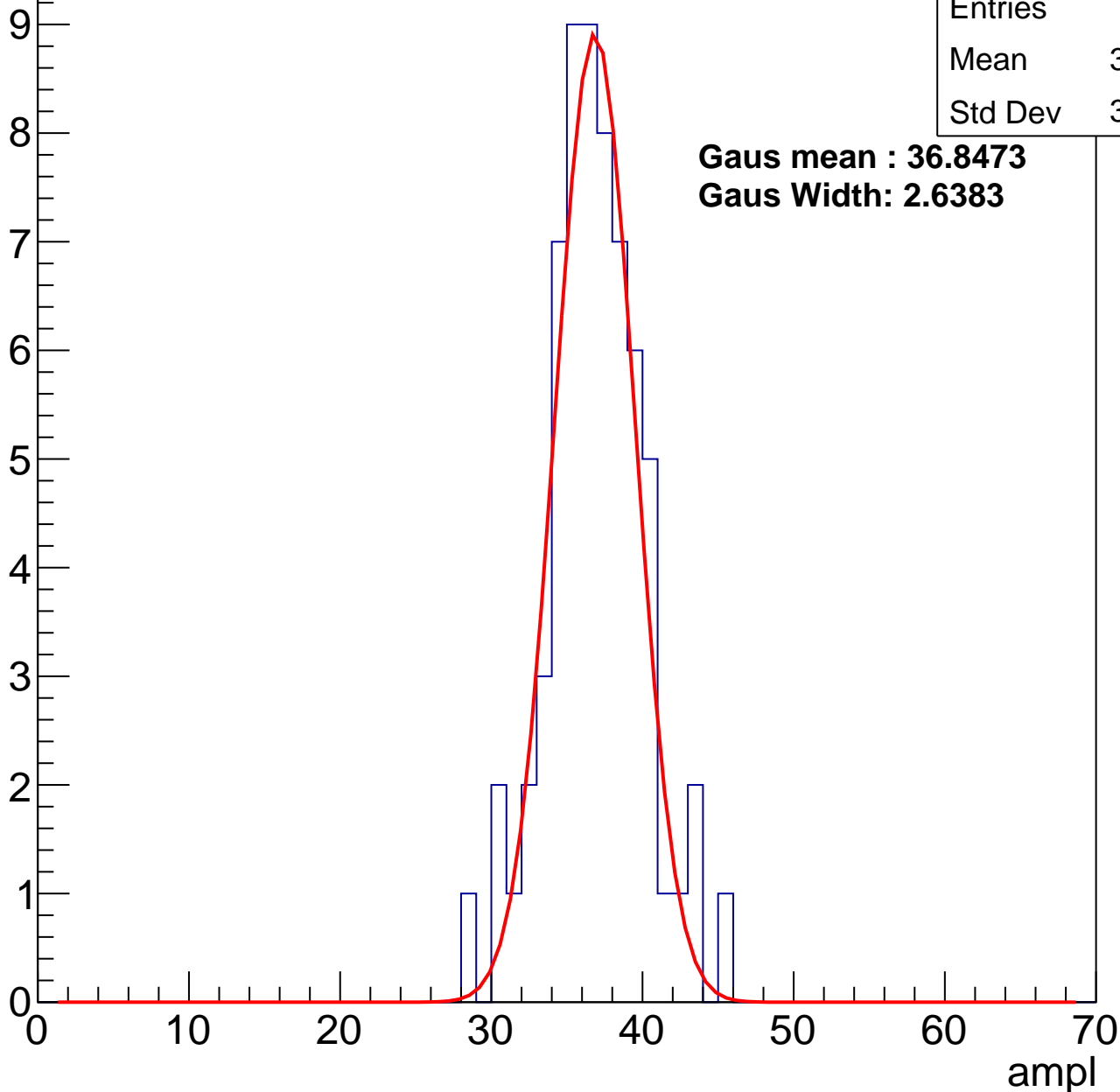
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.45
Std Dev	3.172

Gaus mean : 36.8473

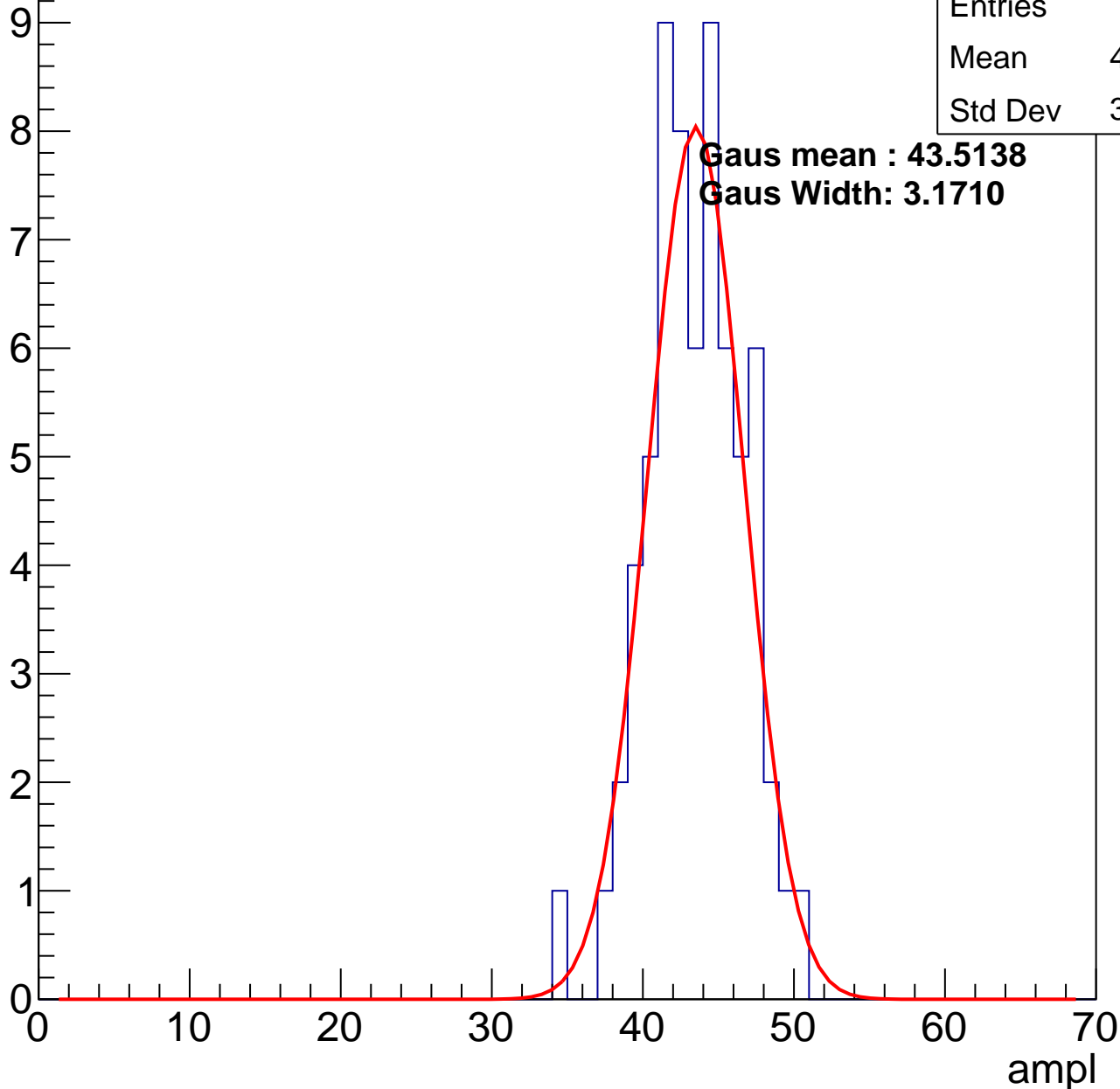
Gaus Width: 2.6383



B1L100S, U14-ch51, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

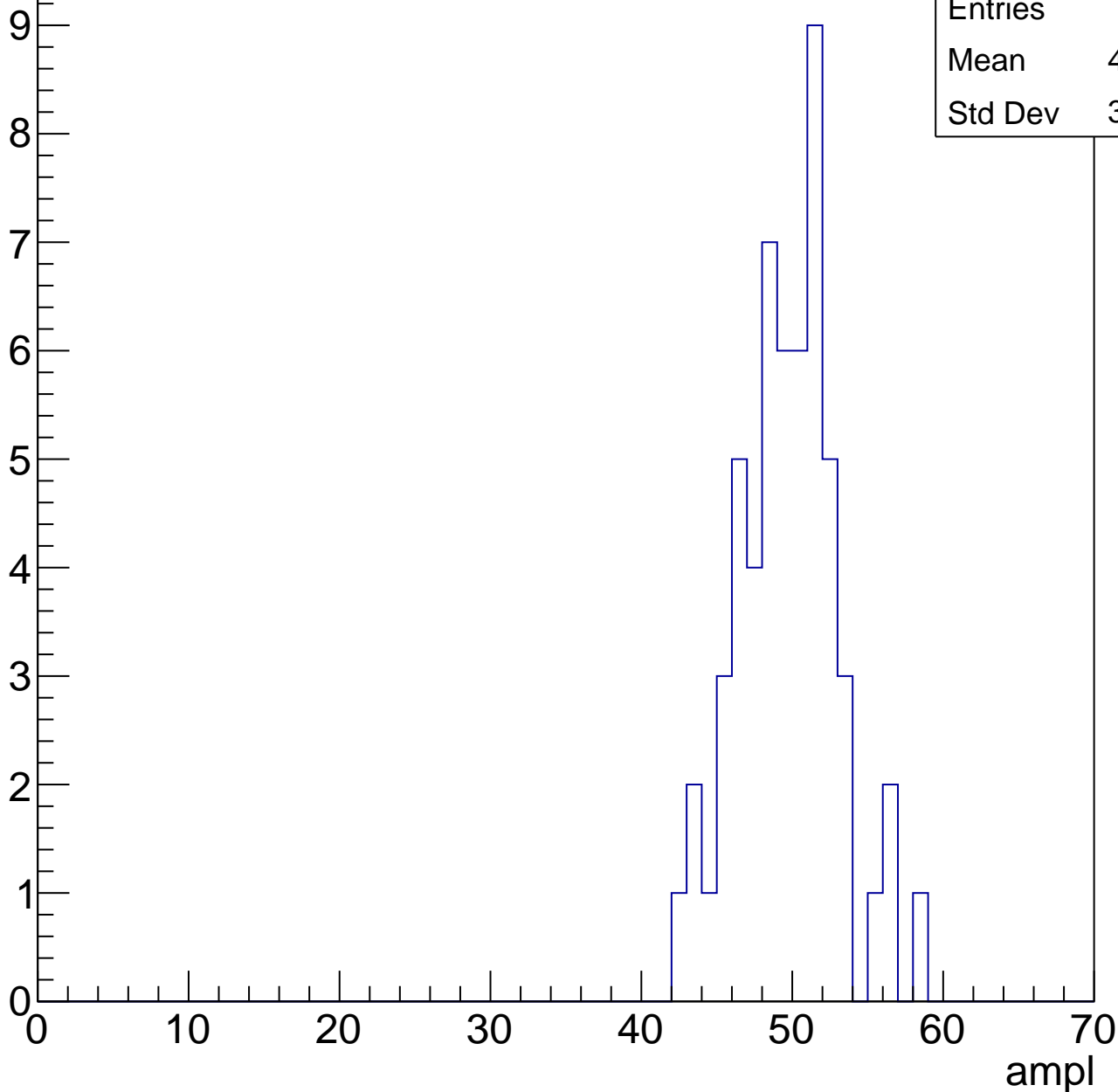


B1L100S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

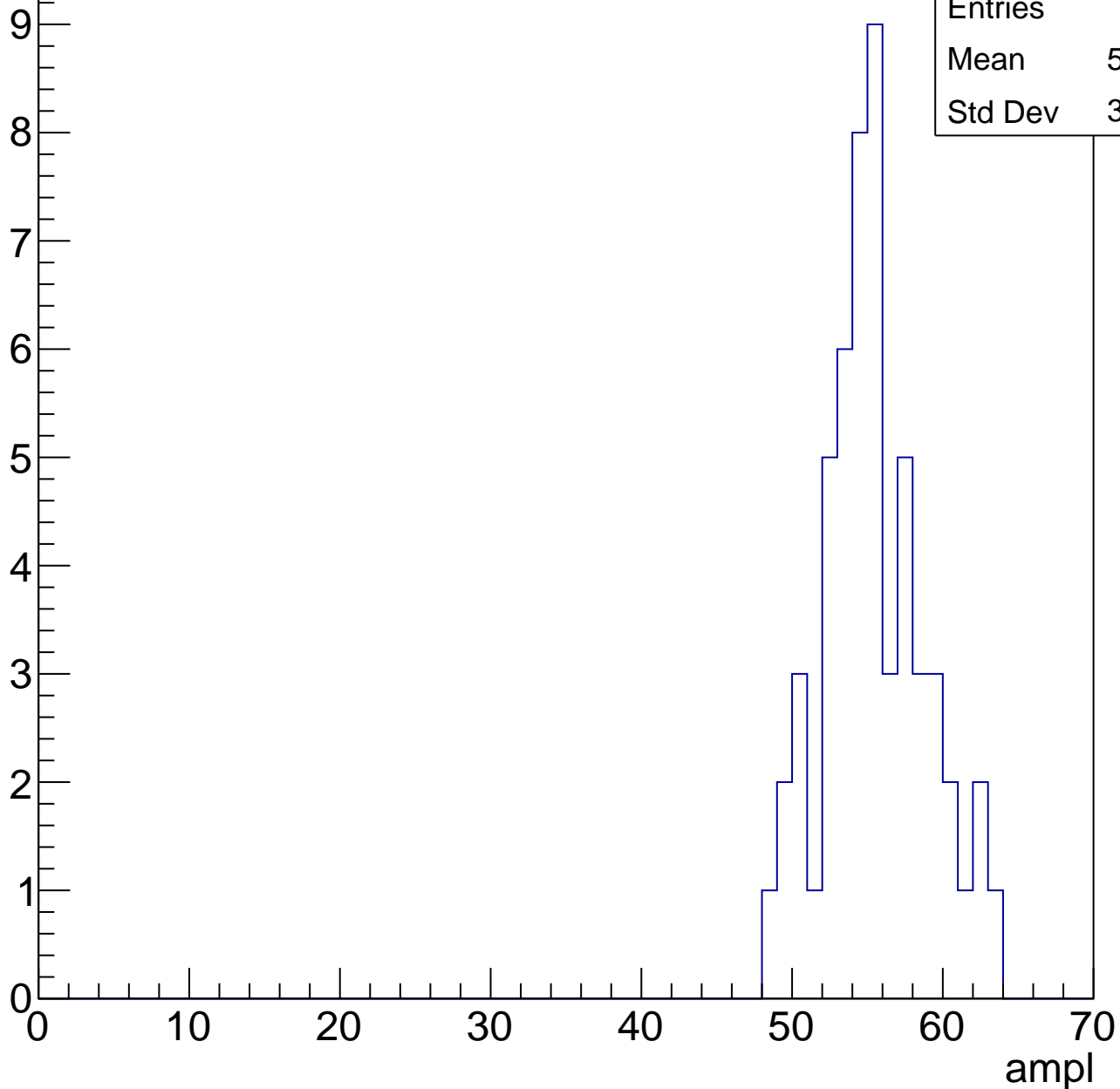
Entries	56
Mean	49.25
Std Dev	3.296



B1L100S, U14-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

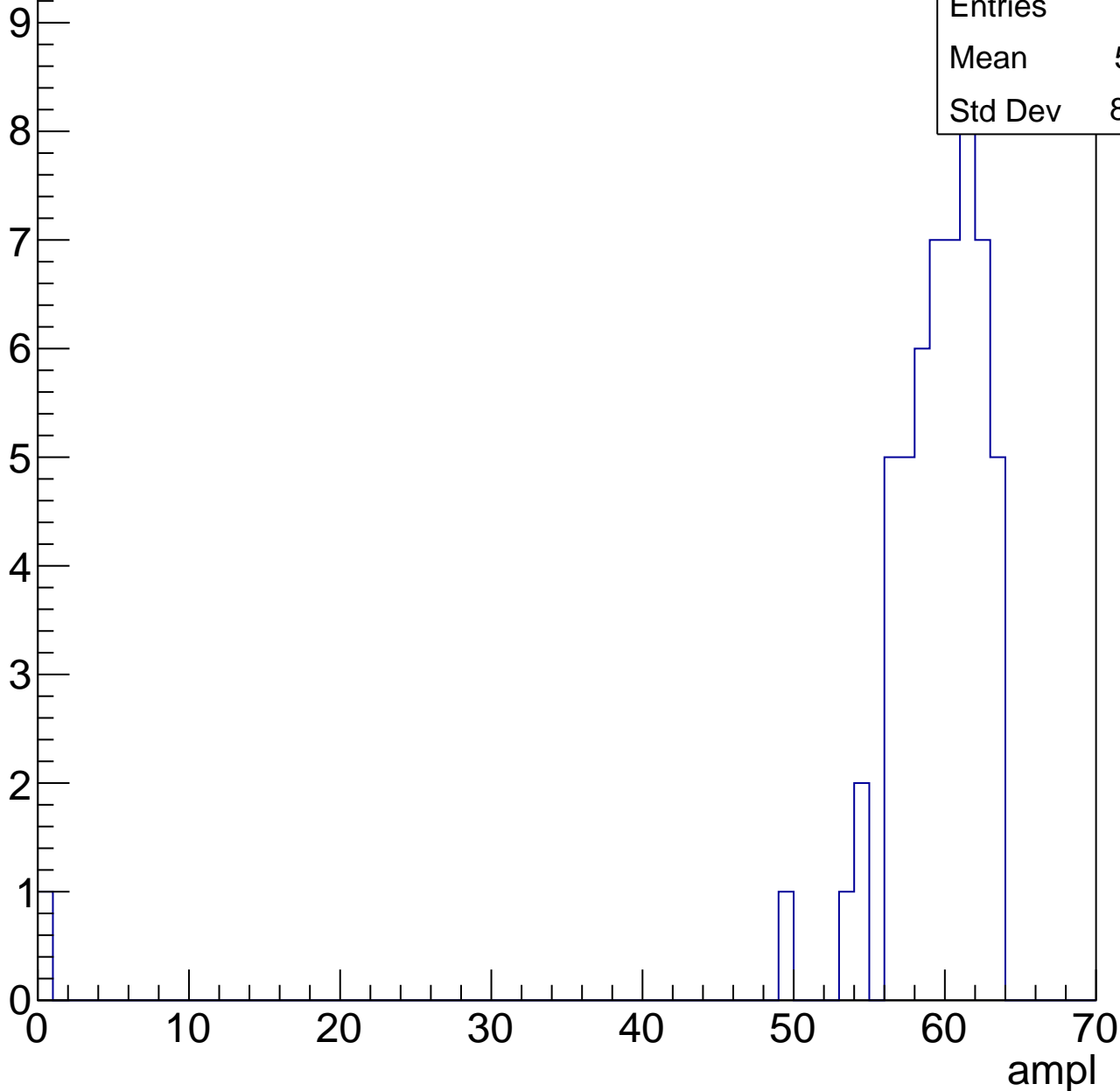


B1L100S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

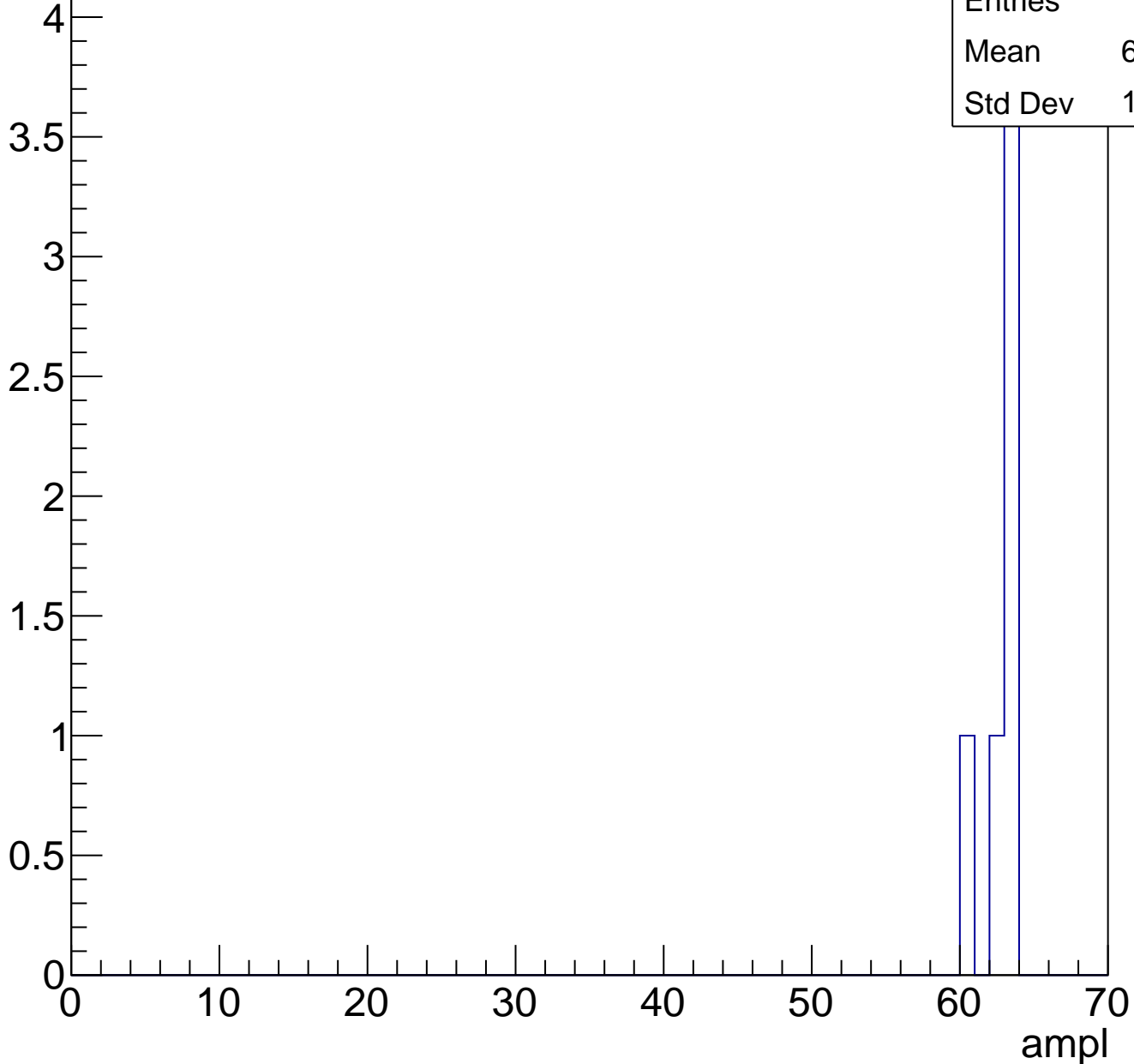
Entries	56
Mean	58.11
Std Dev	8.323



B1L100S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch52, adc0

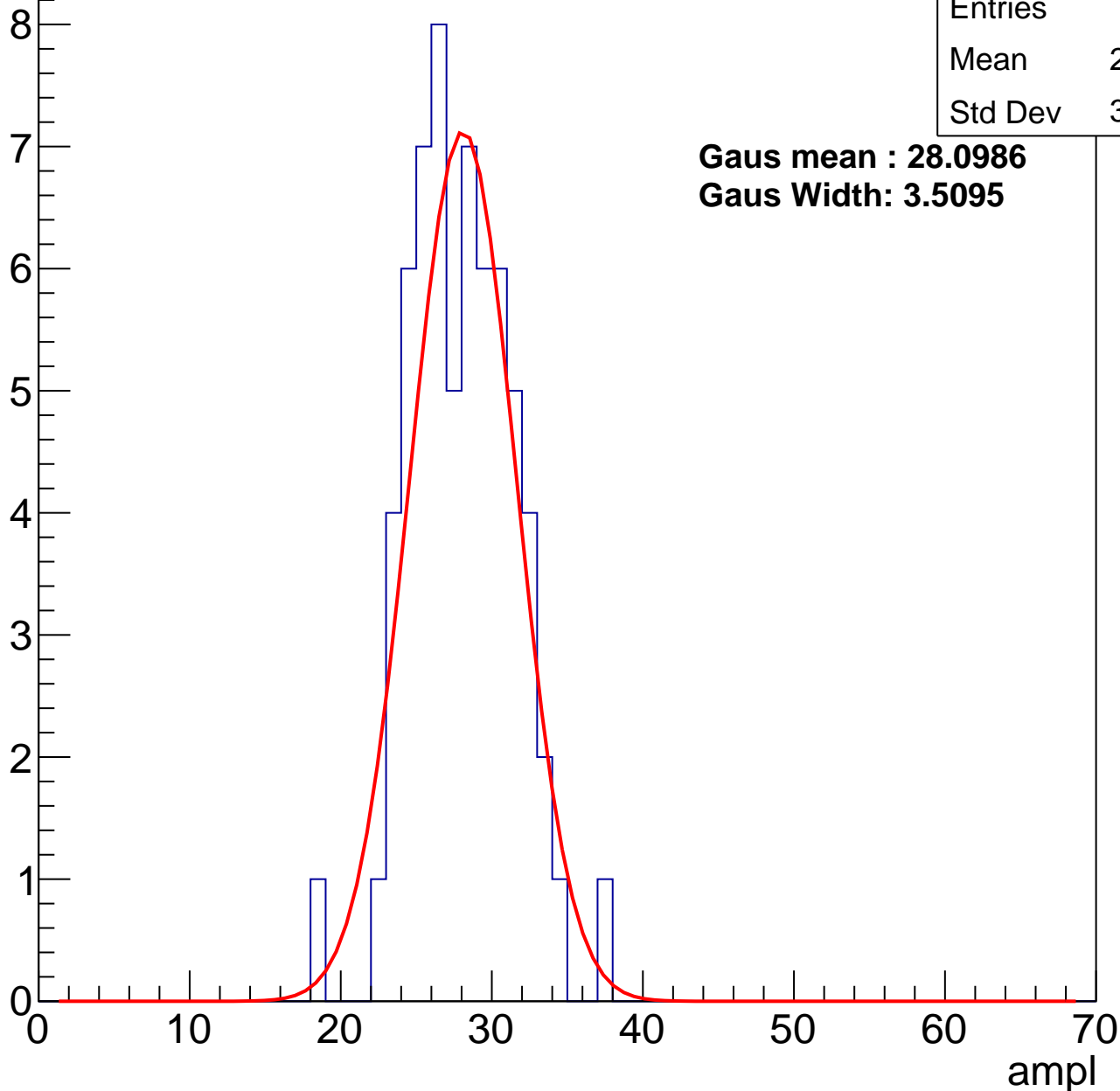
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	27.56
Std Dev	3.367

Gaus mean : 28.0986

Gaus Width: 3.5095



B1L100S, U14-ch52, adc1

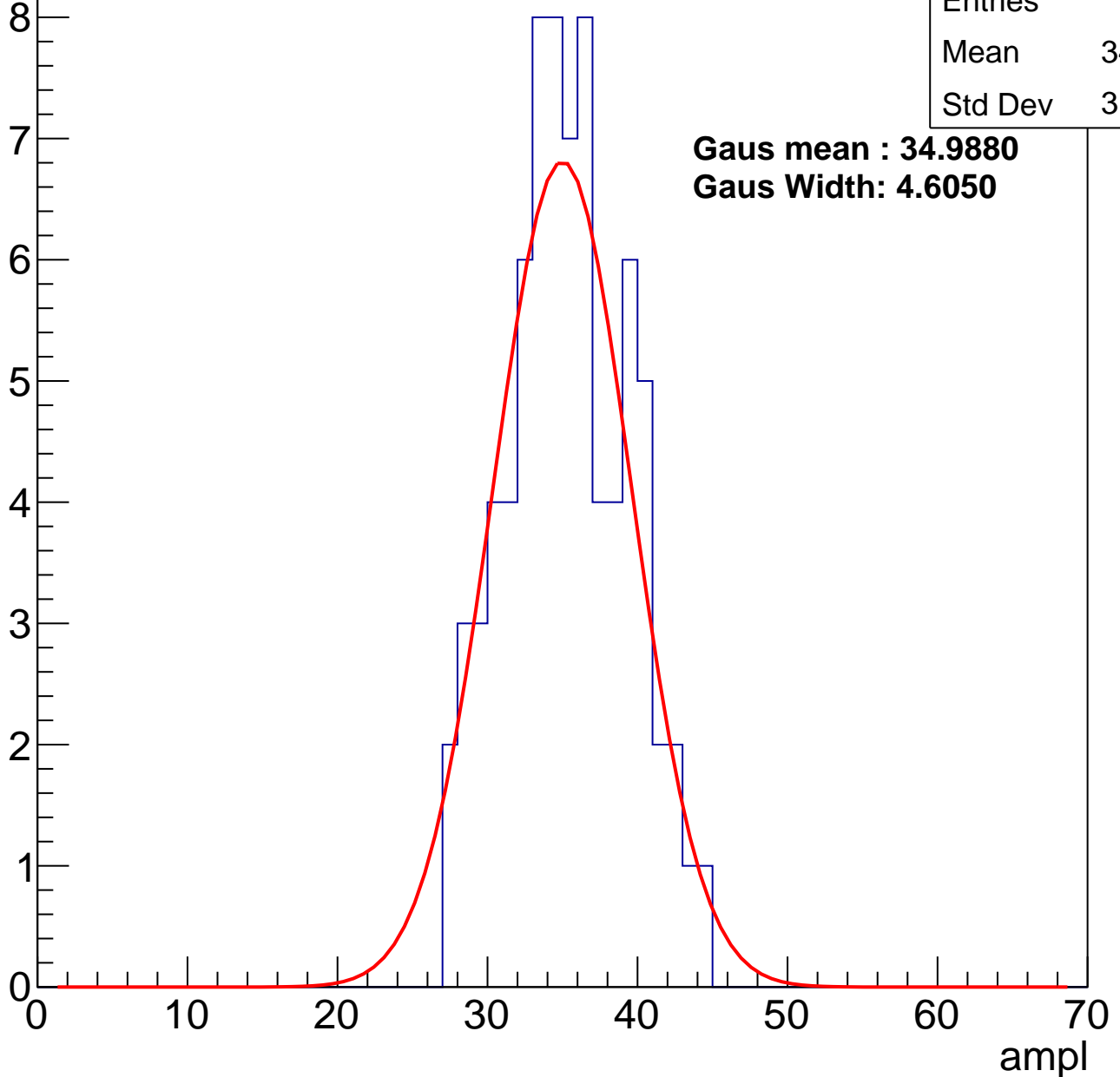
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	34.83
Std Dev	3.979

Gaus mean : 34.9880

Gaus Width: 4.6050



B1L100S, U14-ch52, adc2

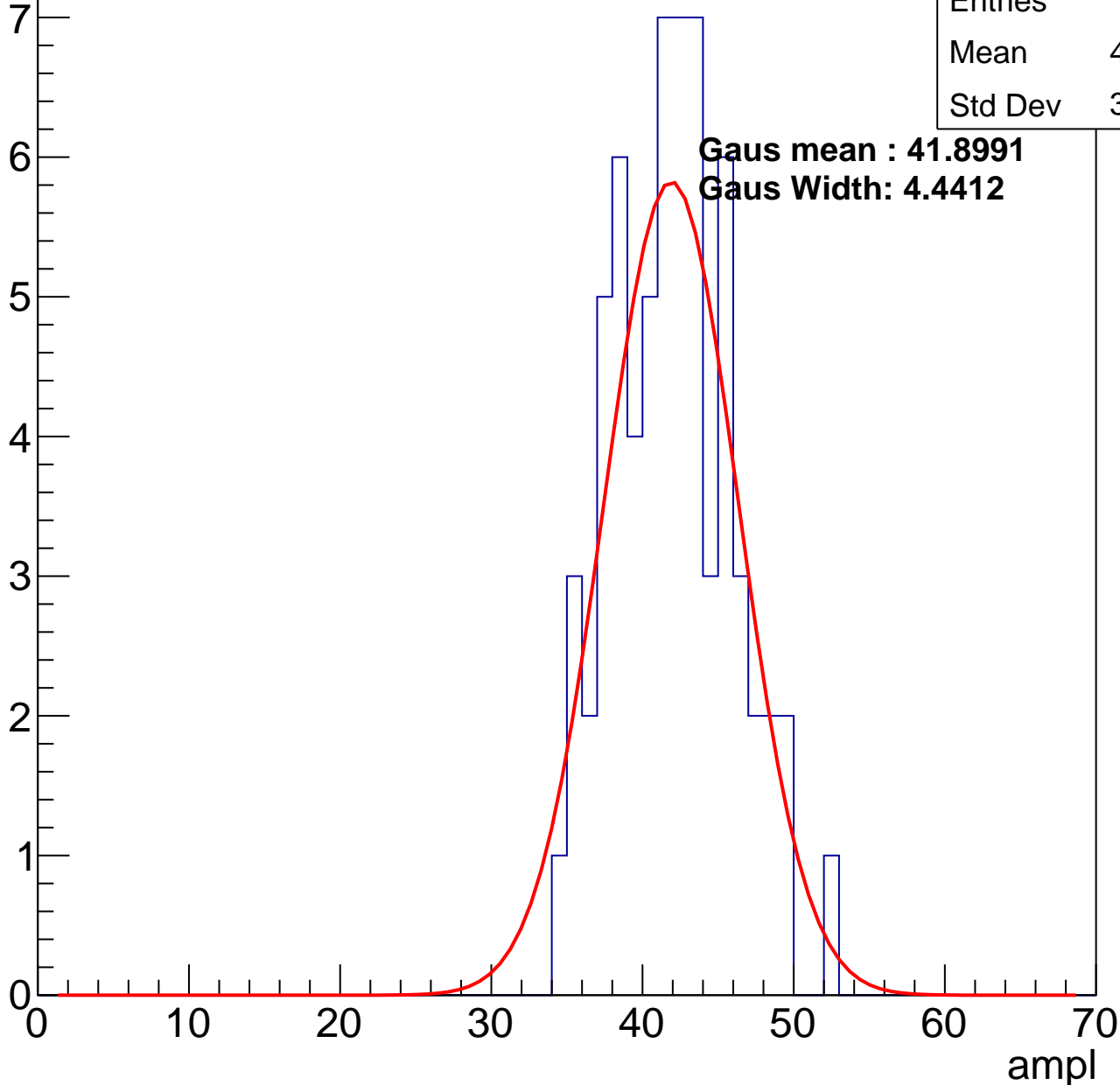
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.55
Std Dev	3.882

Gaus mean : 41.8991

Gaus Width: 4.4412

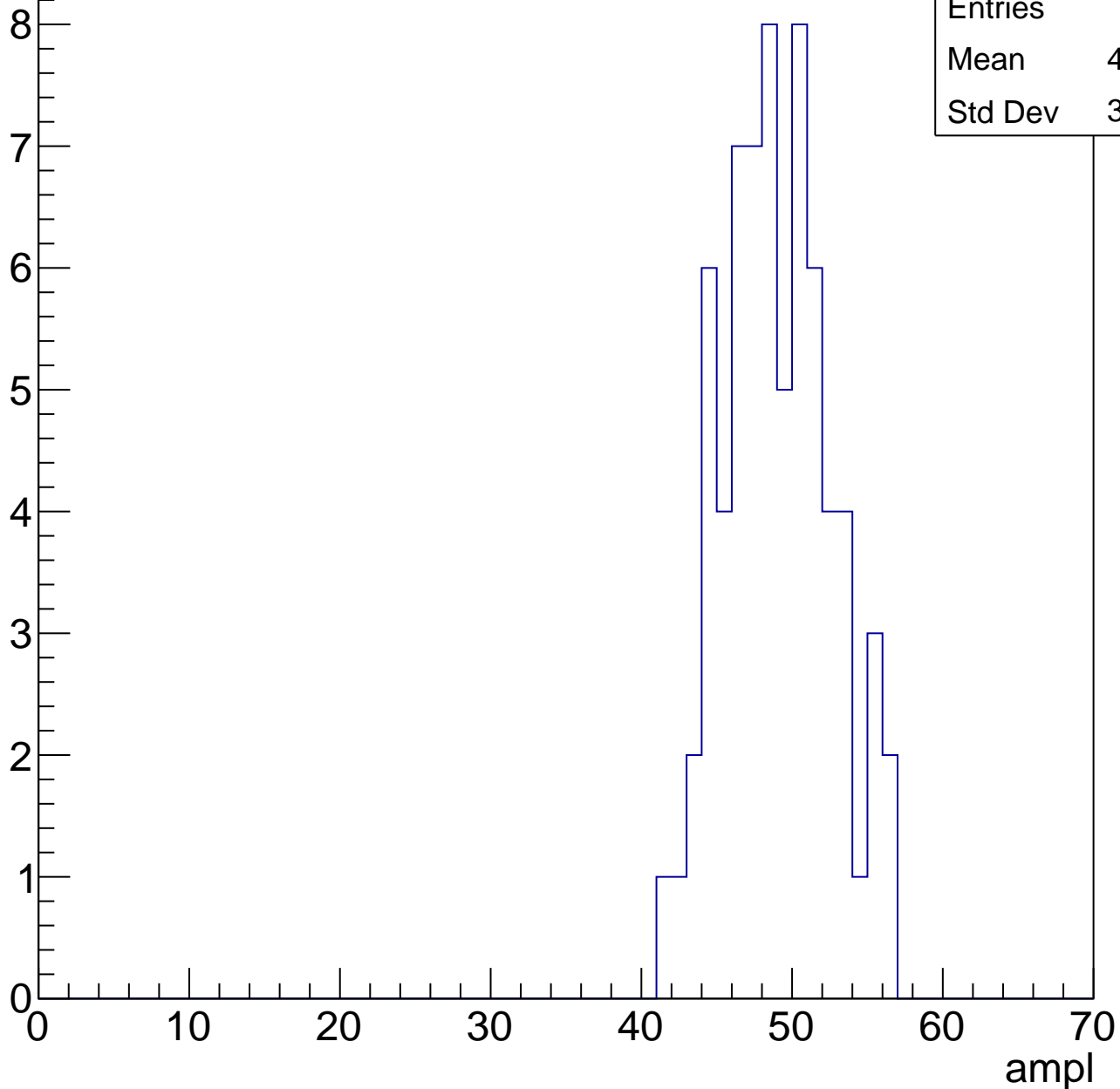


B1L100S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	48.55
Std Dev	3.504

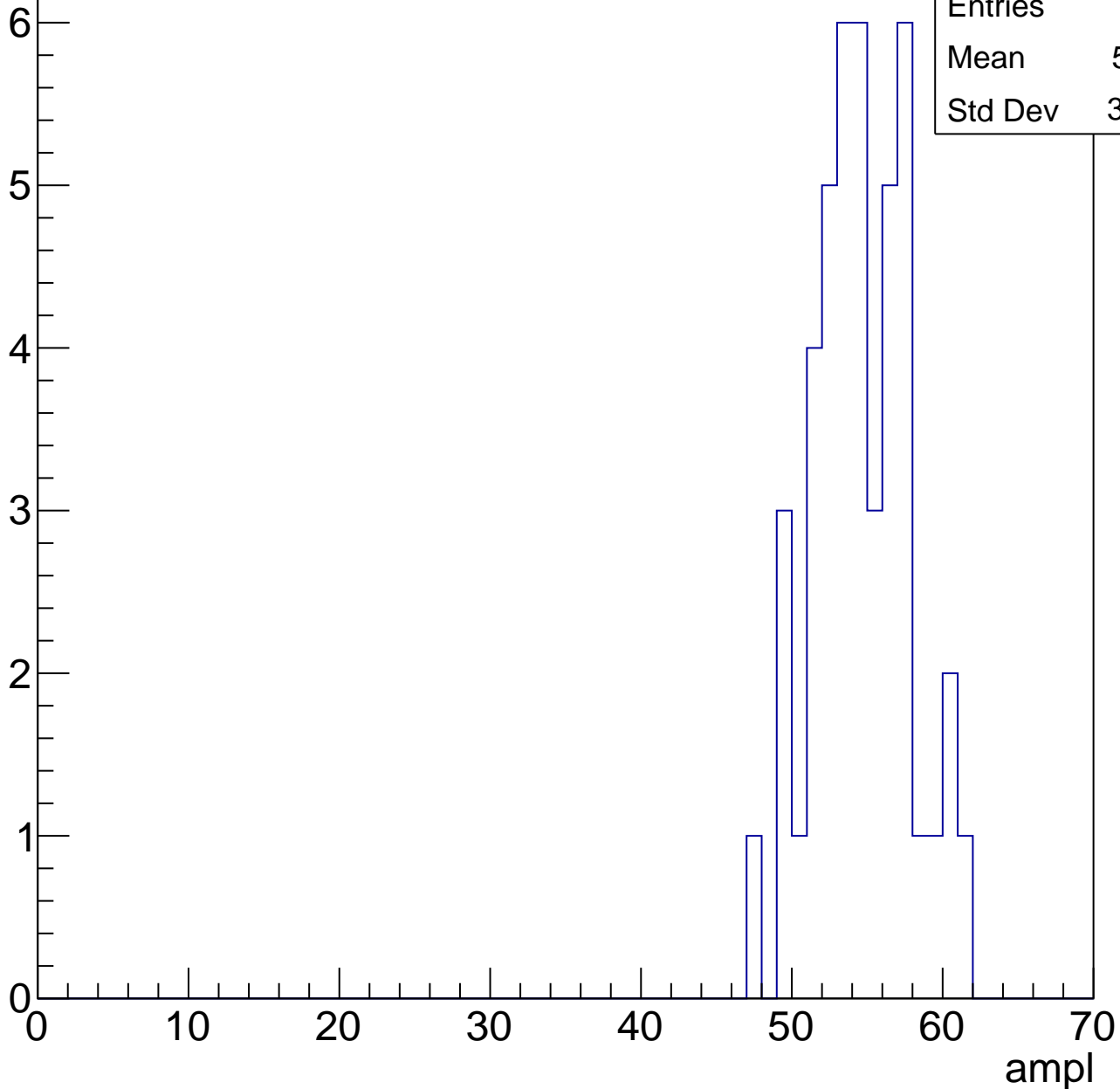


B1L100S, U14-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

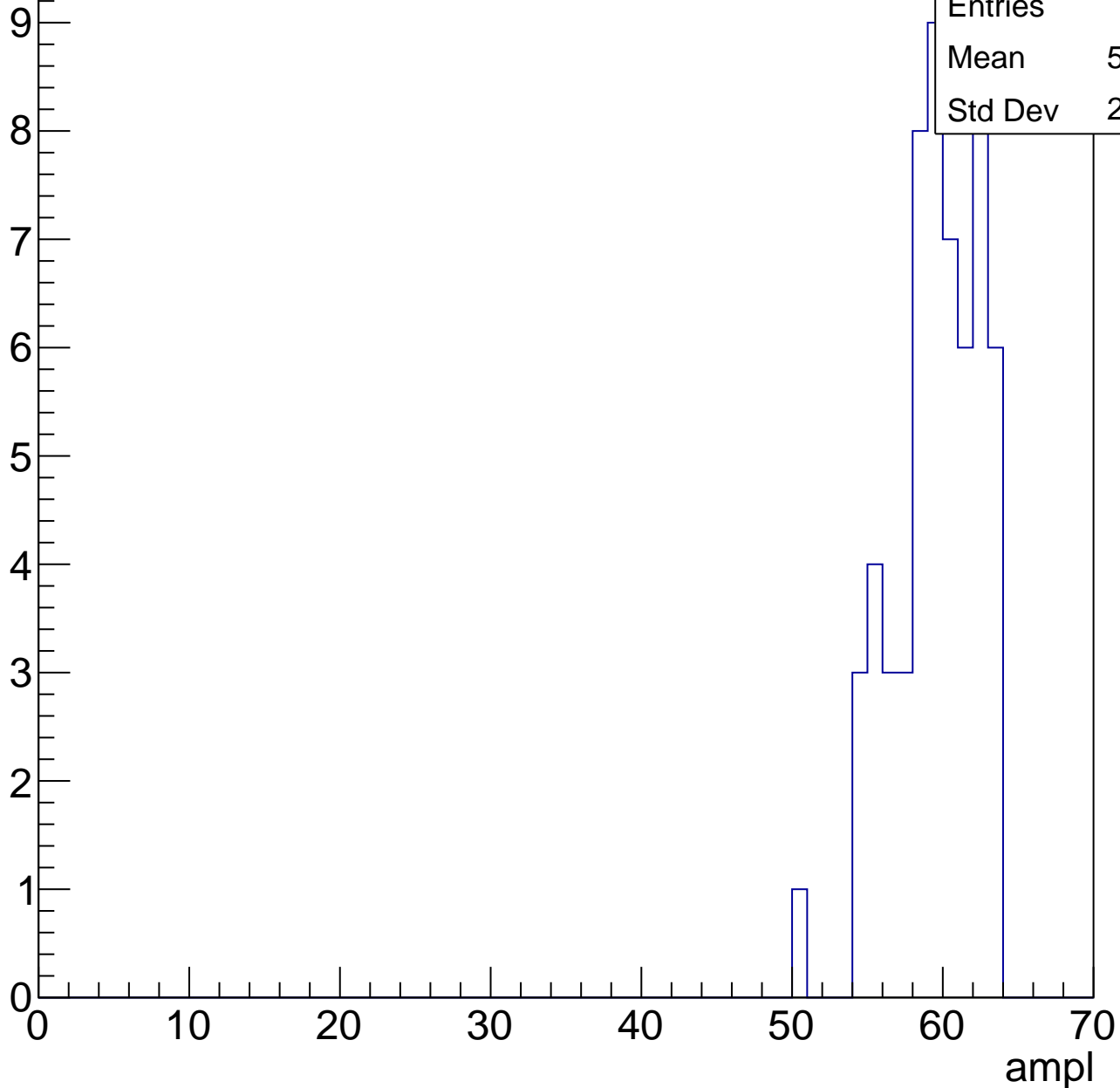
Entries	45
Mean	54.11
Std Dev	3.129



B1L100S, U14-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

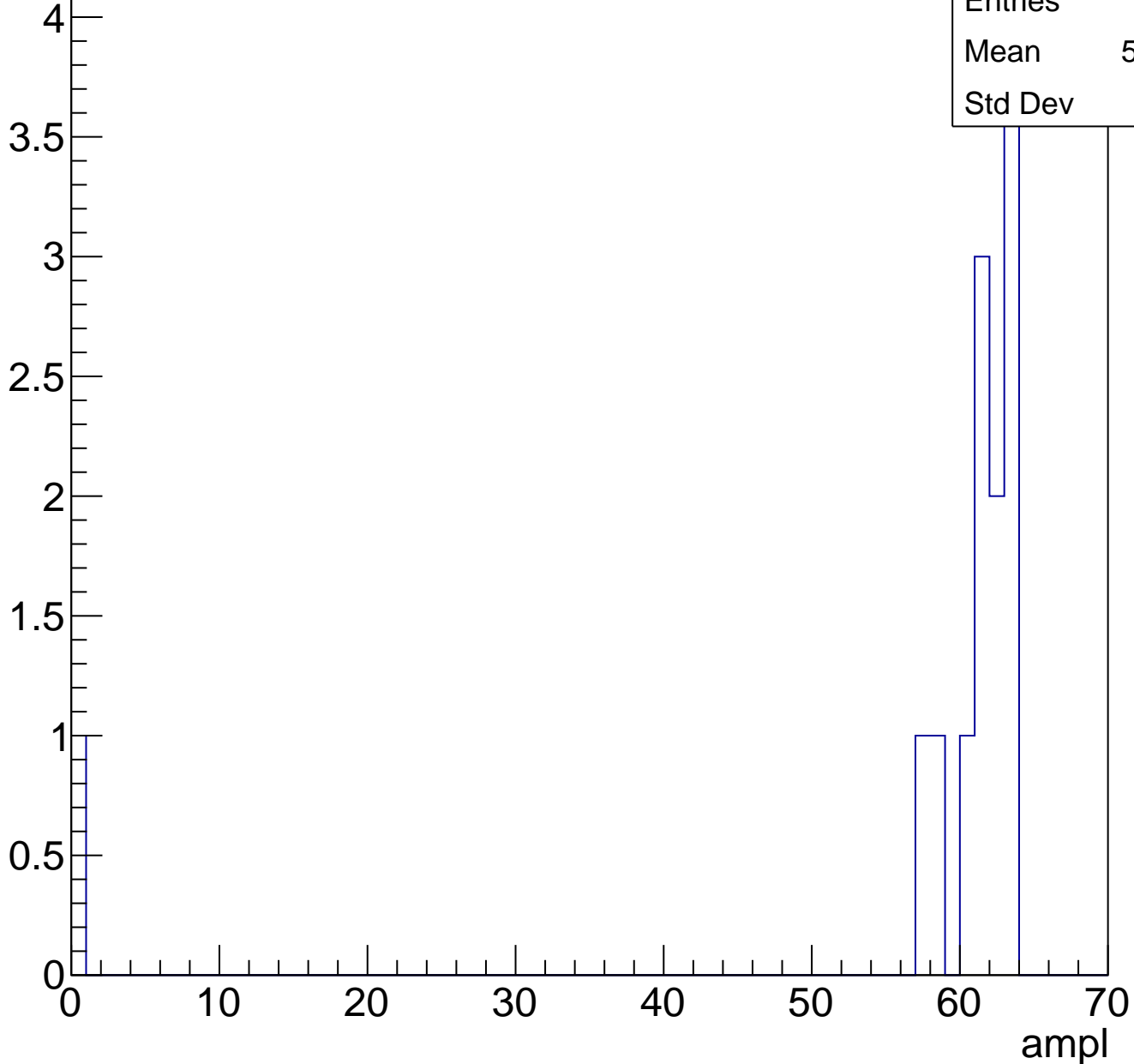
Entry



B1L100S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U14-ch53, adc0

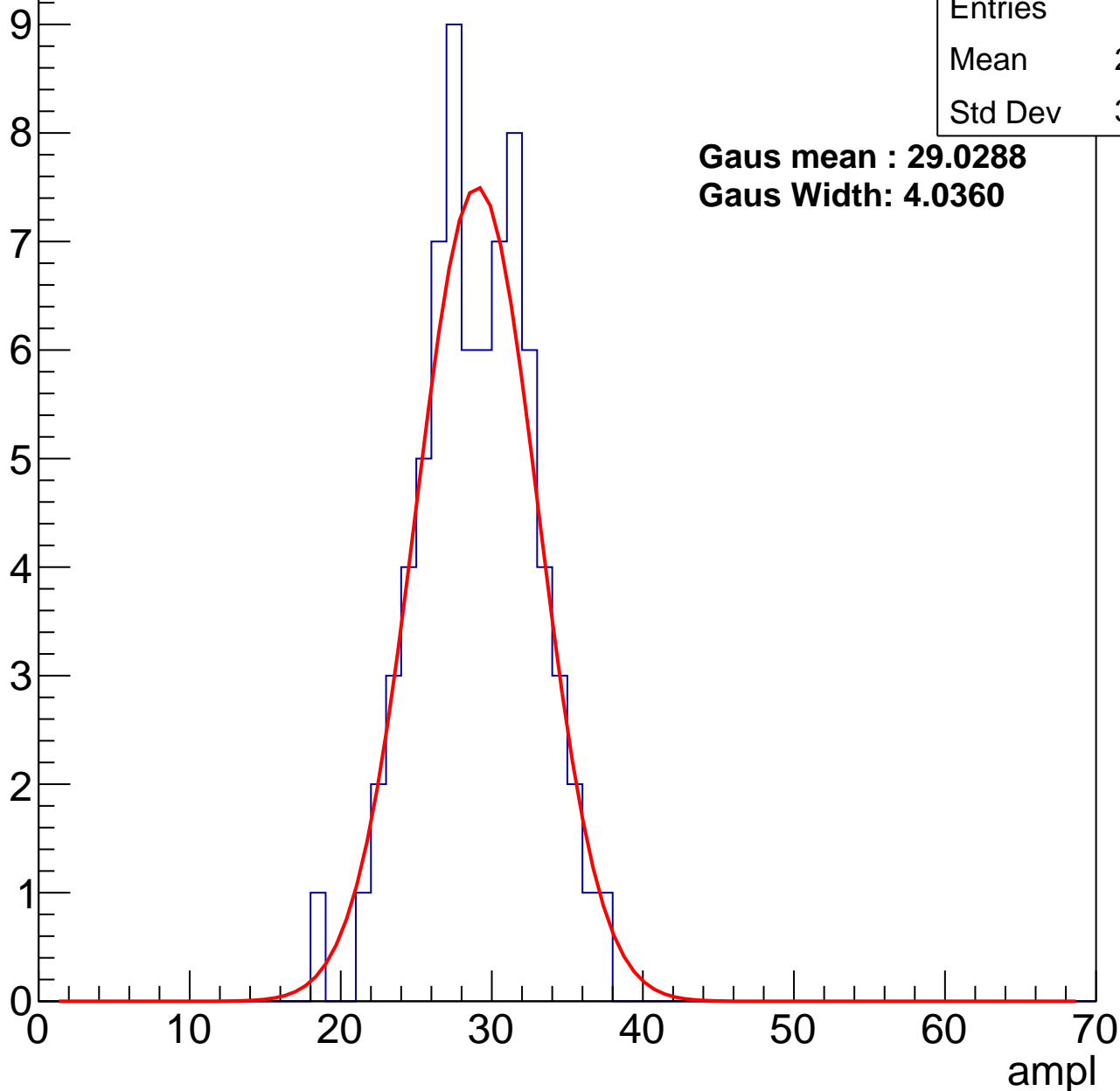
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	28.51
Std Dev	3.761

Gaus mean : 29.0288

Gaus Width: 4.0360



B1L100S, U14-ch53, adc1

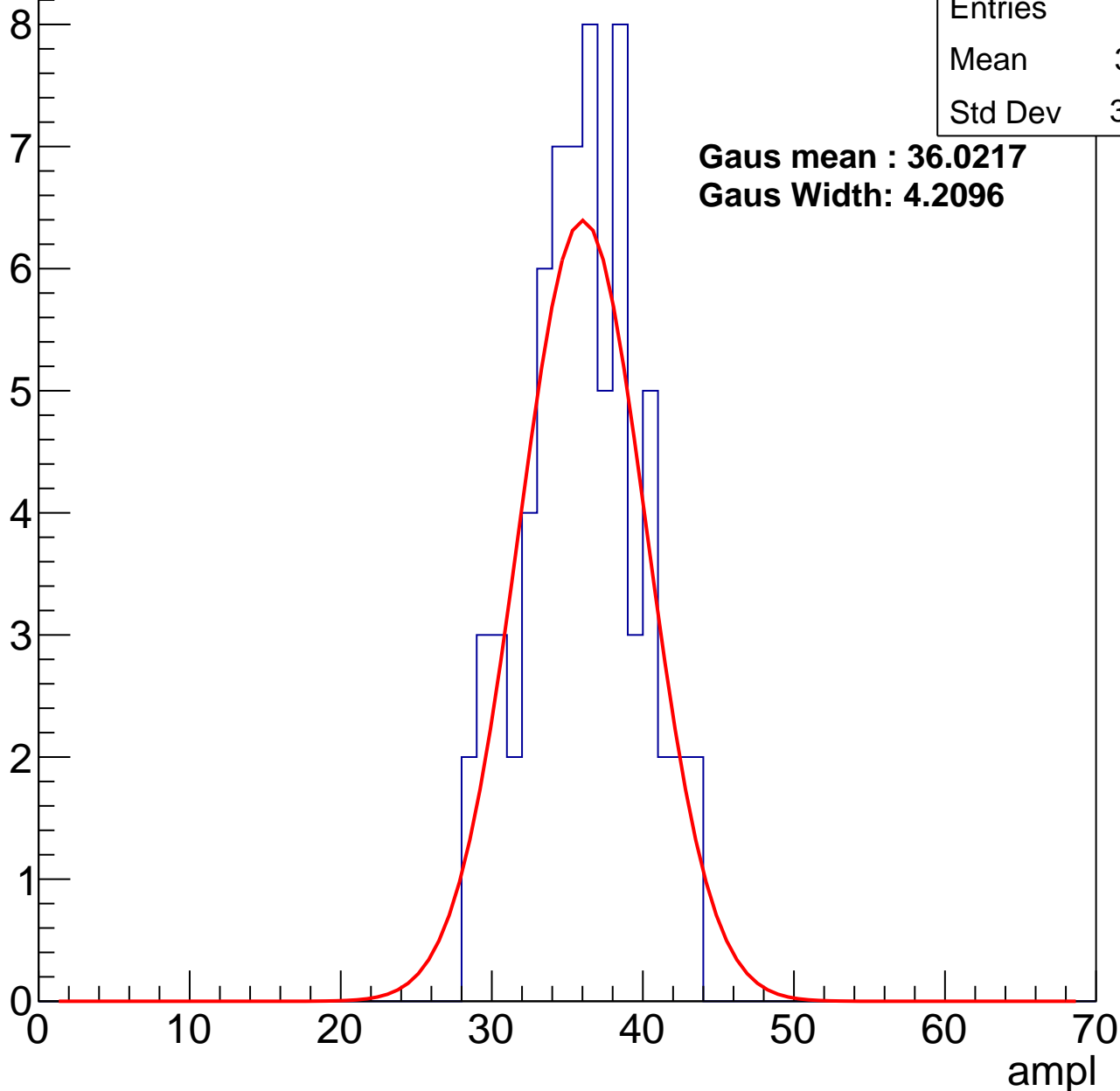
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.51
Std Dev	3.678

Gaus mean : 36.0217

Gaus Width: 4.2096



B1L100S, U14-ch53, adc2

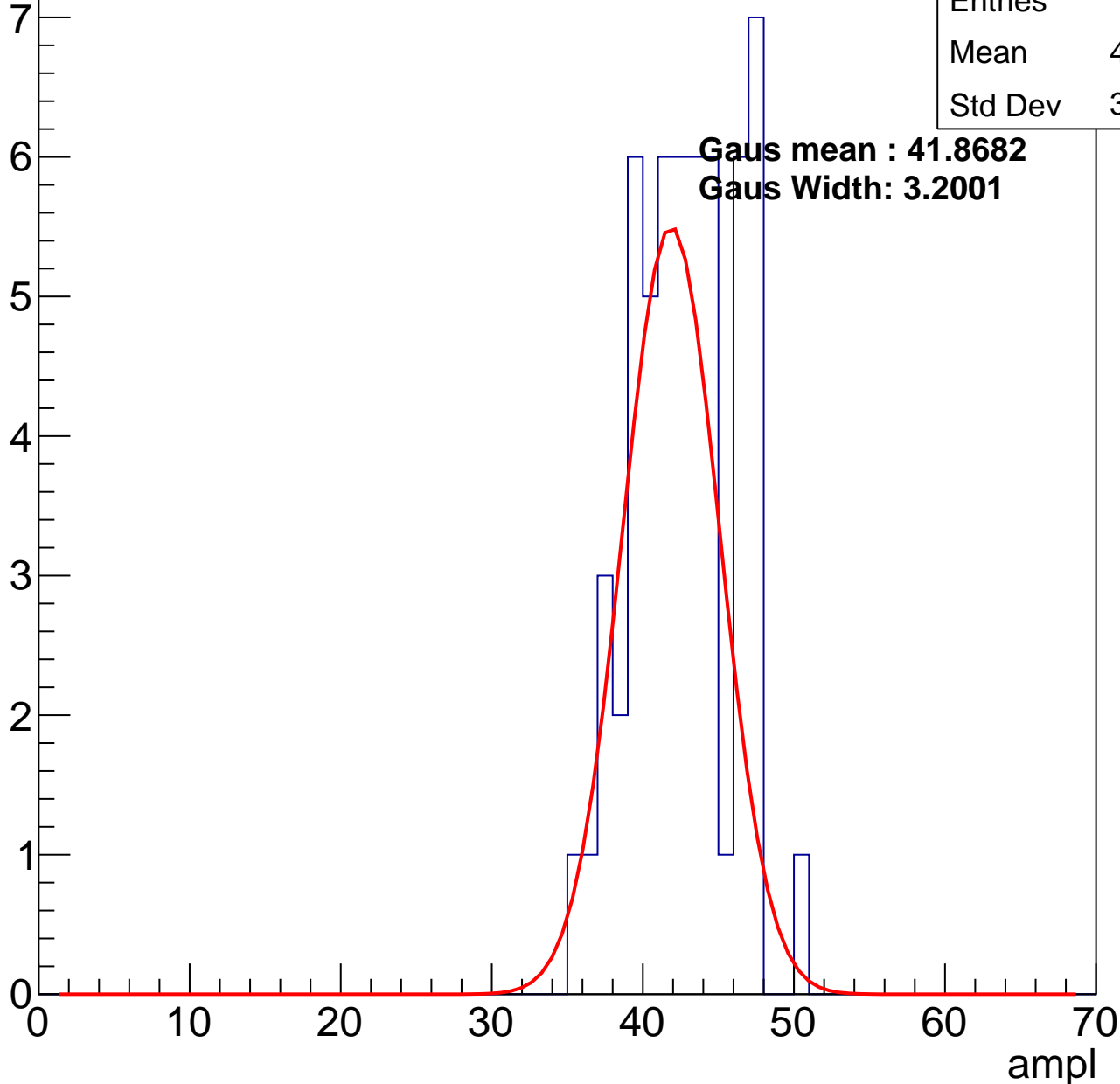
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.32
Std Dev	3.352

Gaus mean : 41.8682

Gaus Width: 3.2001

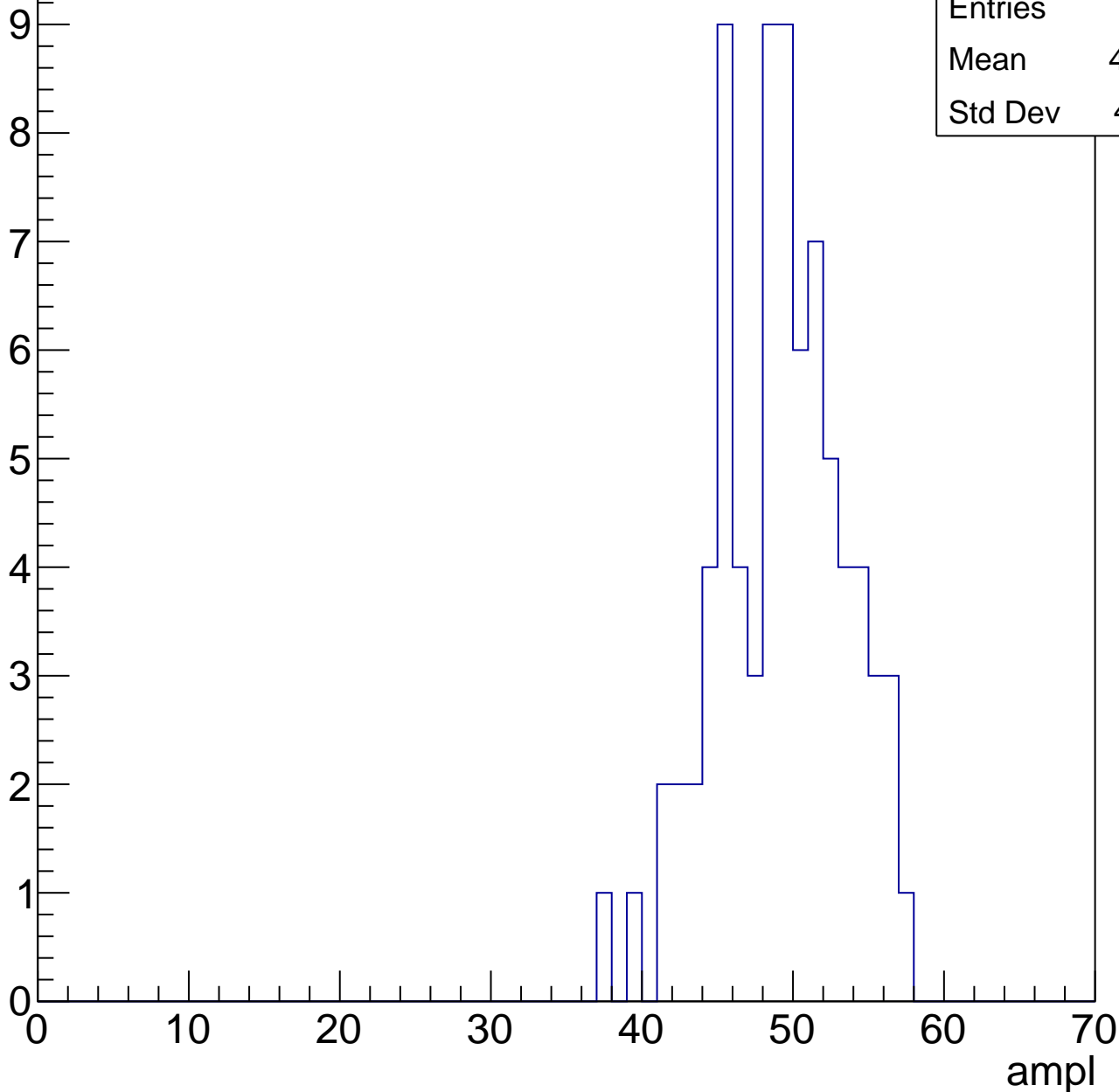


B1L100S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	48.63
Std Dev	4.201

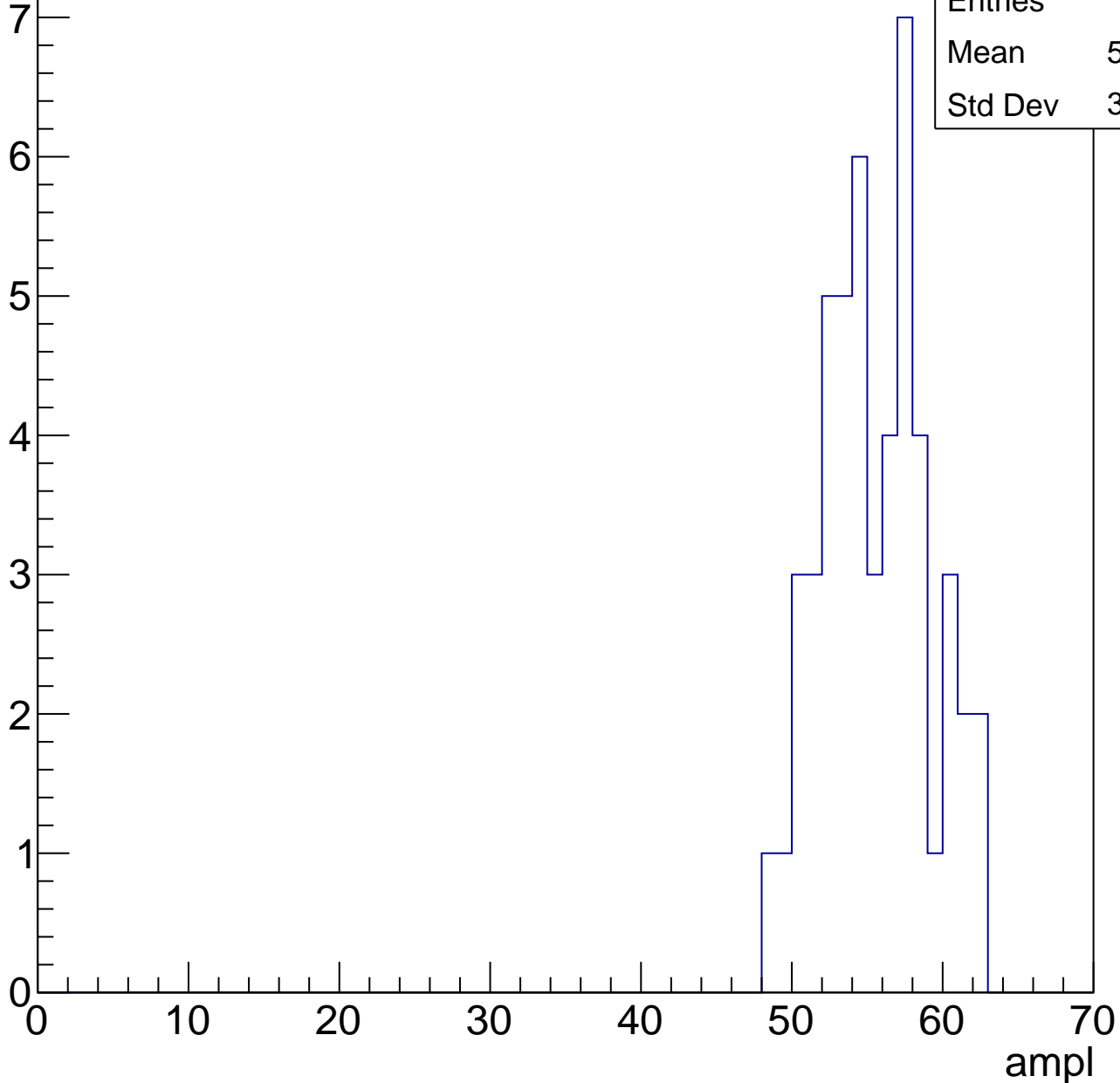


B1L100S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

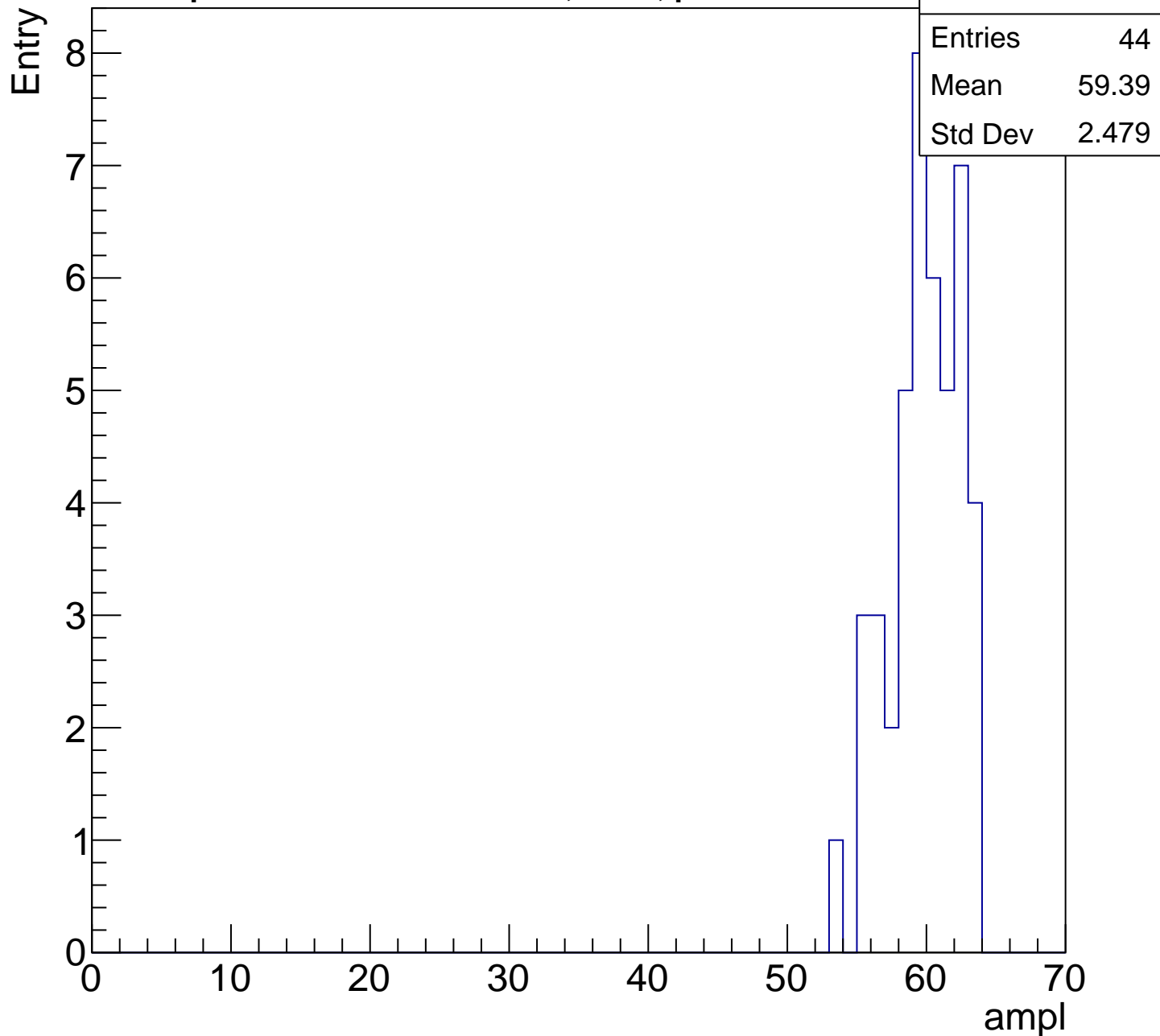
Entry

Entries	50
Mean	55.08
Std Dev	3.486



B1L100S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

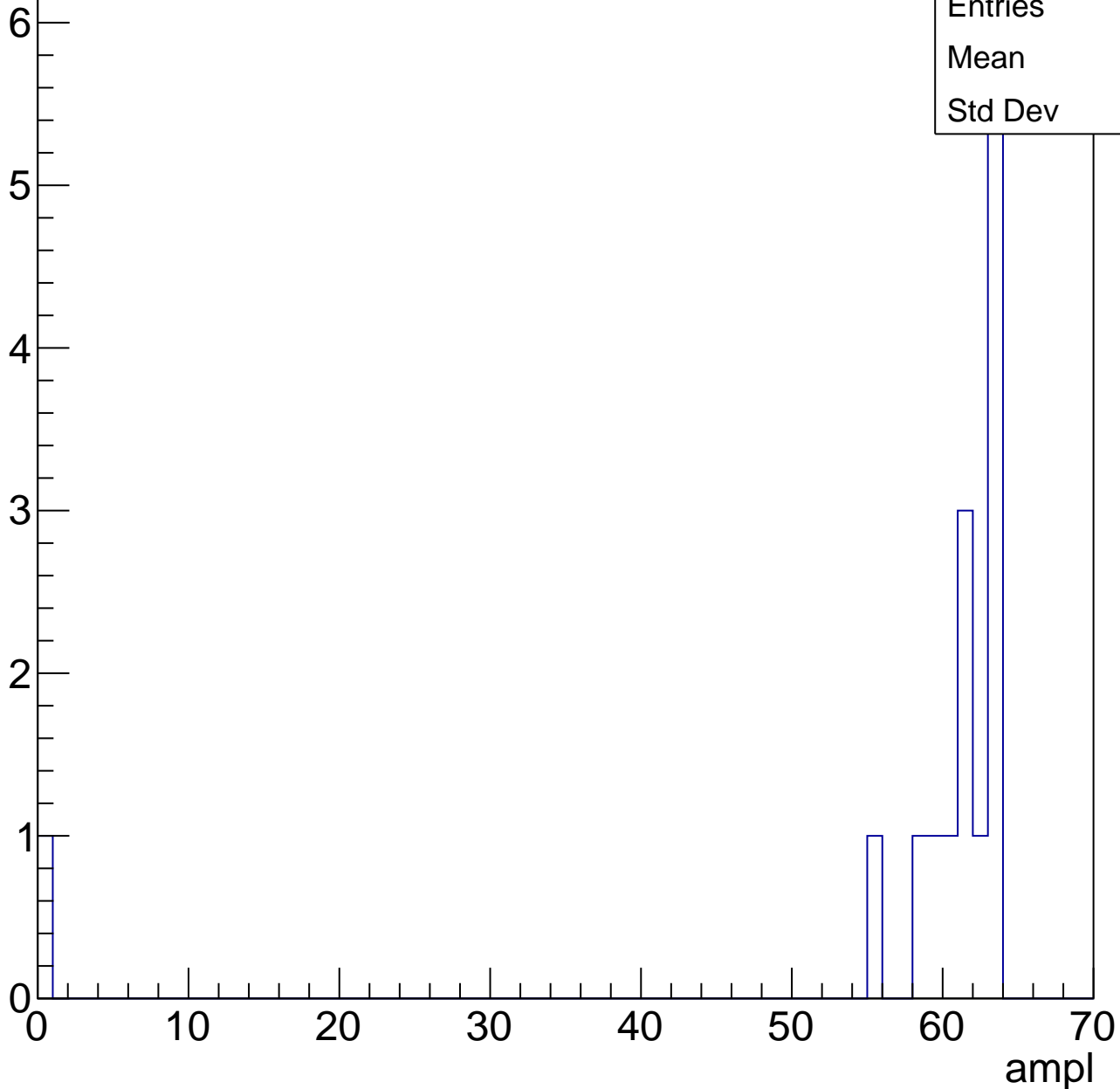


B1L100S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

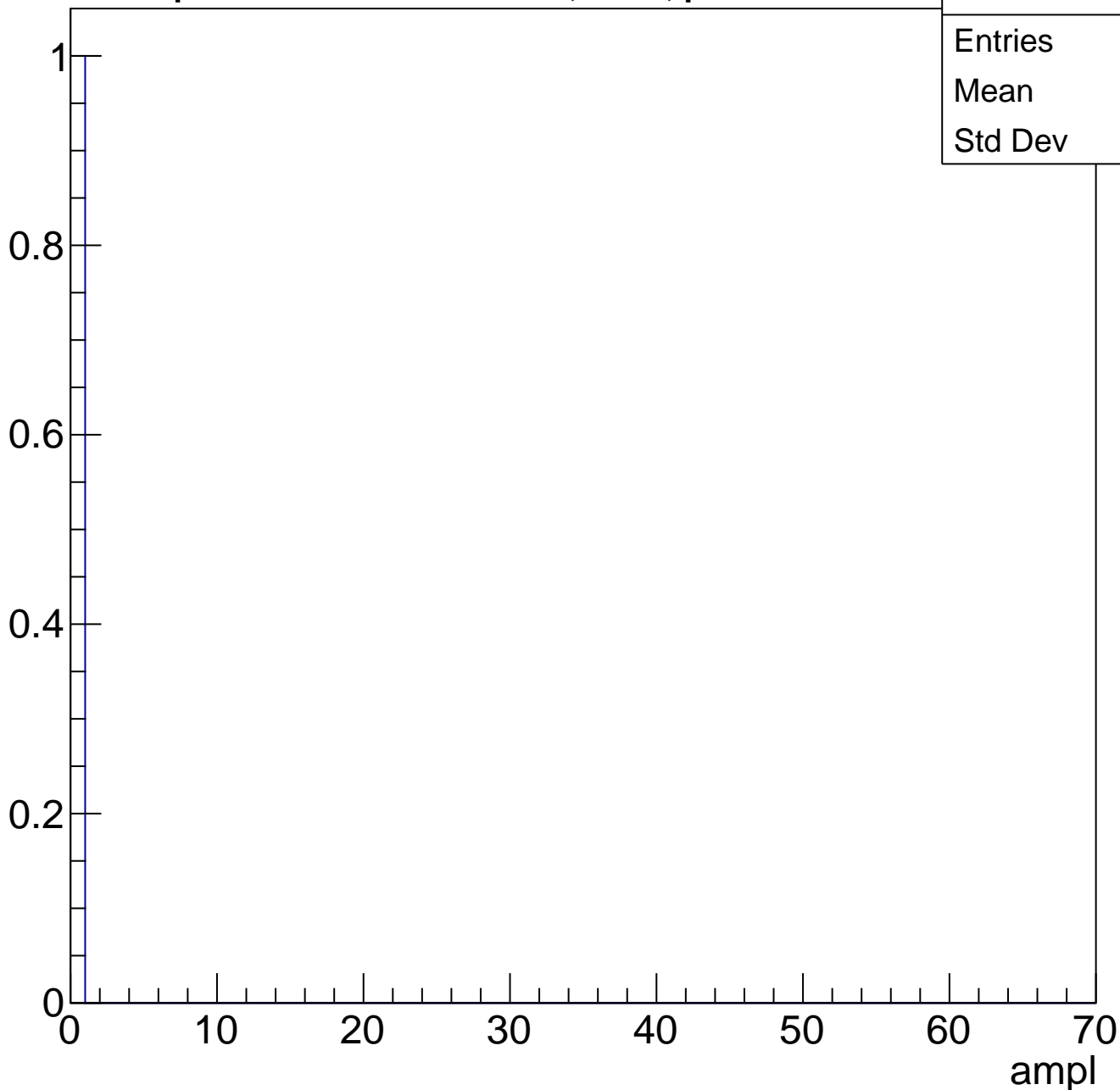
Entries	15
Mean	57
Std Dev	15.4



B1L100S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch54, adc0

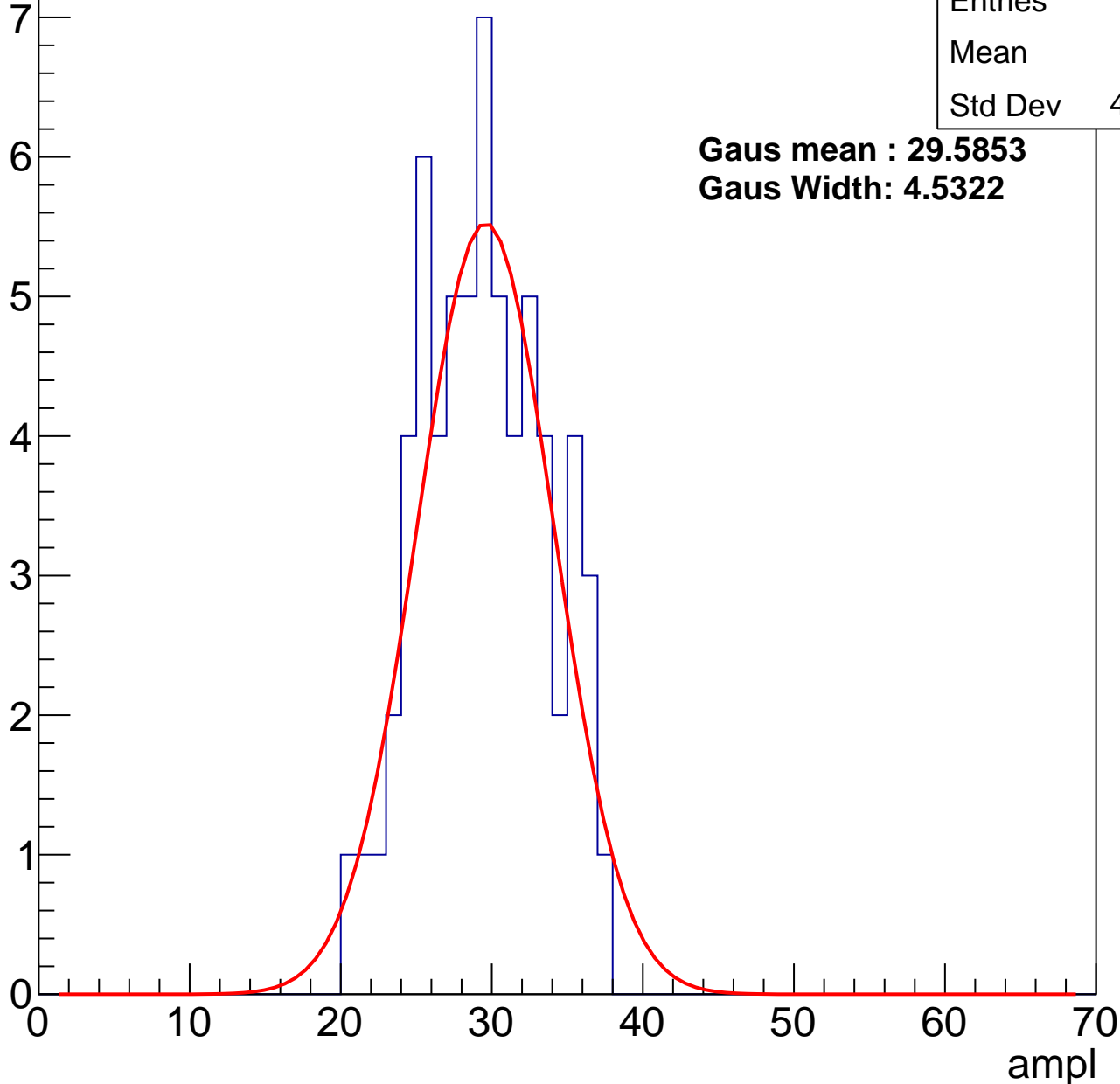
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29
Std Dev	4.066

Gaus mean : 29.5853

Gaus Width: 4.5322



B1L100S, U14-ch54, adc1

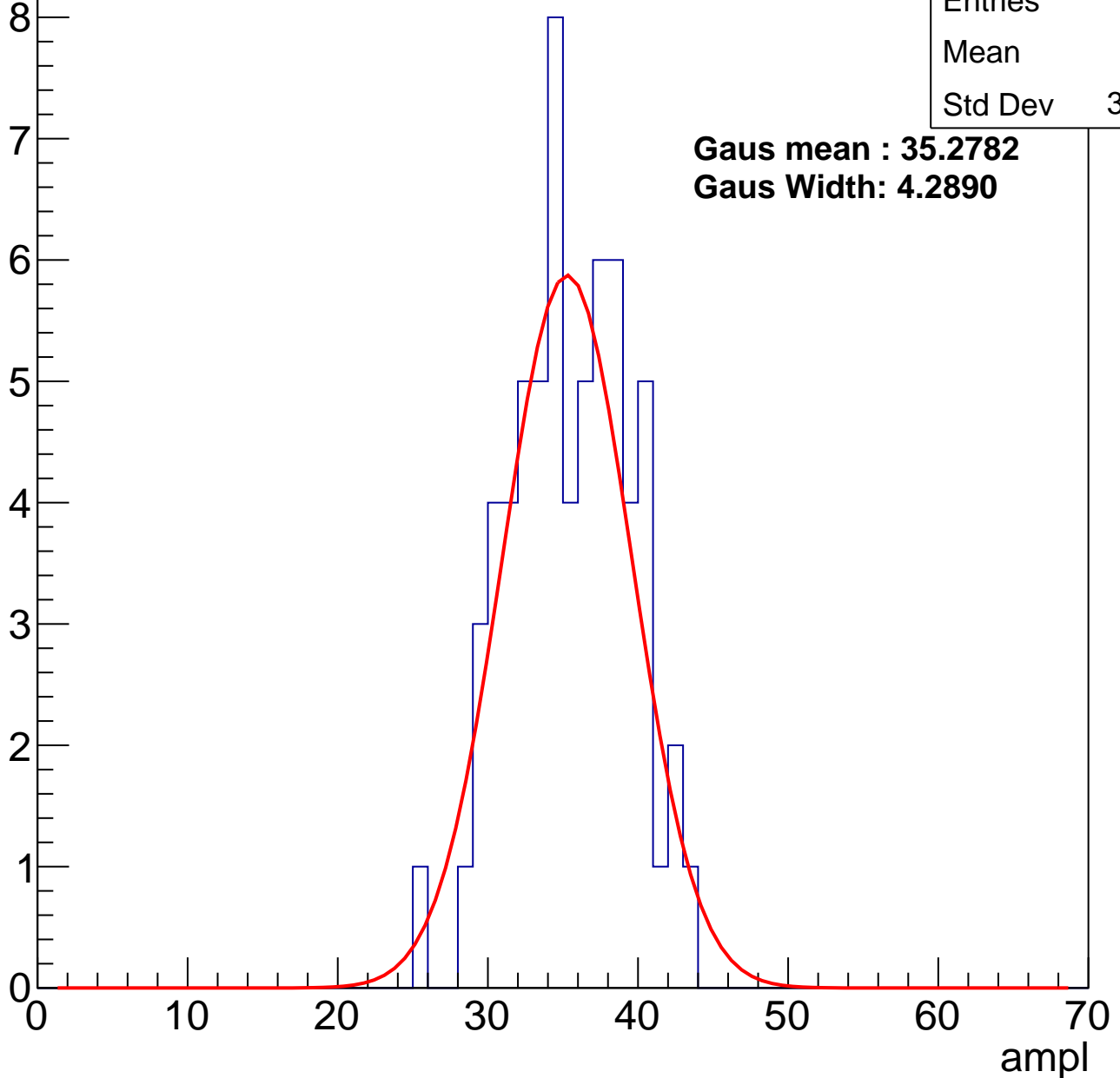
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35
Std Dev	3.851

Gaus mean : 35.2782

Gaus Width: 4.2890



B1L100S, U14-ch54, adc2

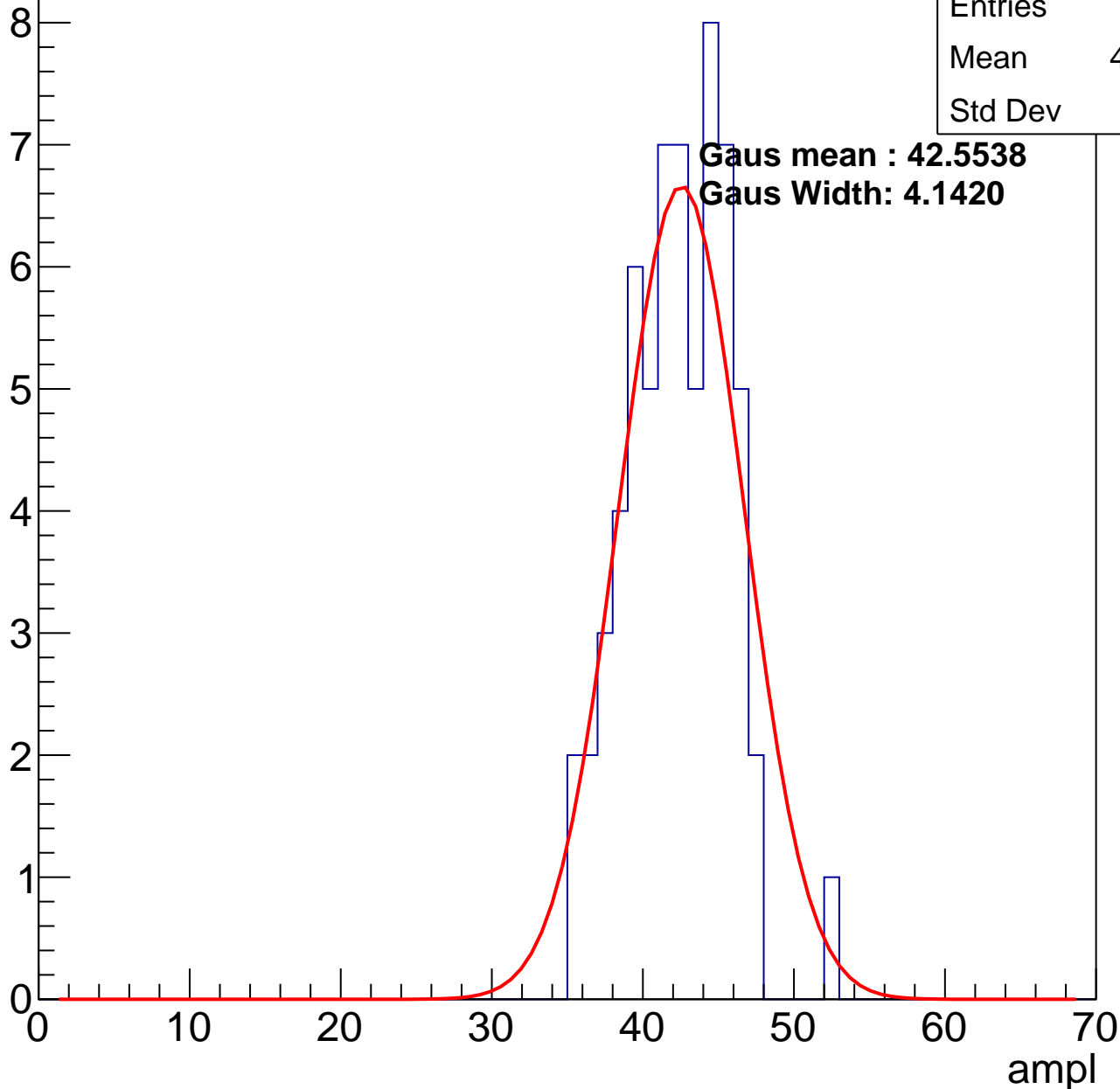
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.84
Std Dev	3.35

Gaus mean : 42.5538

Gaus Width: 4.1420

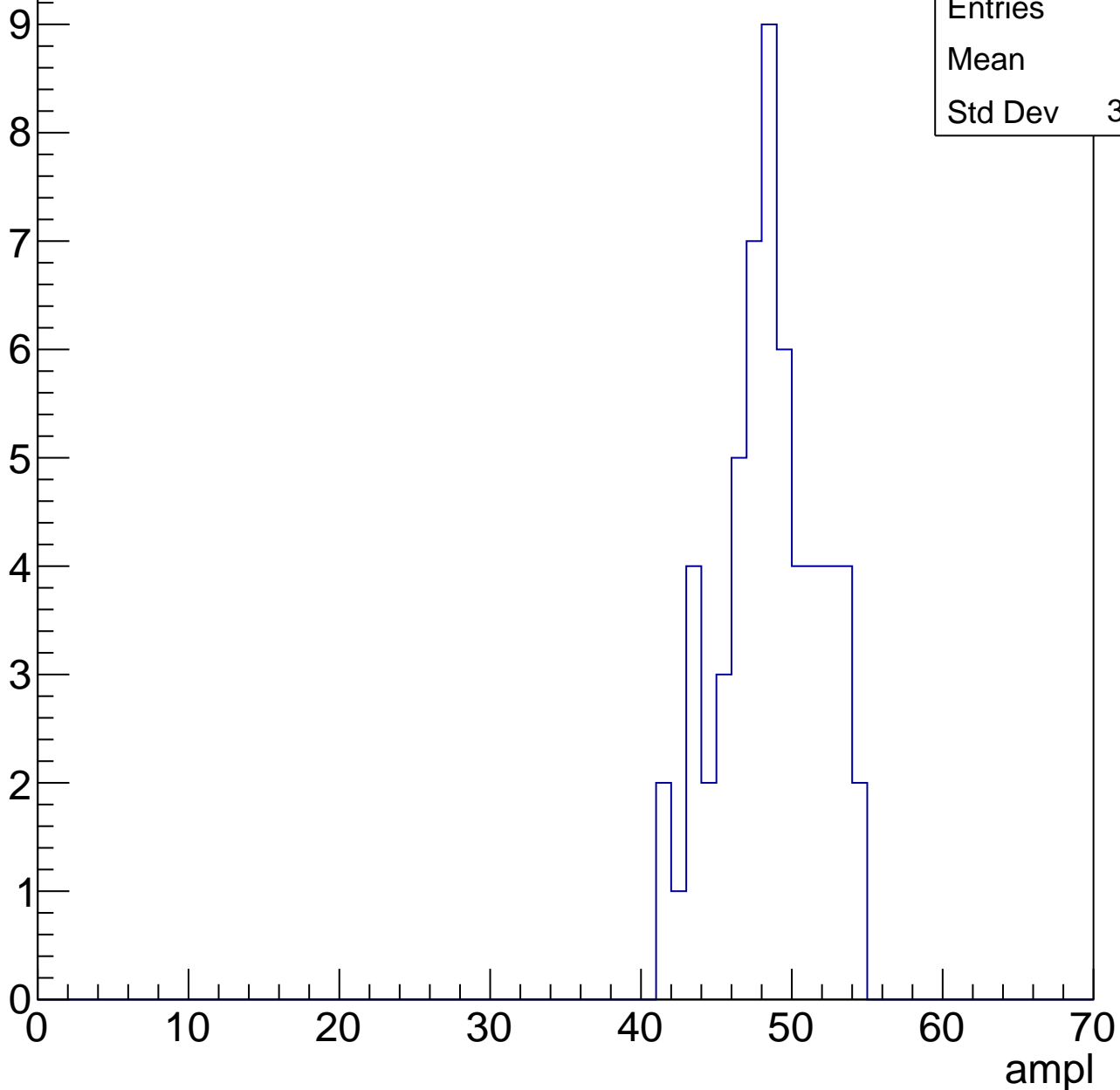


B1L100S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

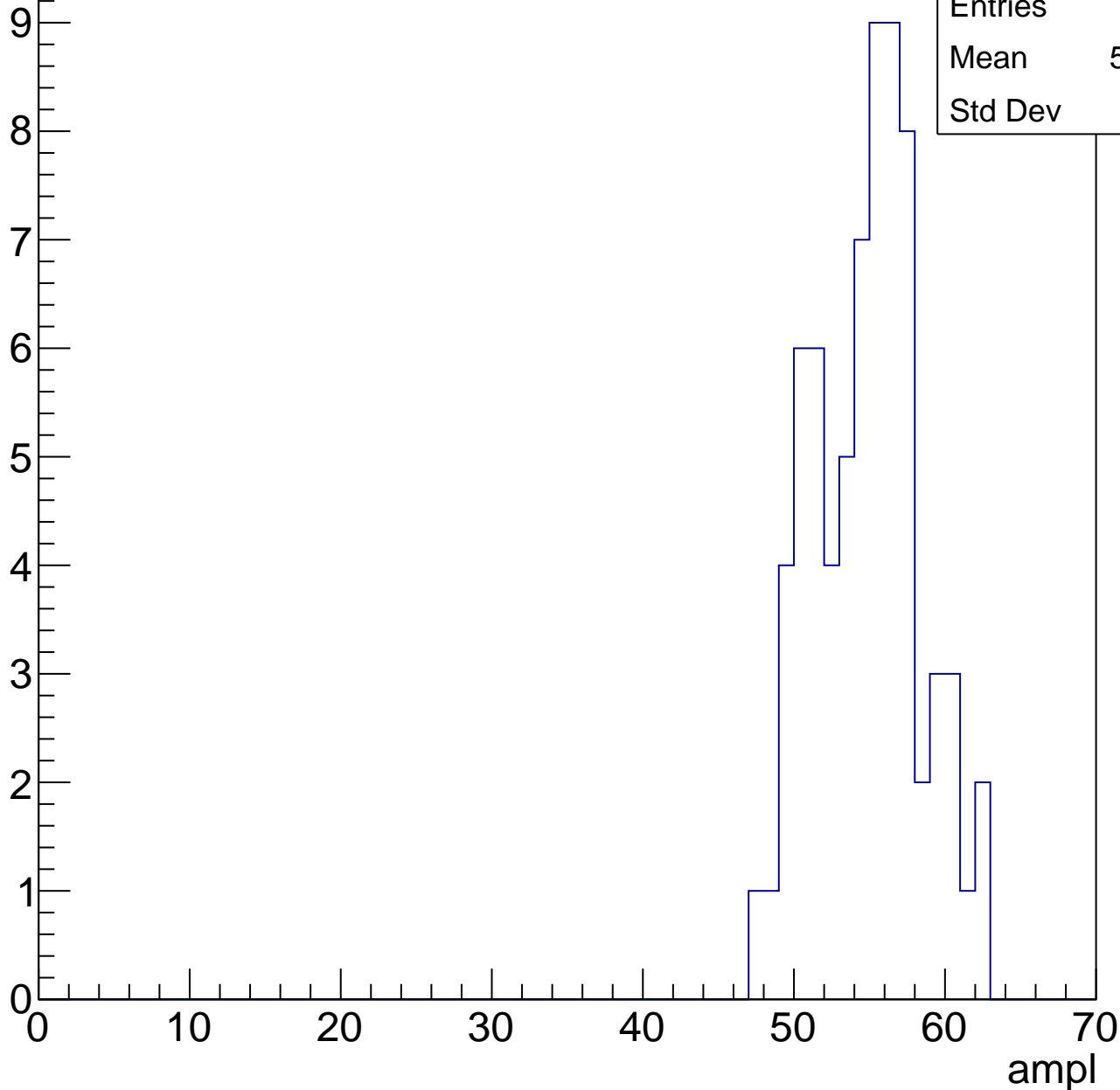
Entries	57
Mean	48
Std Dev	3.282



B1L100S, U14-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

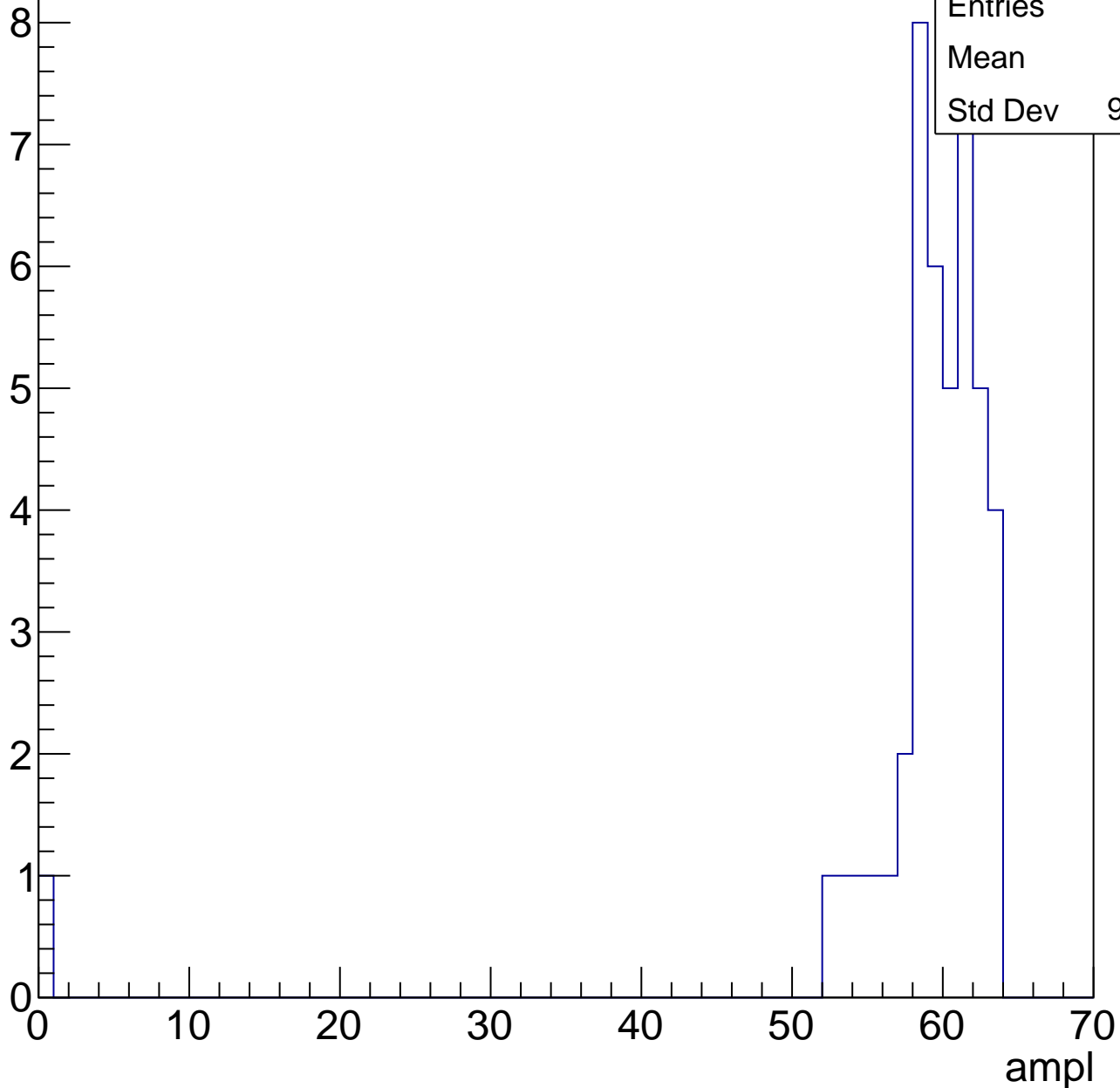
Entry



B1L100S, U14-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



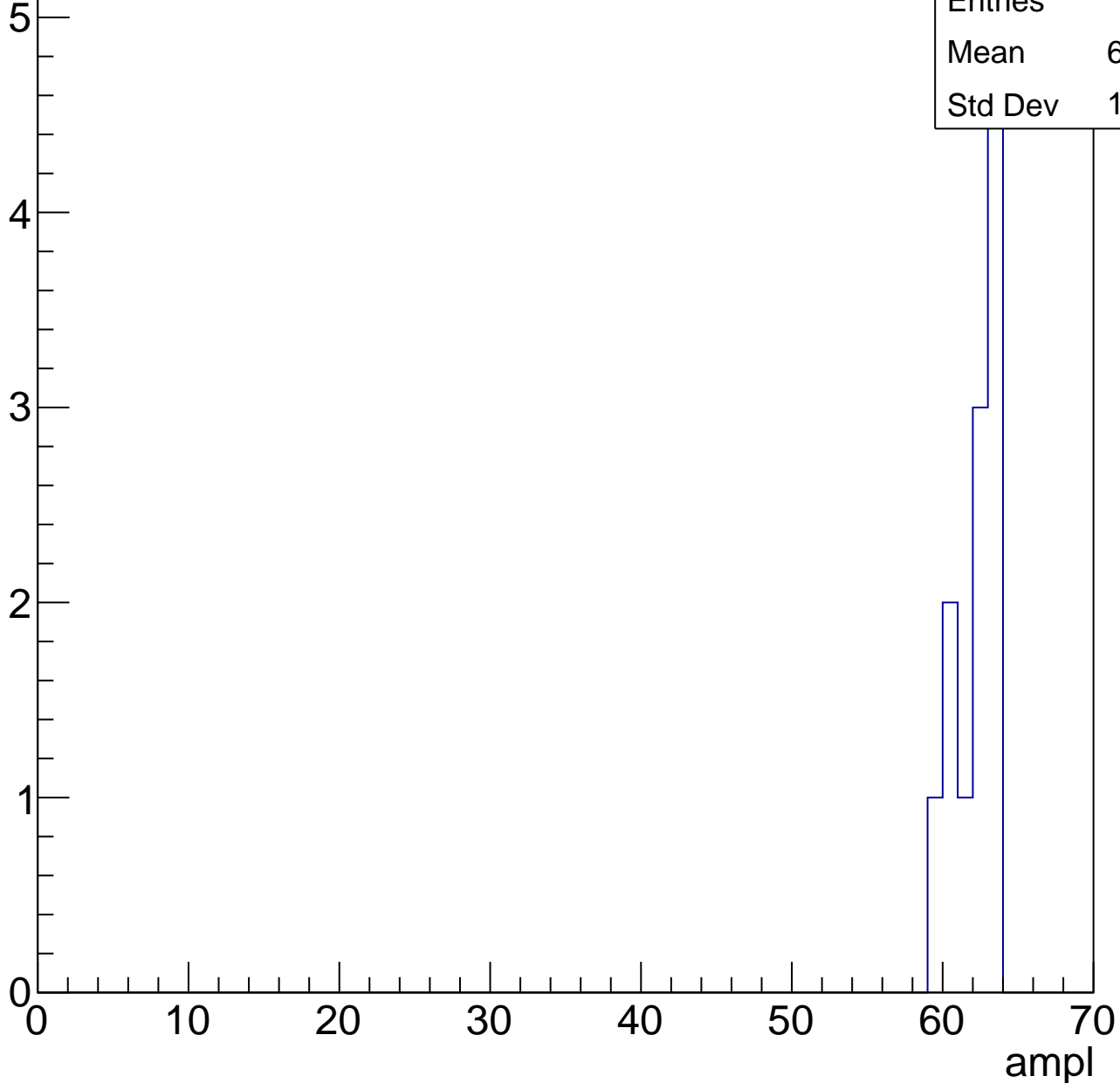
Entries	44
Mean	58
Std Dev	9.212

B1L100S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	61.75
Std Dev	1.362



B1L100S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U14-ch55, adc0

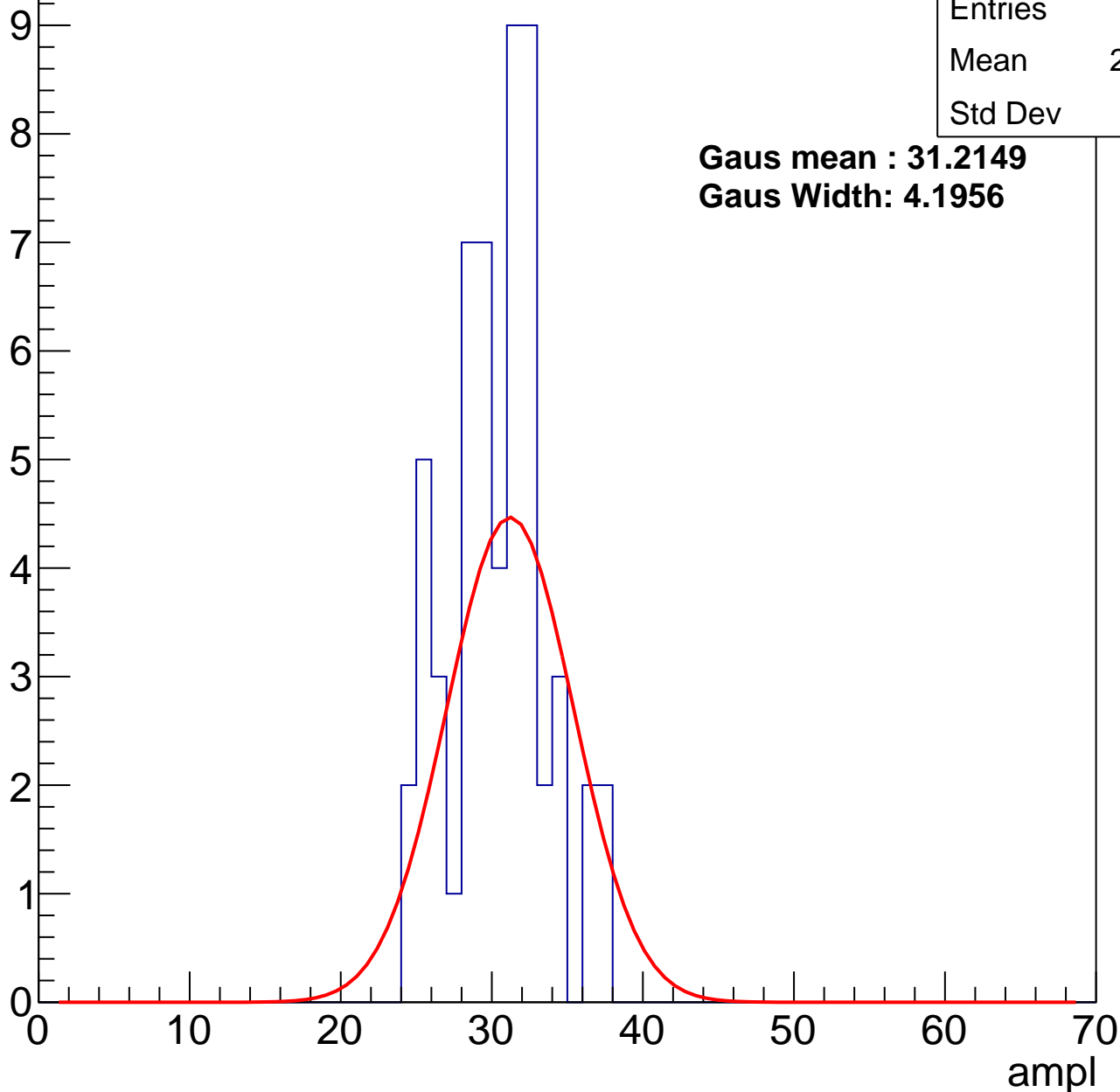
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.96
Std Dev	3.19

Gaus mean : 31.2149

Gaus Width: 4.1956



B1L100S, U14-ch55, adc1

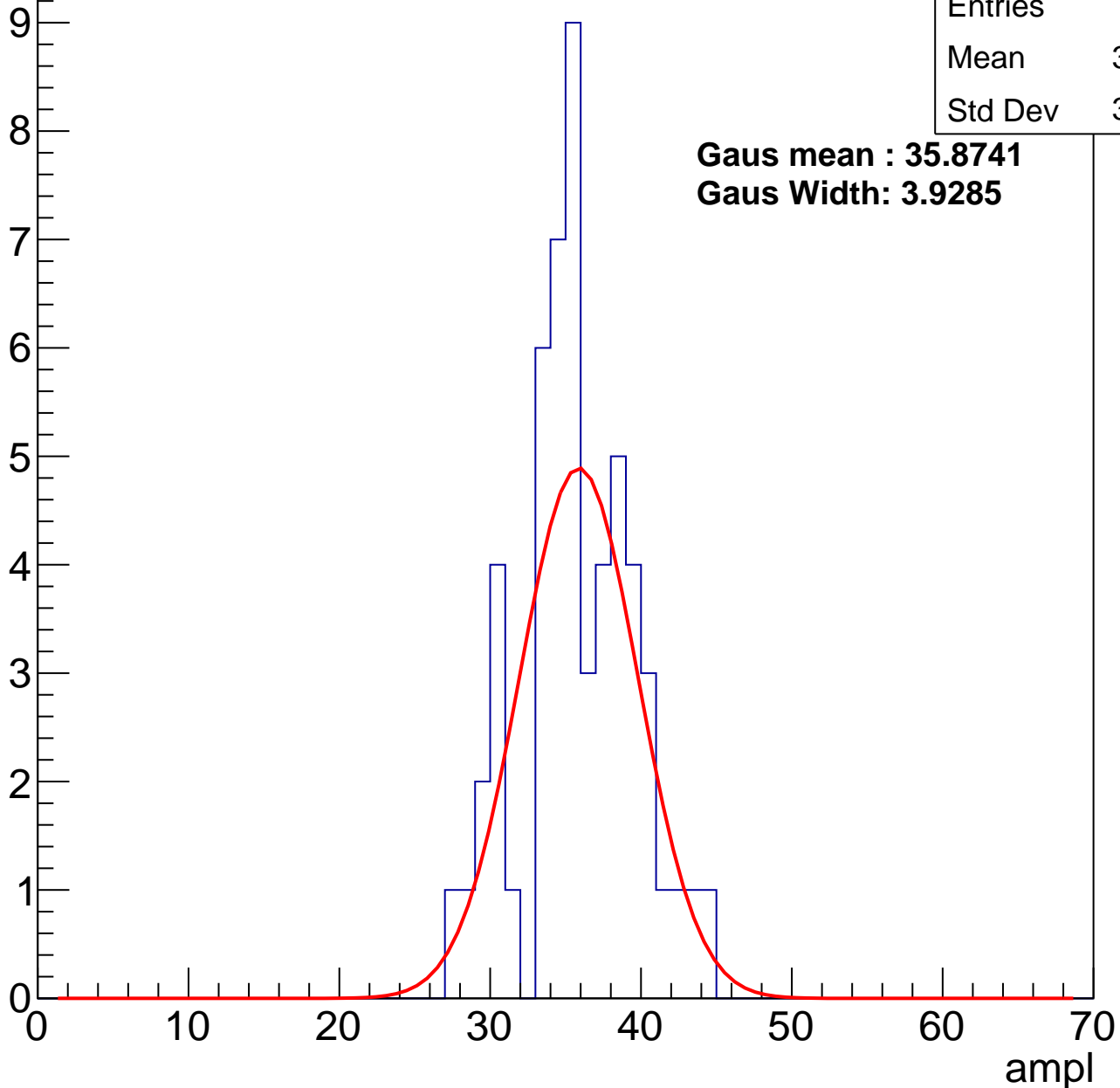
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	35.31
Std Dev	3.751

Gaus mean : 35.8741

Gaus Width: 3.9285



B1L100S, U14-ch55, adc2

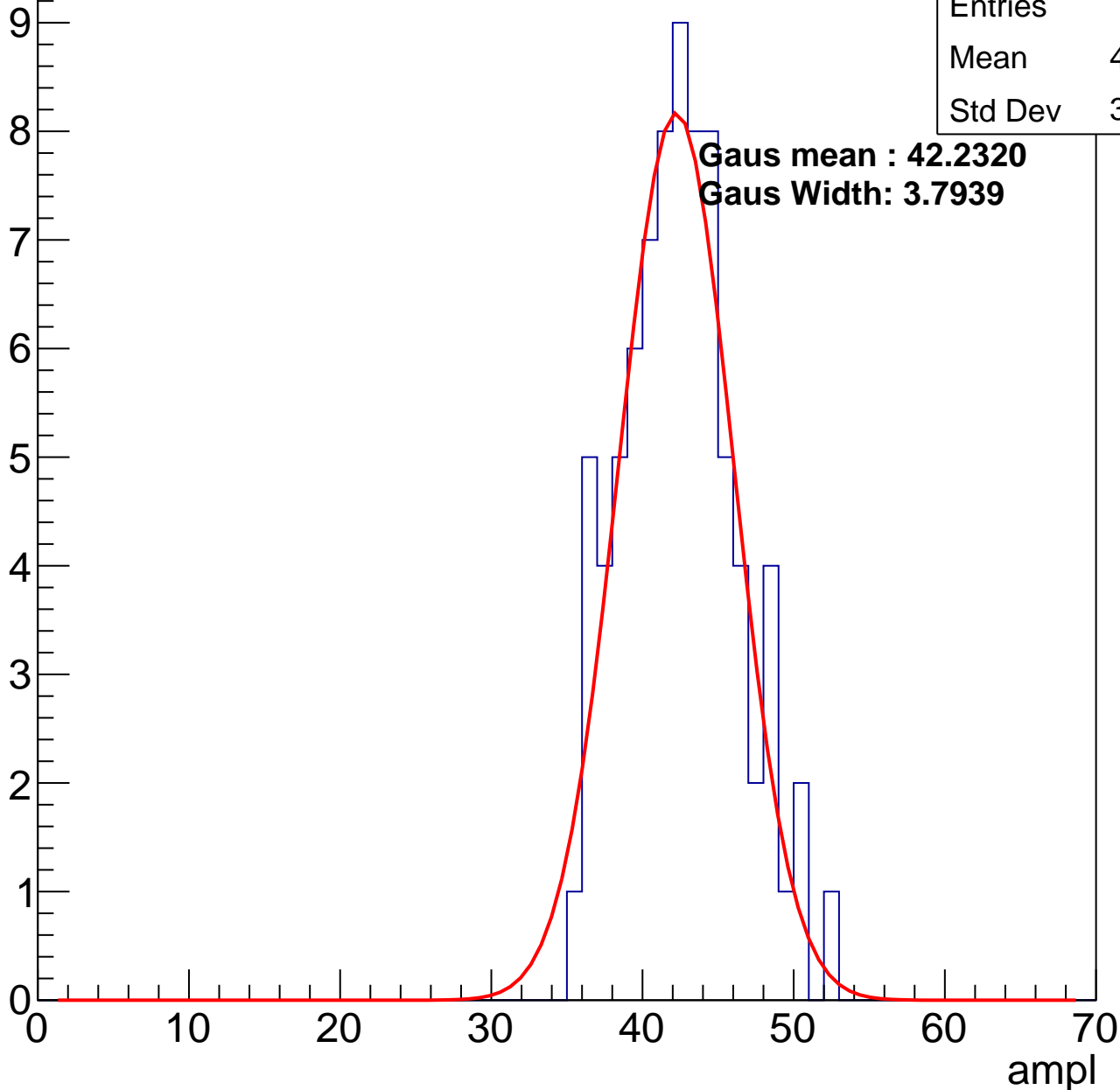
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	42.06
Std Dev	3.739

Gaus mean : 42.2320

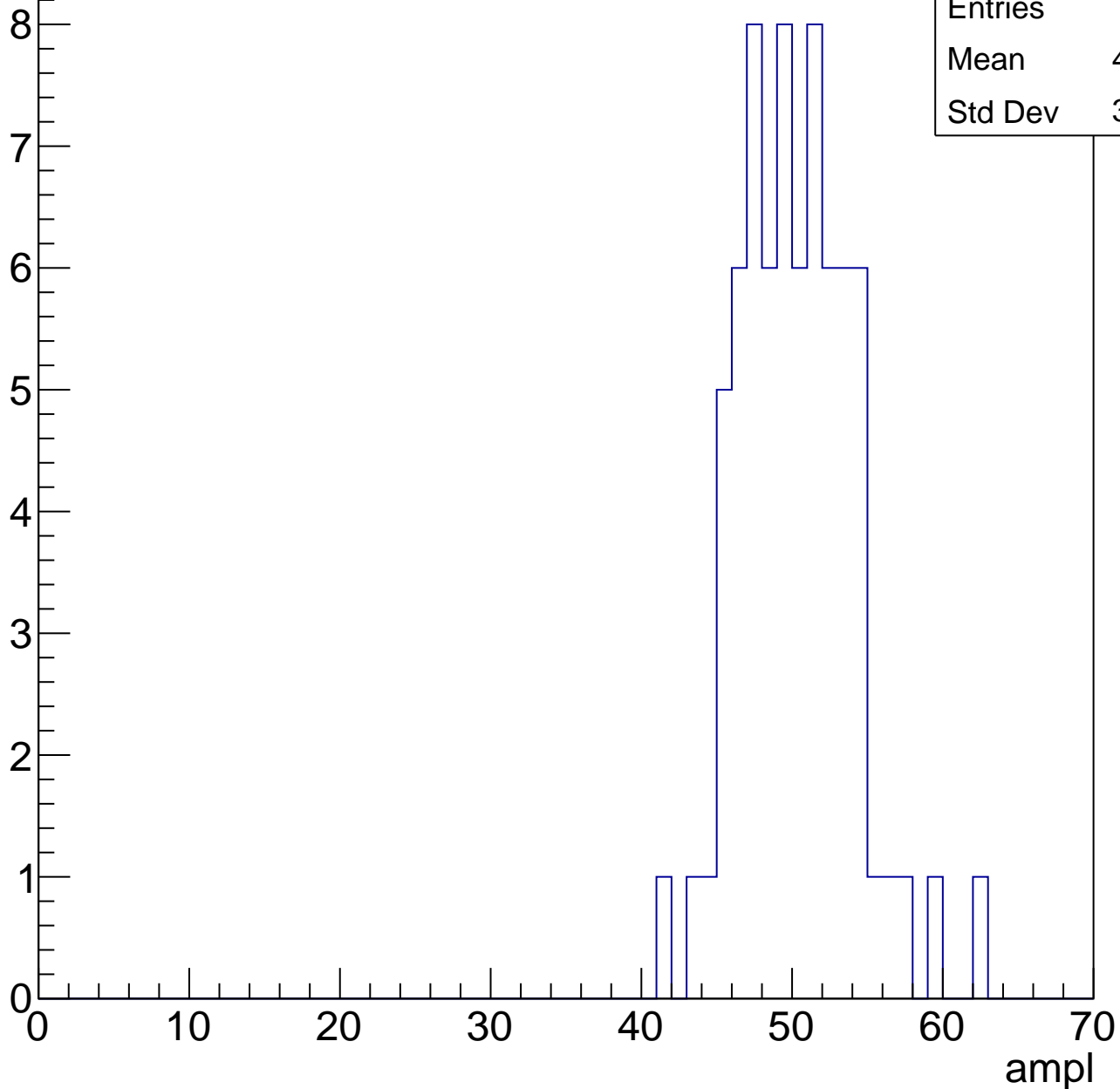
Gaus Width: 3.7939



B1L100S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

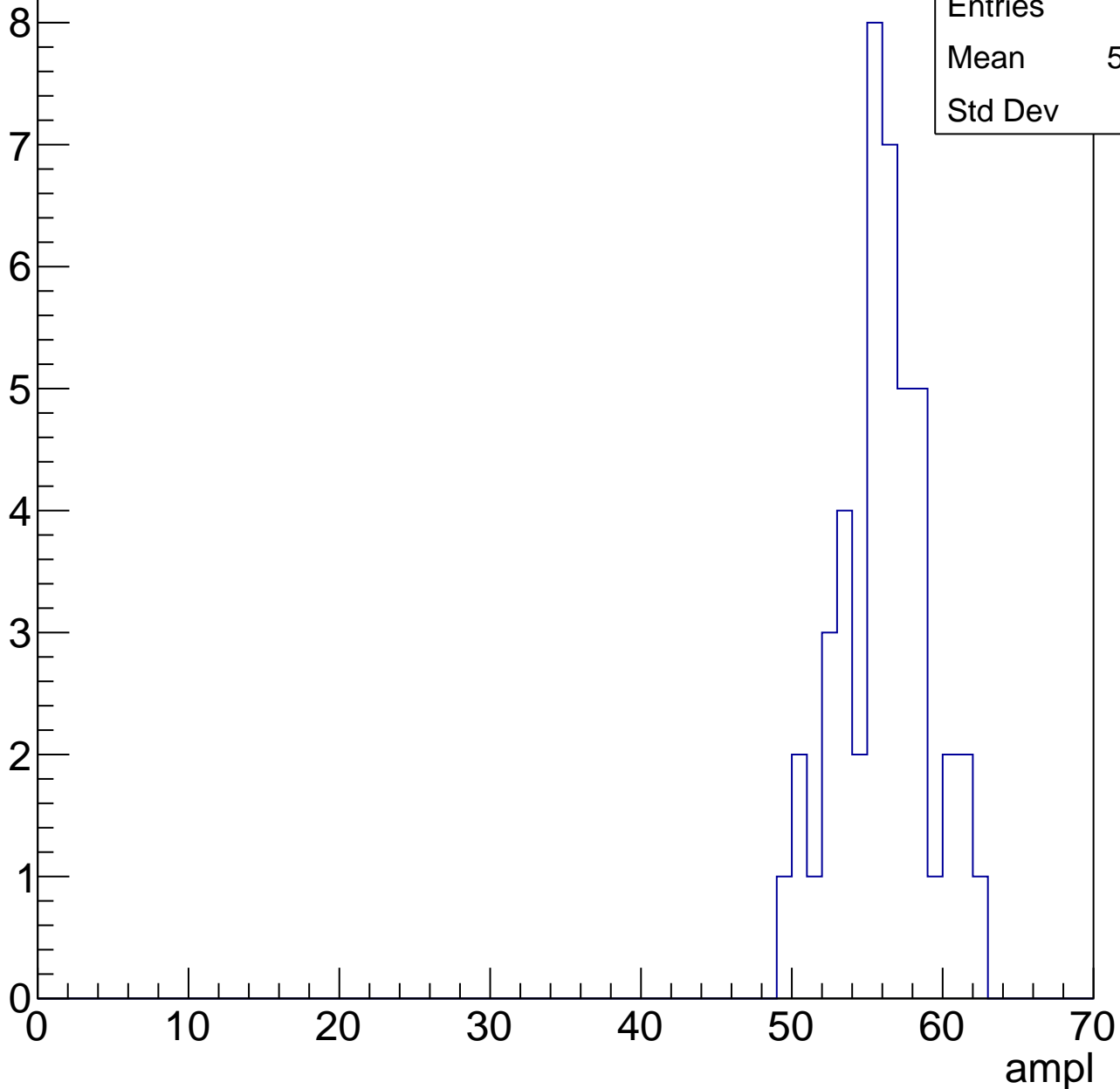


B1L100S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

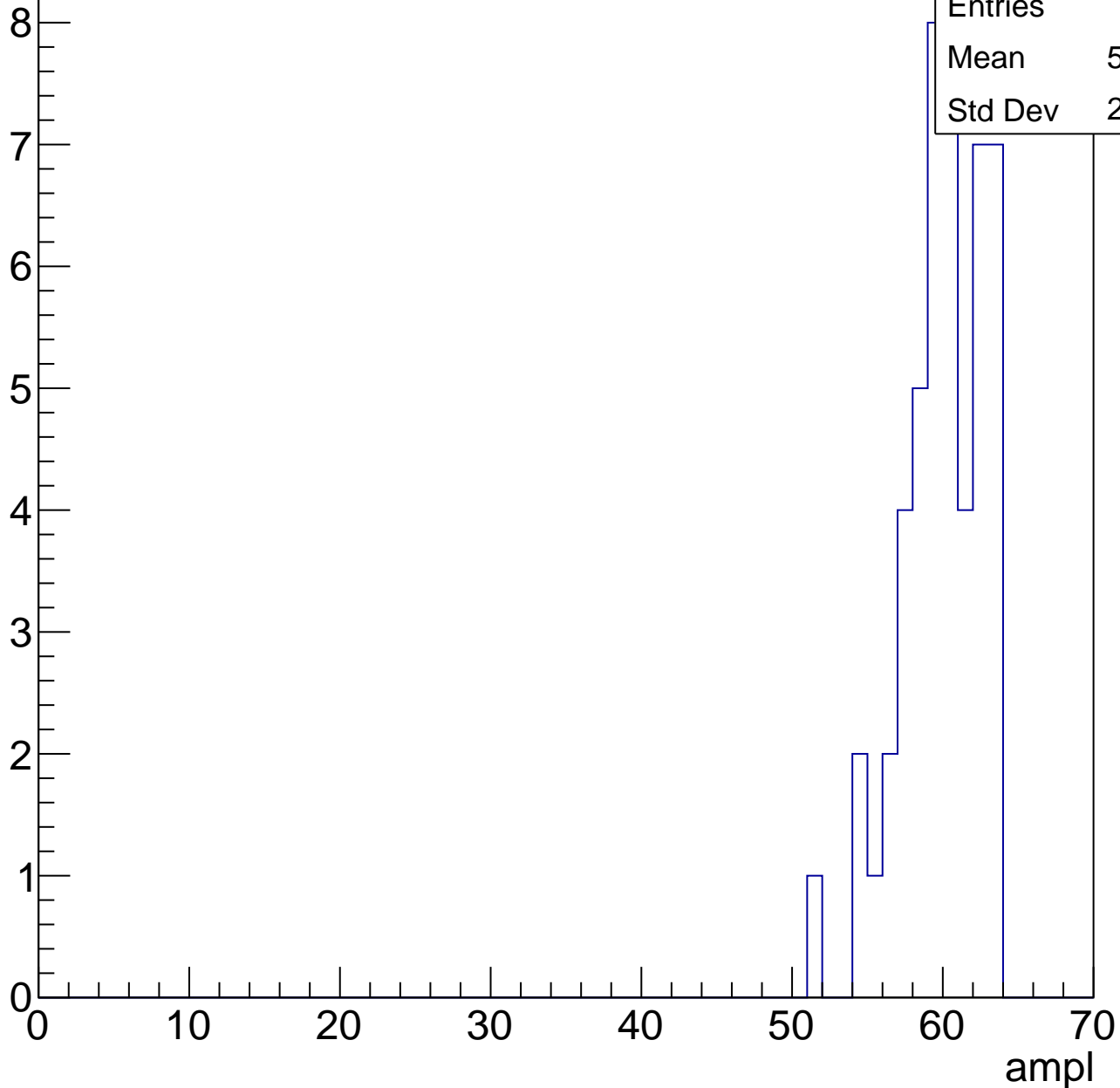
Entries	44
Mean	55.59
Std Dev	2.98



B1L100S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

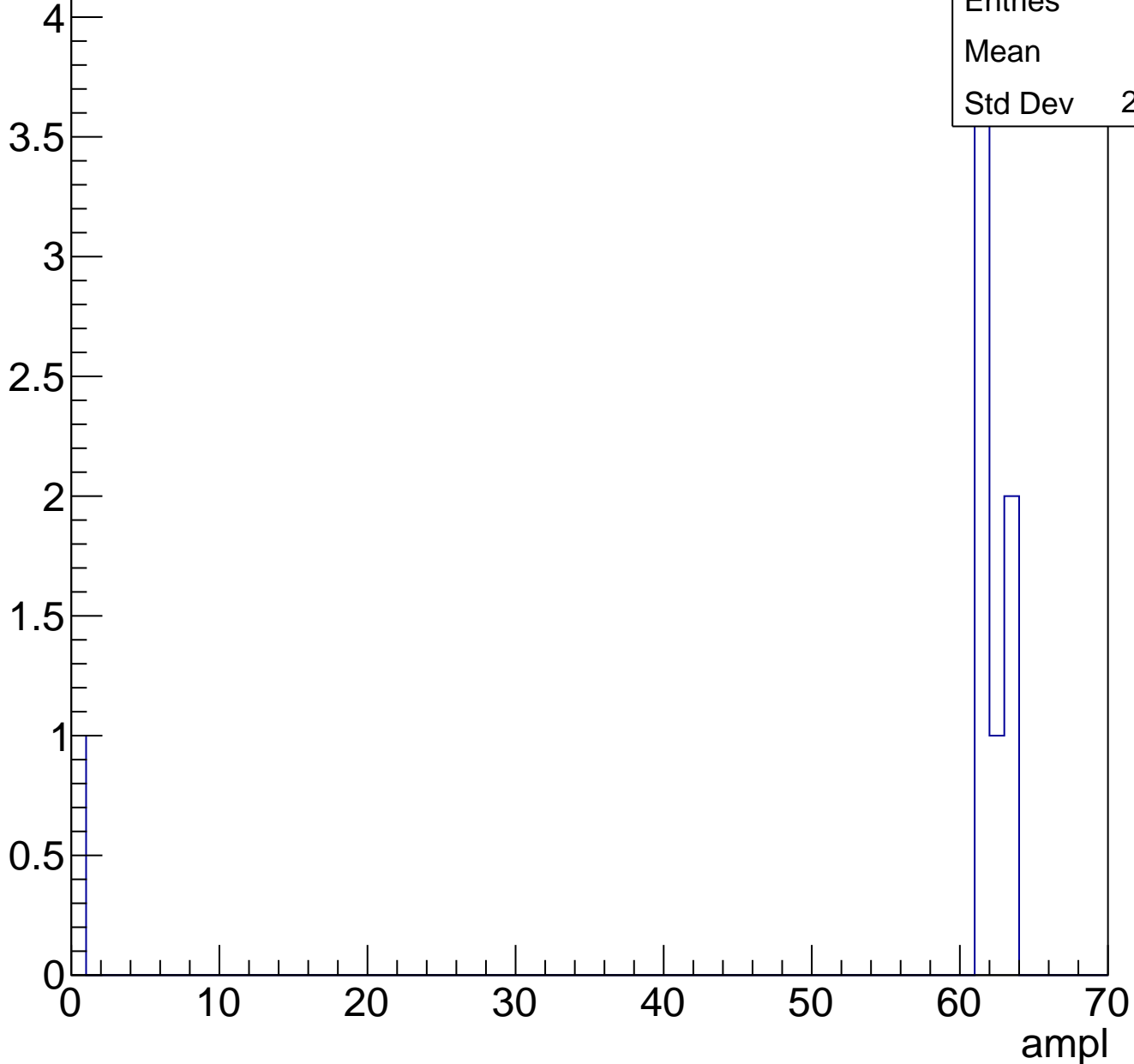
Entry



B1L100S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U14-ch56, adc0

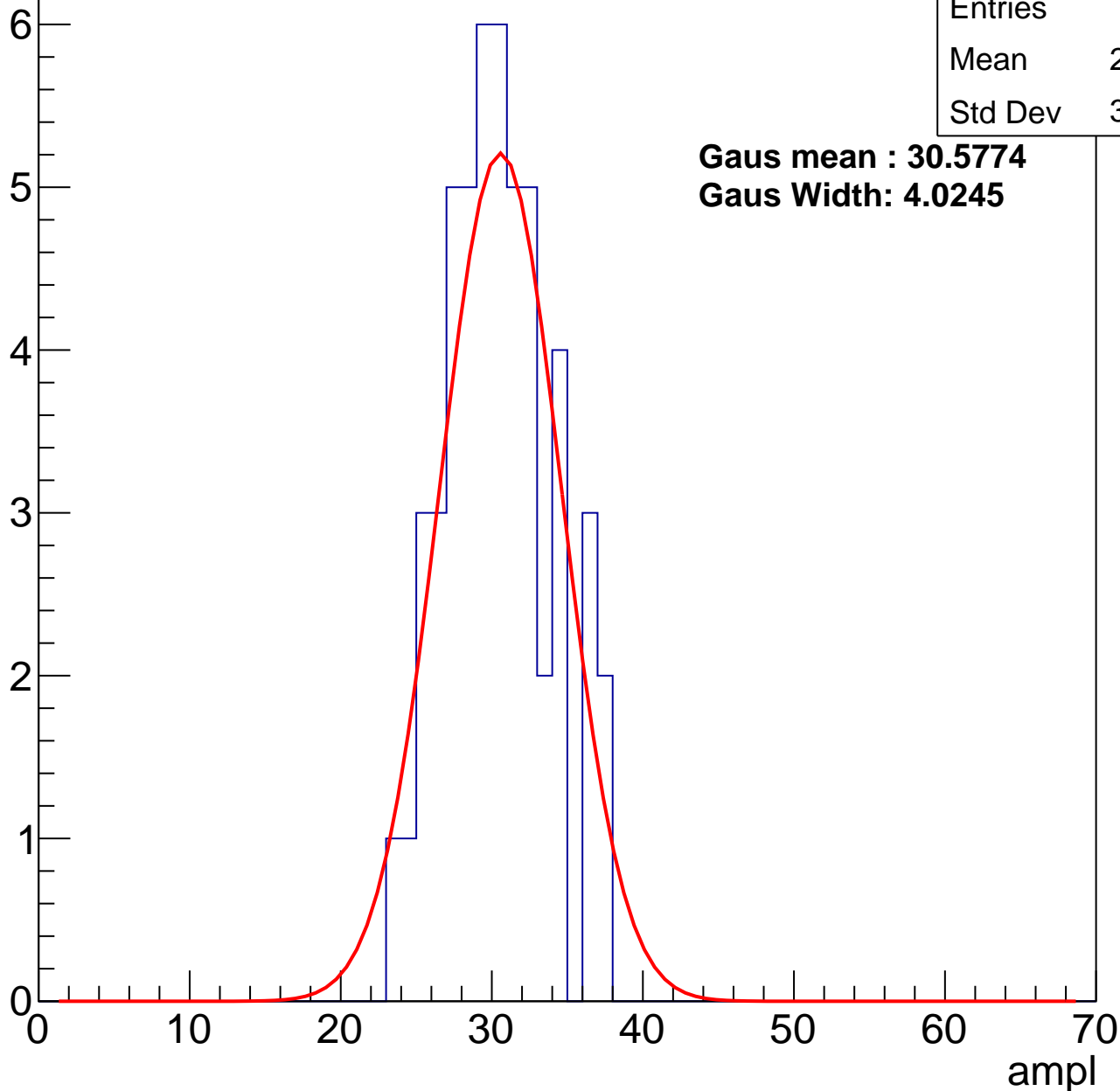
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	29.96
Std Dev	3.407

Gaus mean : 30.5774

Gaus Width: 4.0245



B1L100S, U14-ch56, adc1

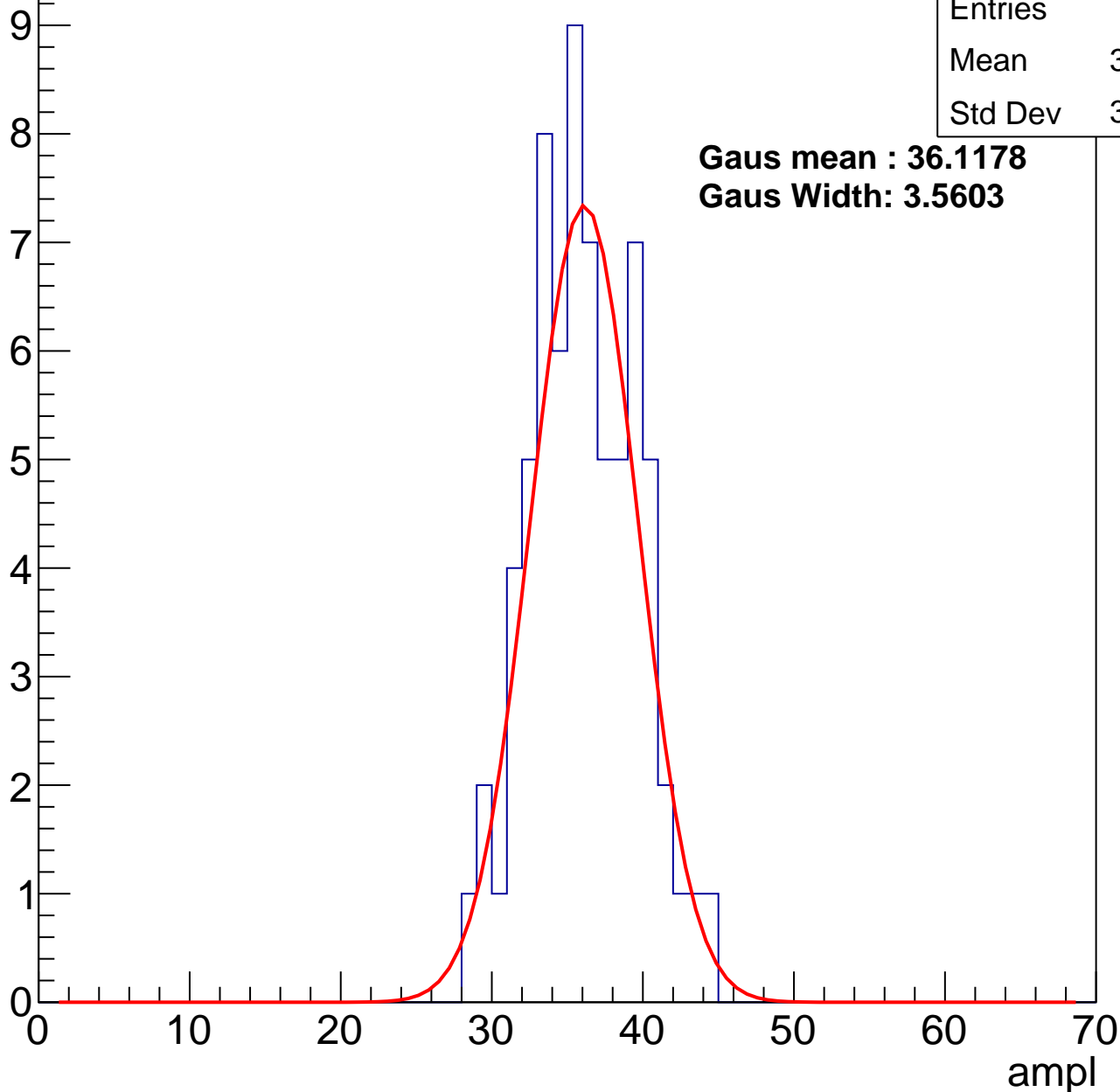
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.63
Std Dev	3.465

Gaus mean : 36.1178

Gaus Width: 3.5603



B1L100S, U14-ch56, adc2

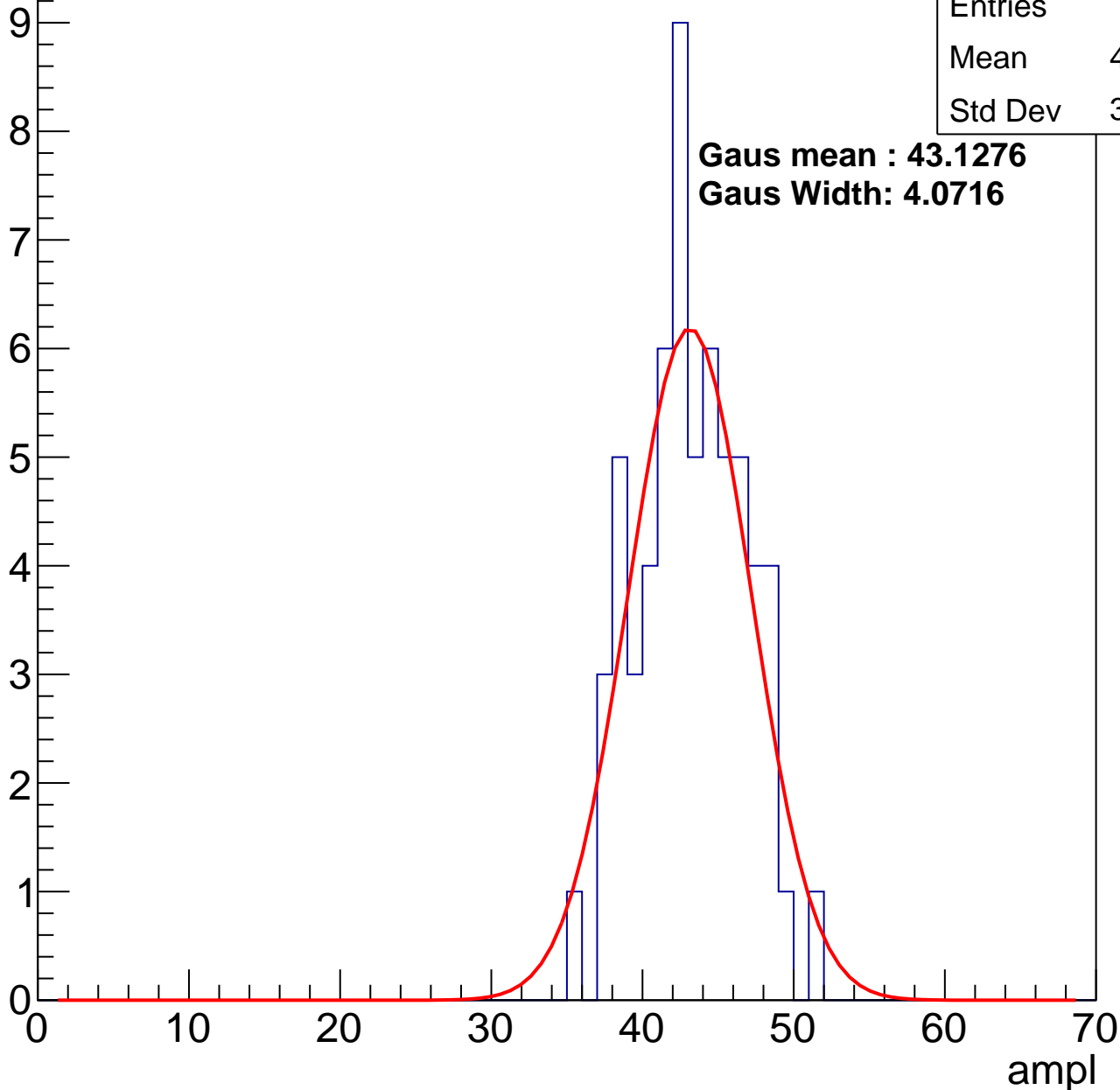
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.76
Std Dev	3.467

Gaus mean : 43.1276

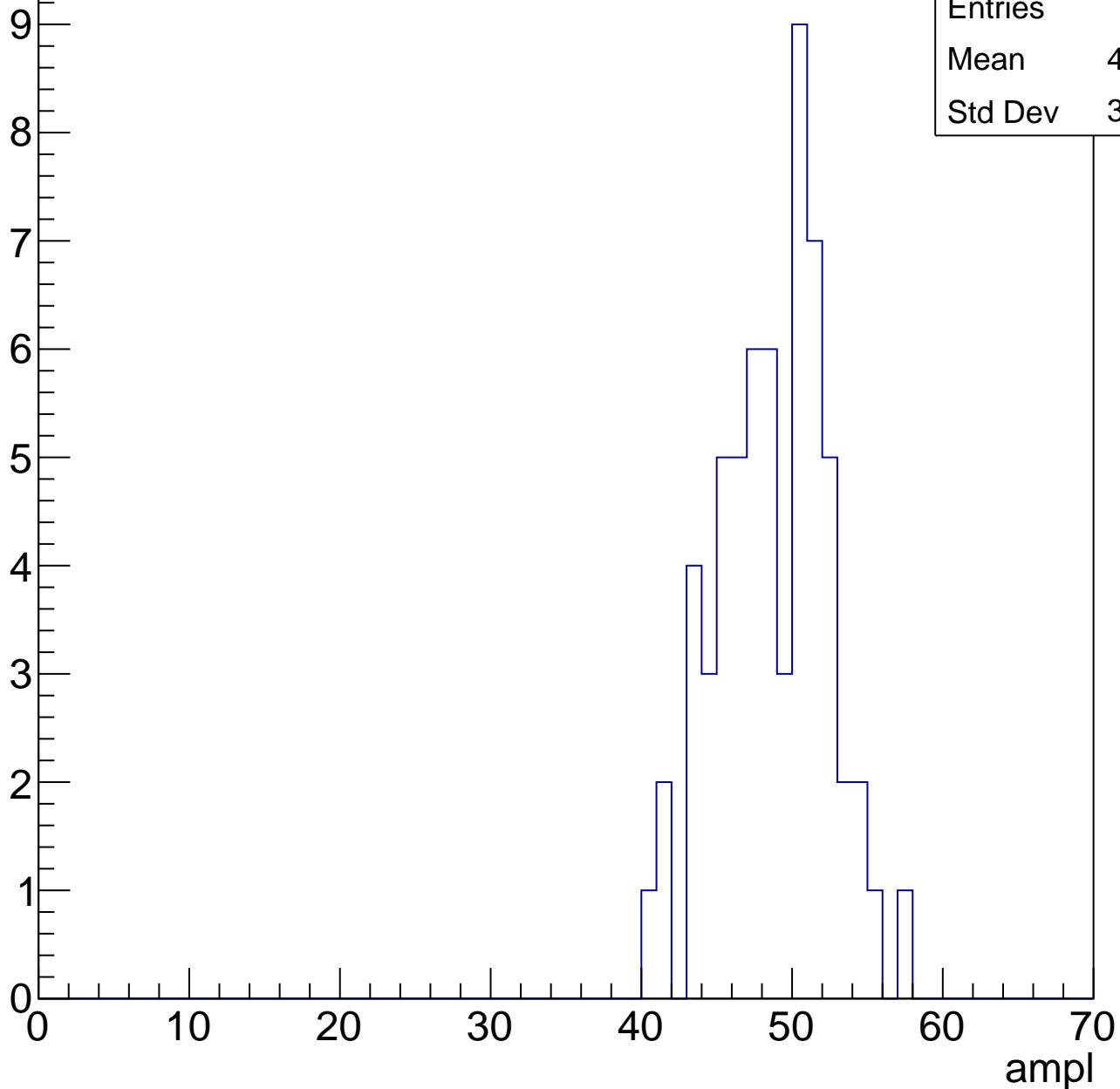
Gaus Width: 4.0716



B1L100S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

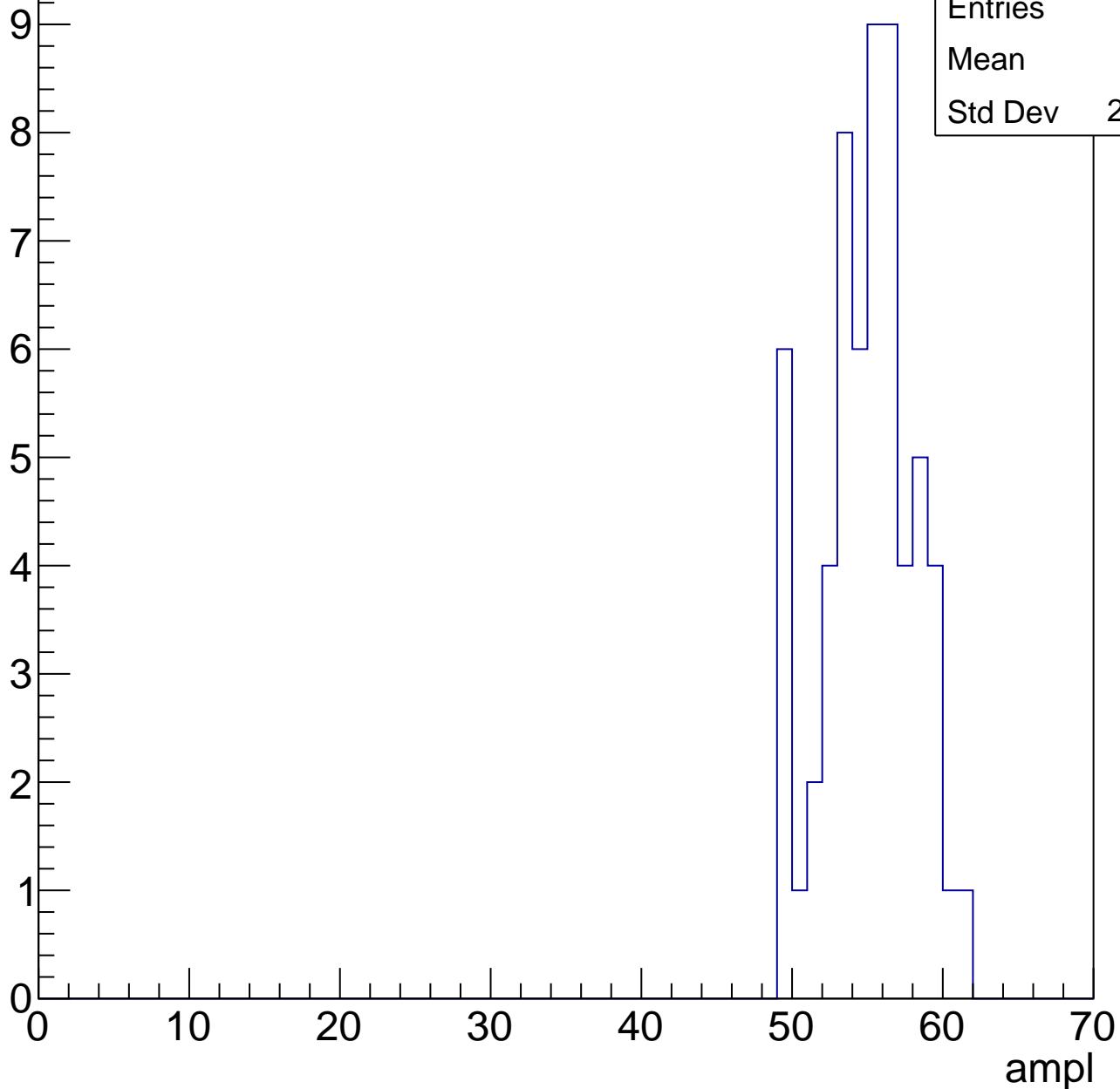
Entry



B1L100S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

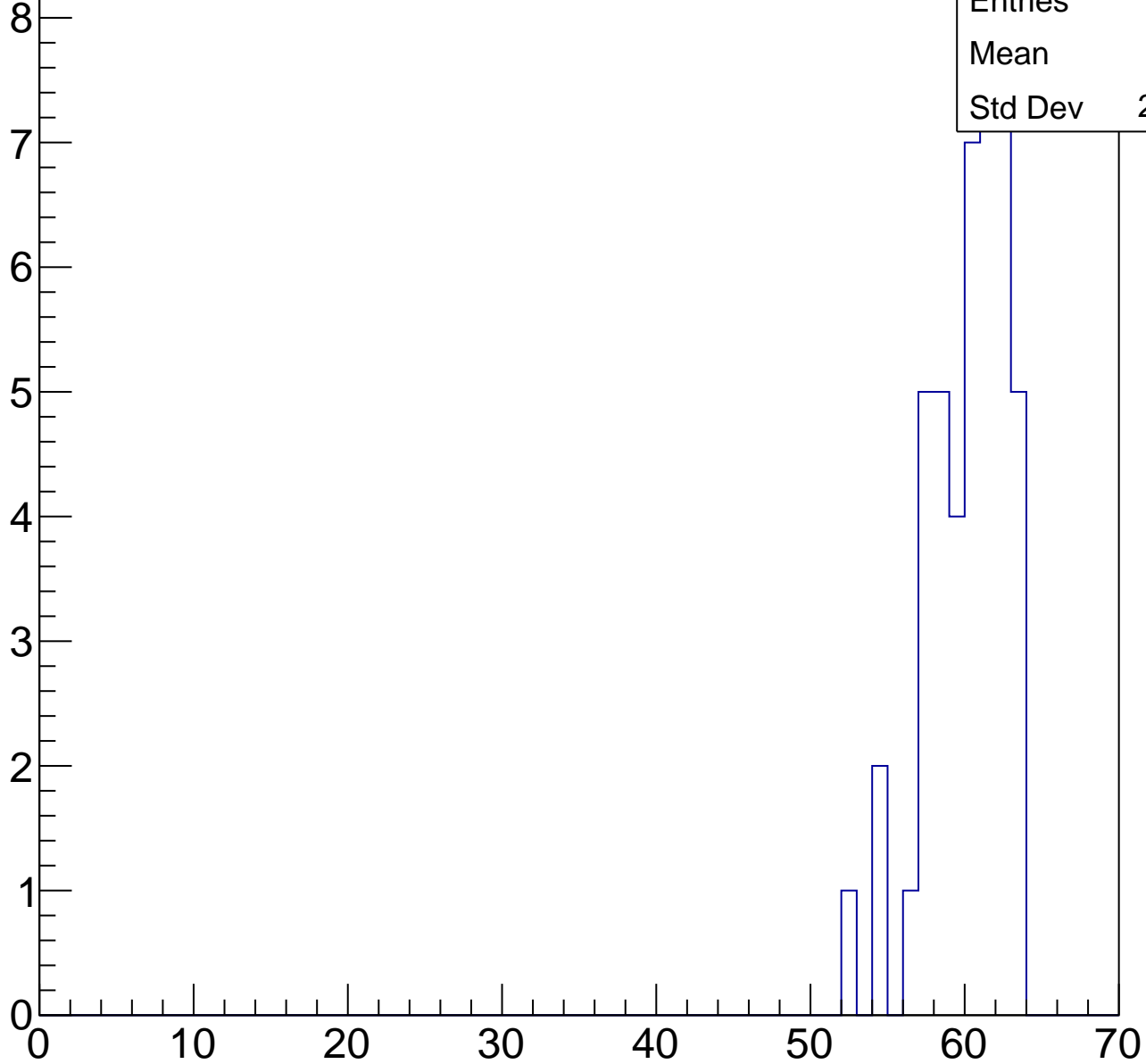
Entry



B1L100S, U14-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



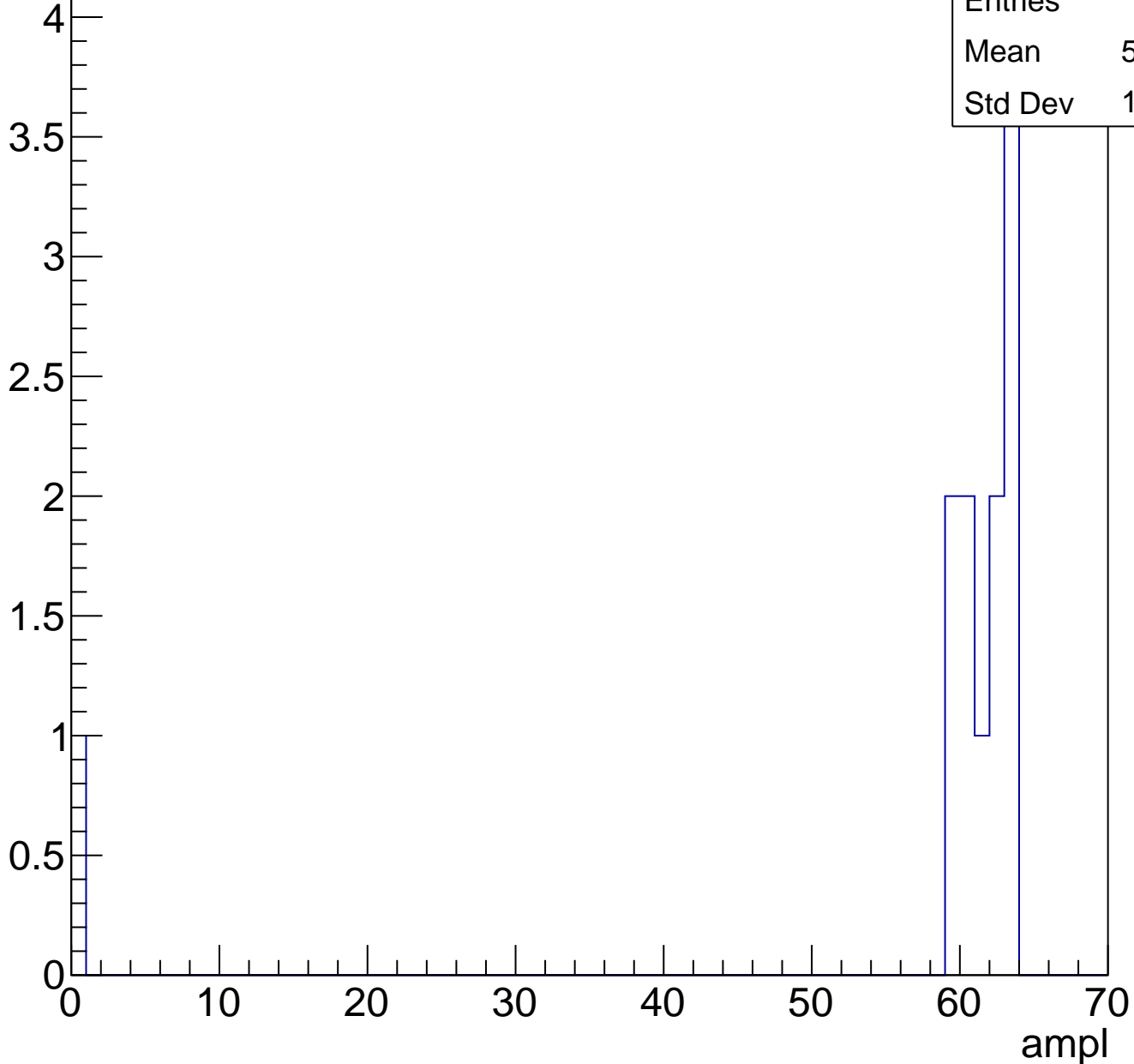
Entries	46
Mean	59.7
Std Dev	2.561

ampl

B1L100S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch57, adc0

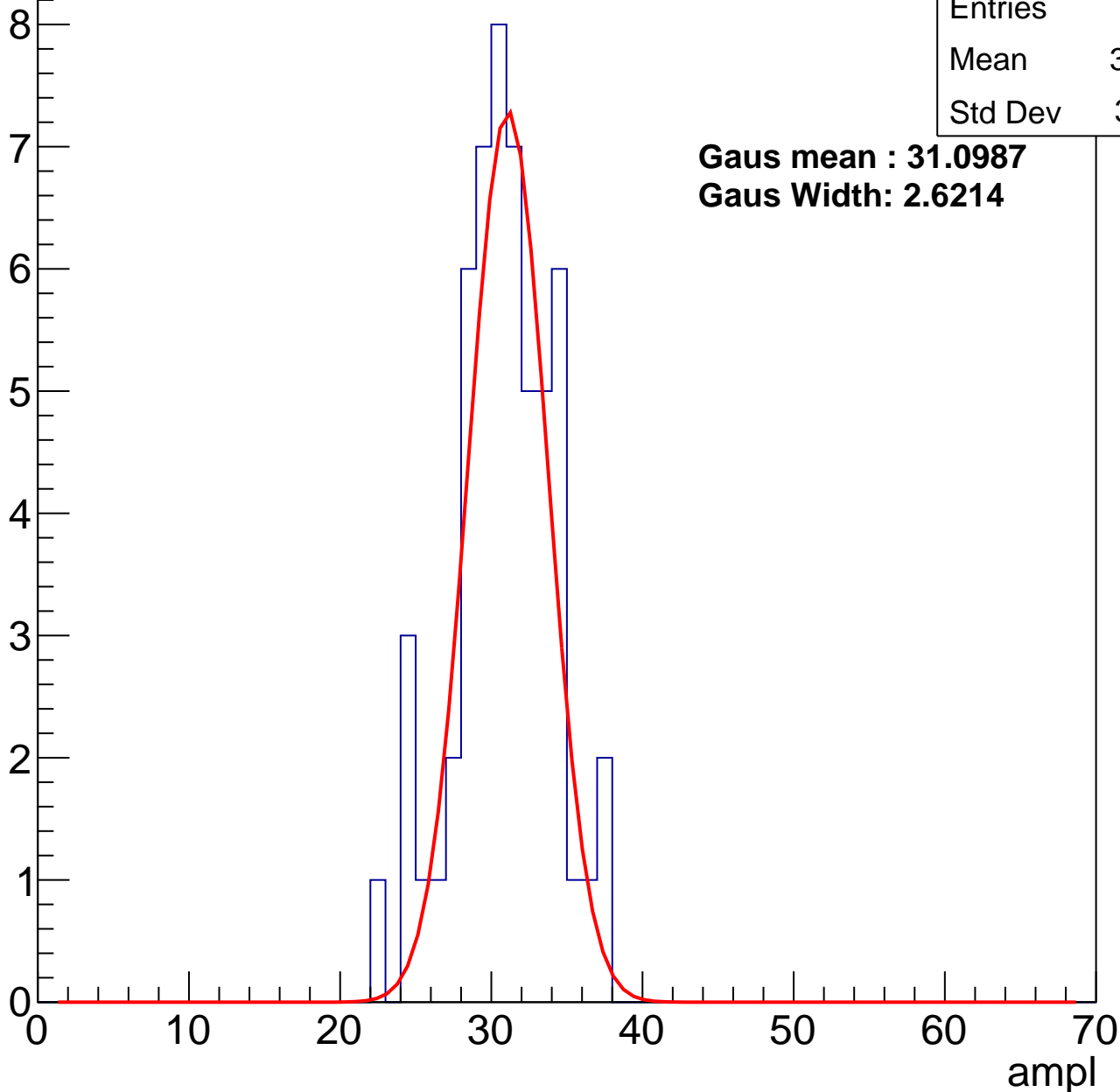
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.38
Std Dev	3.221

Gaus mean : 31.0987

Gaus Width: 2.6214



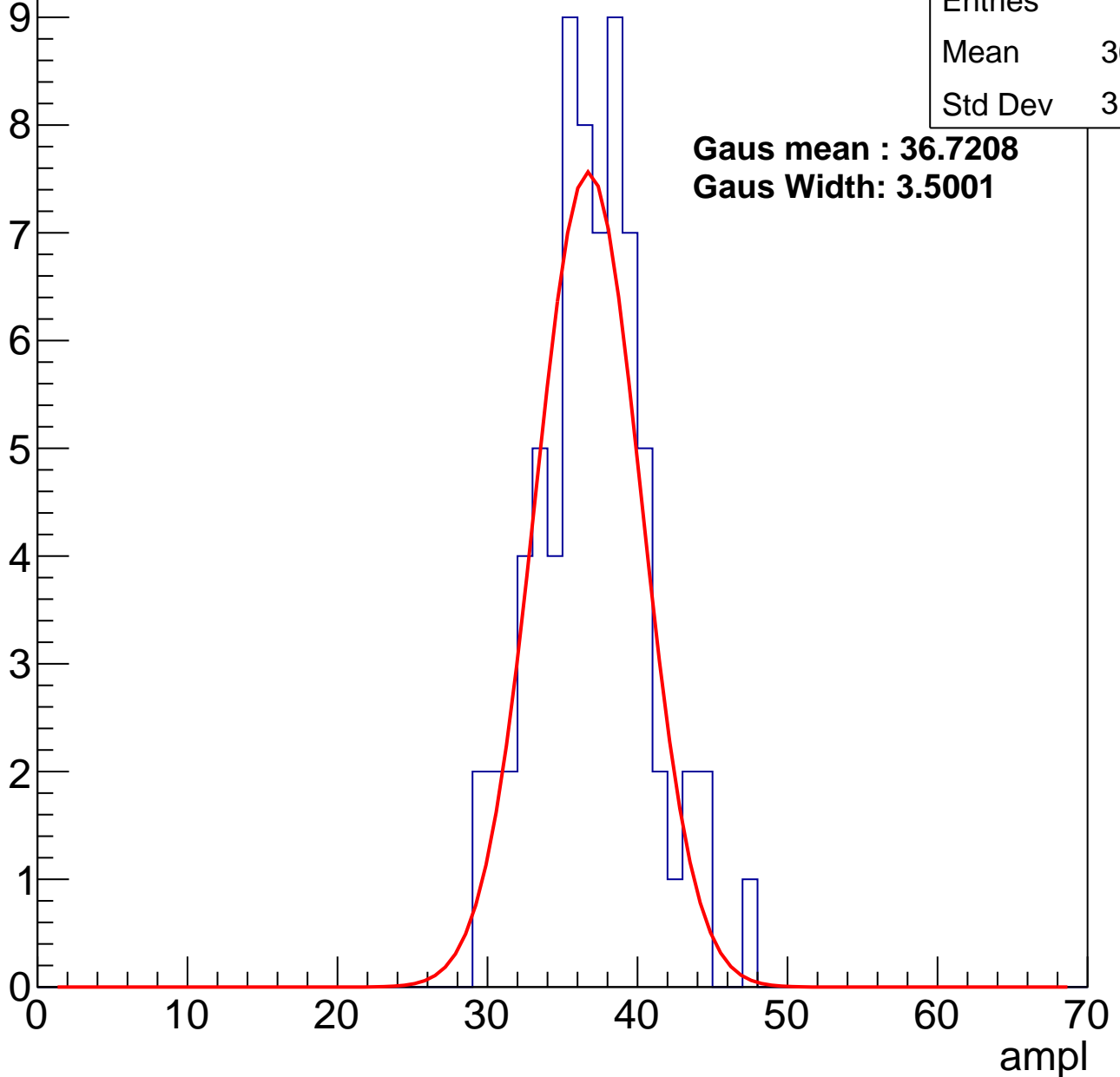
B1L100S, U14-ch57, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.54
Std Dev	3.636

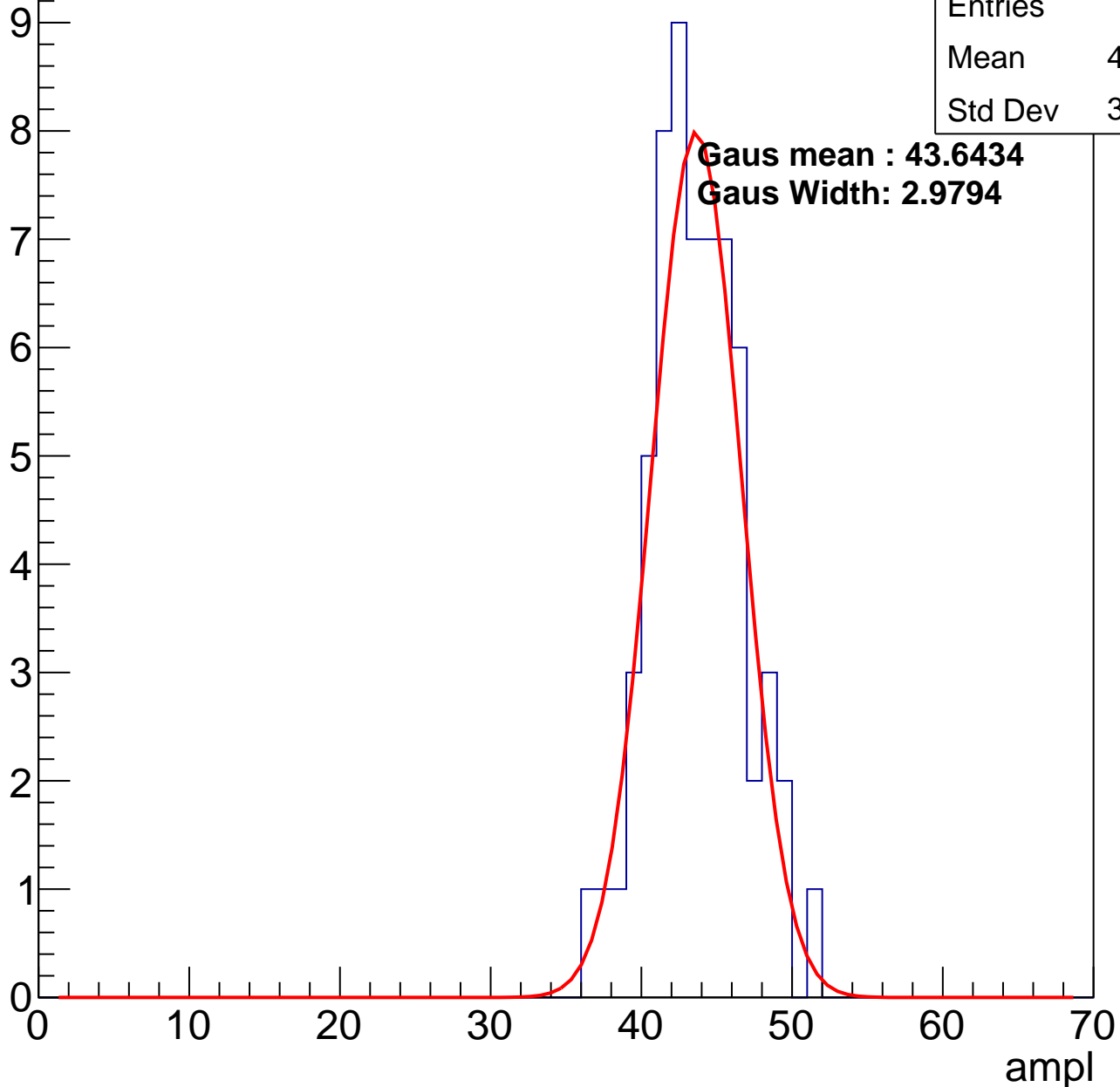
Gaus mean : 36.7208
Gaus Width: 3.5001



B1L100S, U14-ch57, adc2

calib_packv5_042523_0143.root, FC#4, port A2

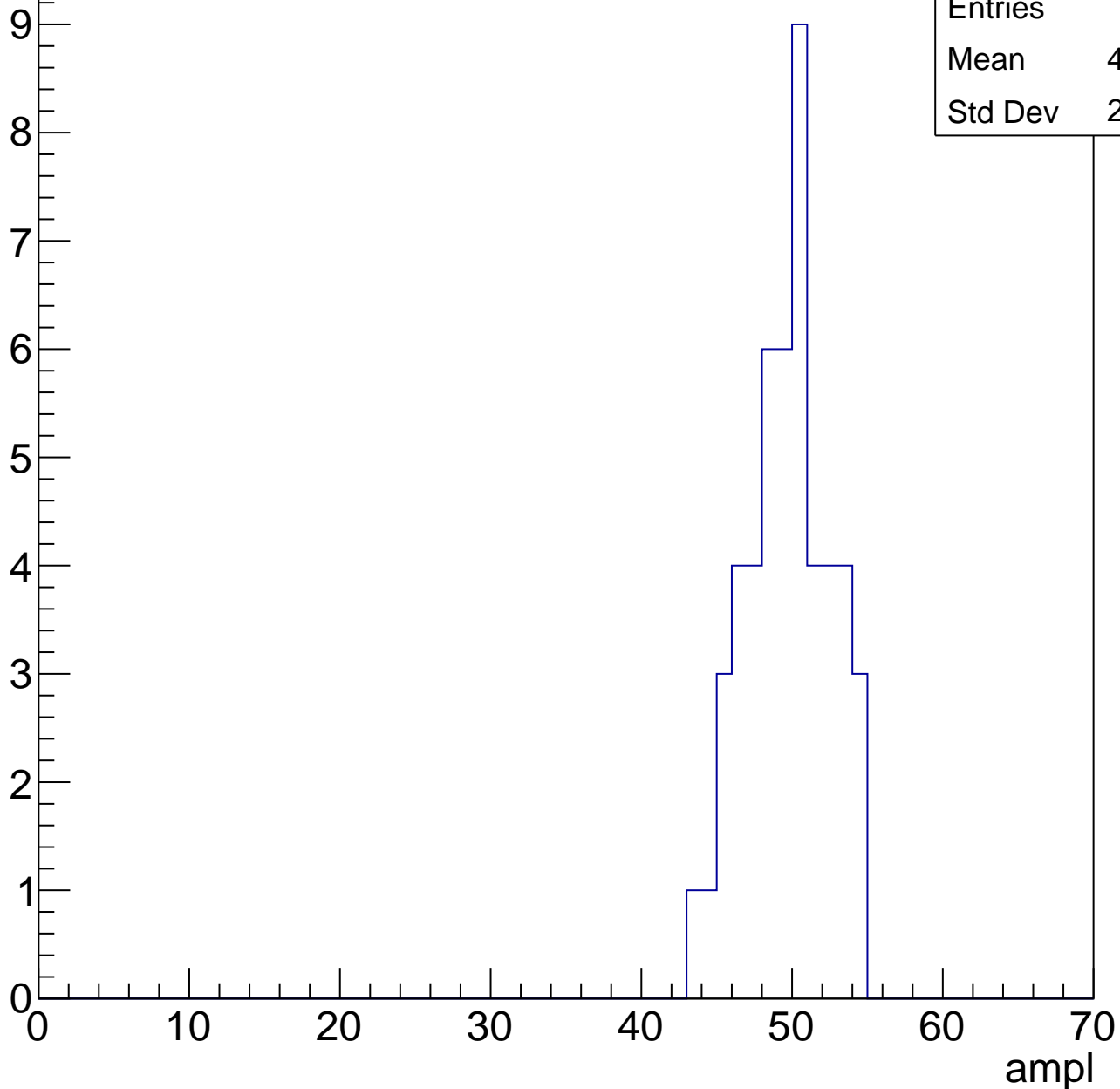
Entry



B1L100S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

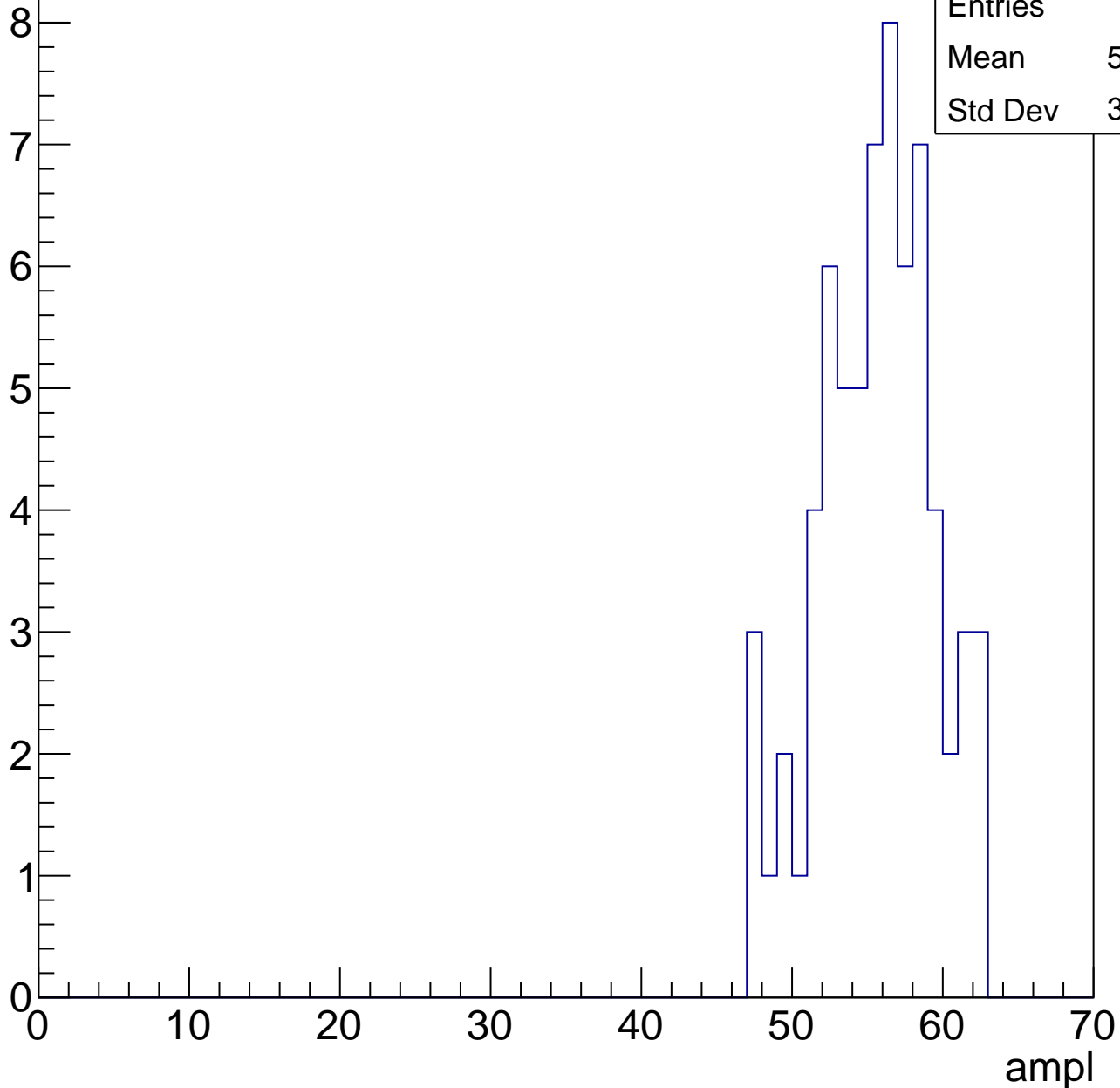


Entries	49
Mean	49.22
Std Dev	2.728

B1L100S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



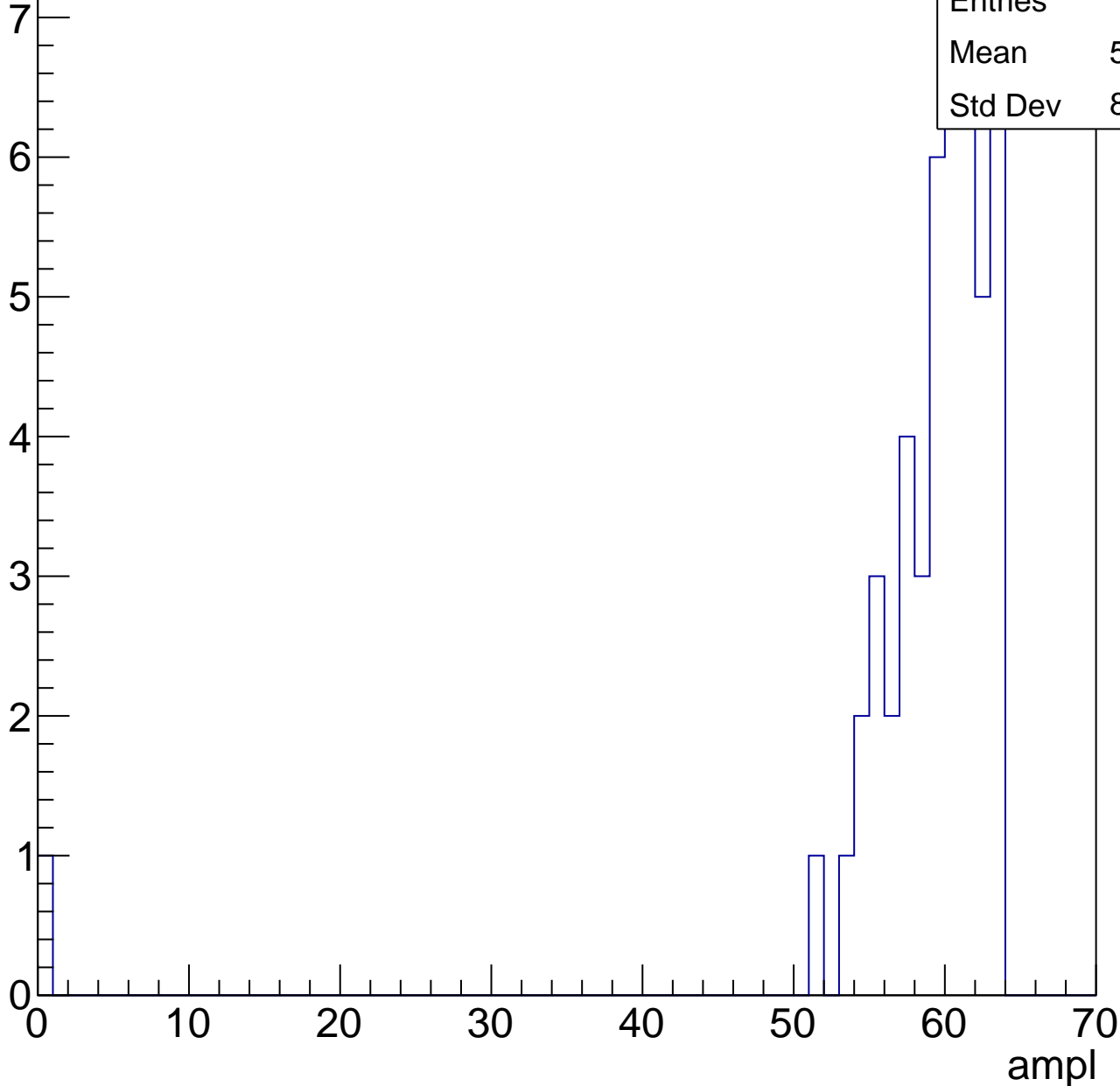
Entries	67
Mean	55.13
Std Dev	3.753

B1L100S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.02
Std Dev	8.877



B1L100S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch58, adc0

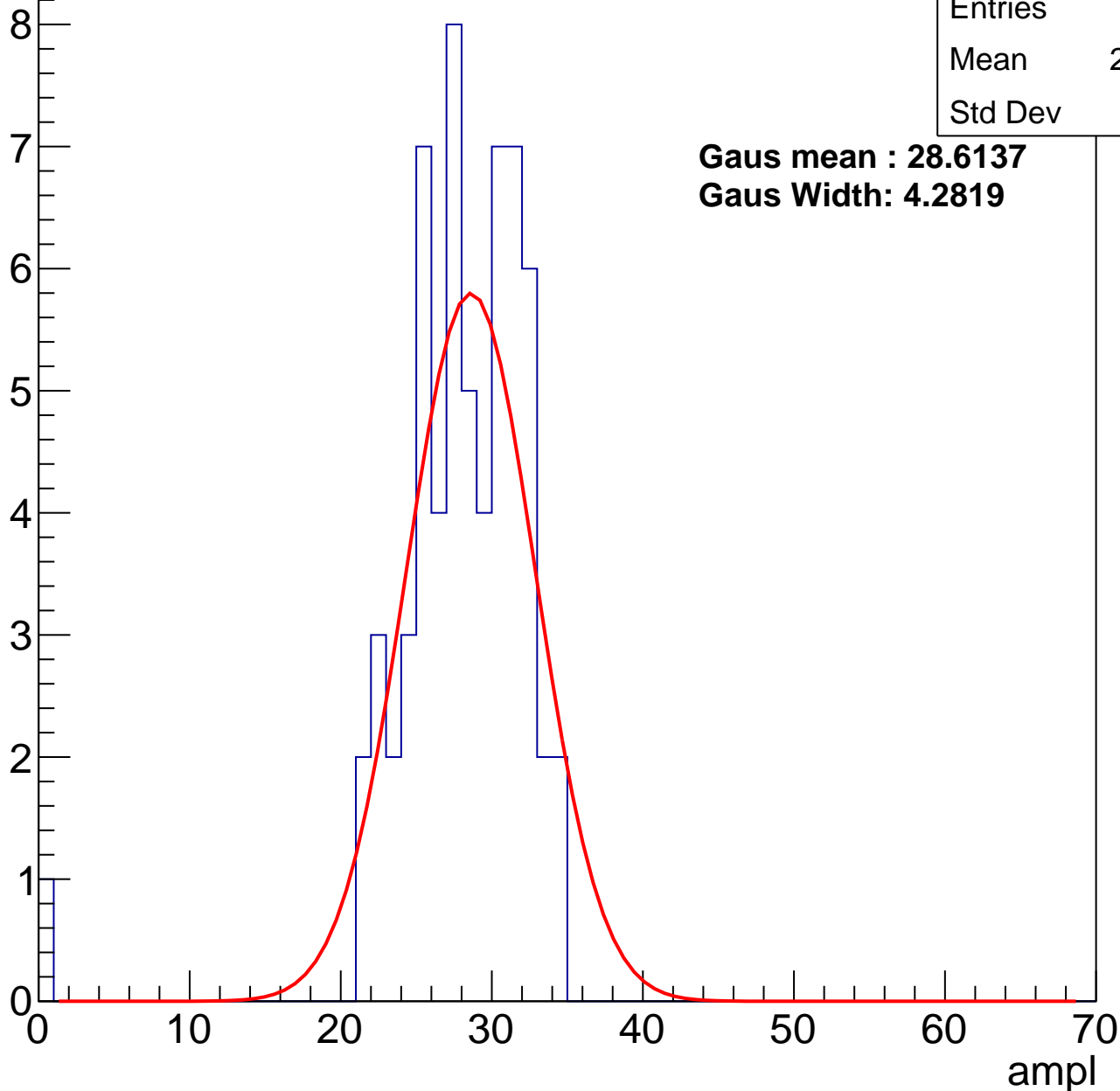
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.46
Std Dev	4.83

Gaus mean : 28.6137

Gaus Width: 4.2819



B1L100S, U14-ch58, adc1

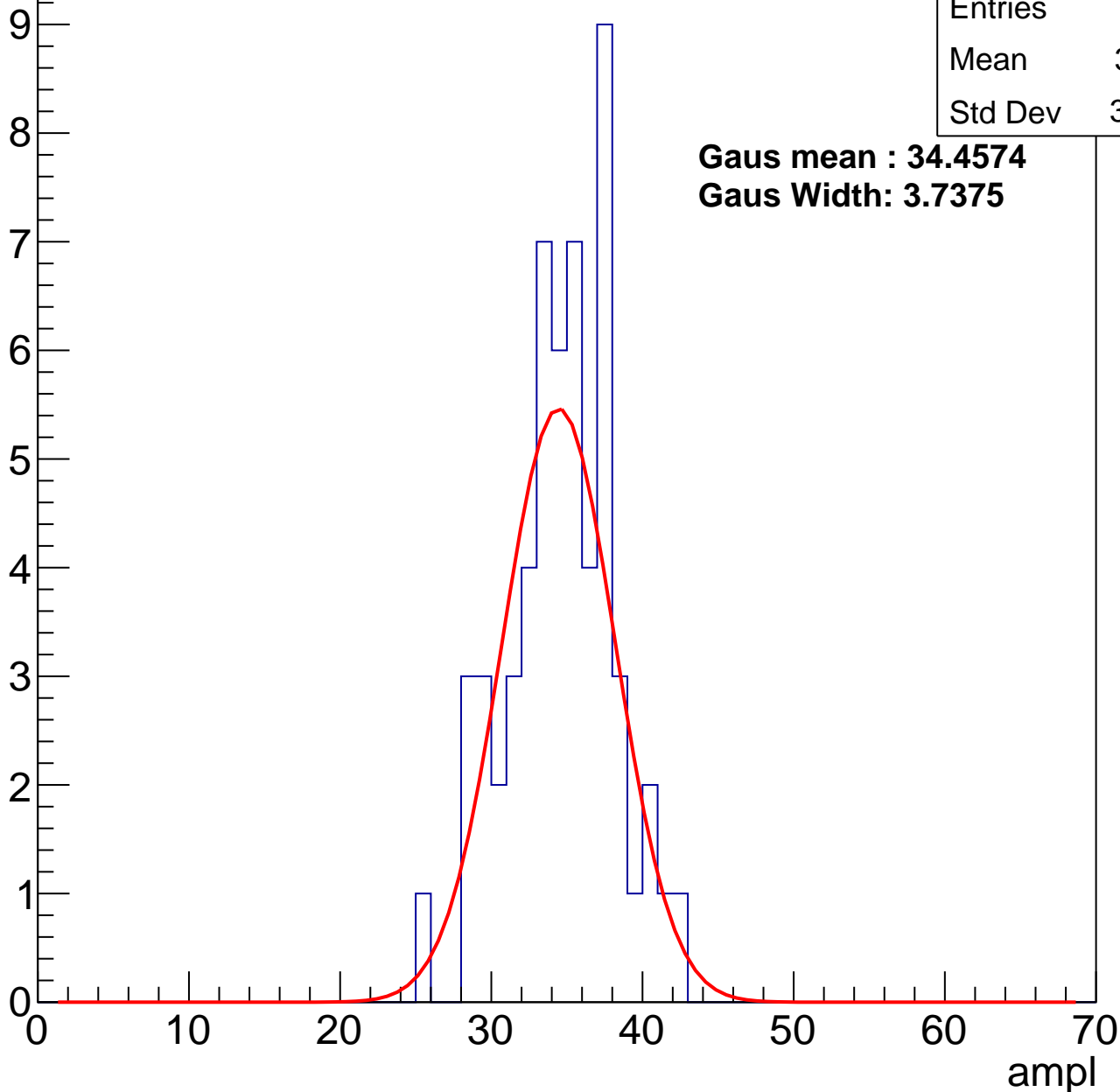
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.21
Std Dev	3.518

Gaus mean : 34.4574

Gaus Width: 3.7375



B1L100S, U14-ch58, adc2

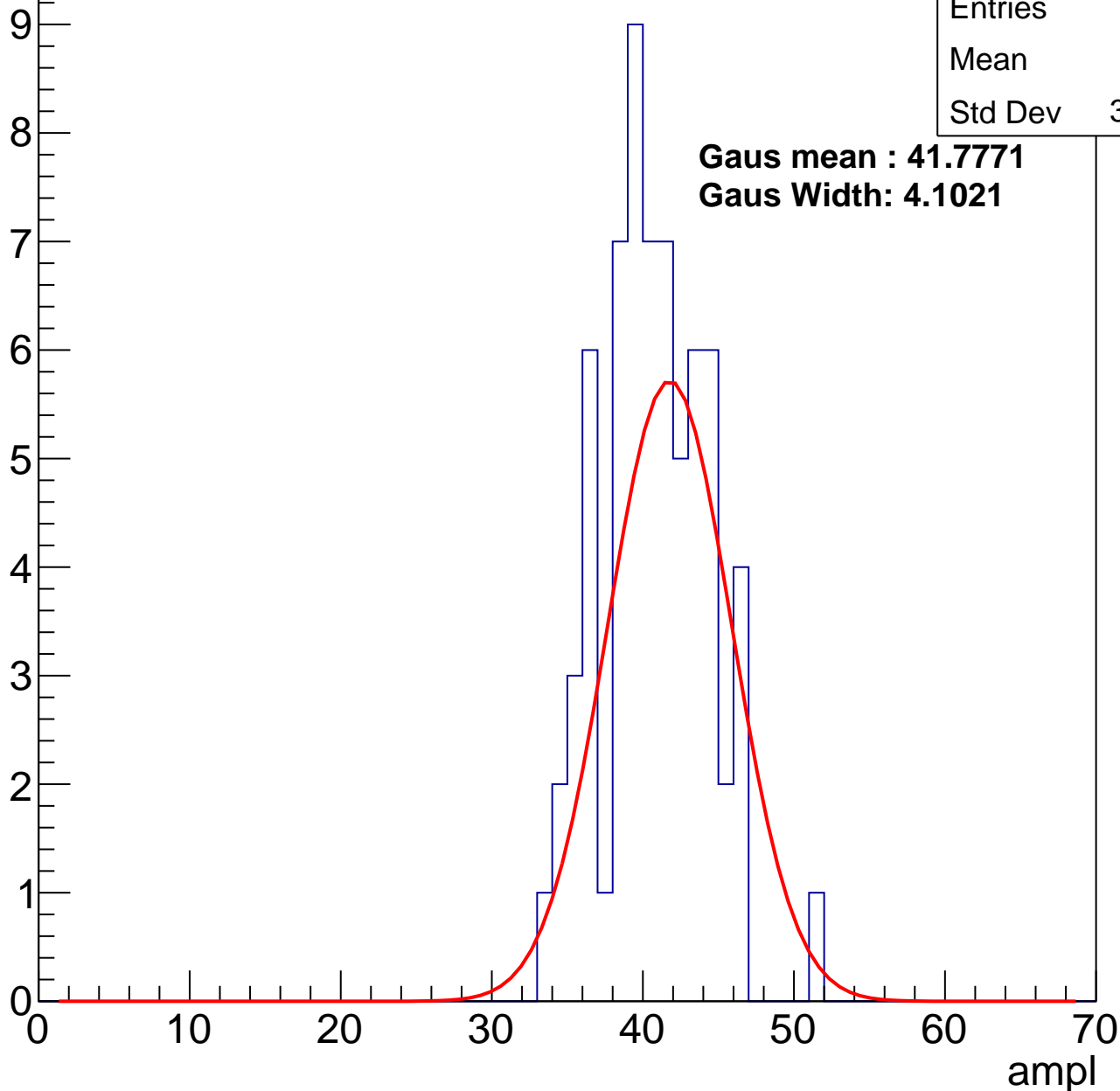
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	40.3
Std Dev	3.507

Gaus mean : 41.7771

Gaus Width: 4.1021

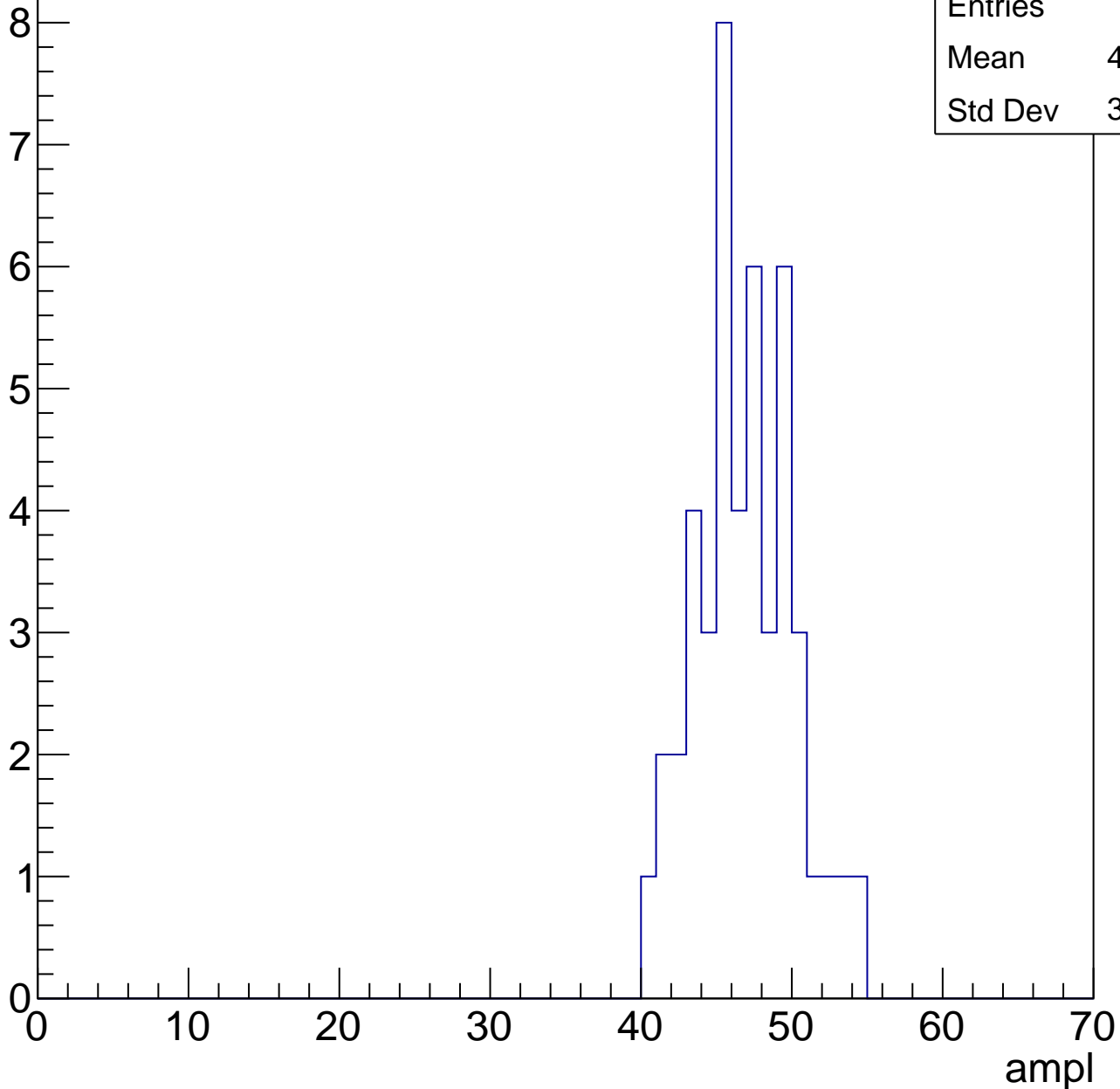


B1L100S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	46.39
Std Dev	3.166

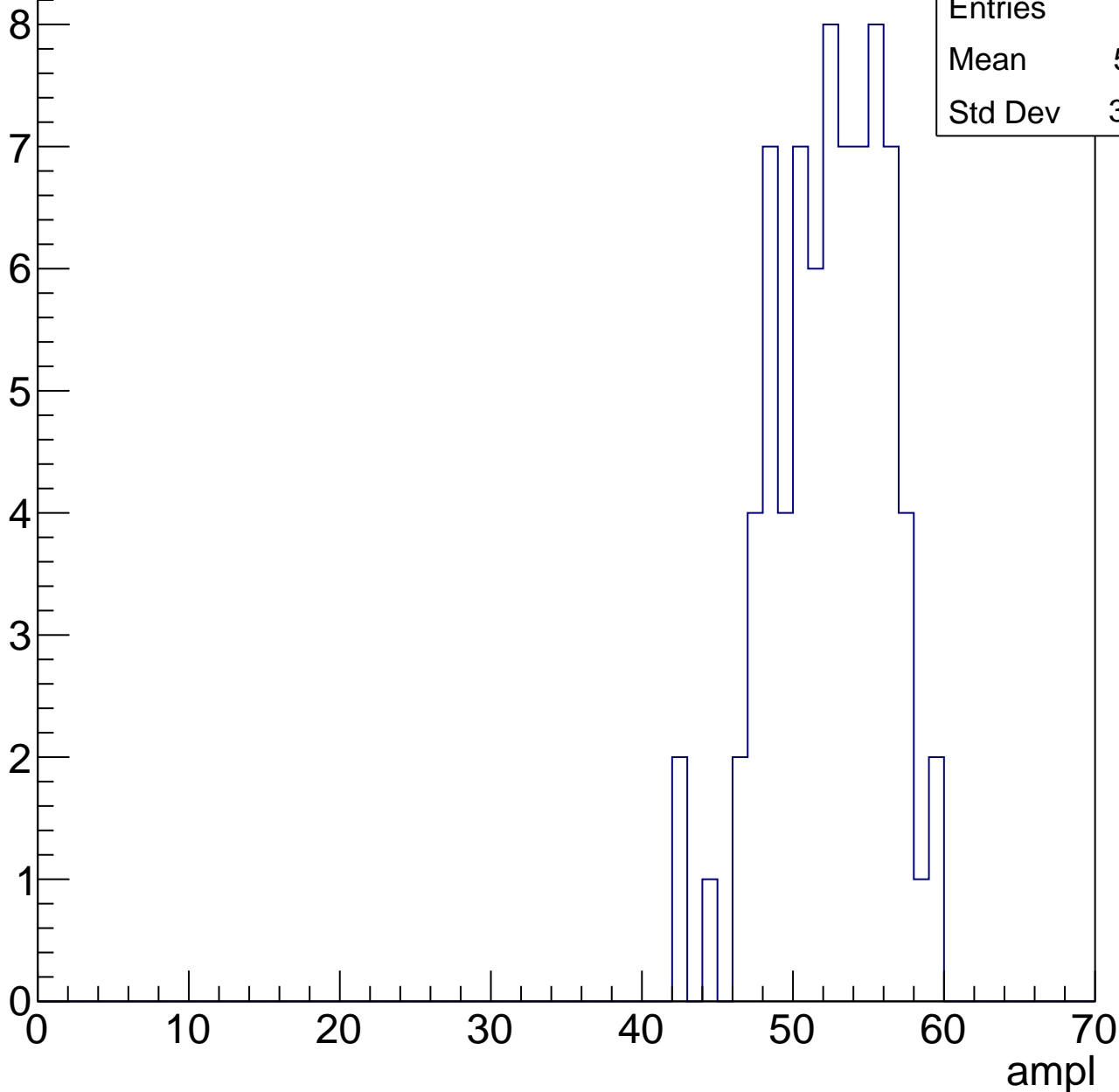


B1L100S, U14-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	51.91
Std Dev	3.728

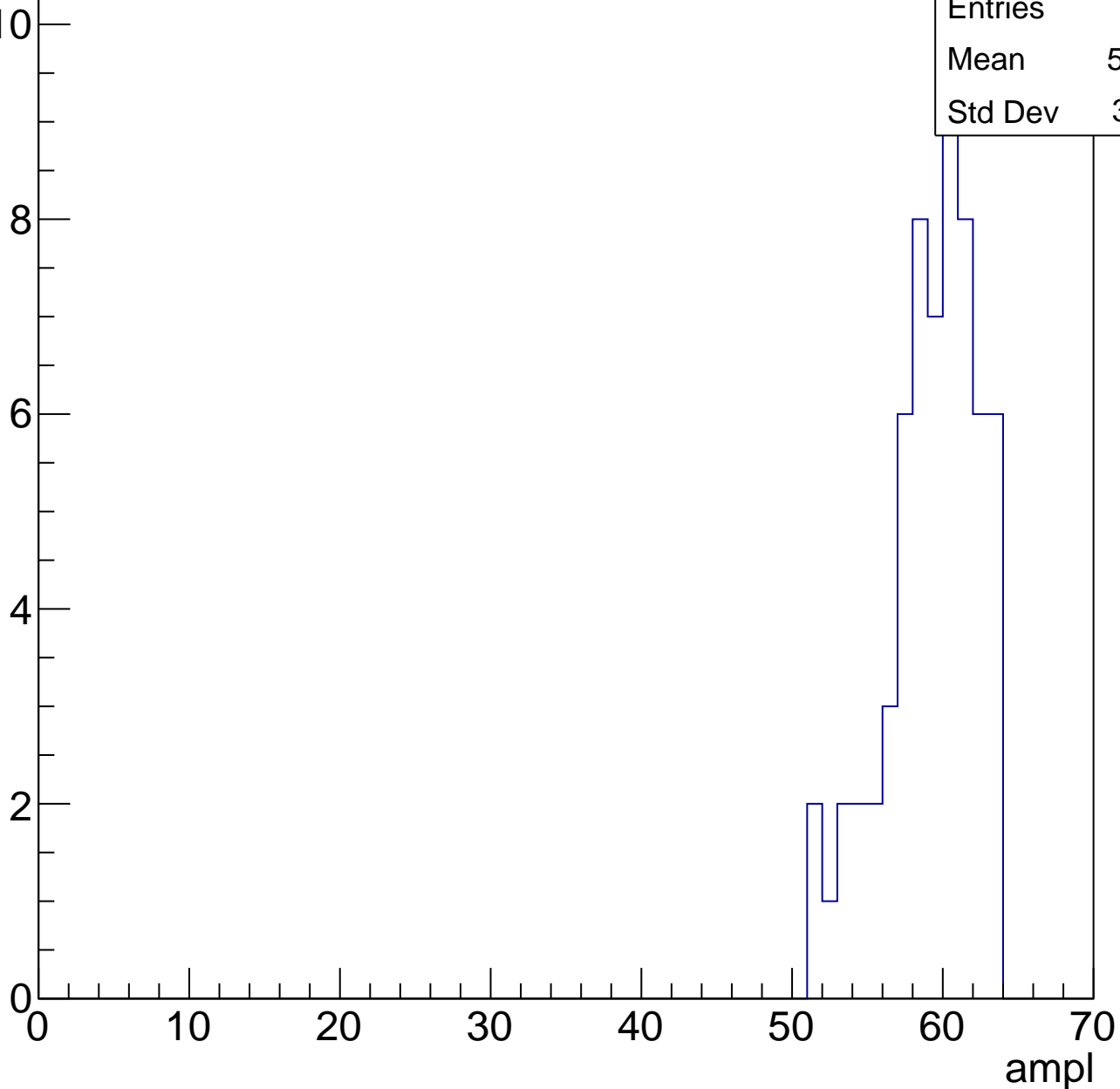


B1L100S, U14-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

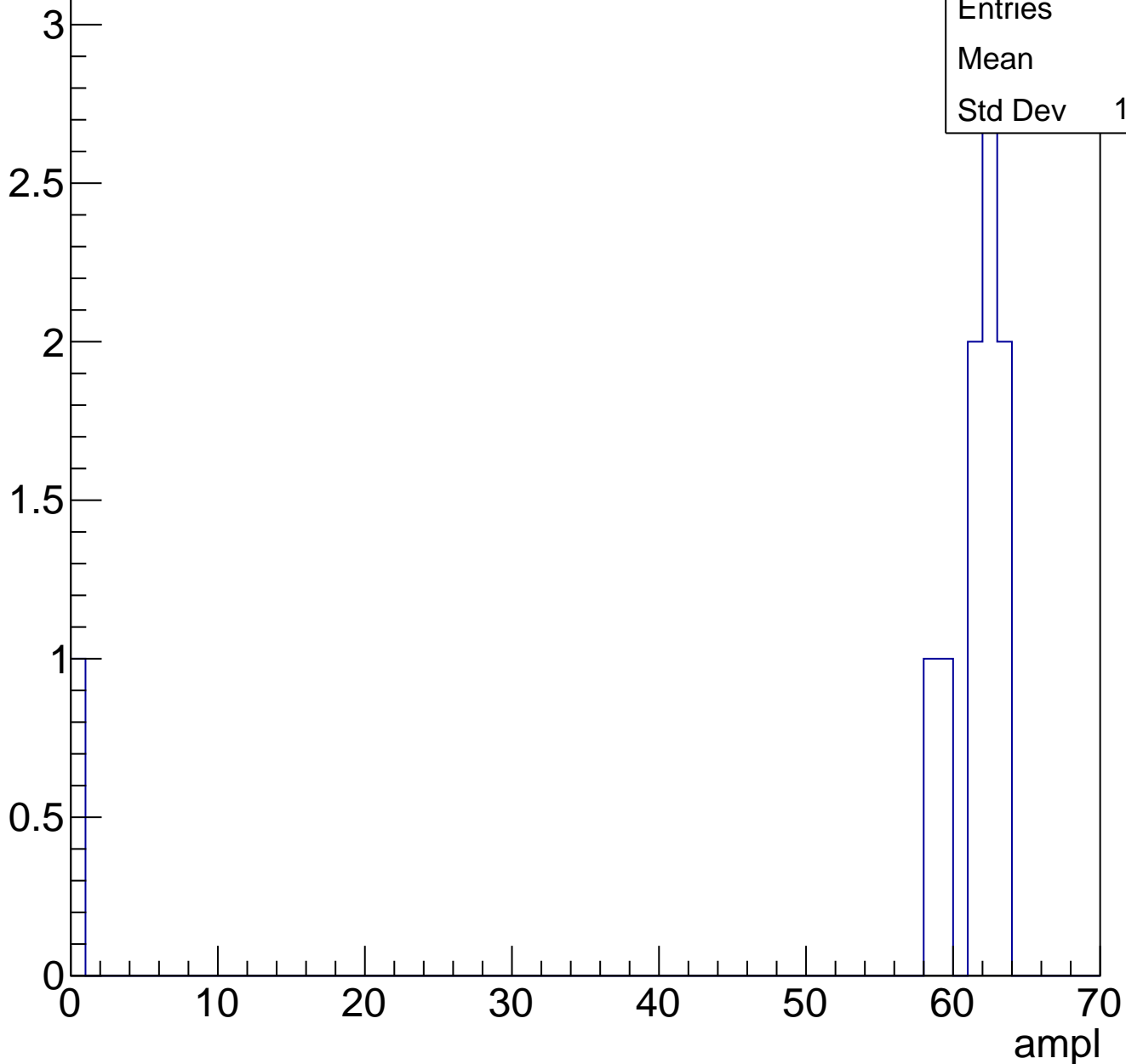
Entries	63
Mean	58.78
Std Dev	3.031



B1L100S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

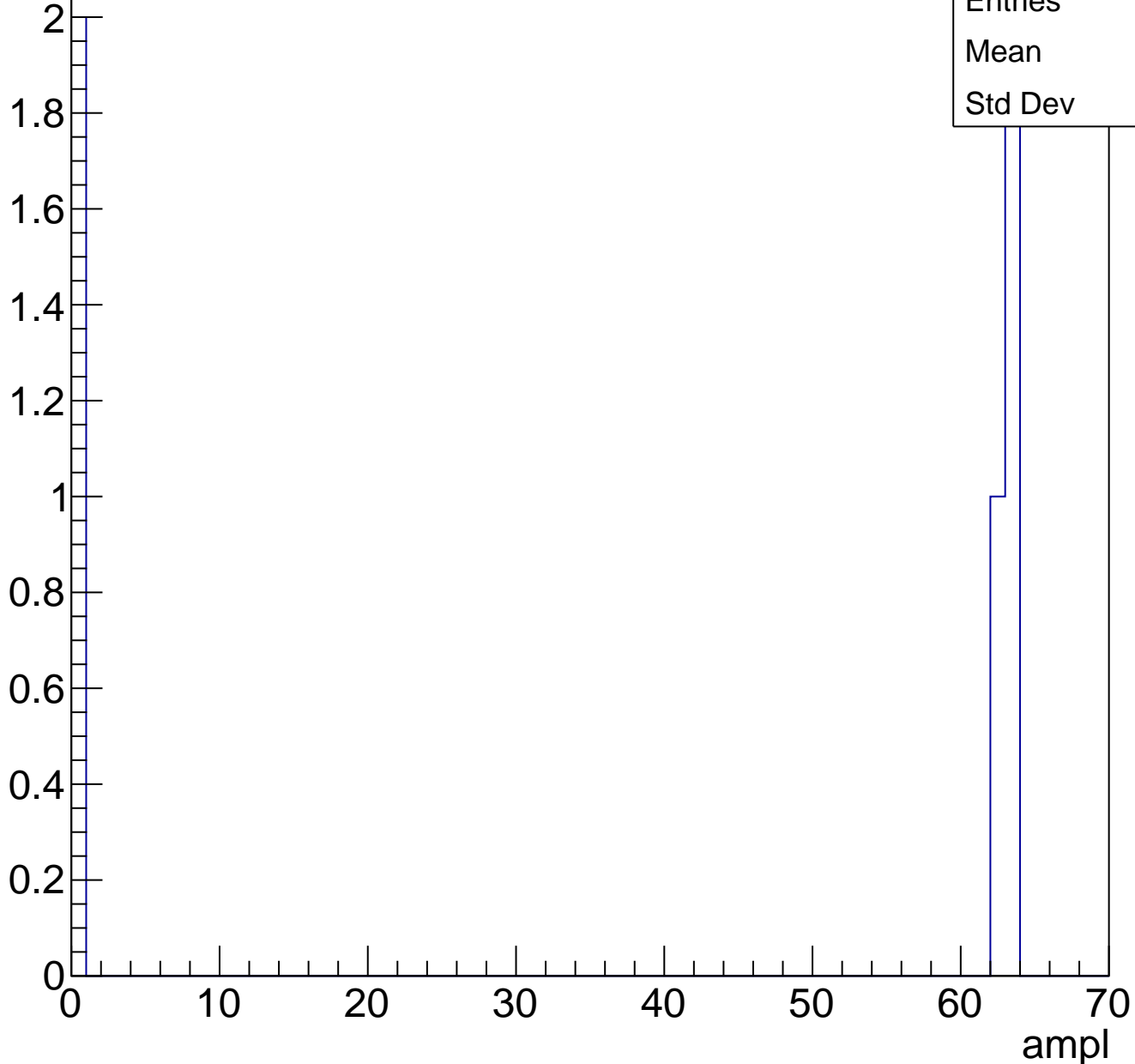
Entry



B1L100S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	37.6
Std Dev	30.7

B1L100S, U14-ch59, adc0

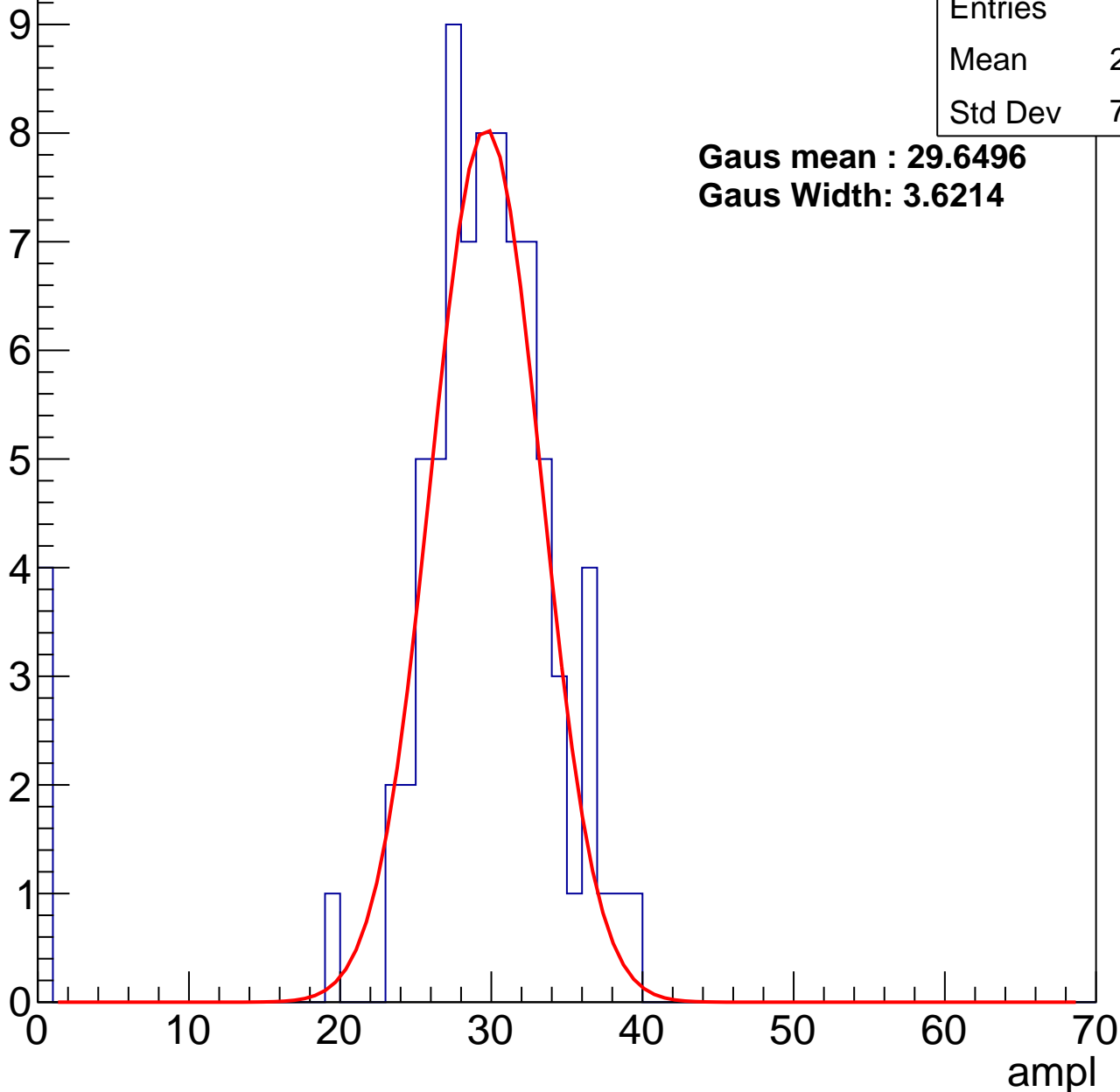
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	28.15
Std Dev	7.392

Gaus mean : 29.6496

Gaus Width: 3.6214



B1L100S, U14-ch59, adc1

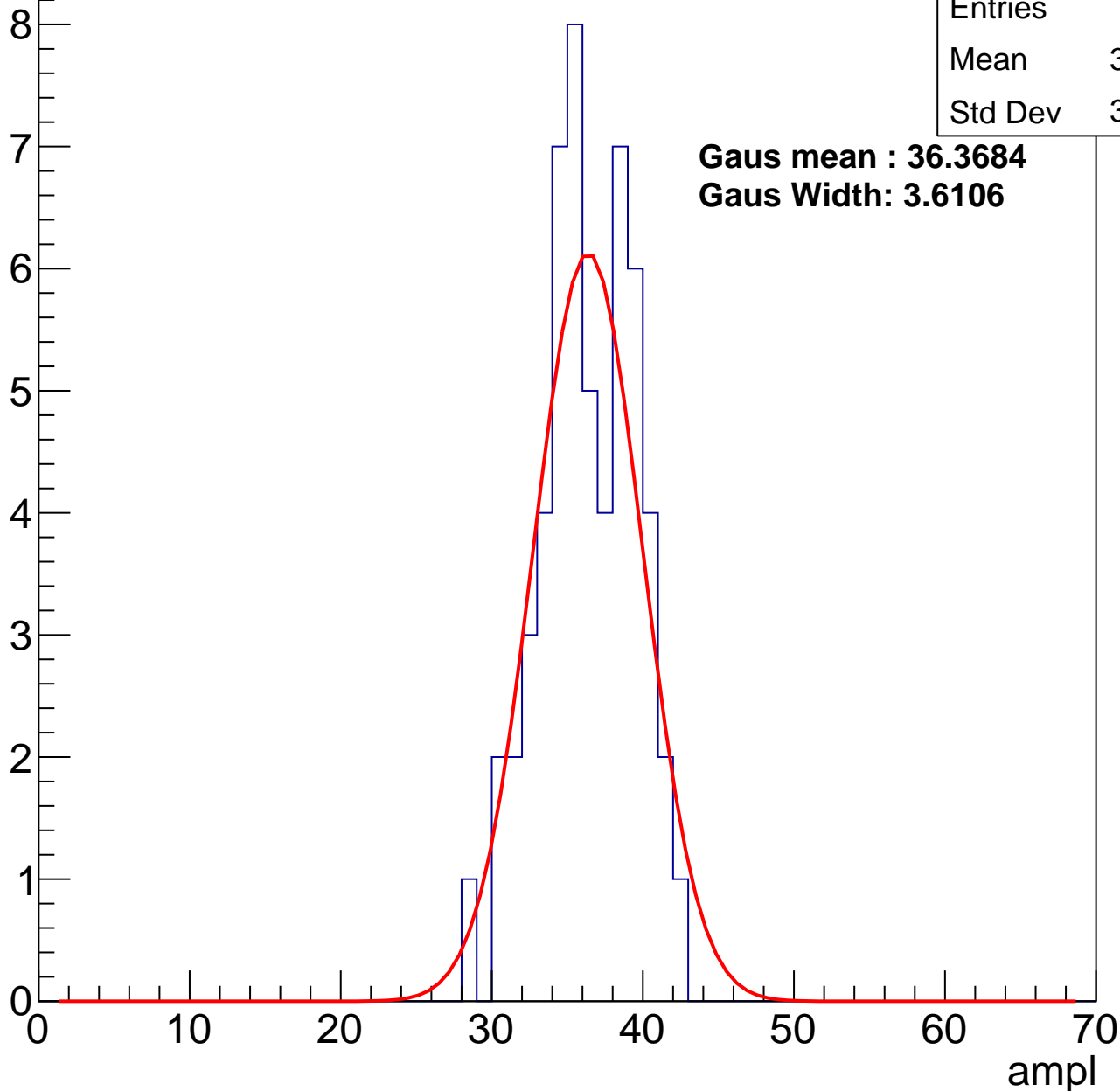
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	35.86
Std Dev	3.108

Gaus mean : 36.3684

Gaus Width: 3.6106



B1L100S, U14-ch59, adc2

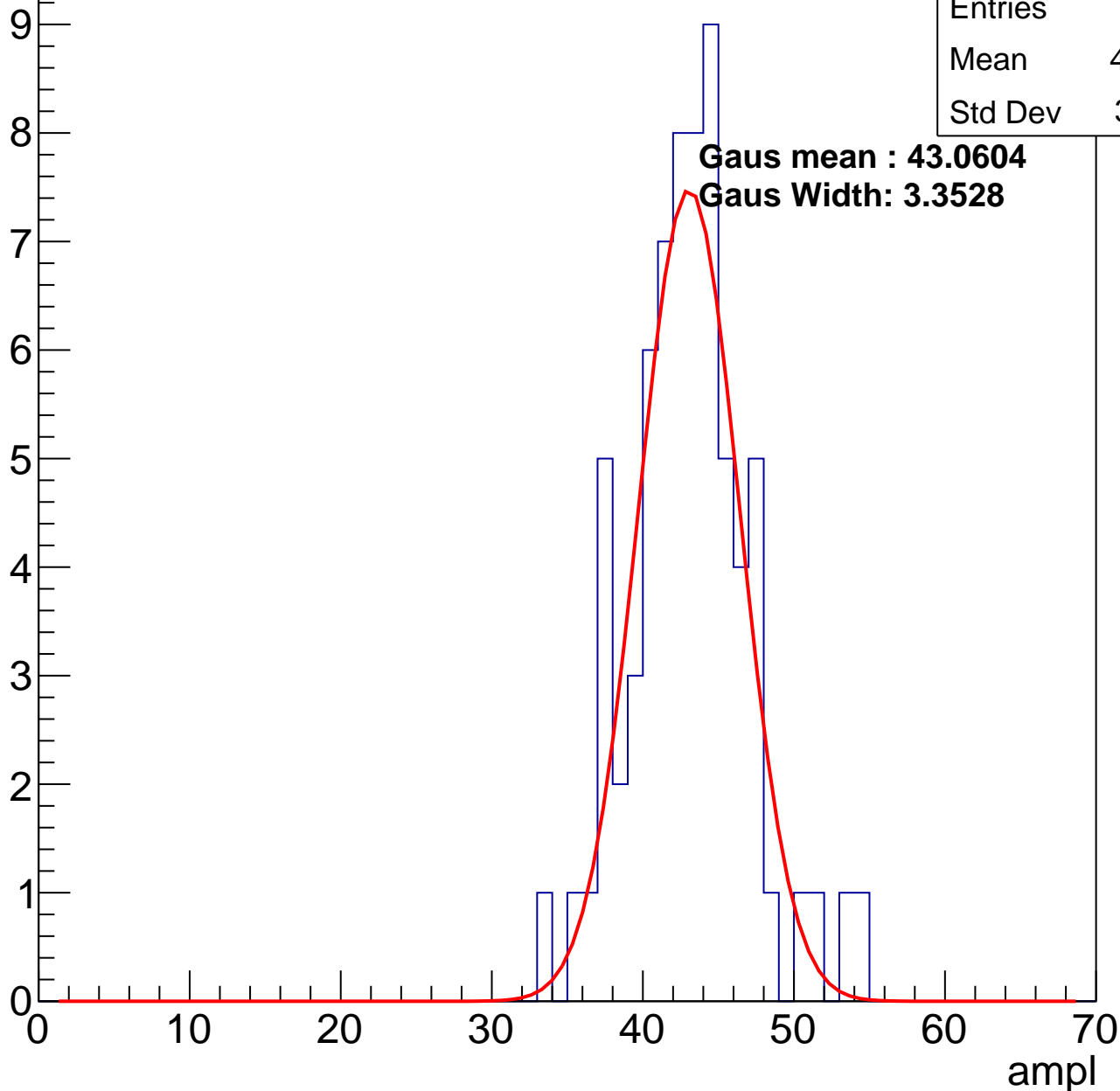
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.64
Std Dev	3.921

Gaus mean : 43.0604

Gaus Width: 3.3528

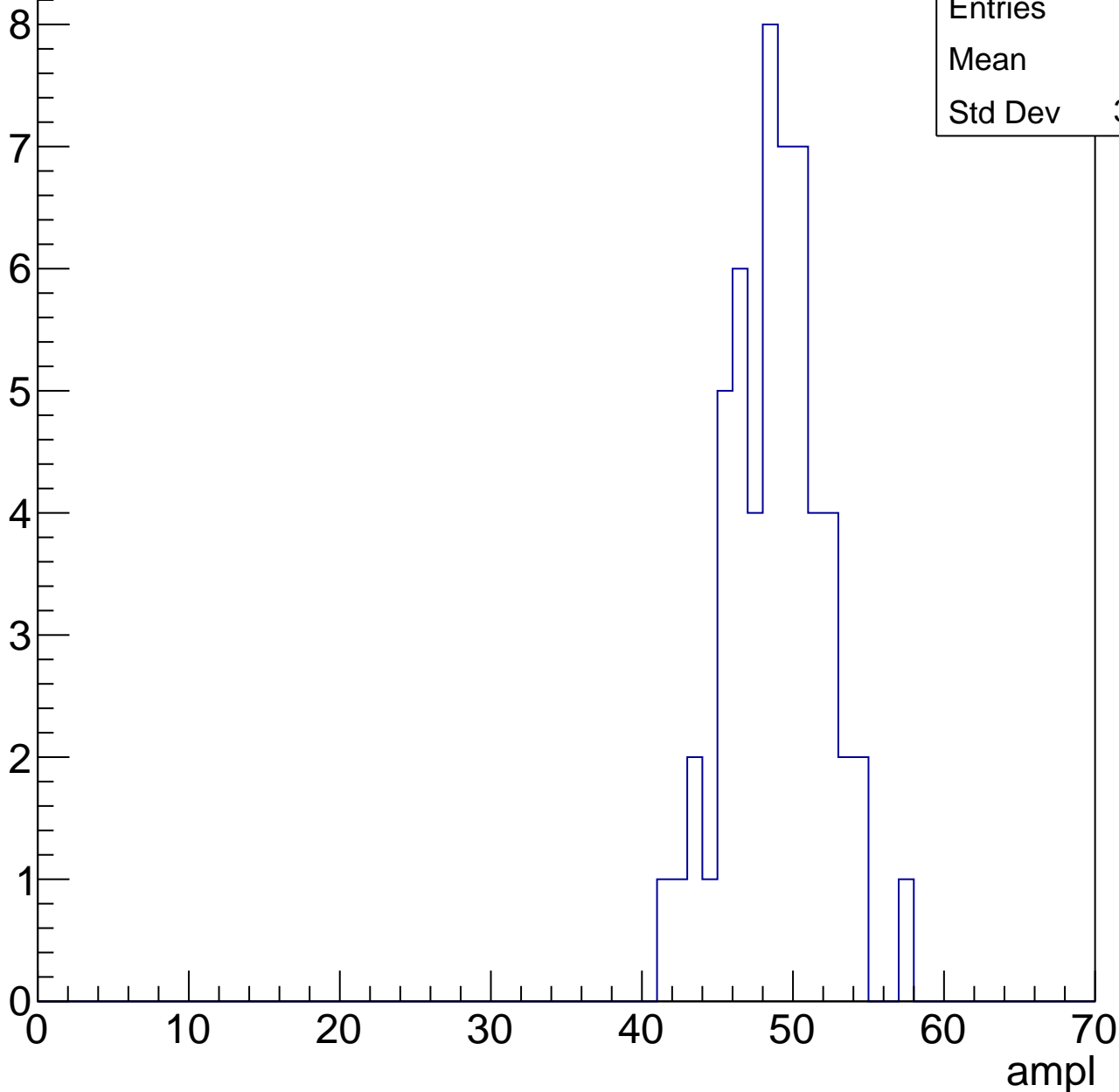


B1L100S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	48.4
Std Dev	3.171

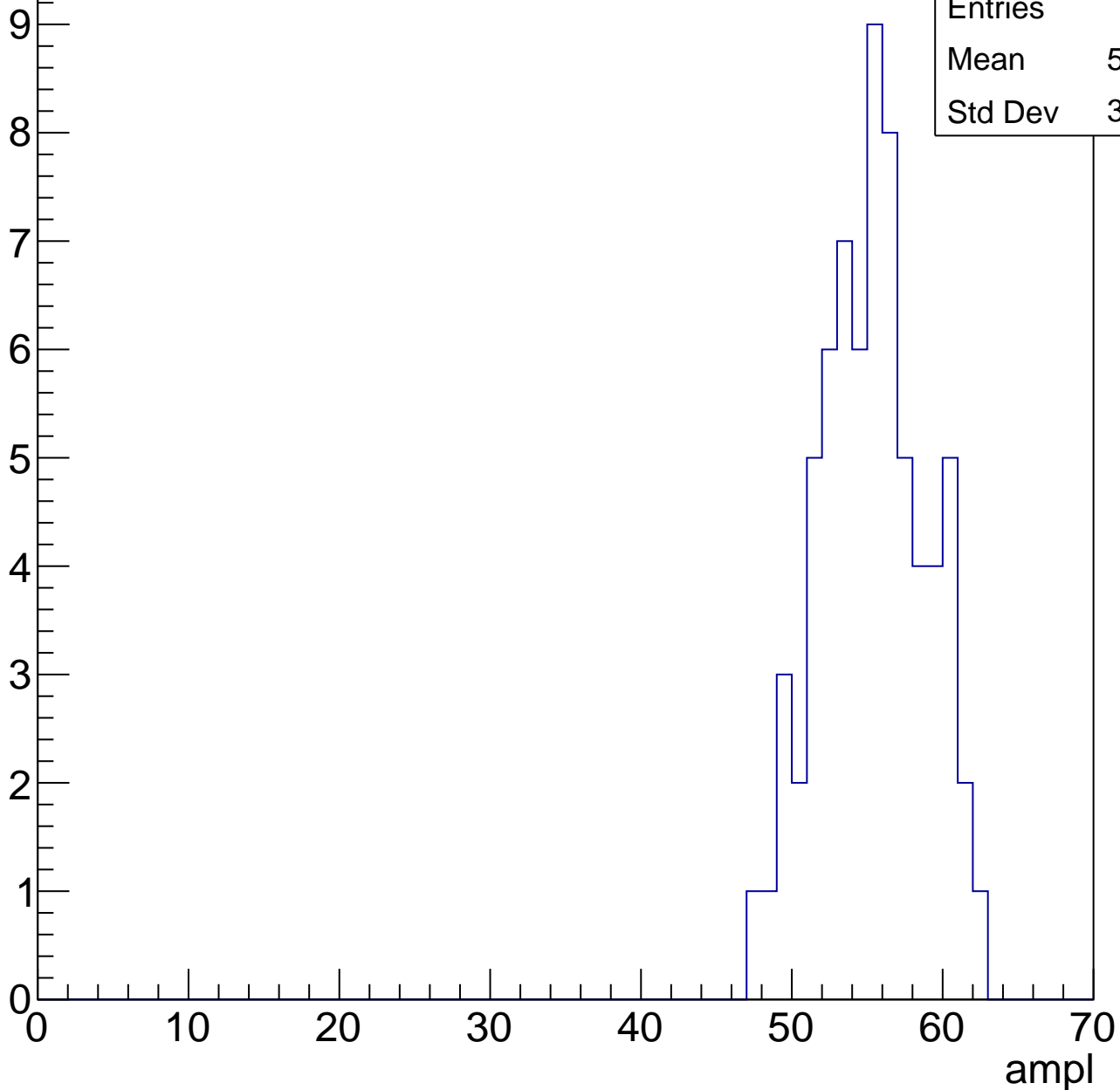


B1L100S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	54.84
Std Dev	3.429

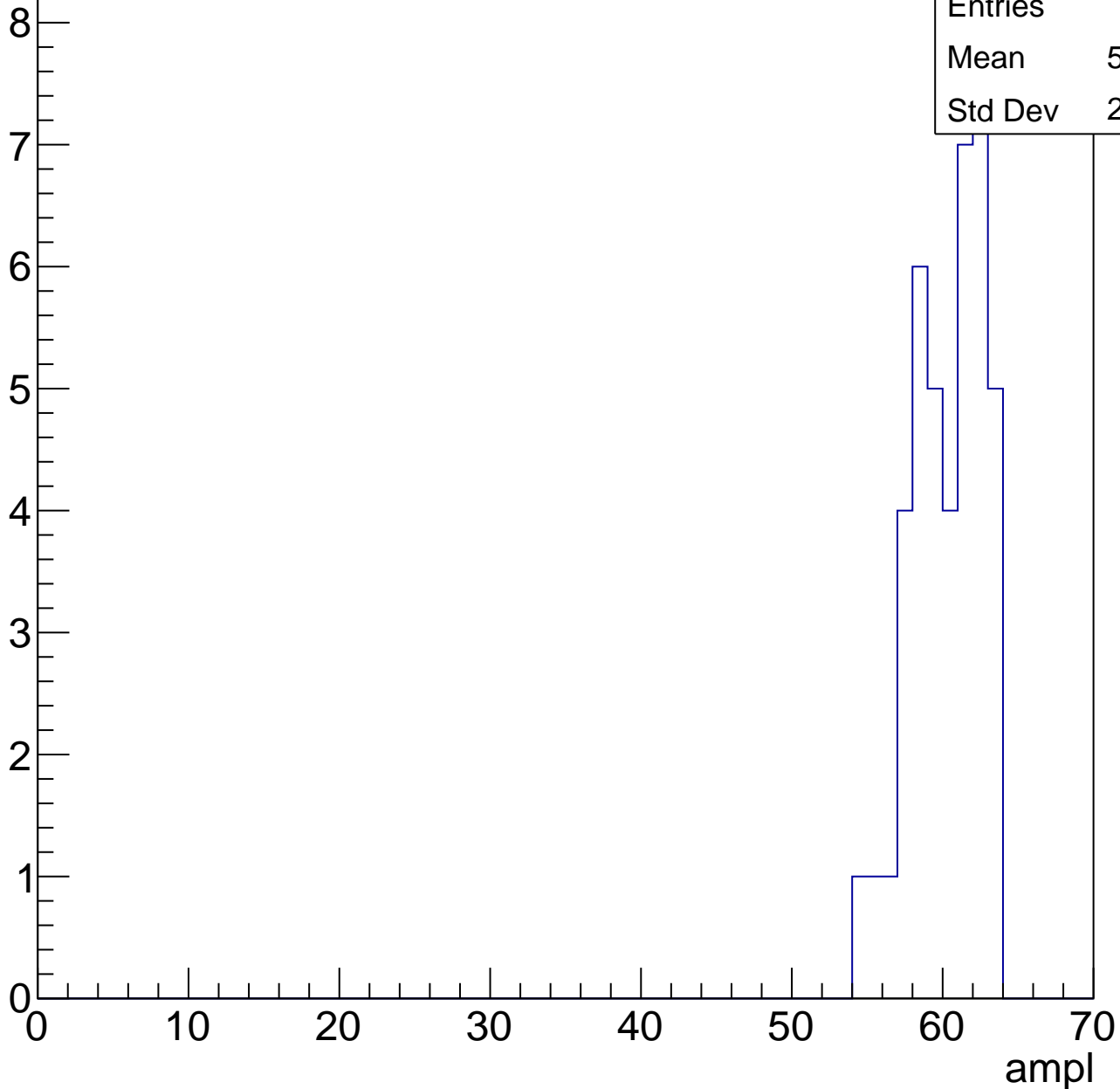


B1L100S, U14-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	59.86
Std Dev	2.315

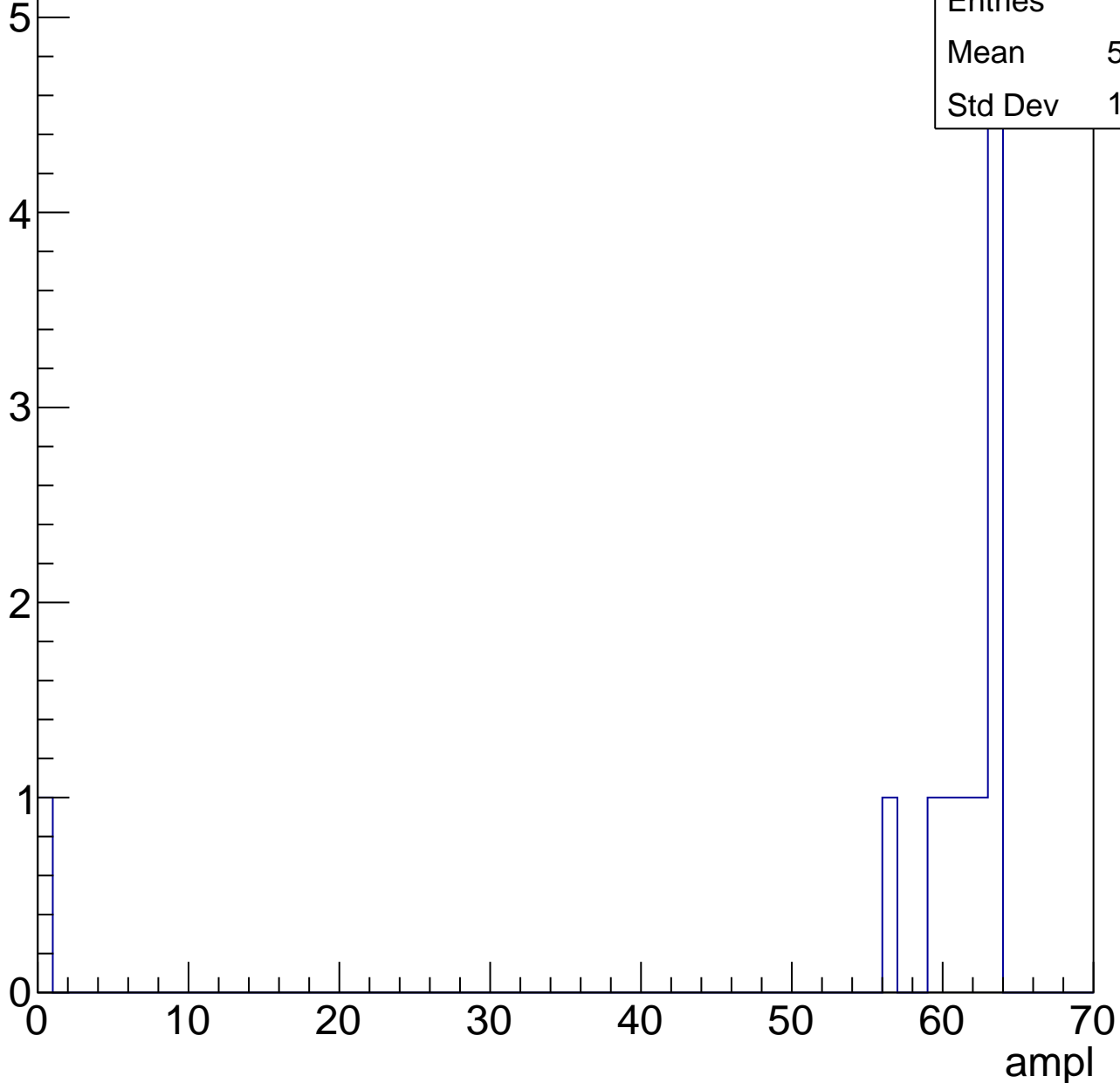


B1L100S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	55.73
Std Dev	17.75



B1L100S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch60, adc0

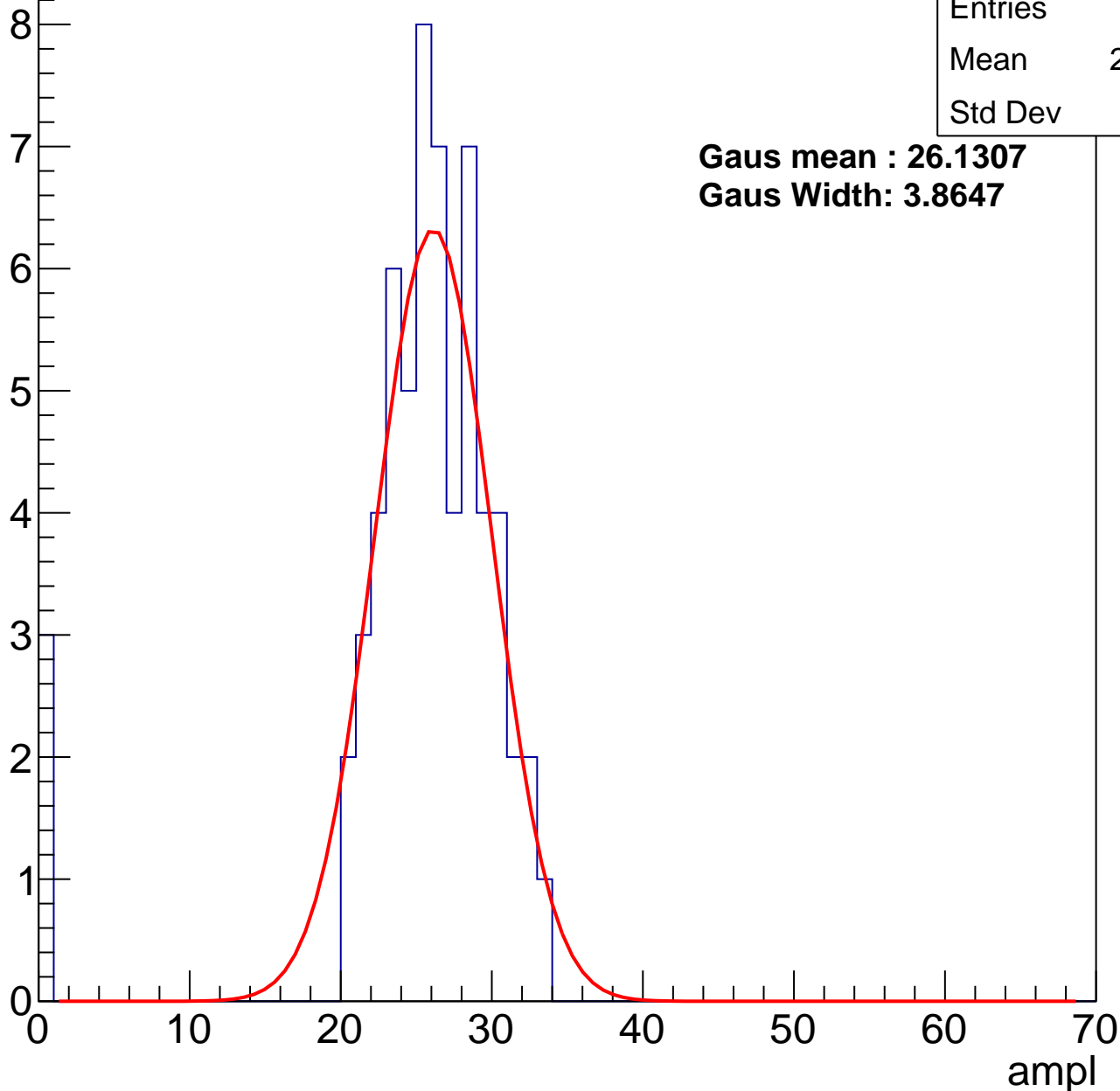
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	24.68
Std Dev	6.37

Gaus mean : 26.1307

Gaus Width: 3.8647



B1L100S, U14-ch60, adc1

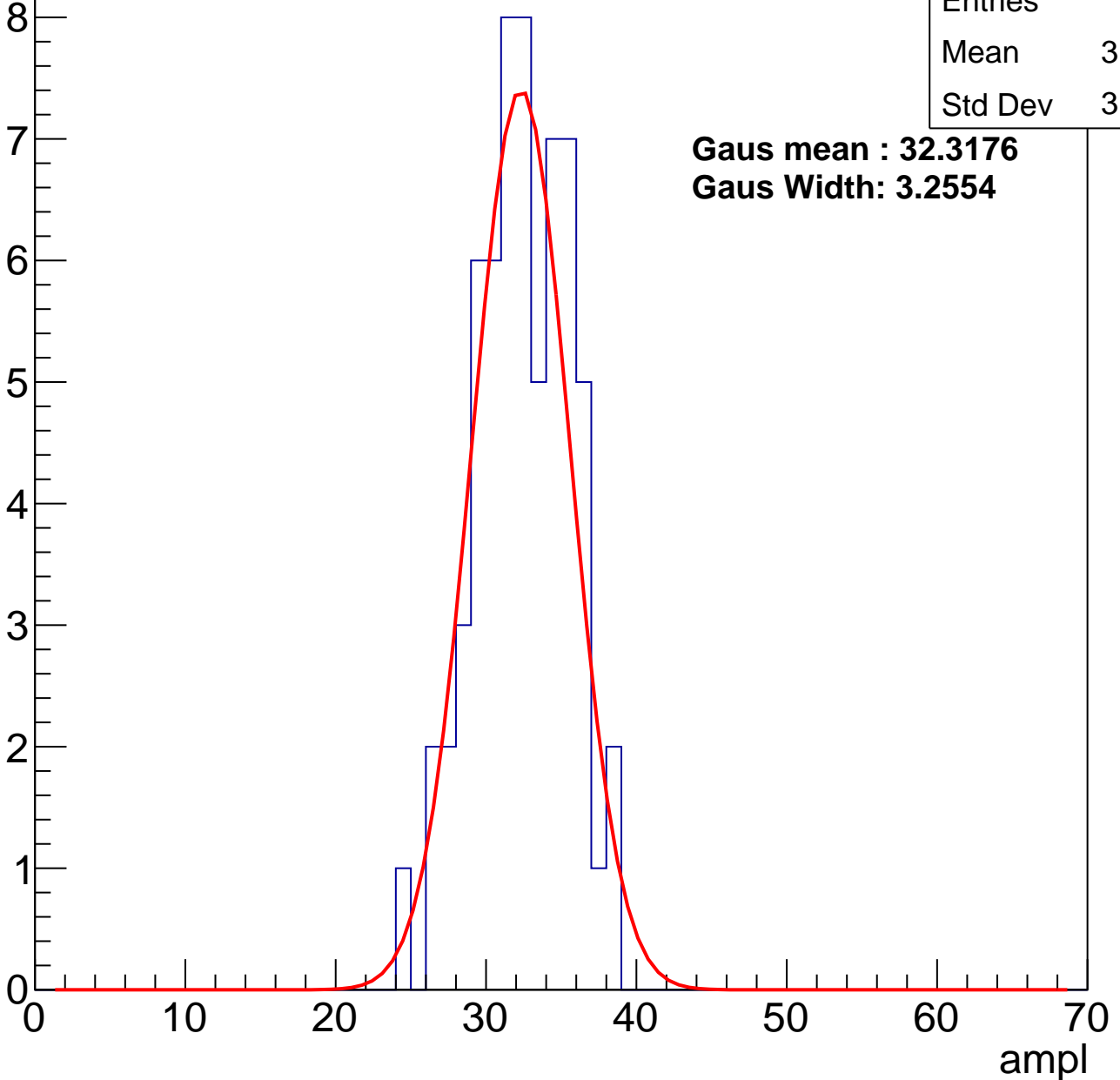
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	31.95
Std Dev	3.068

Gaus mean : 32.3176

Gaus Width: 3.2554



B1L100S, U14-ch60, adc2

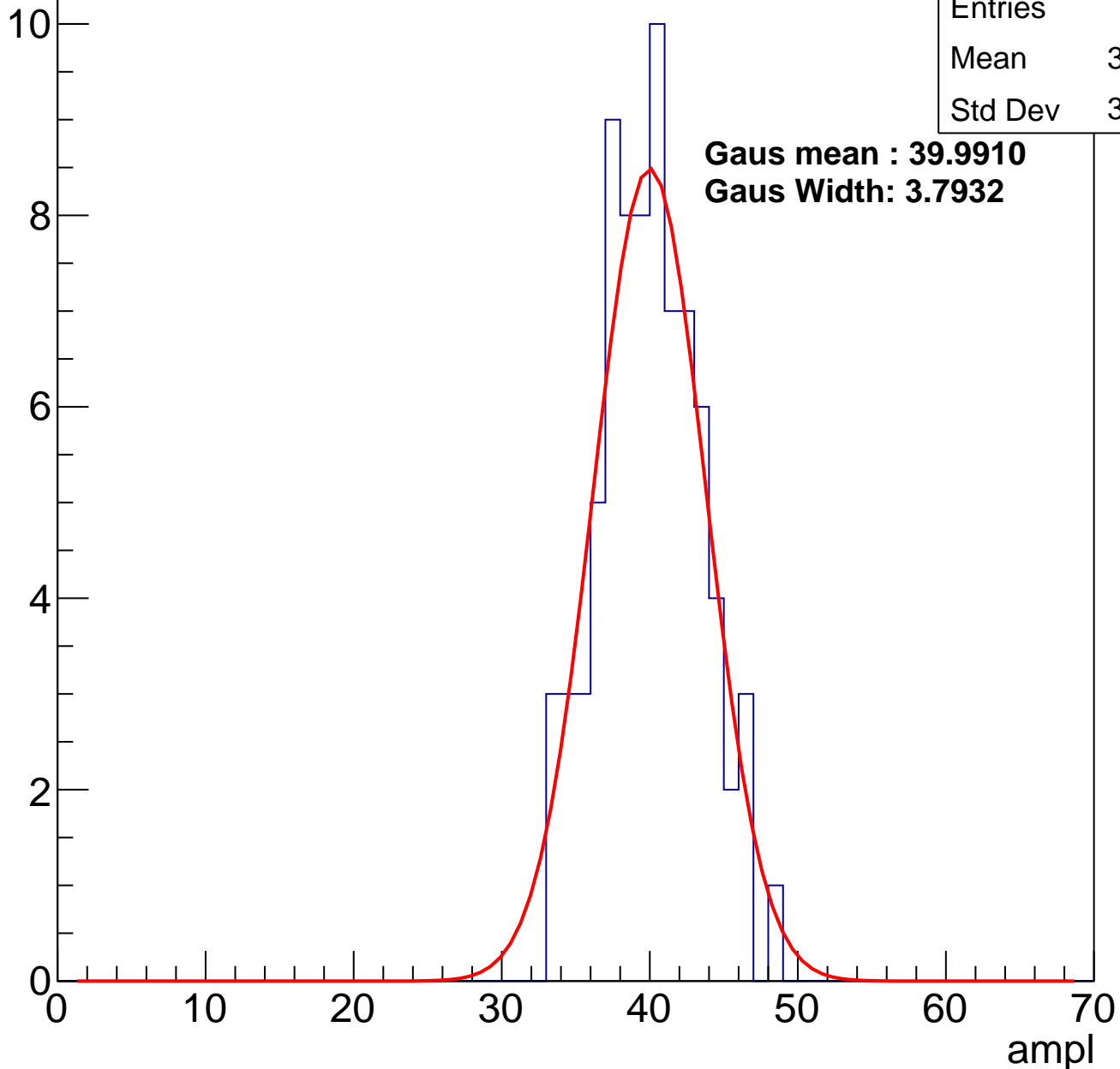
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	39.57
Std Dev	3.359

Gaus mean : 39.9910

Gaus Width: 3.7932

Entry

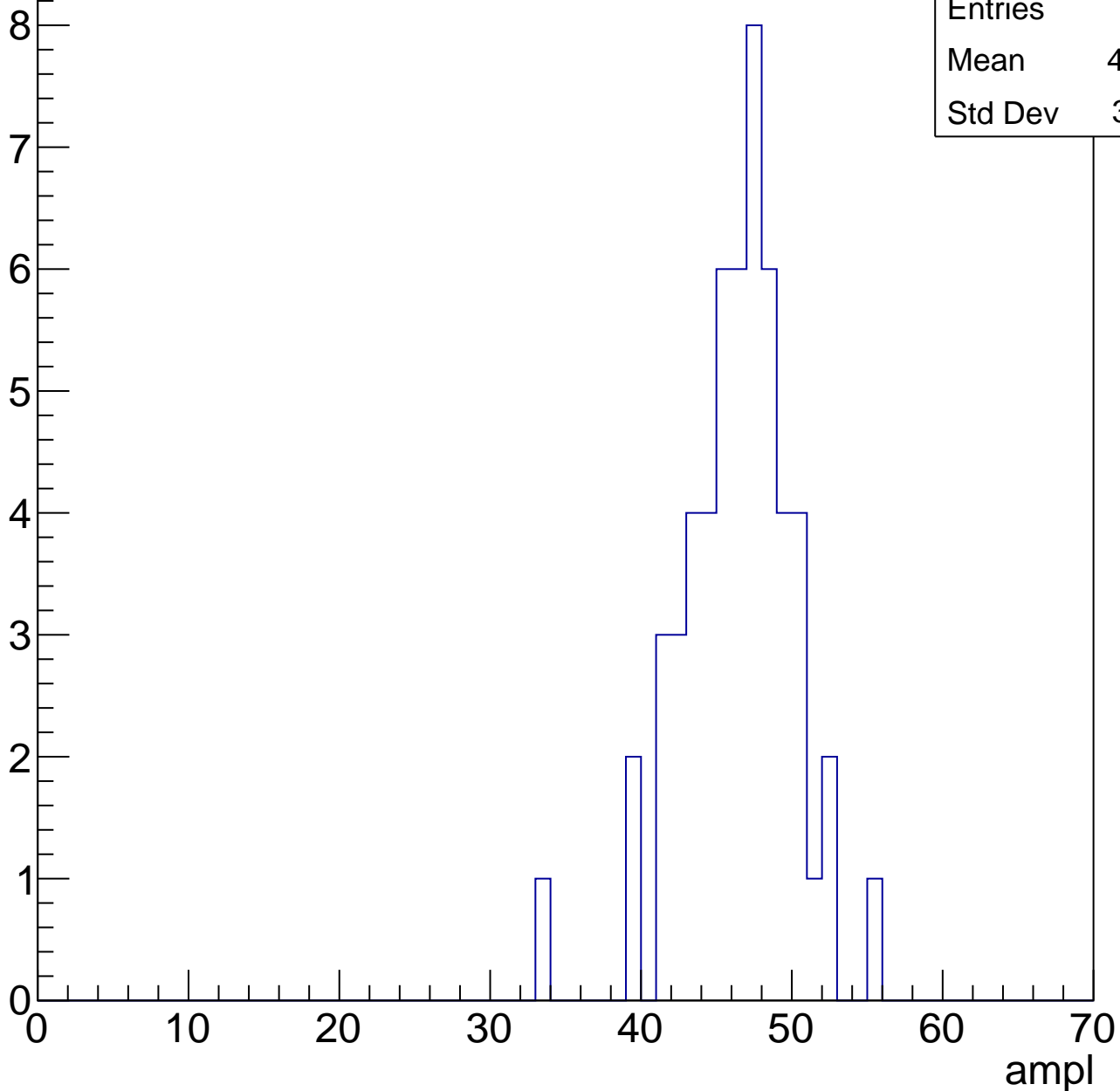


B1L100S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	45.89
Std Dev	3.711

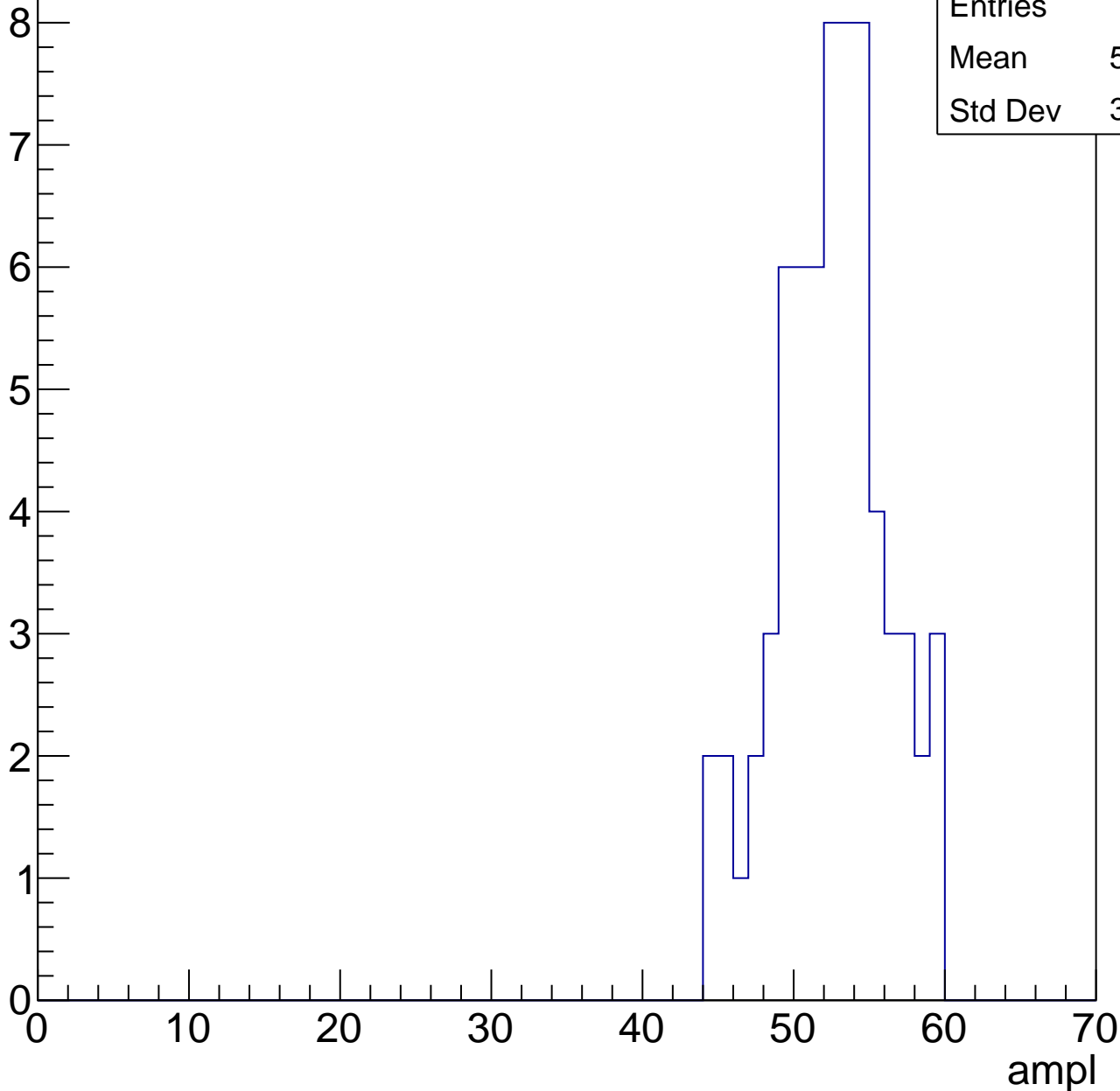


B1L100S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

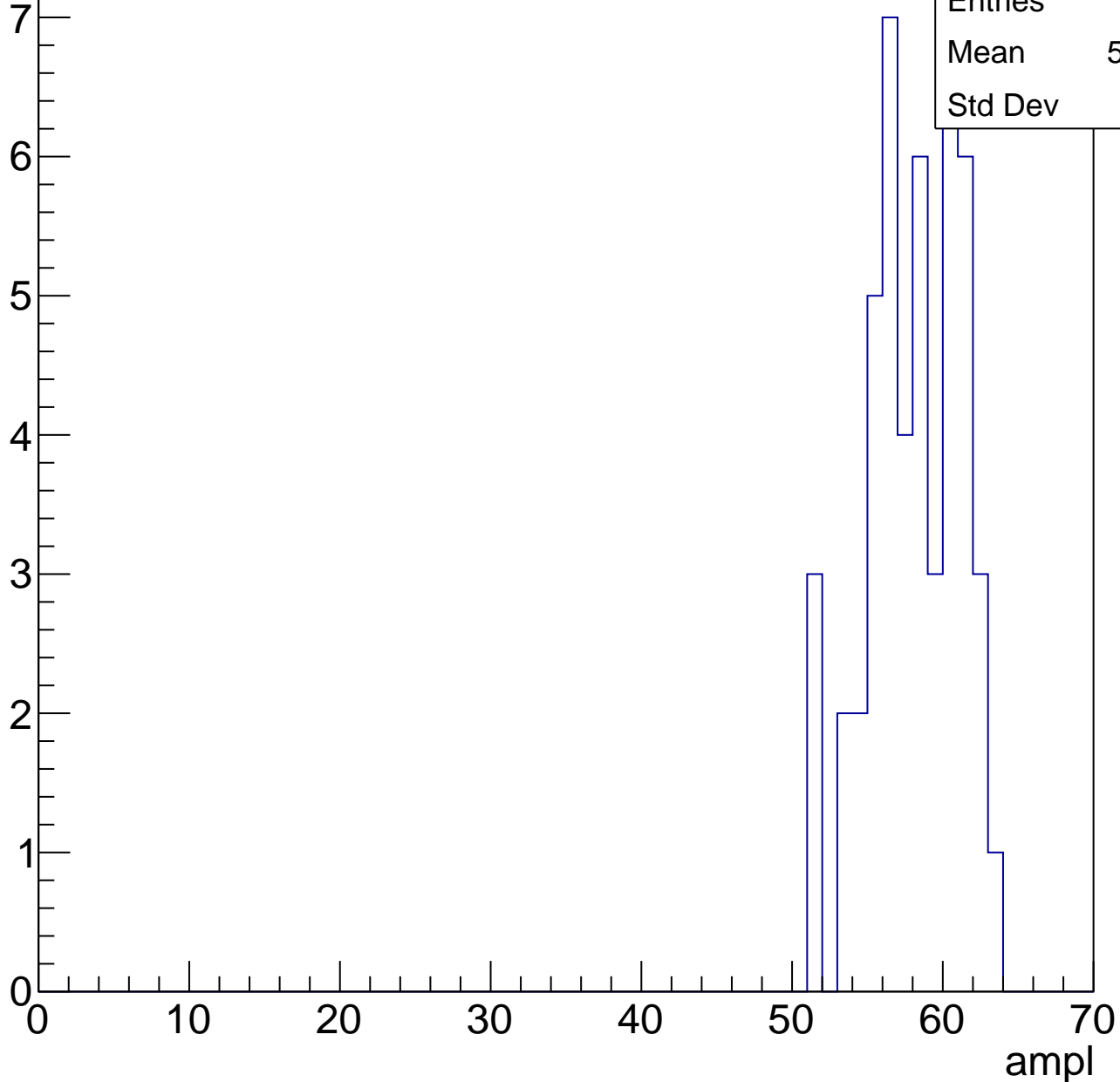
Entries	67
Mean	52.03
Std Dev	3.587



B1L100S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



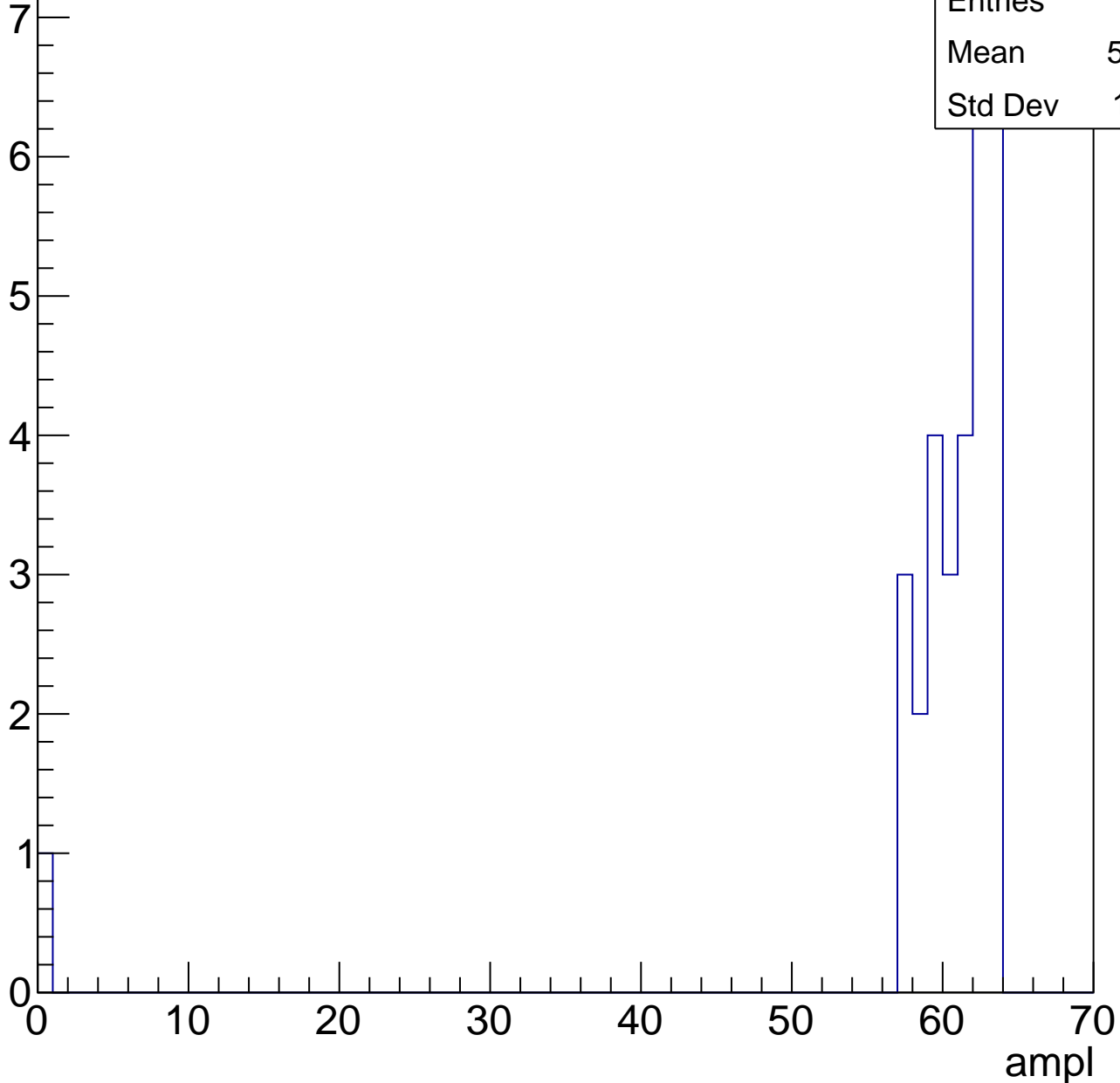
Entries	49
Mean	57.59
Std Dev	3.05

B1L100S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.77
Std Dev	10.91



B1L100S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch61, adc0

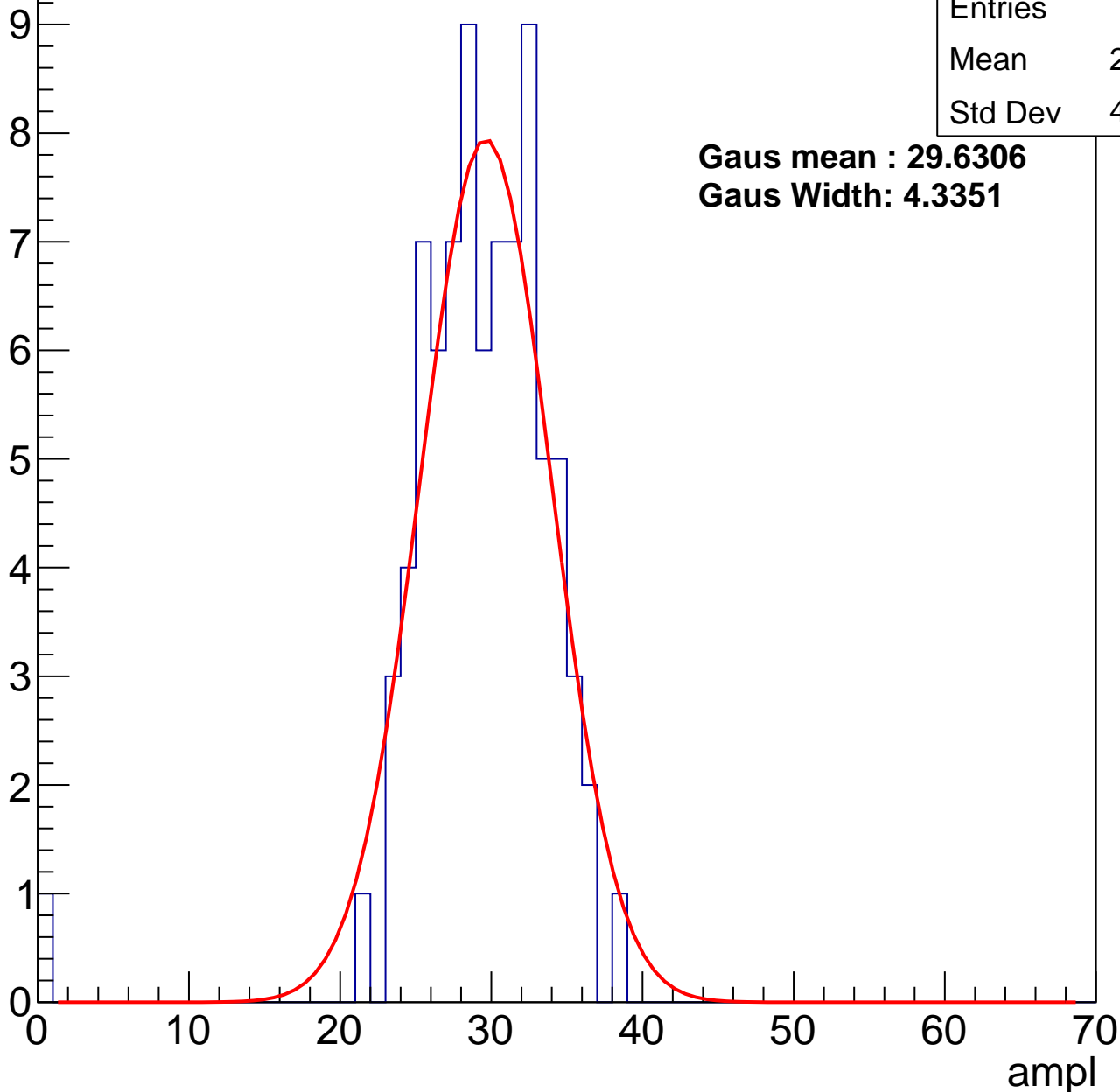
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	28.88
Std Dev	4.813

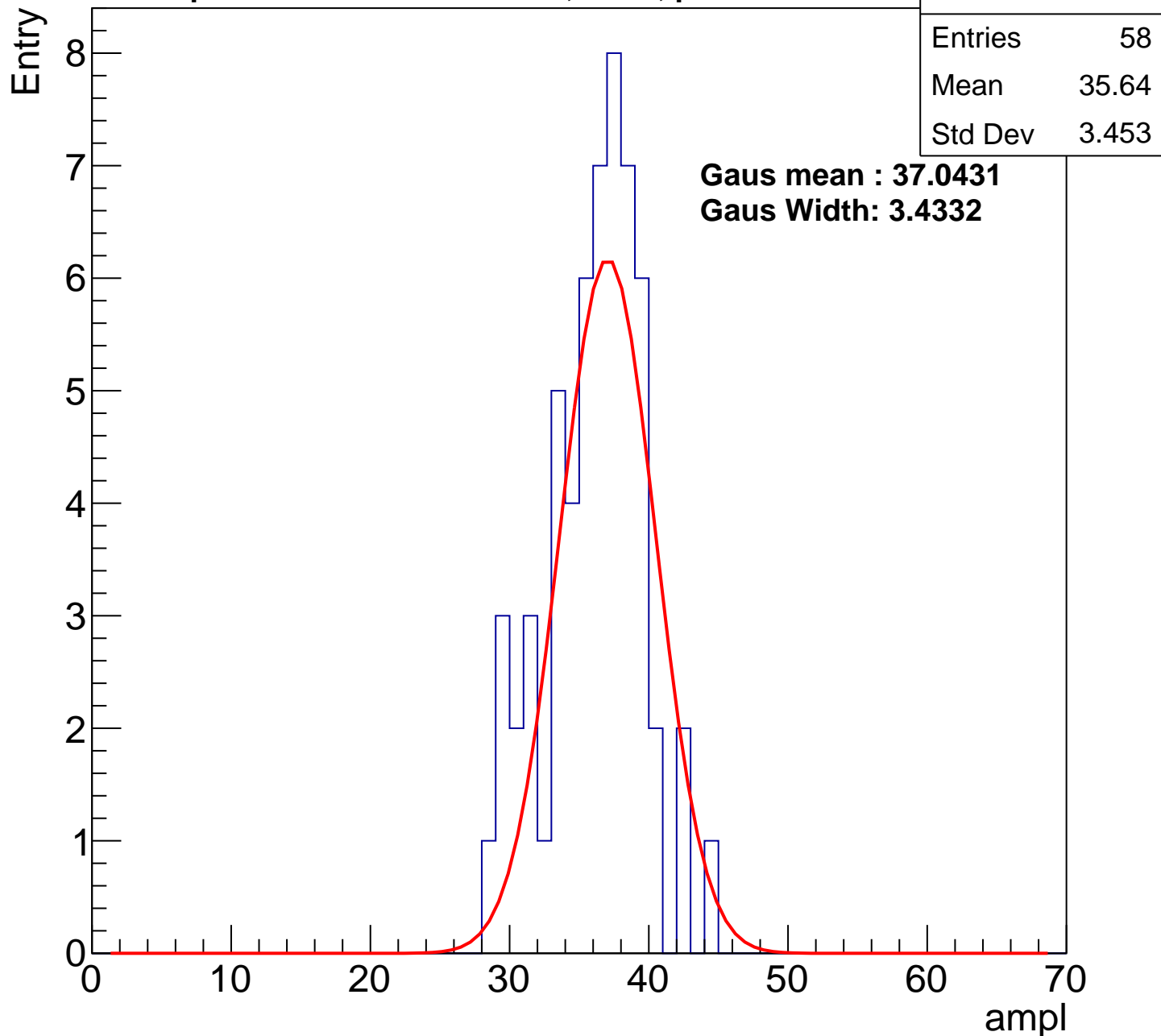
Gaus mean : 29.6306

Gaus Width: 4.3351



B1L100S, U14-ch61, adc1

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch61, adc2

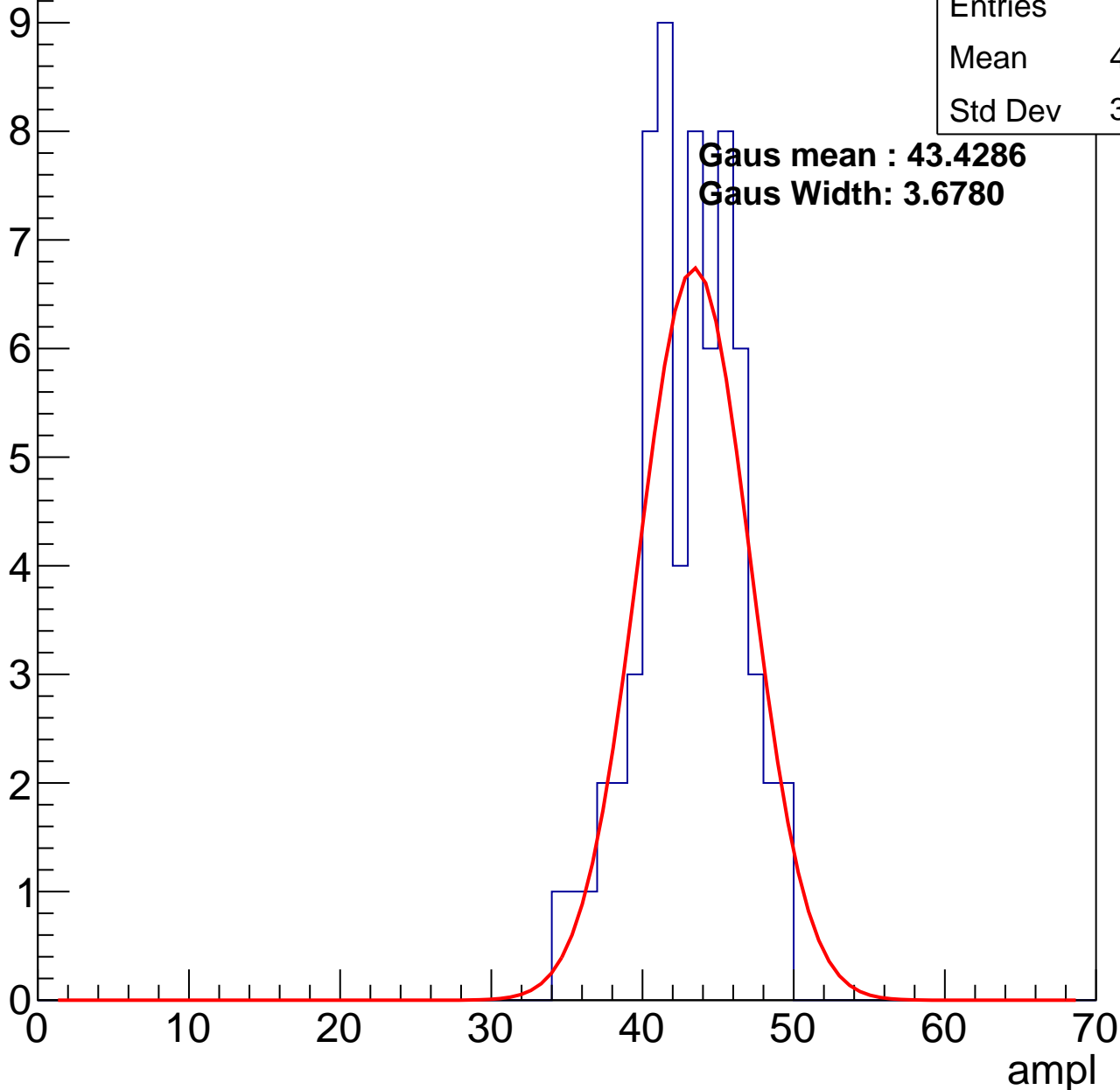
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.55
Std Dev	3.313

Gaus mean : 43.4286

Gaus Width: 3.6780

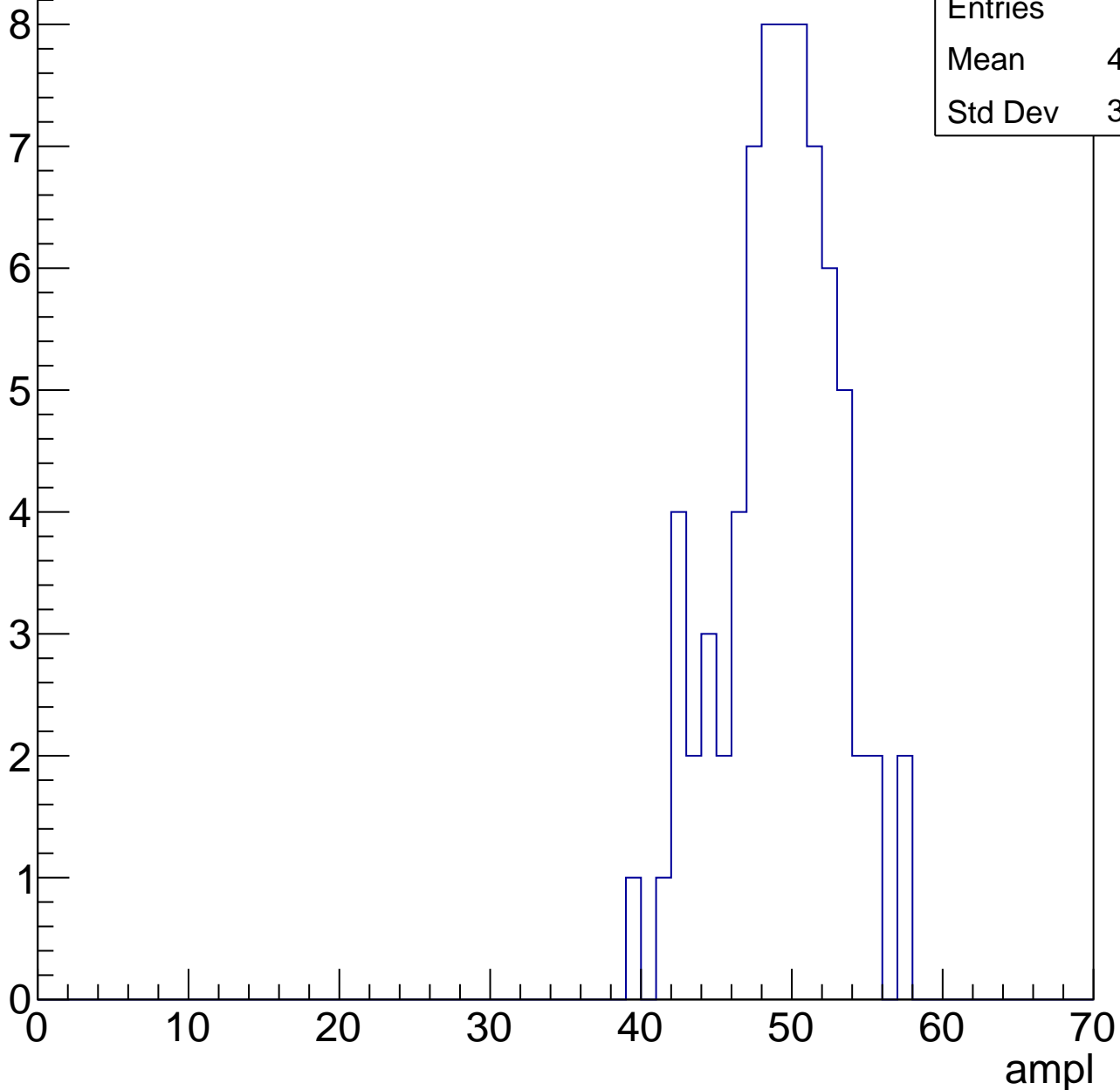


B1L100S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

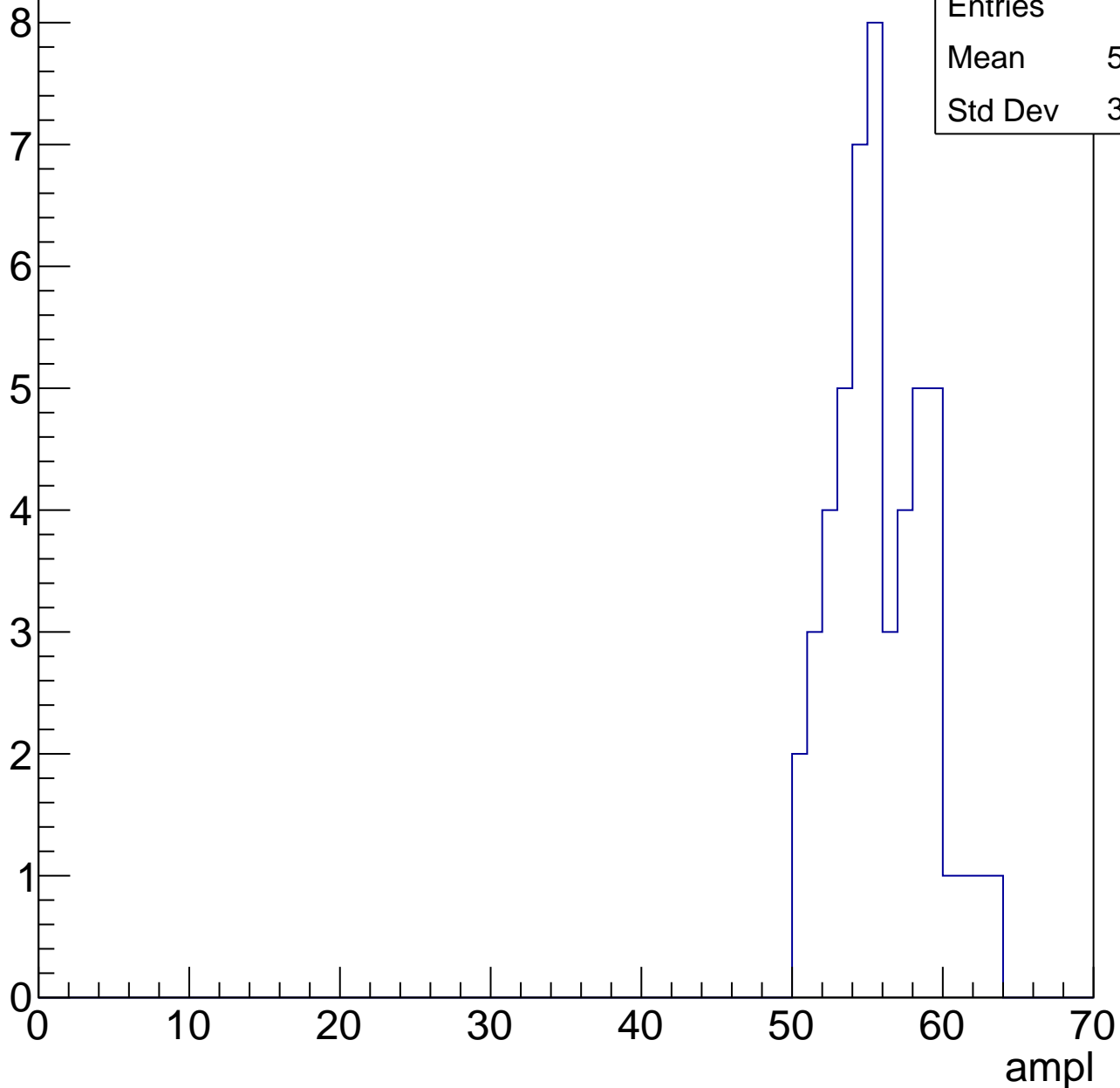
Entries	72
Mean	48.76
Std Dev	3.777



B1L100S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.6
Std Dev	8.556

ampl

0

10

20

30

40

50

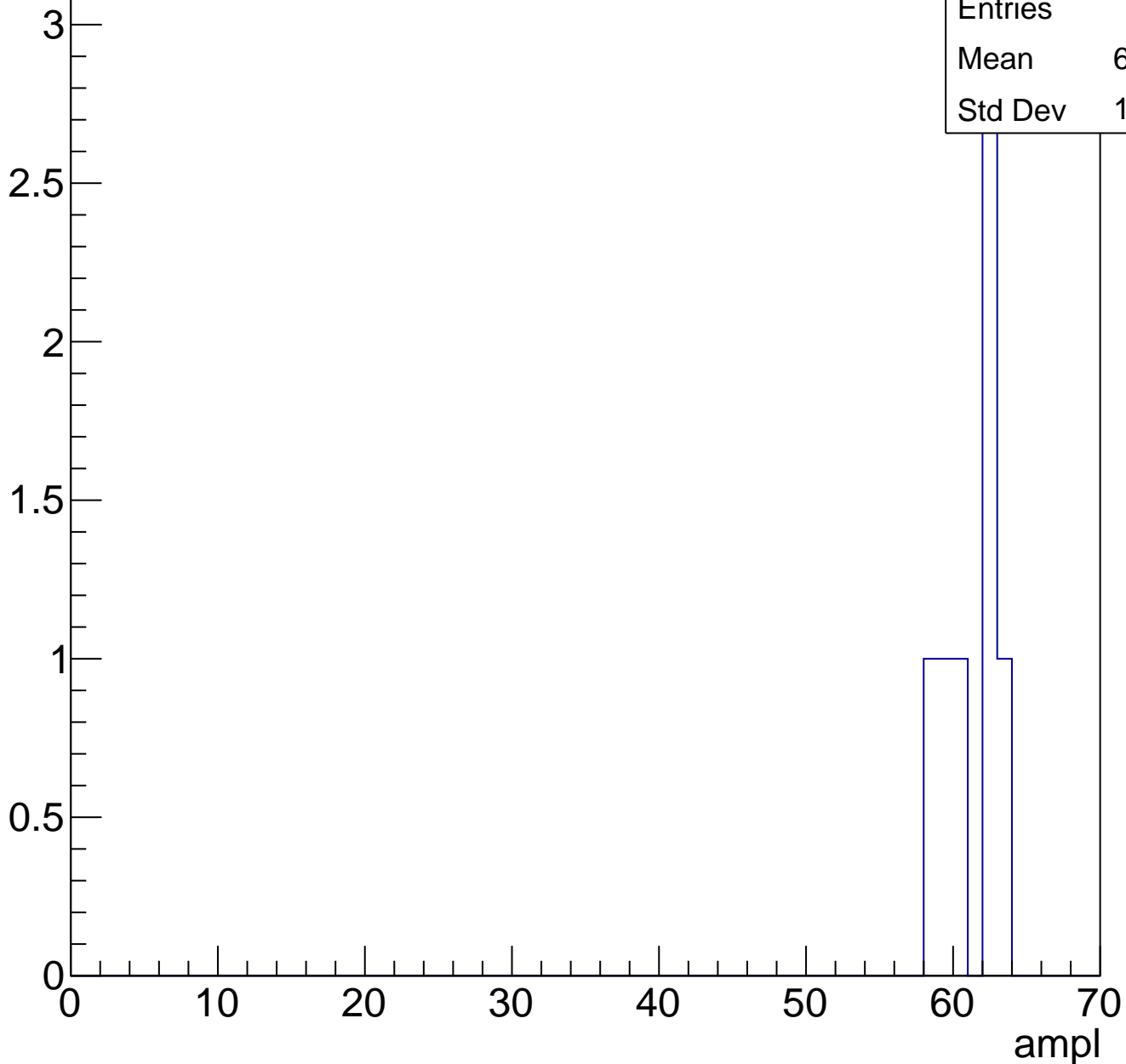
60

70

B1L100S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch62, adc0

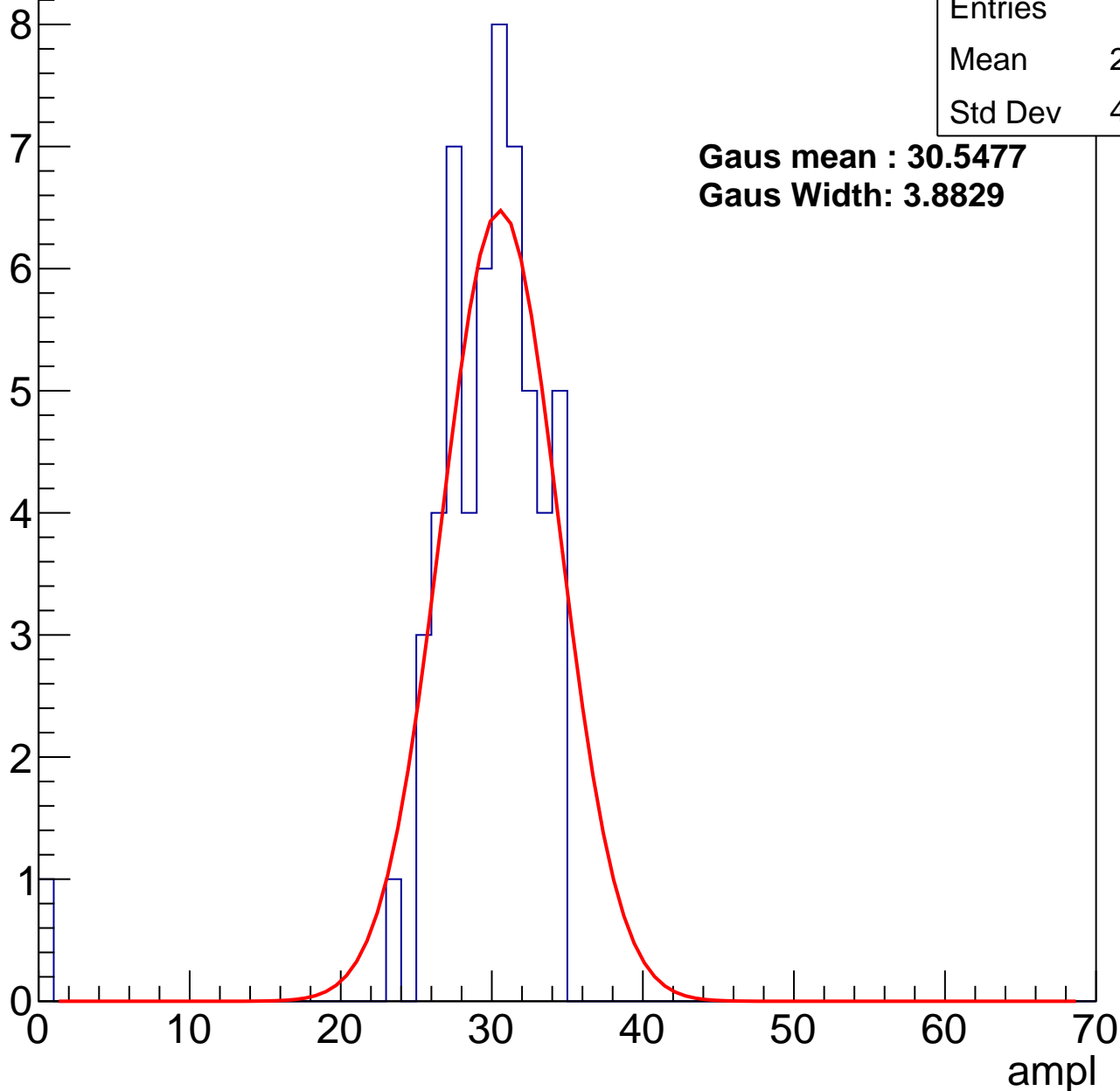
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.02
Std Dev	4.792

Gaus mean : 30.5477

Gaus Width: 3.8829



B1L100S, U14-ch62, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	36.08
Std Dev	3.644

Gaus mean : 36.8980

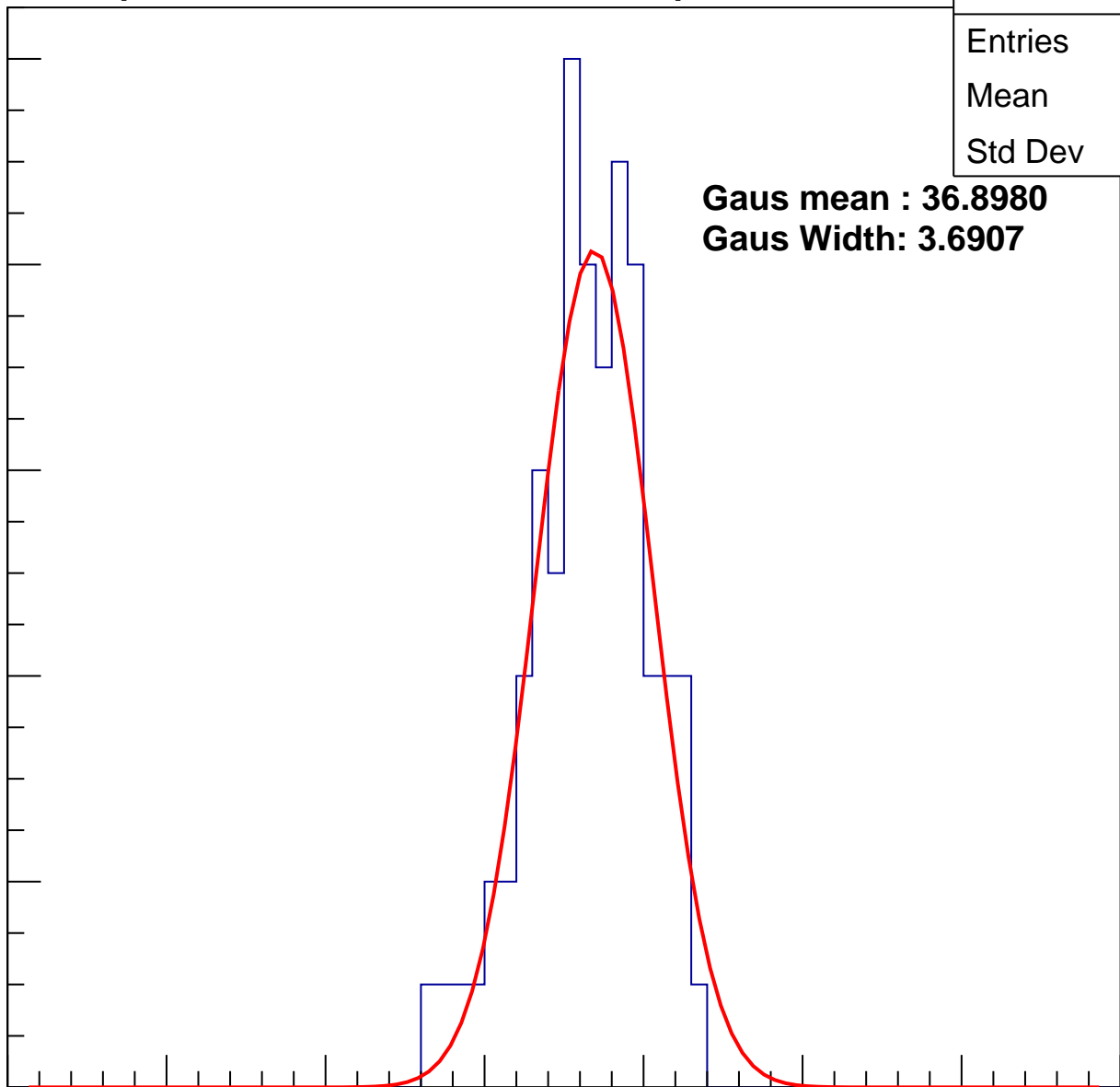
Gaus Width: 3.6907

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch62, adc2

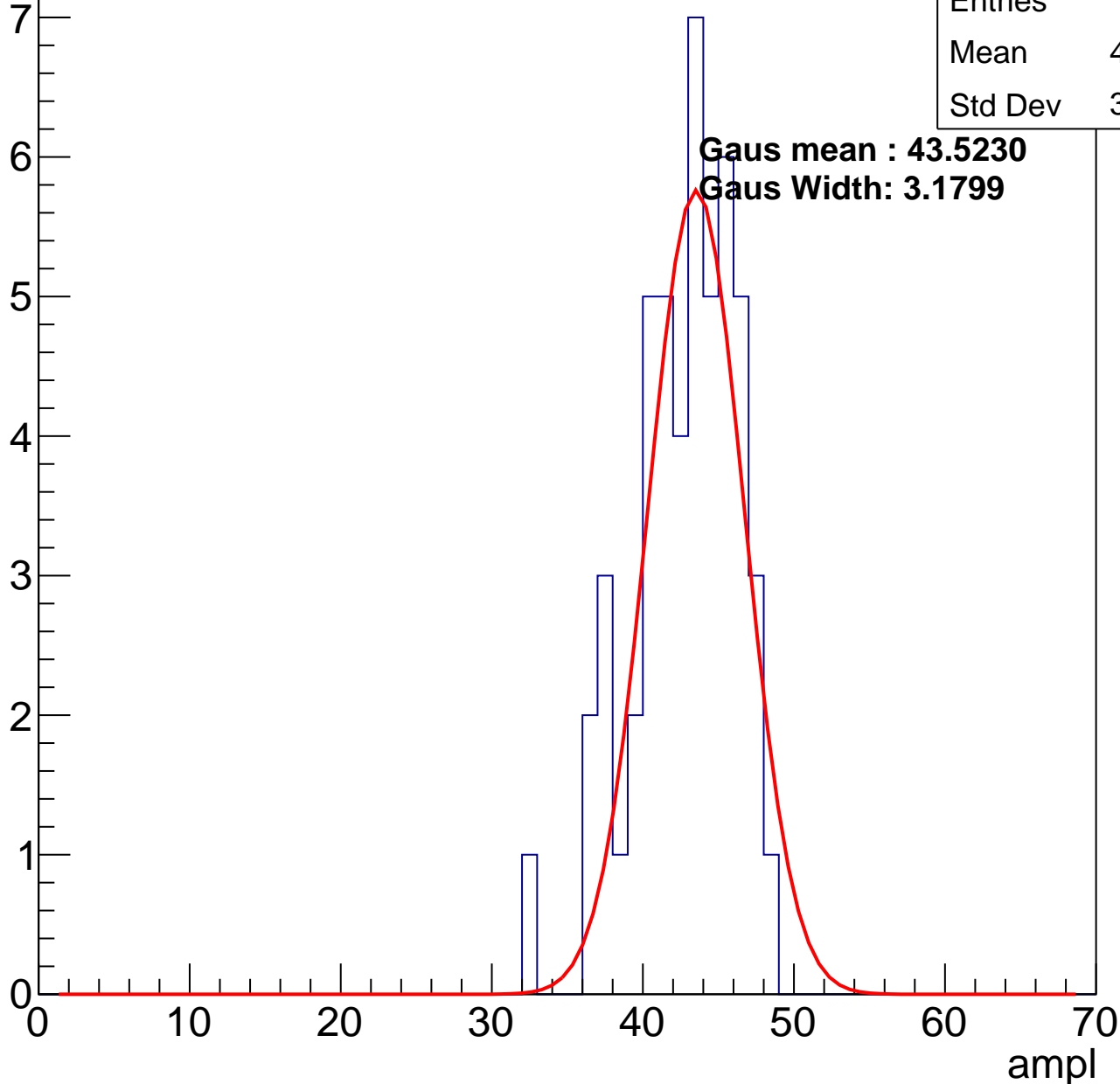
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	42.28
Std Dev	3.388

Gaus mean : 43.5230

Gaus Width: 3.1799

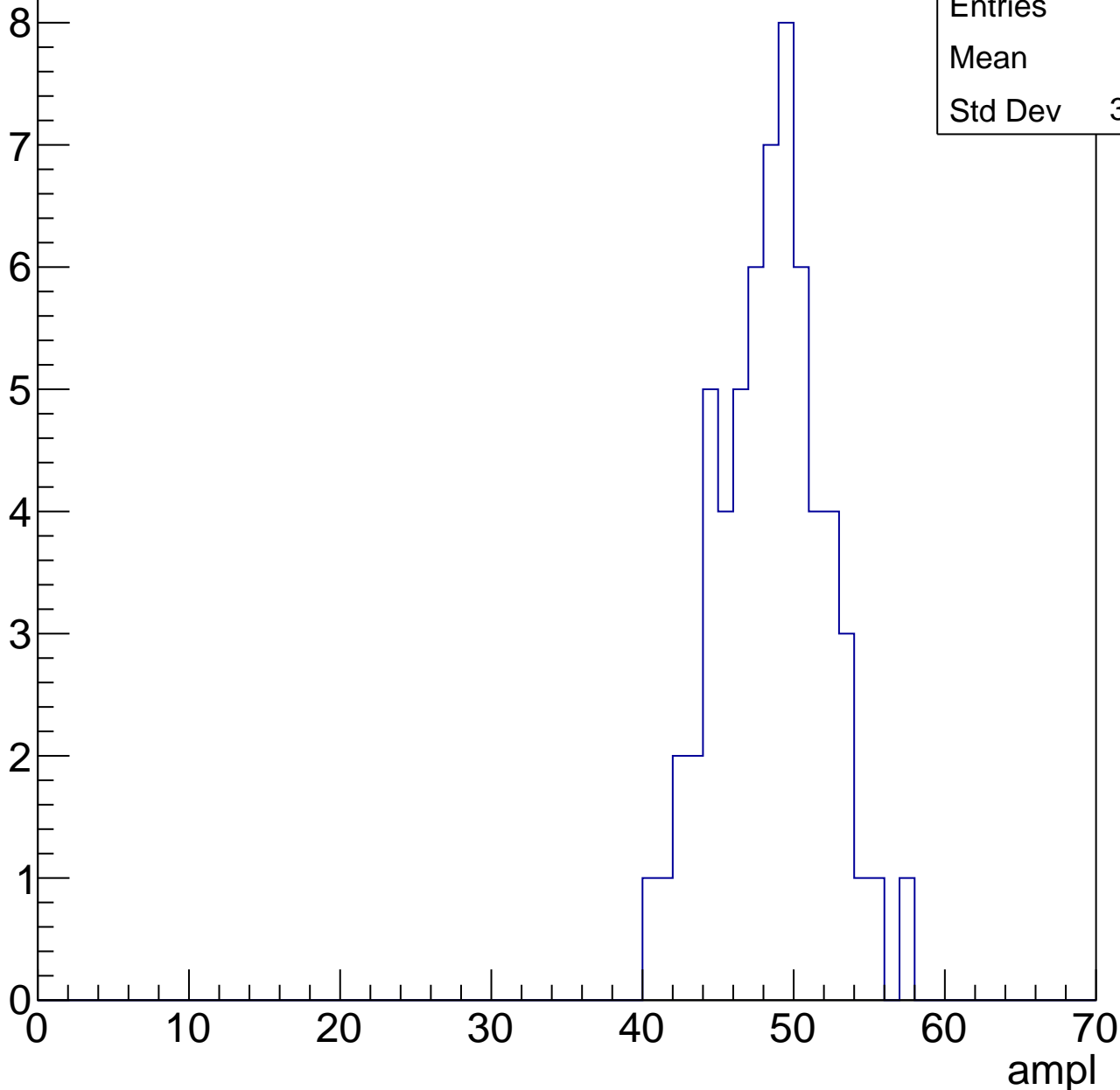


B1L100S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48
Std Dev	3.506

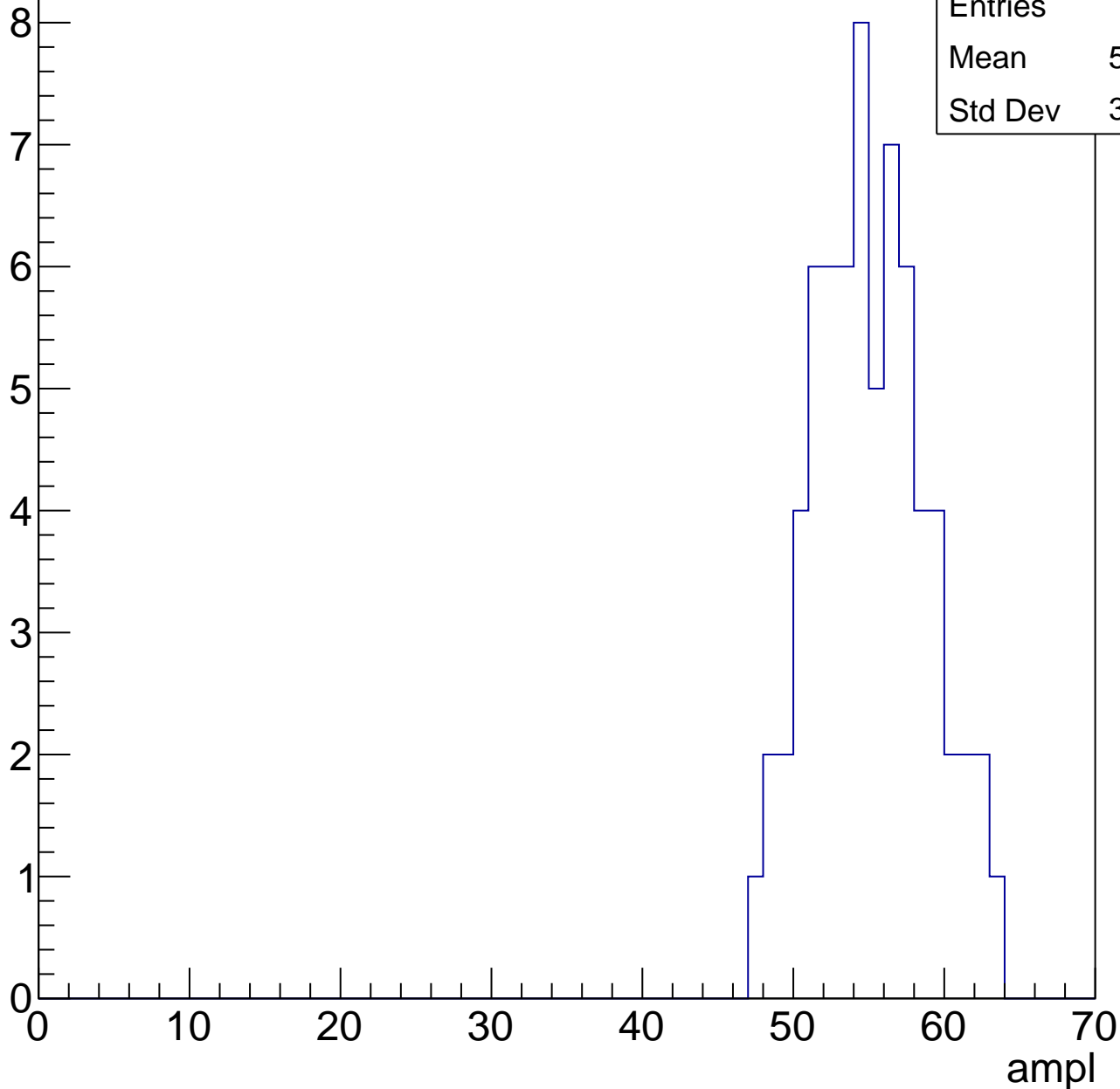


B1L100S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	54.63
Std Dev	3.682

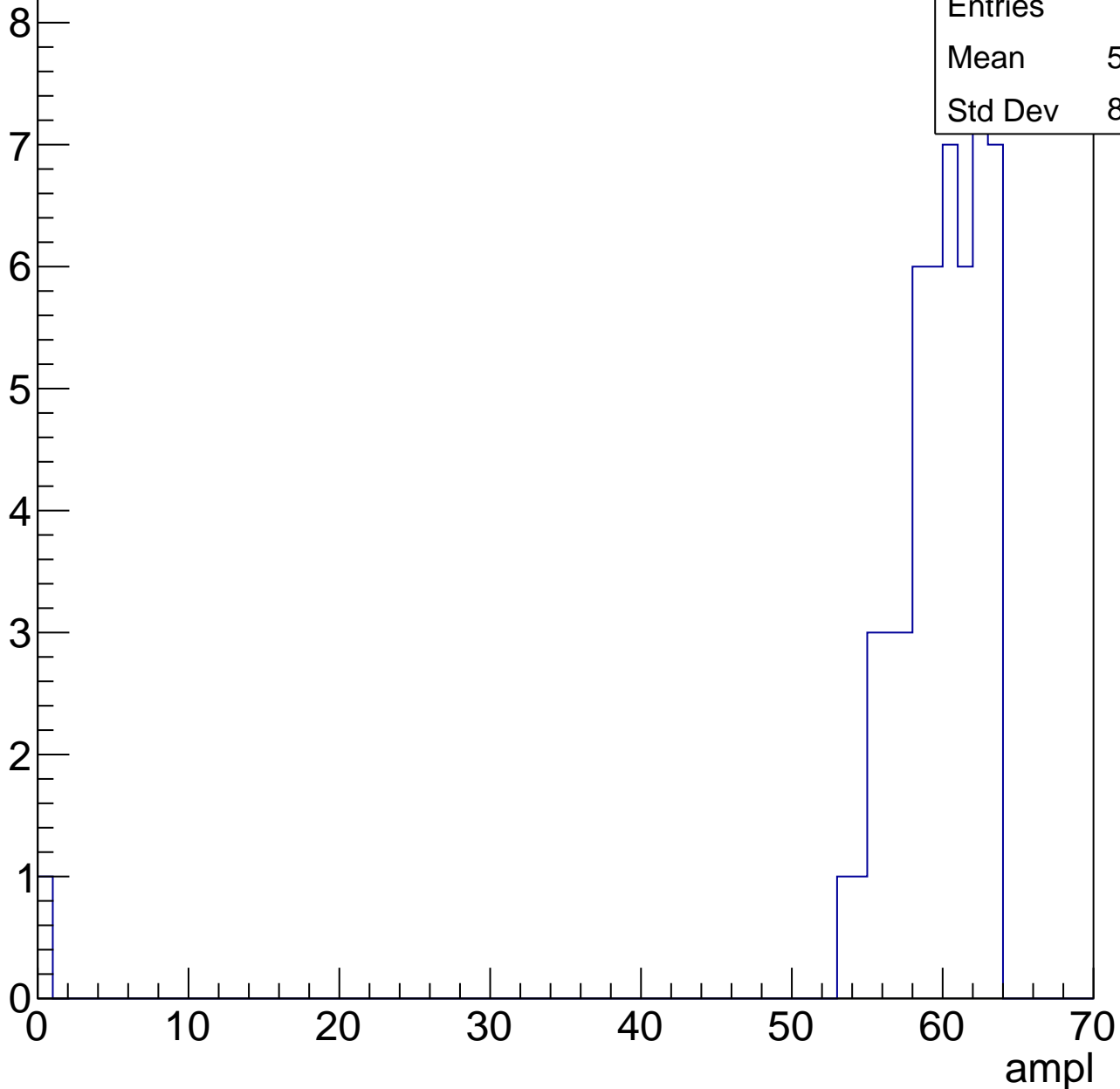


B1L100S, U14-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

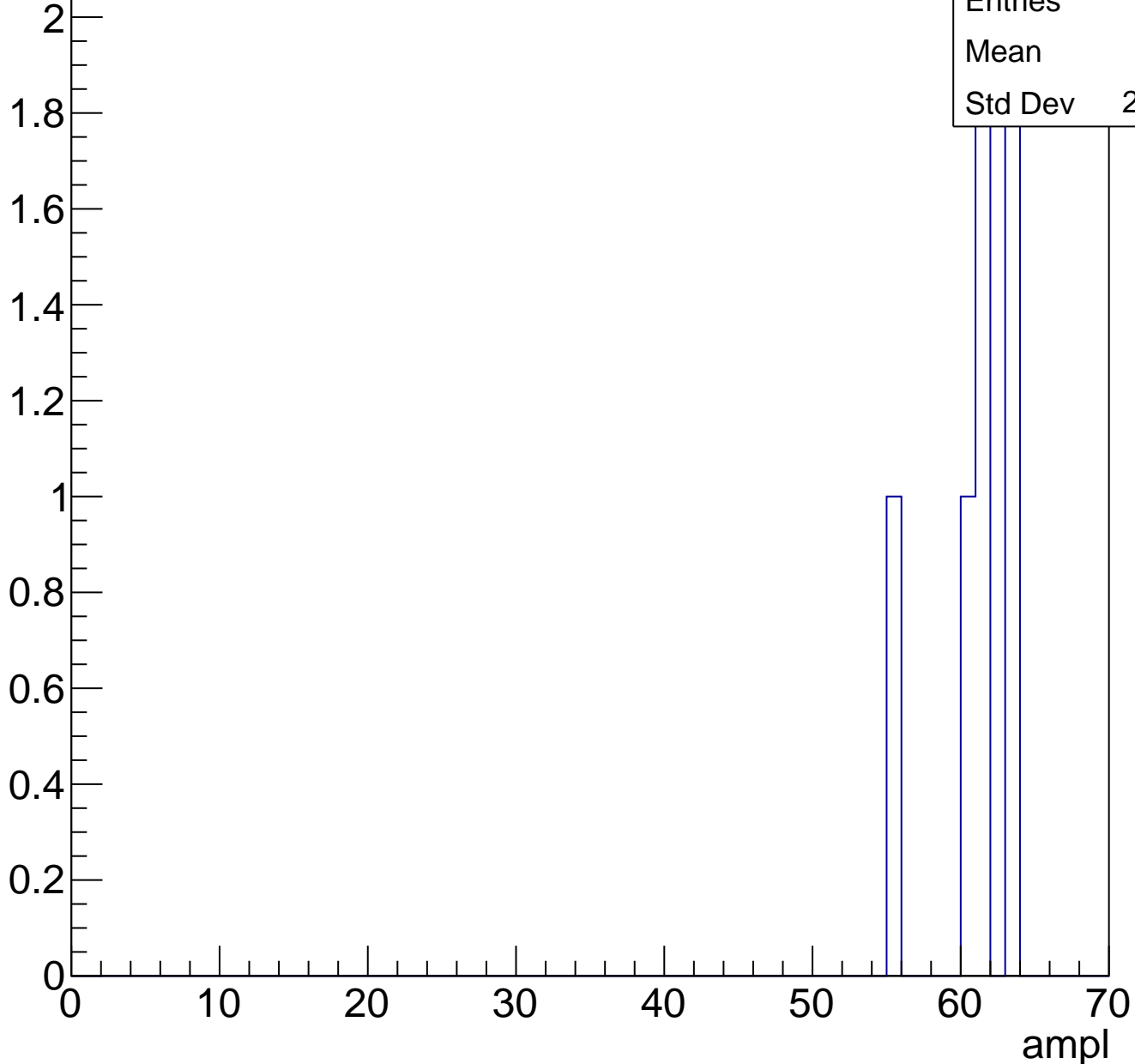
Entries	52
Mean	58.38
Std Dev	8.583



B1L100S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	6
Mean	60.5
Std Dev	2.693

B1L100S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch63, adc0

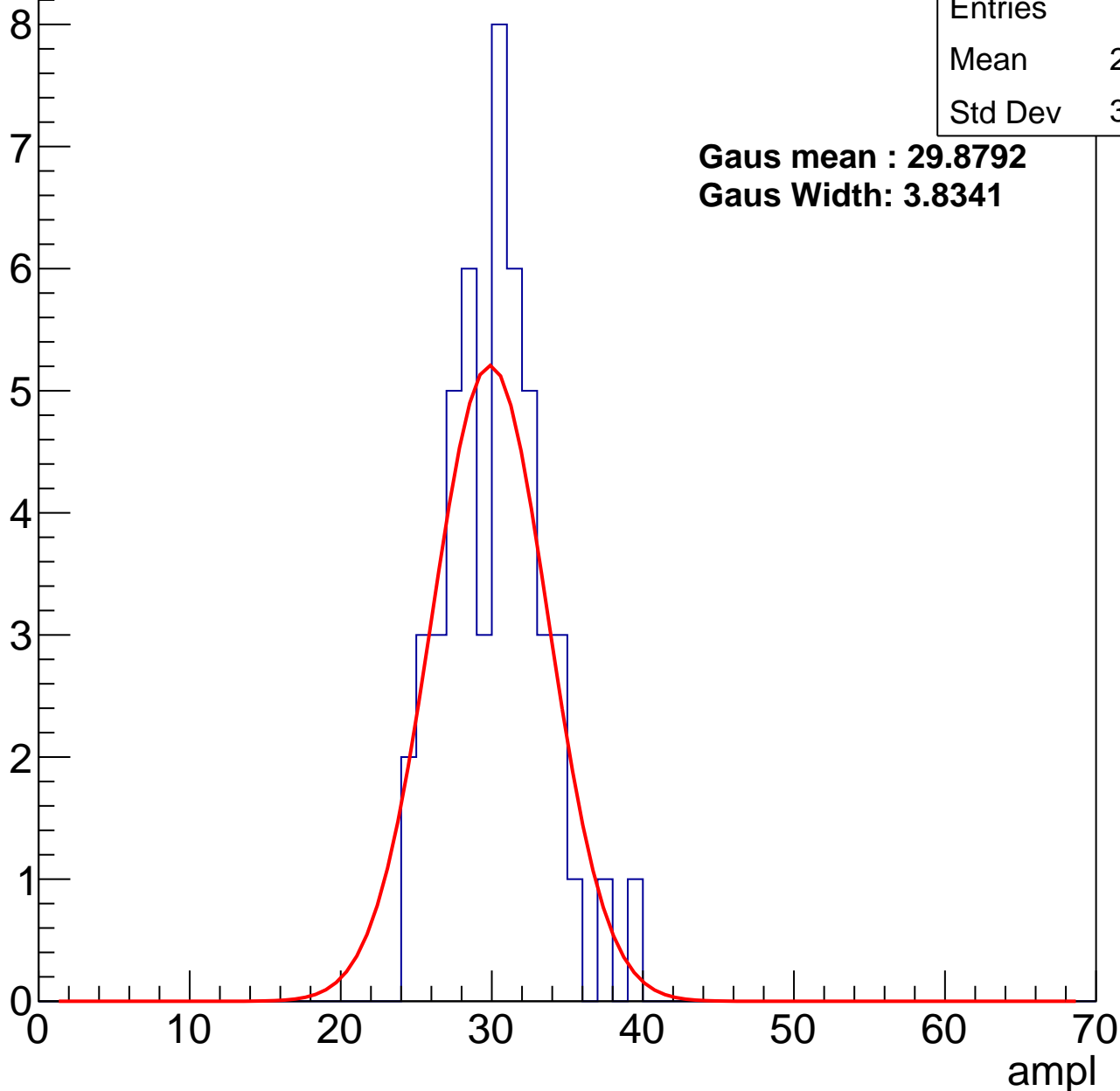
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	29.78
Std Dev	3.227

Gaus mean : 29.8792

Gaus Width: 3.8341



B1L100S, U14-ch63, adc1

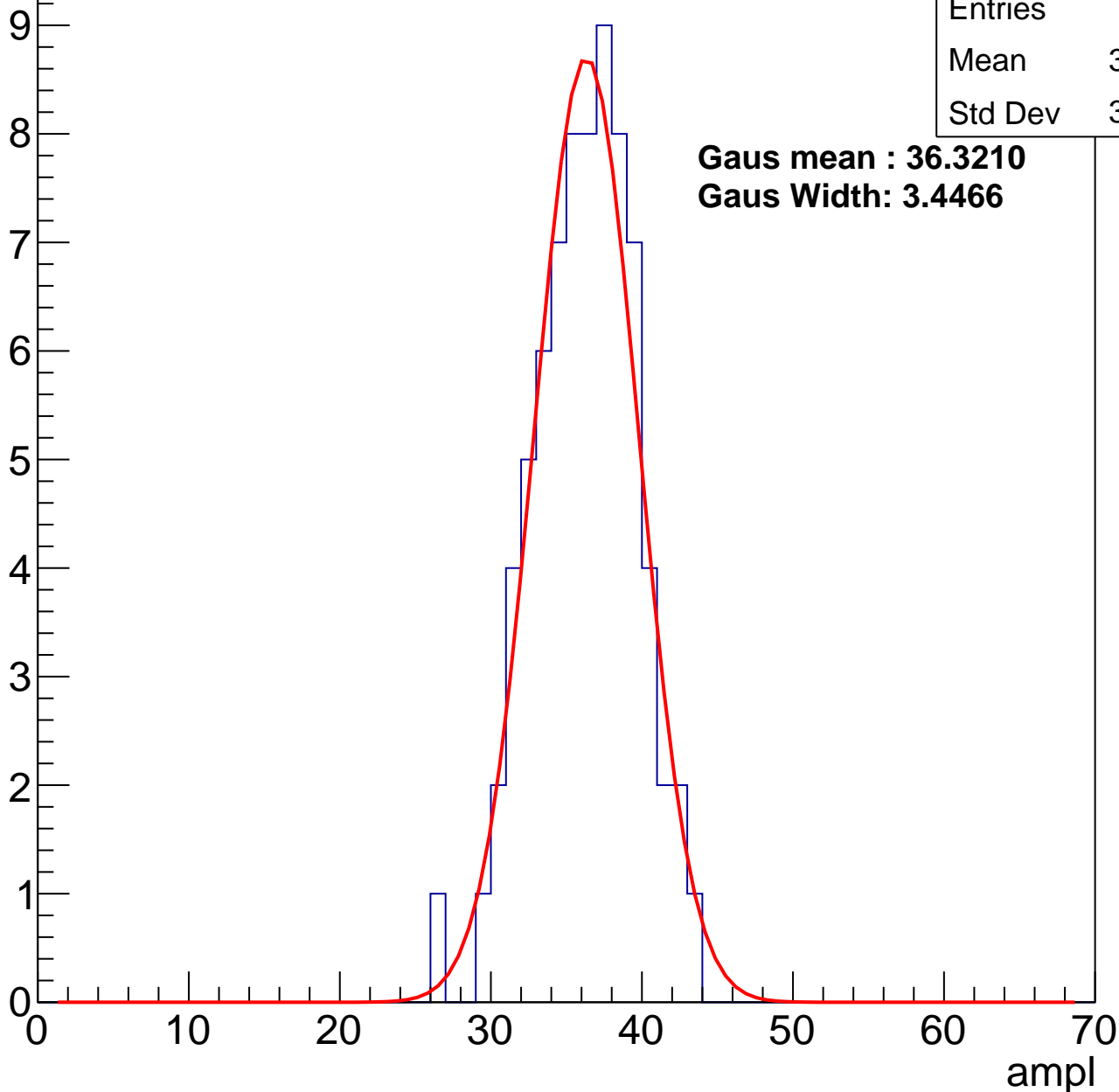
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	35.76
Std Dev	3.318

Gaus mean : 36.3210

Gaus Width: 3.4466



B1L100S, U14-ch63, adc2

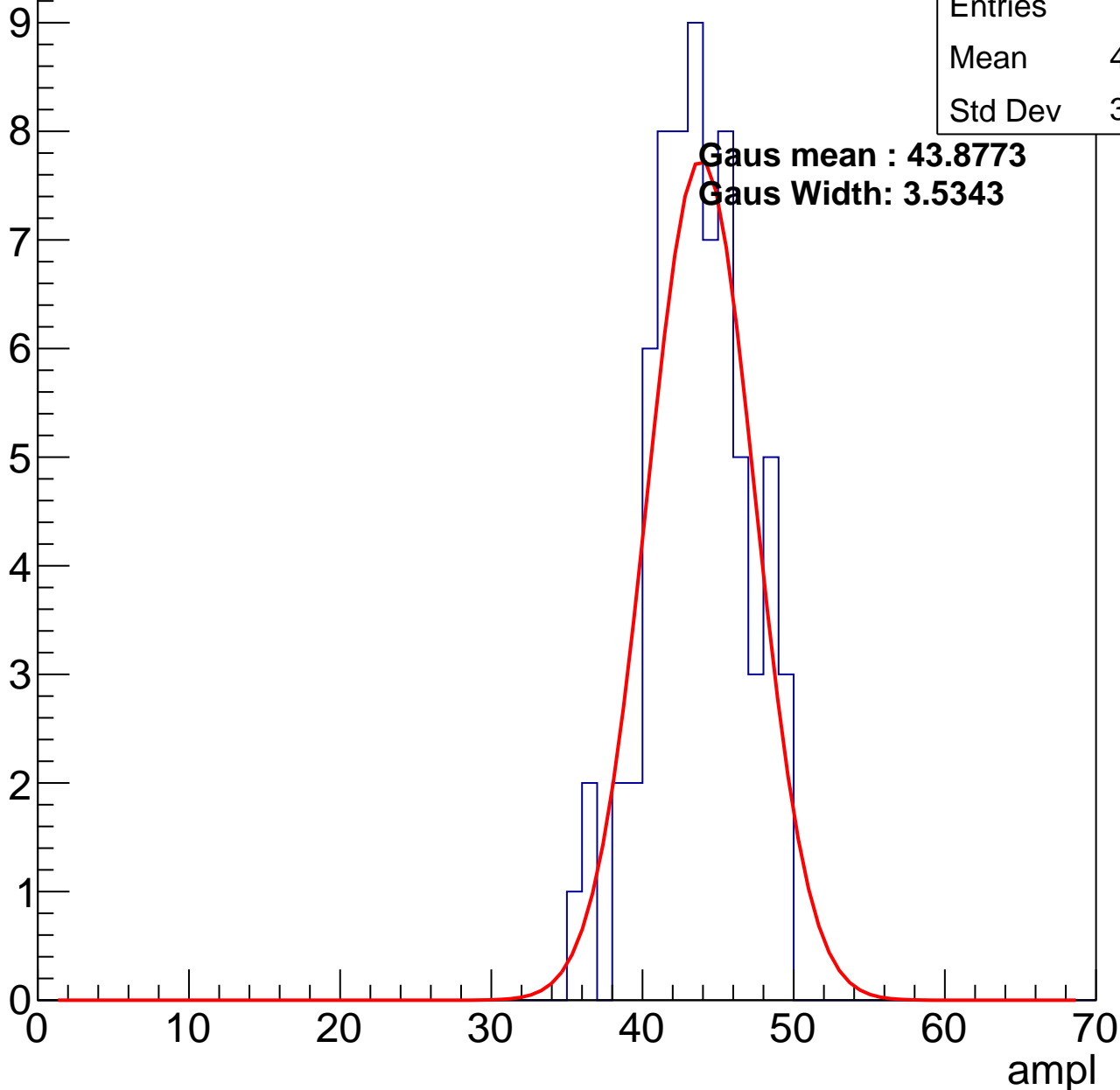
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.16
Std Dev	3.188

Gaus mean : 43.8773

Gaus Width: 3.5343

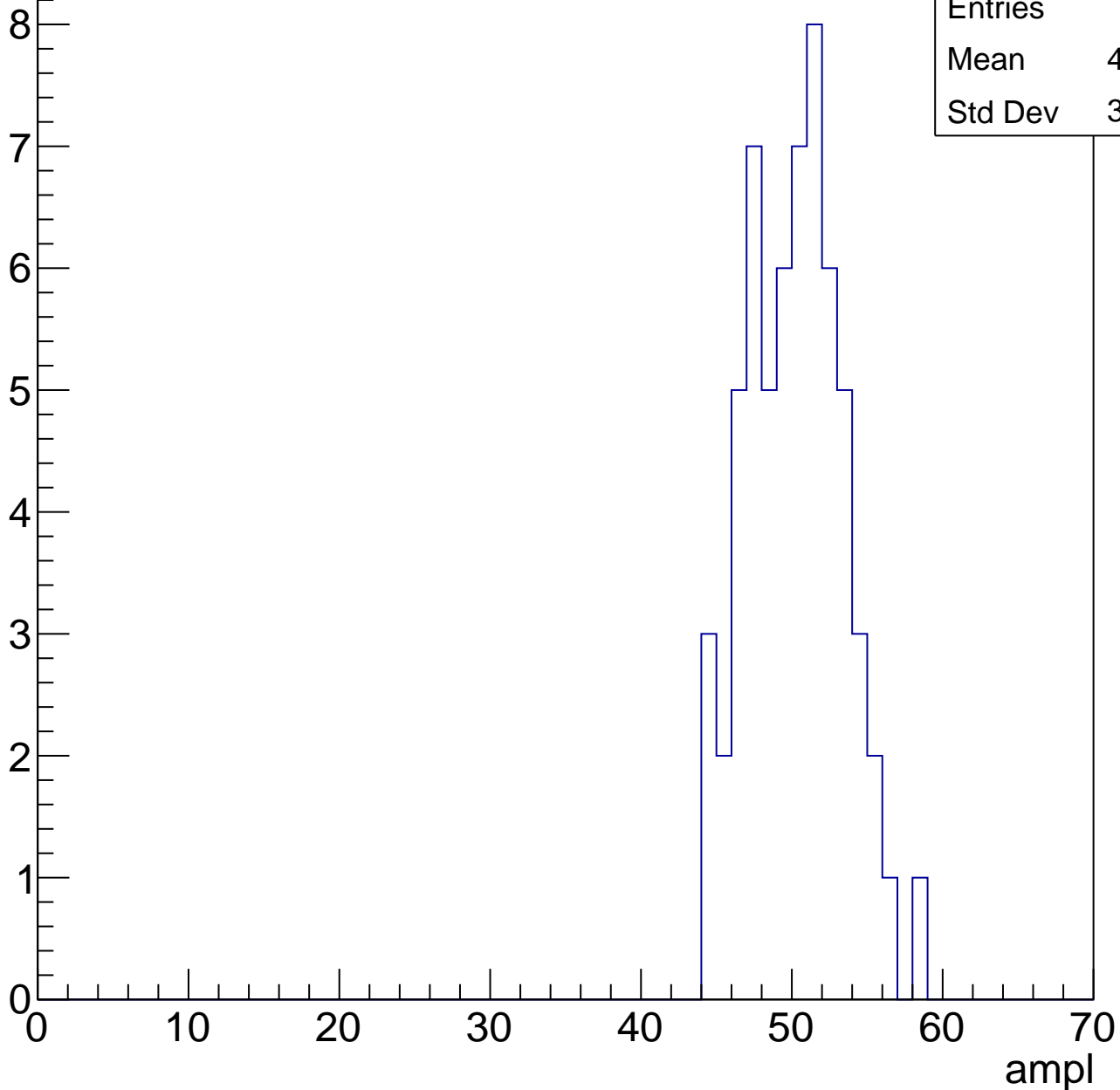


B1L100S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.77
Std Dev	3.133

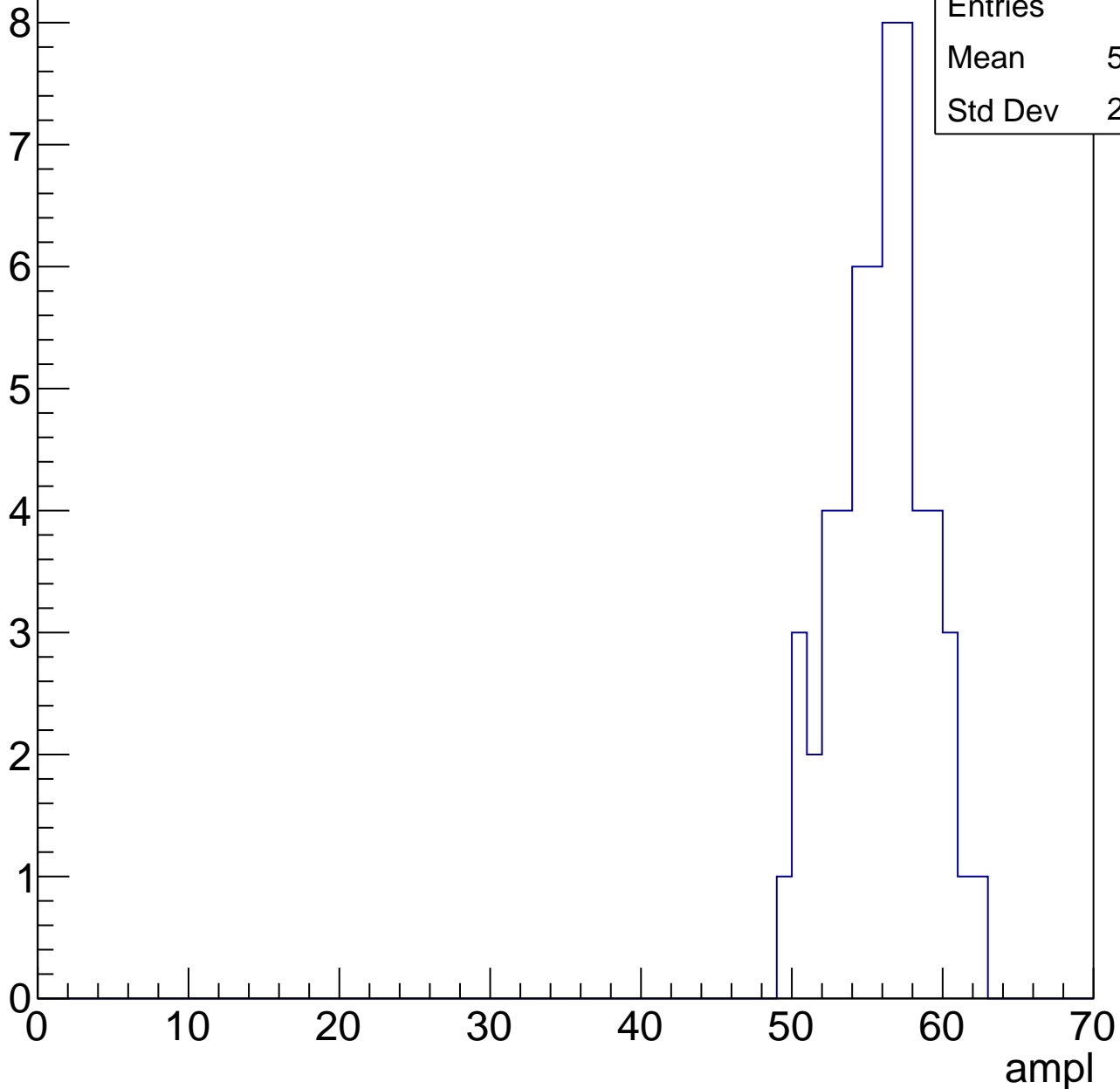


B1L100S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

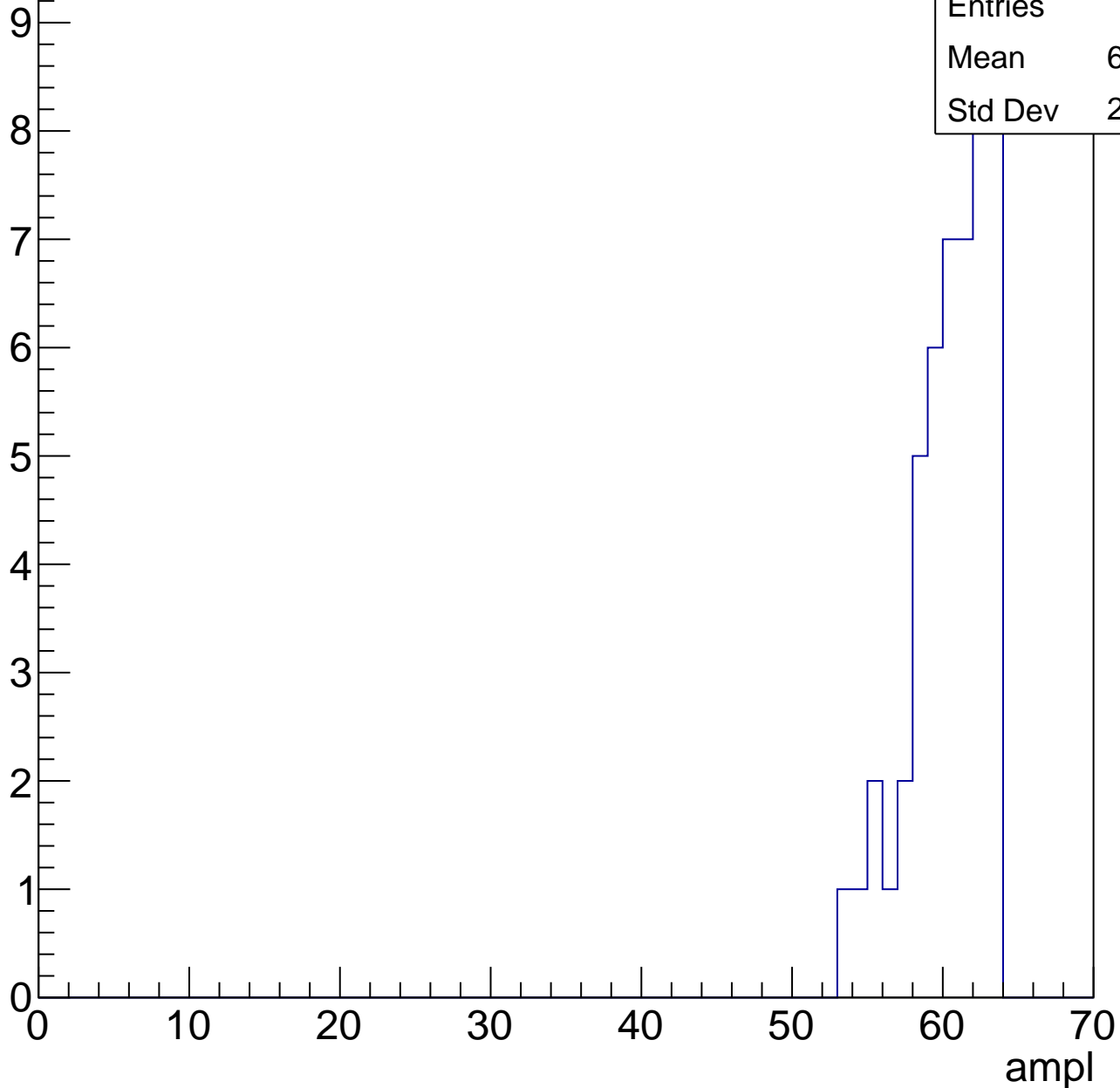
Entries	55
Mean	55.45
Std Dev	2.984



B1L100S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

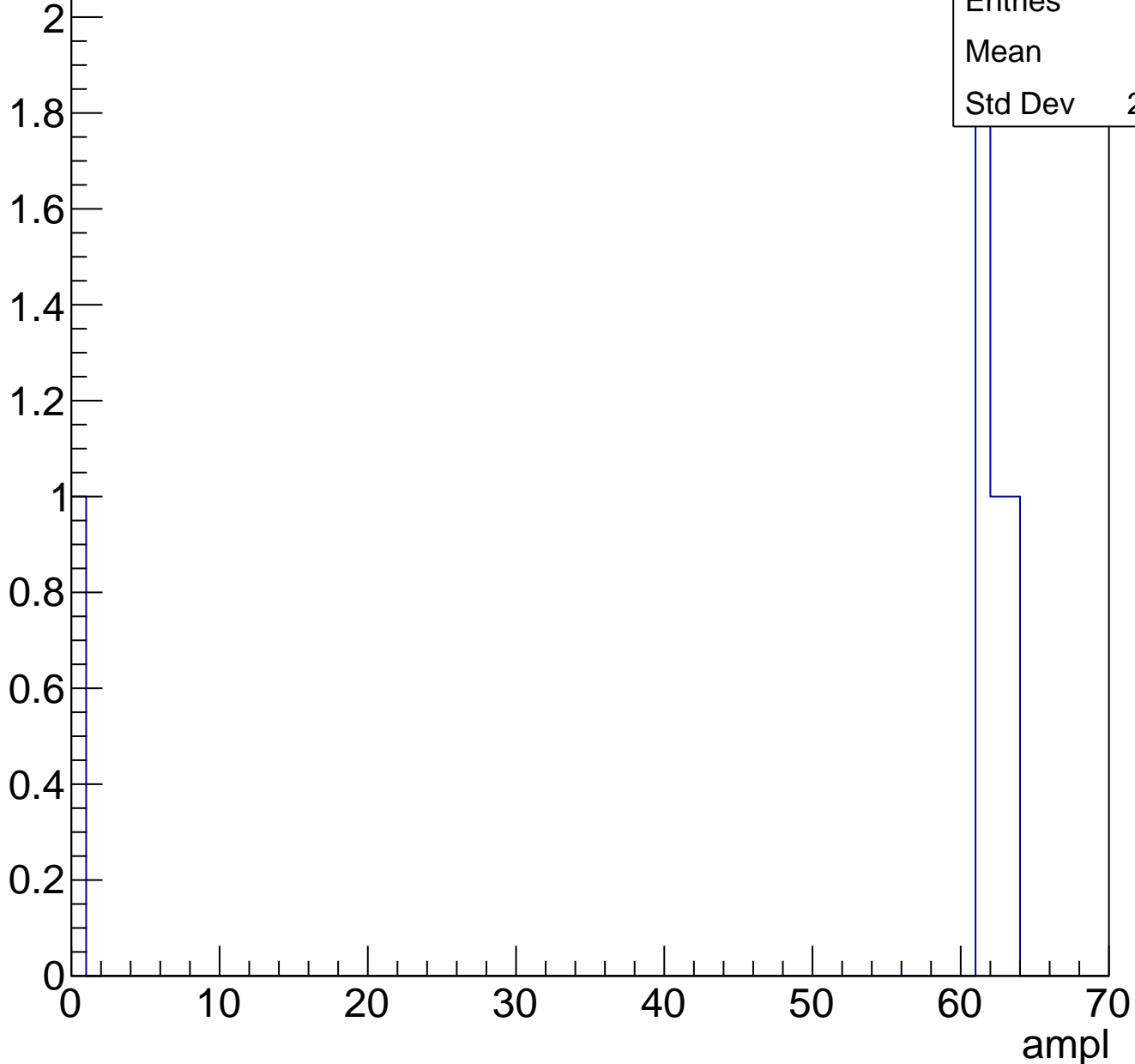


Entries	49
Mean	60.02
Std Dev	2.535

B1L100S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch64, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	27.51
Std Dev	5.995

Gaus mean : 29.0384

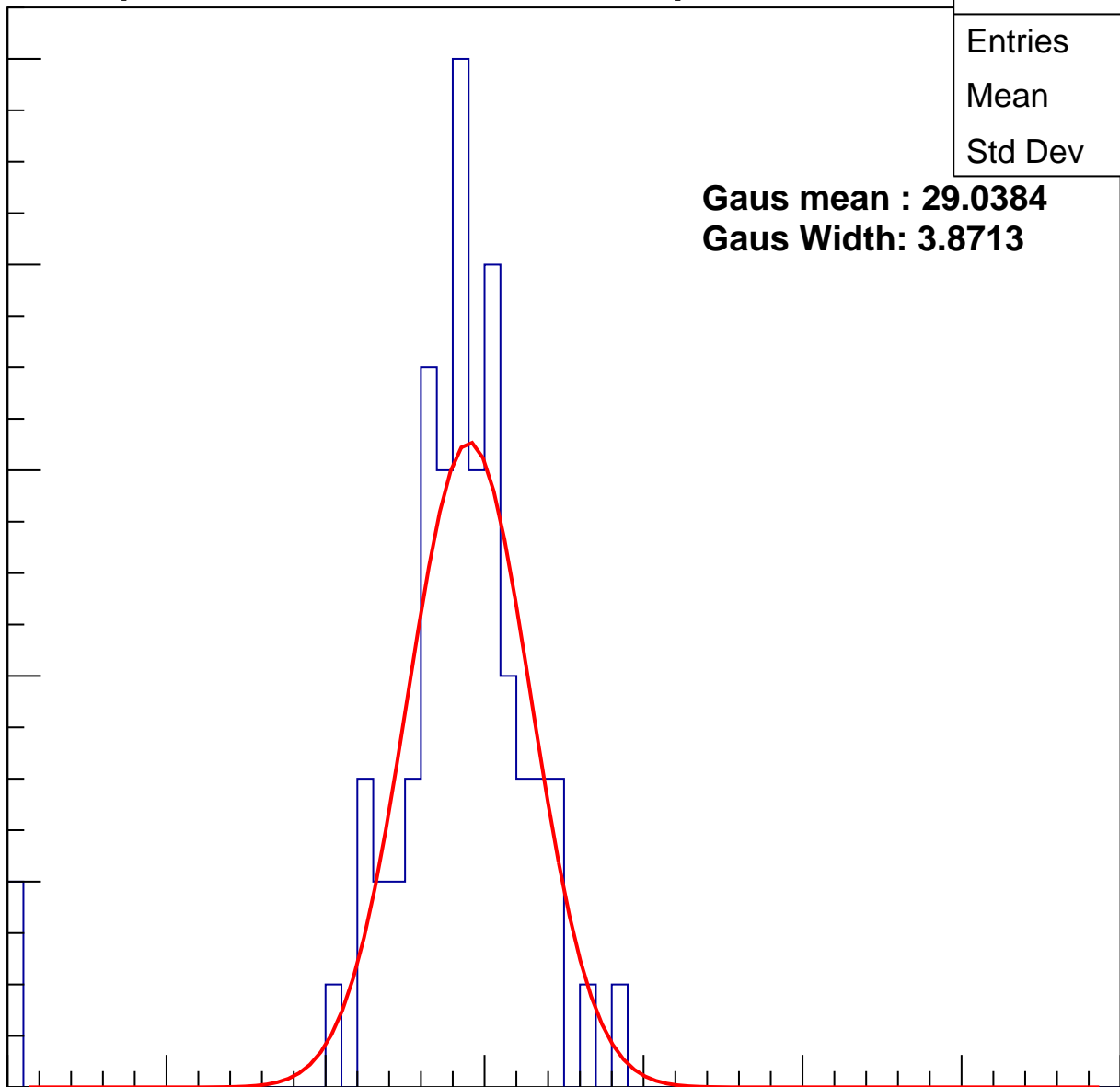
Gaus Width: 3.8713

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch64, adc1

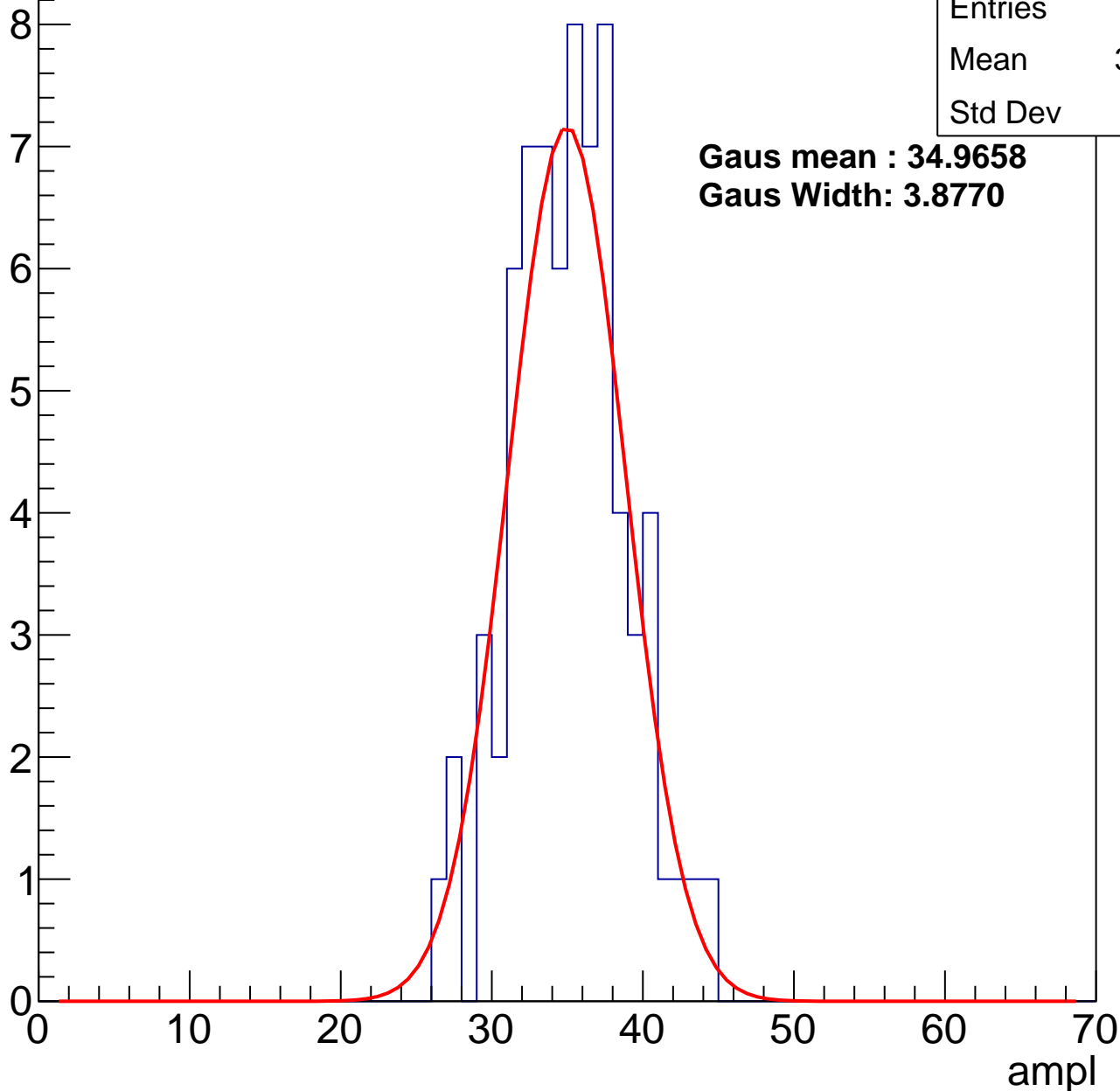
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	34.71
Std Dev	3.74

Gaus mean : 34.9658

Gaus Width: 3.8770

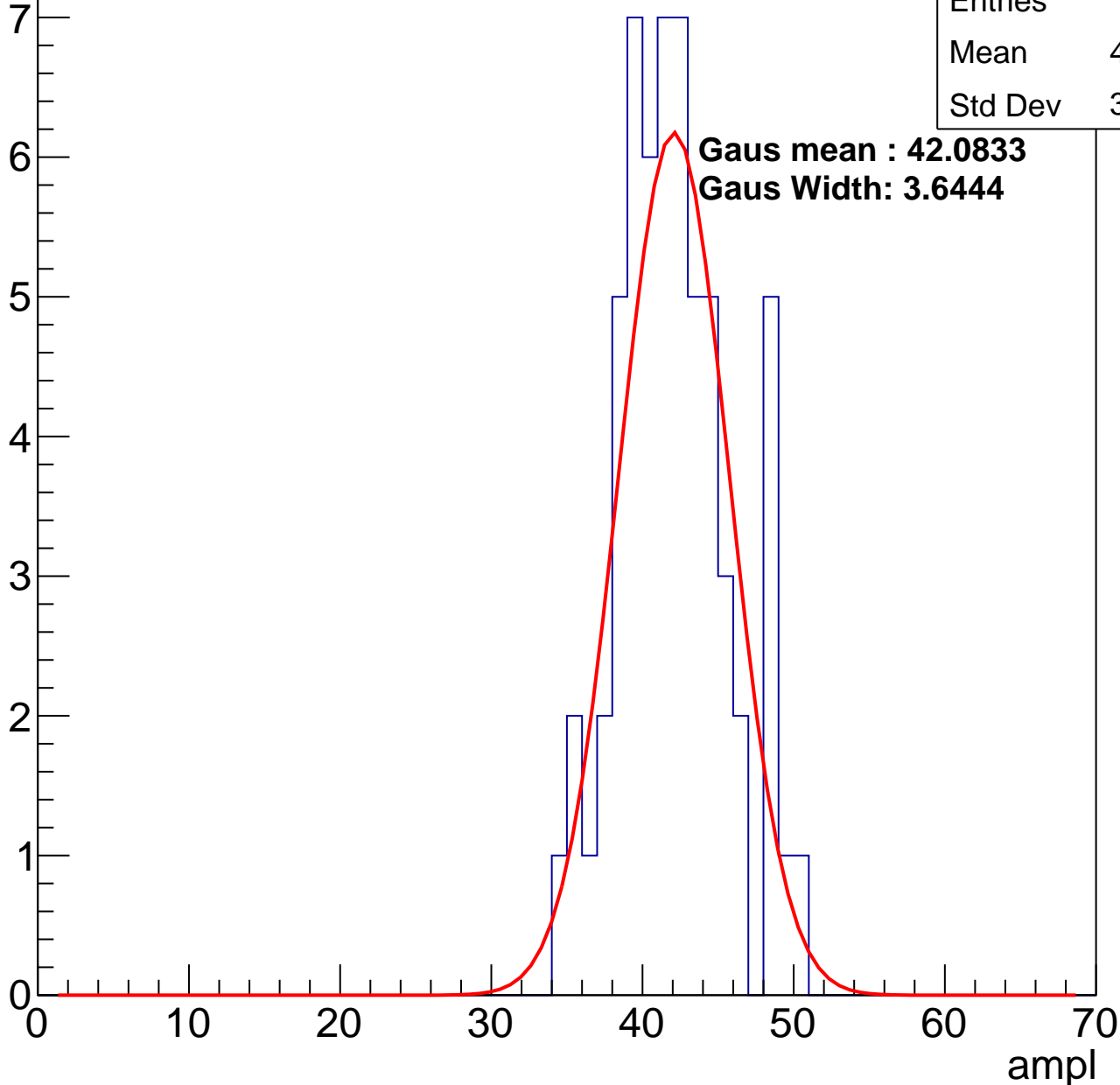


B1L100S, U14-ch64, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.65
Std Dev	3.632

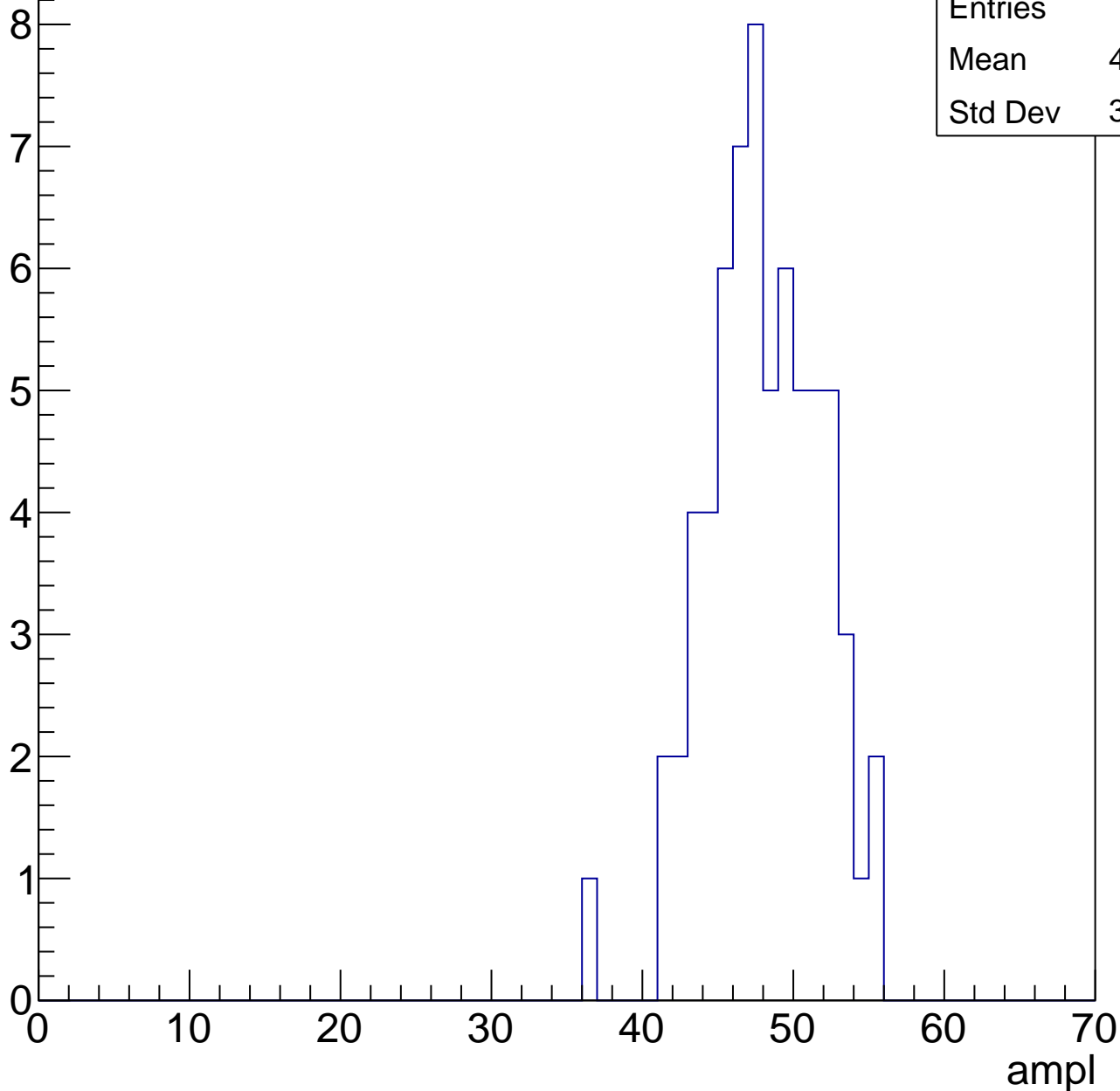


B1L100S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

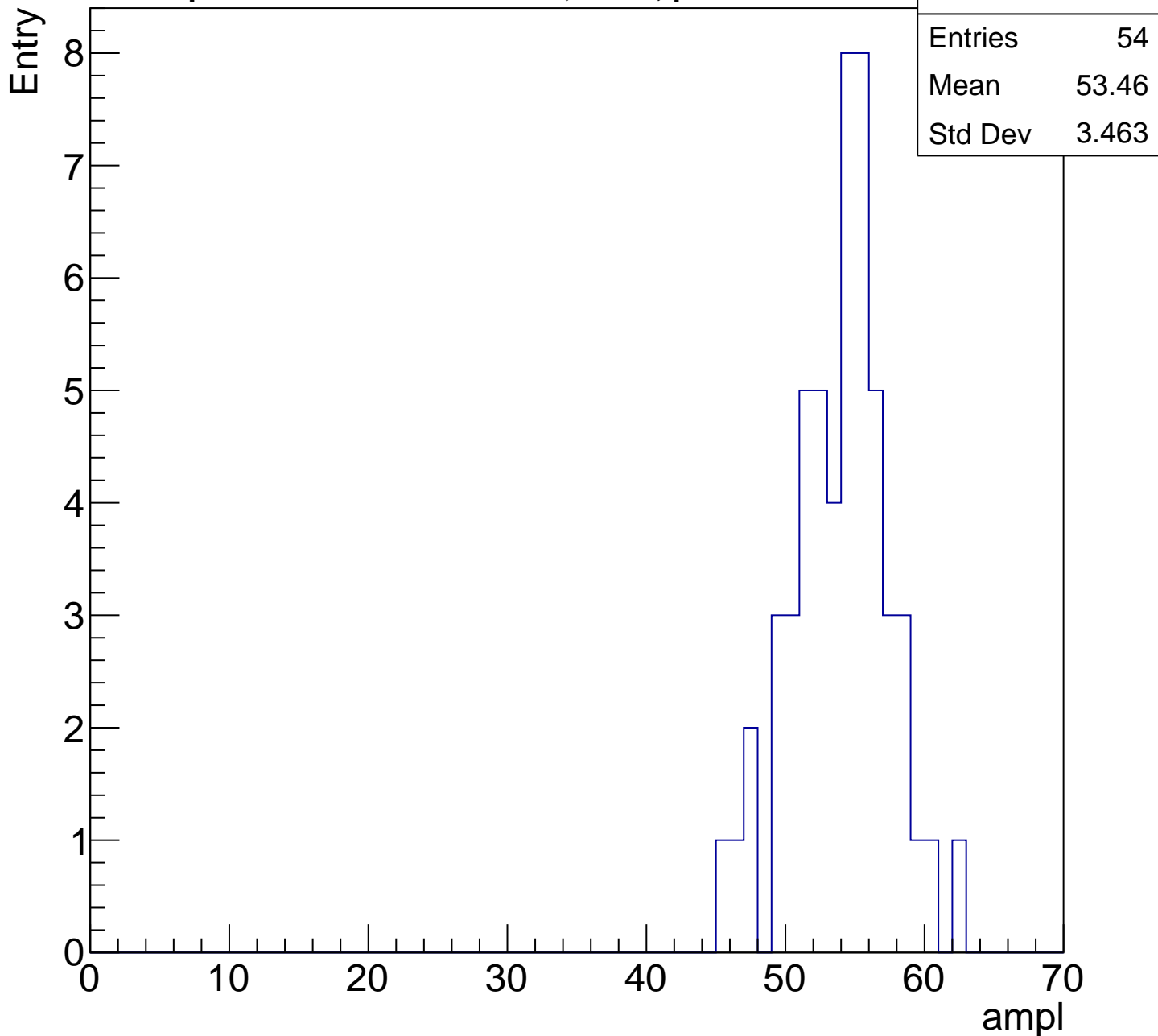
Entry

Entries	66
Mean	47.58
Std Dev	3.726



B1L100S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

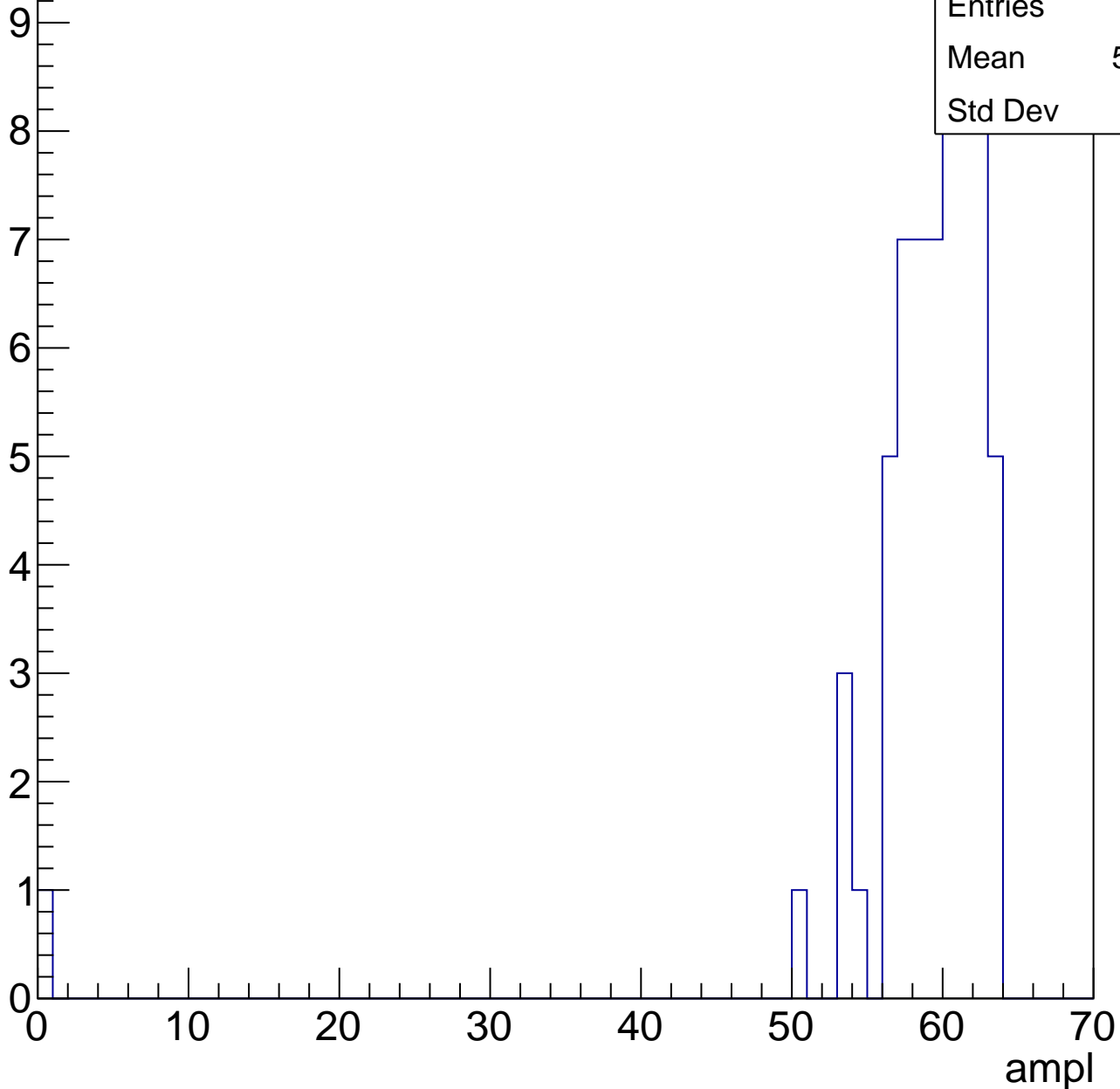


B1L100S, U14-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	58.11
Std Dev	7.89

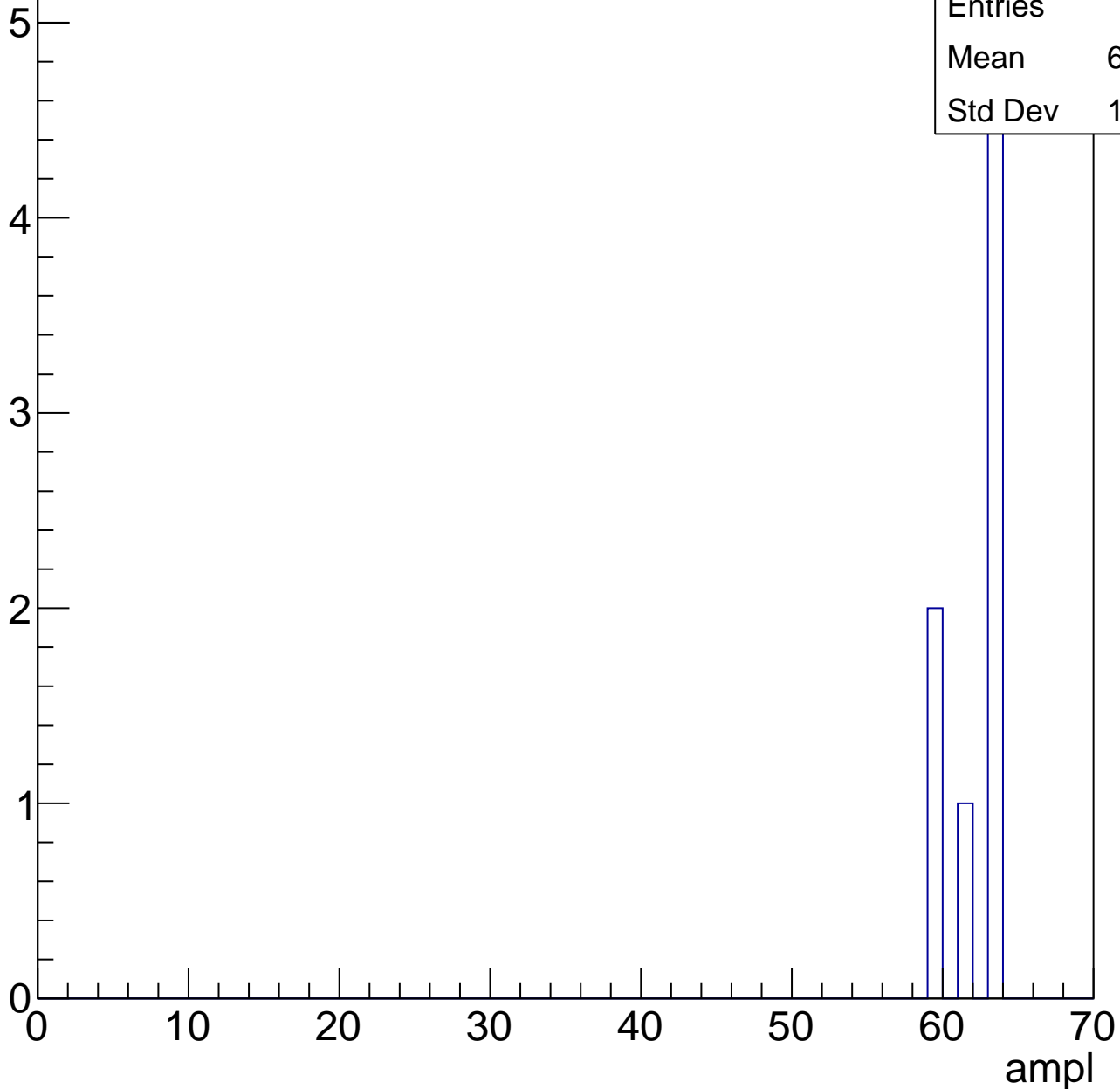


B1L100S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	8
Mean	61.75
Std Dev	1.714



B1L100S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch65, adc0

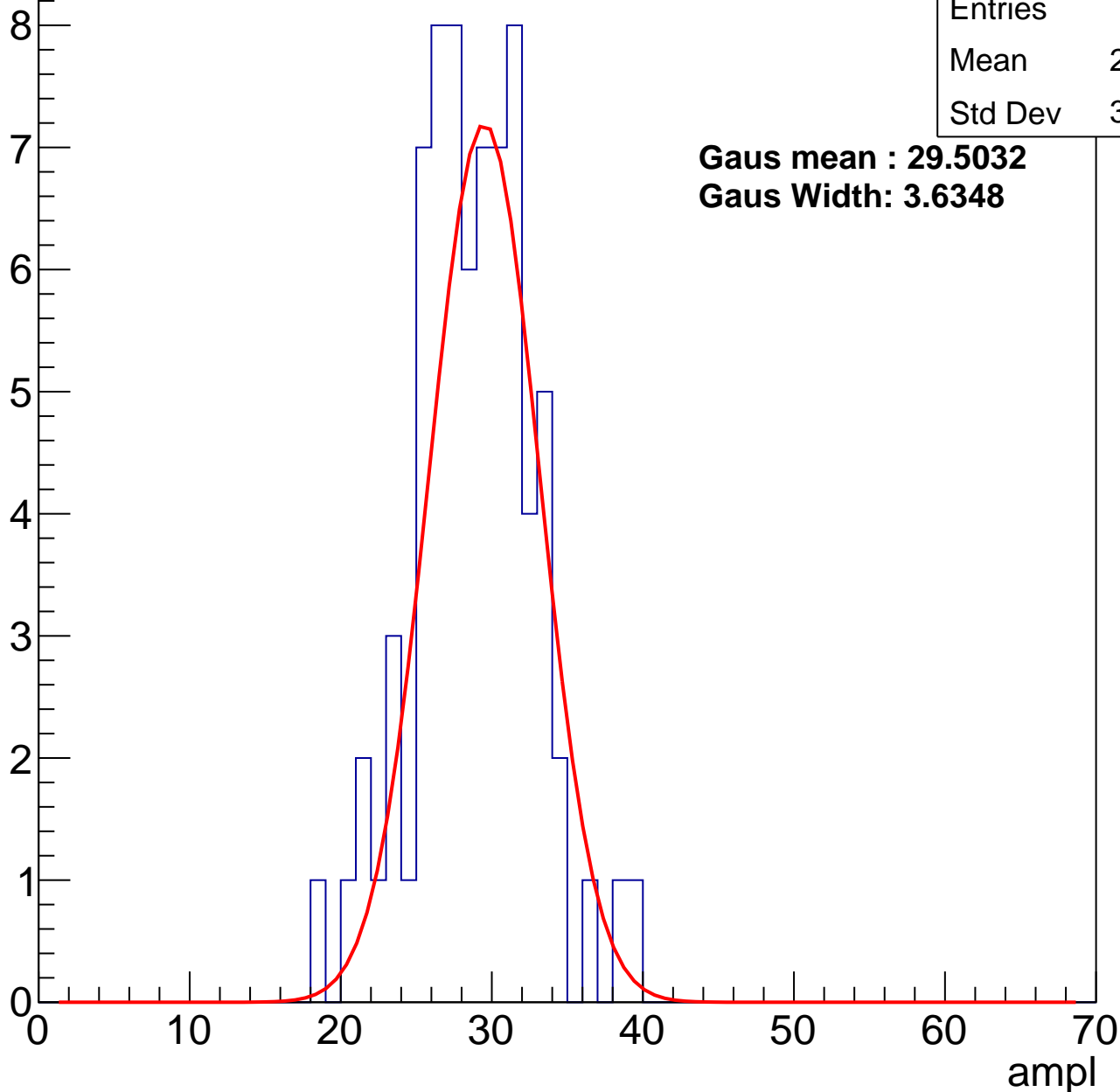
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.34
Std Dev	3.919

Gaus mean : 29.5032

Gaus Width: 3.6348



B1L100S, U14-ch65, adc1

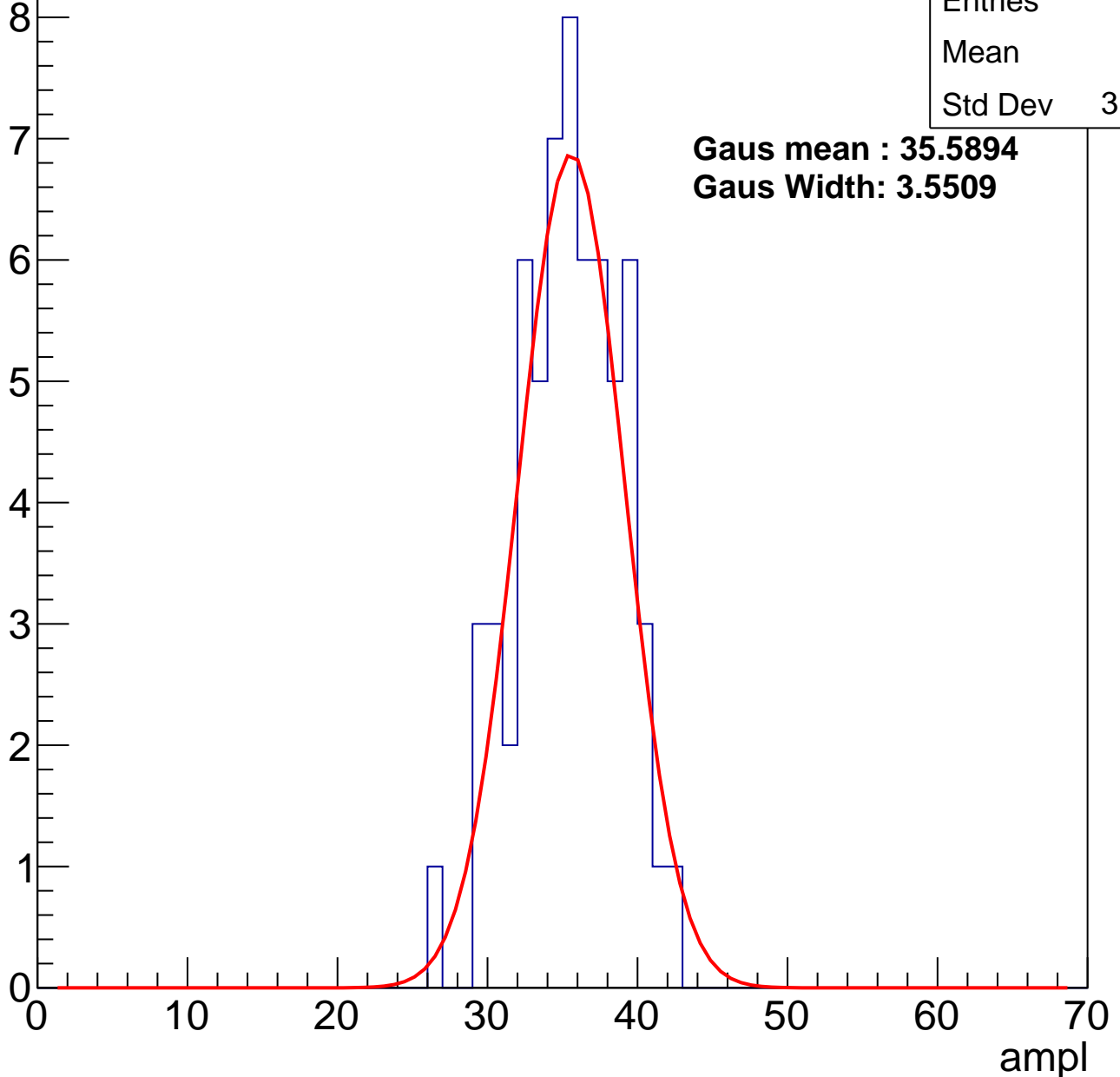
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	35
Std Dev	3.352

Gaus mean : 35.5894

Gaus Width: 3.5509



B1L100S, U14-ch65, adc2

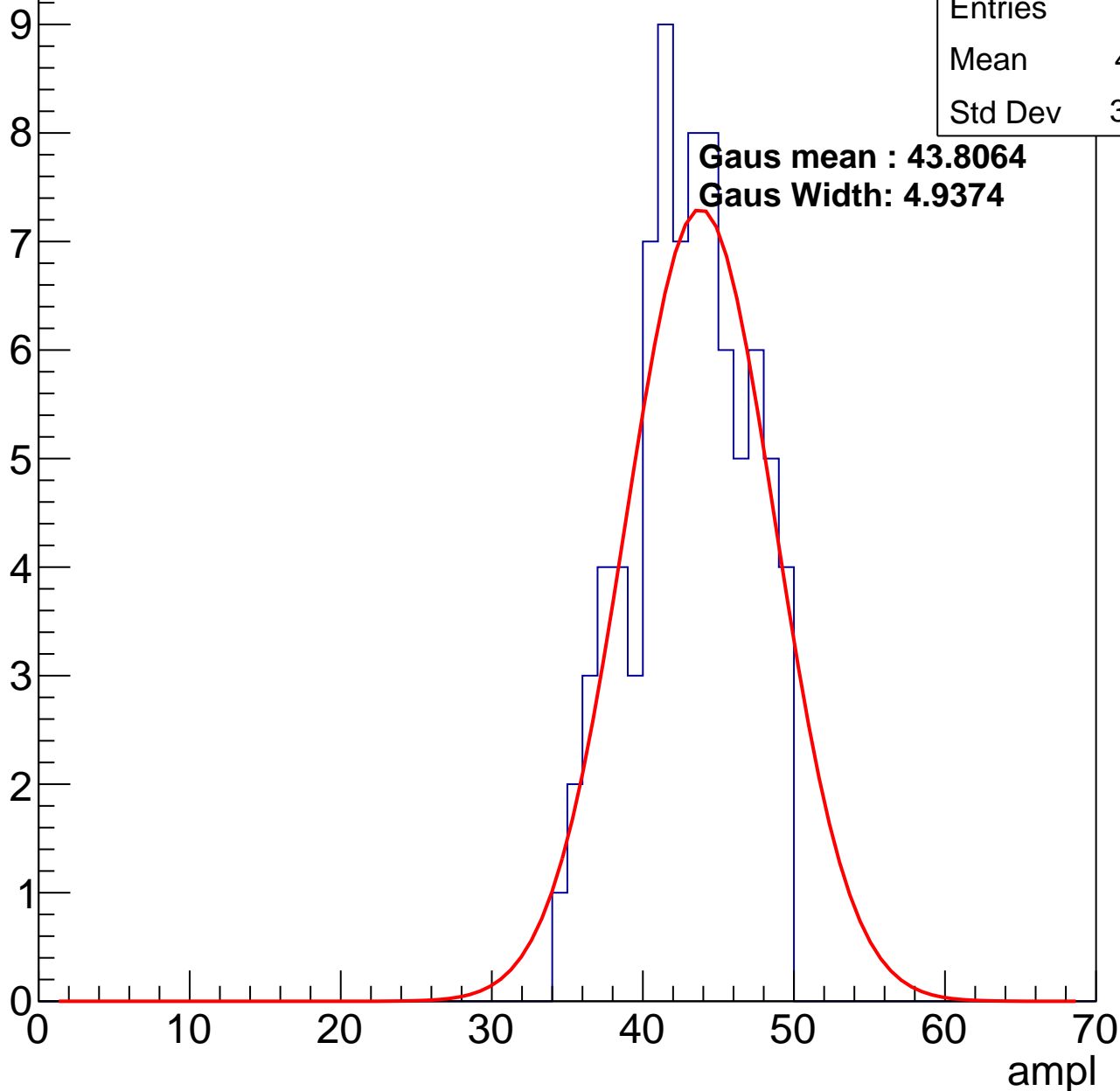
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	42.51
Std Dev	3.788

Gaus mean : 43.8064

Gaus Width: 4.9374

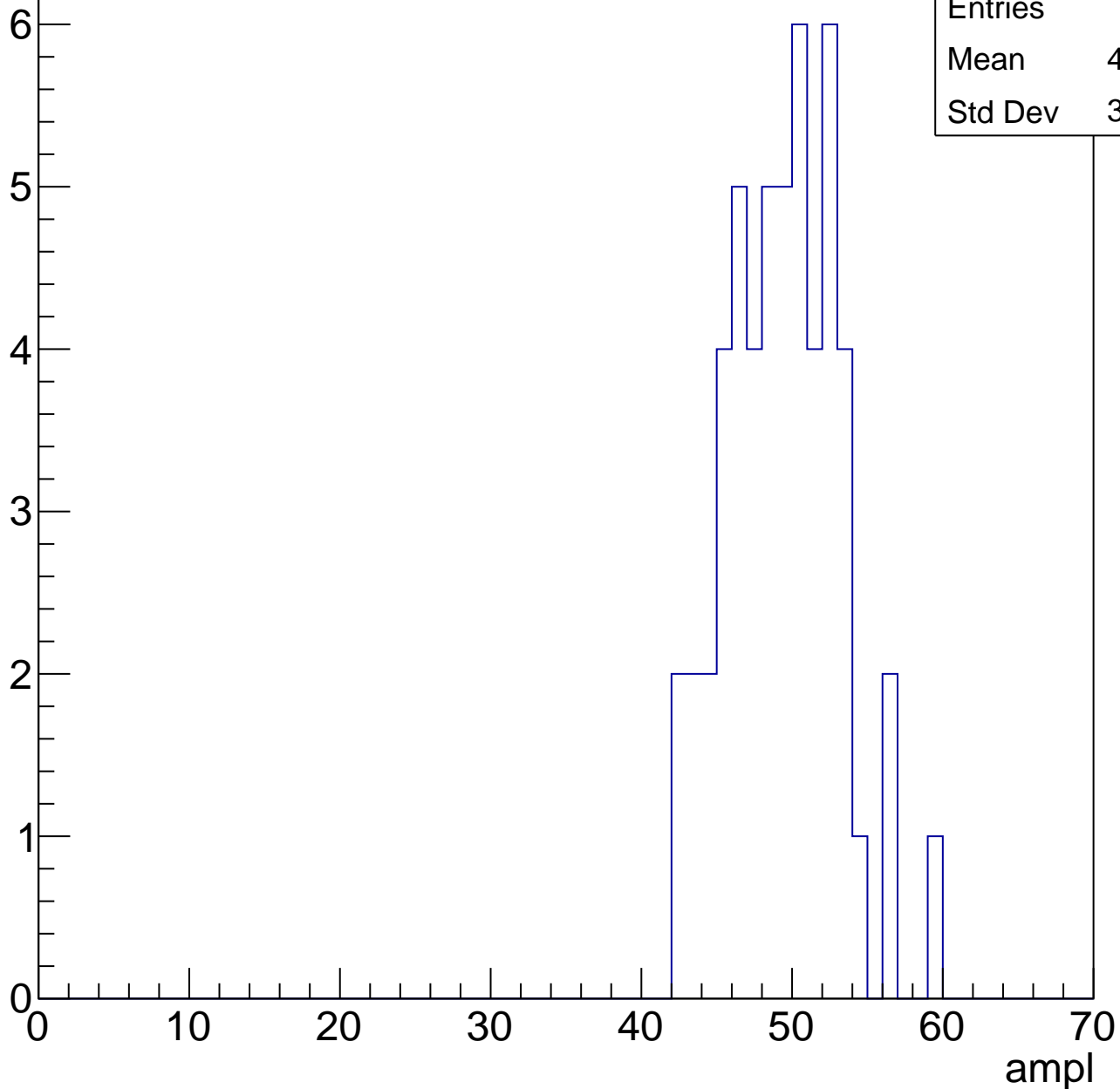


B1L100S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

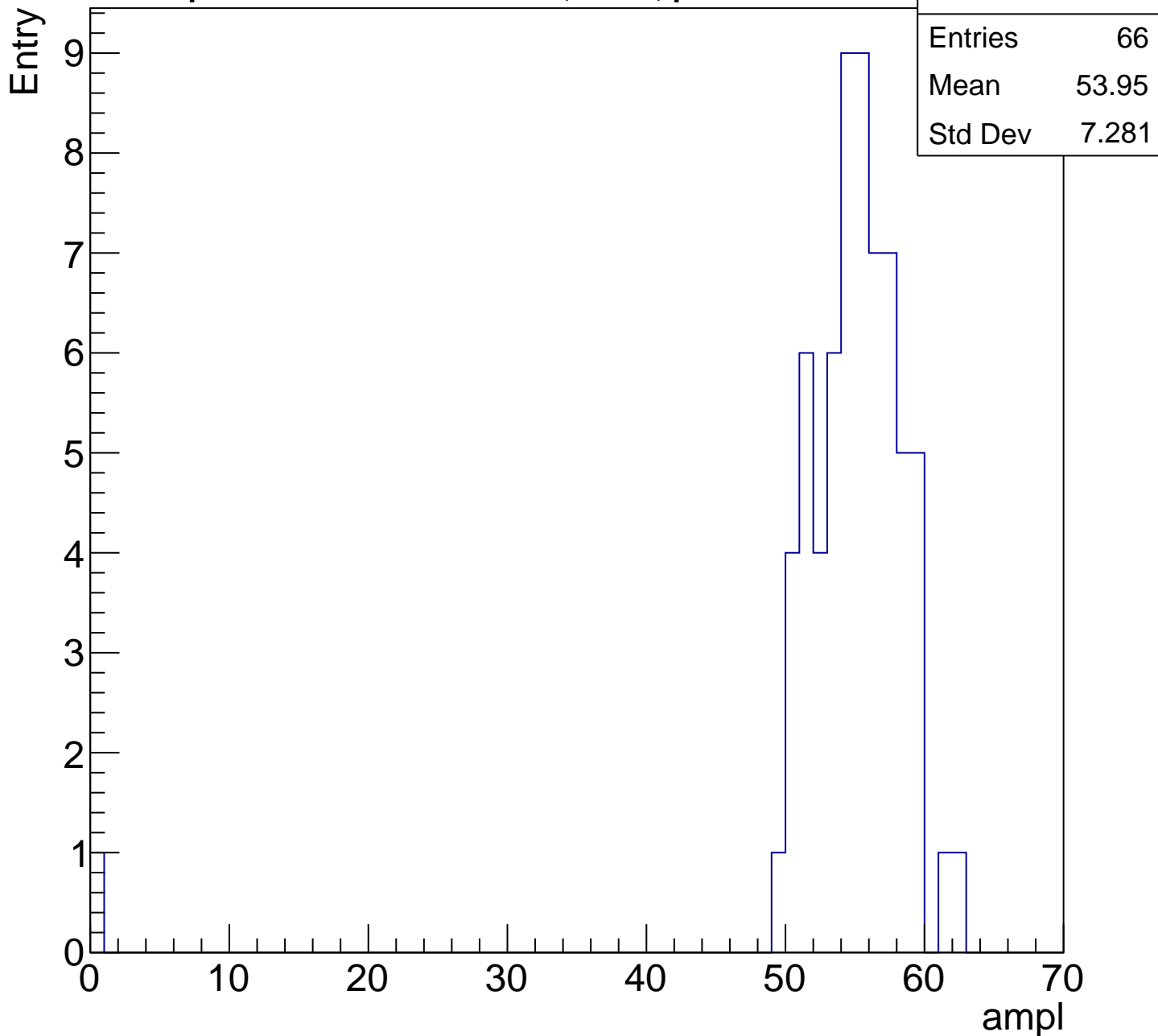
Entry

Entries	53
Mean	48.94
Std Dev	3.667



B1L100S, U14-ch65, adc4

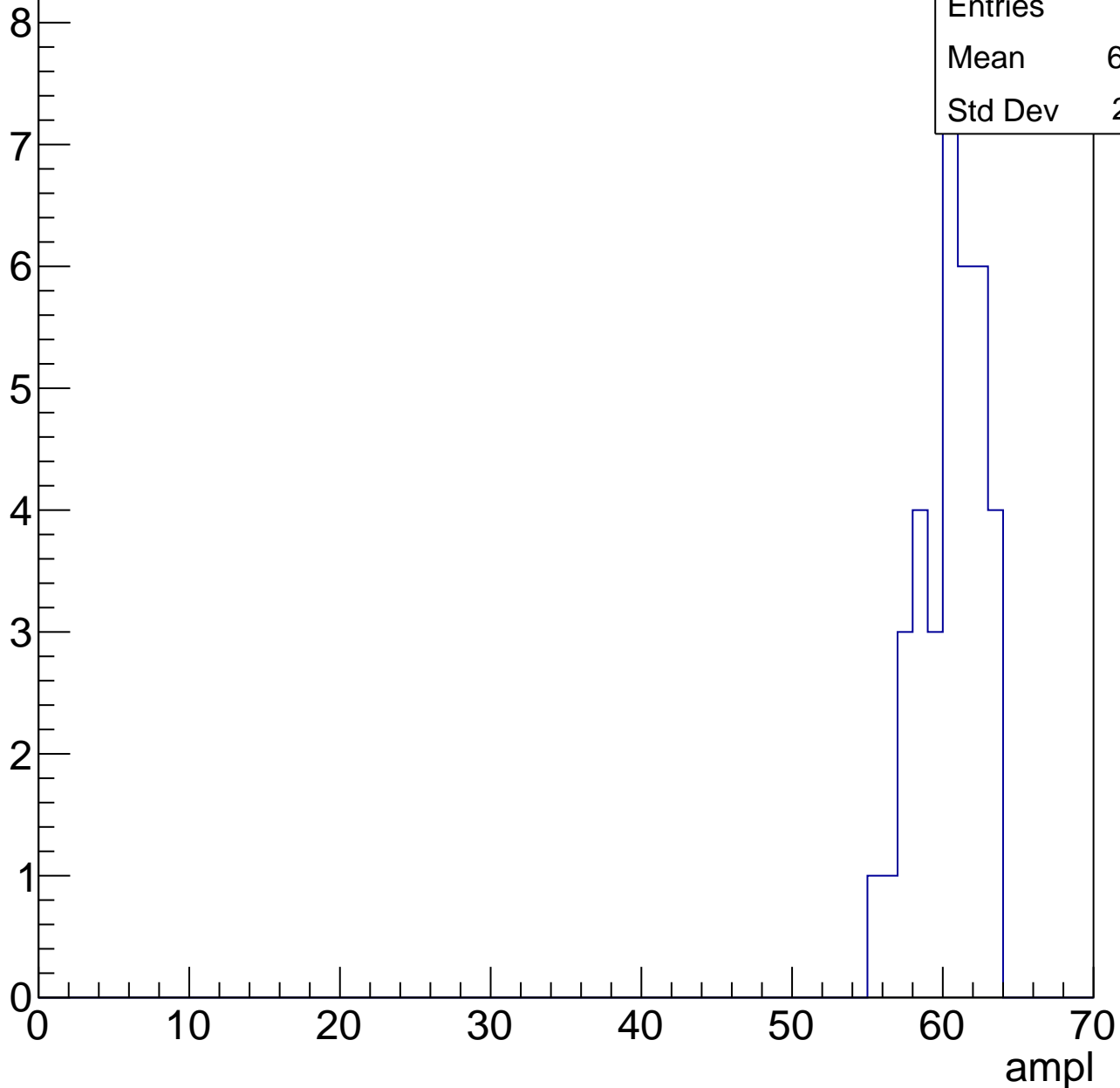
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

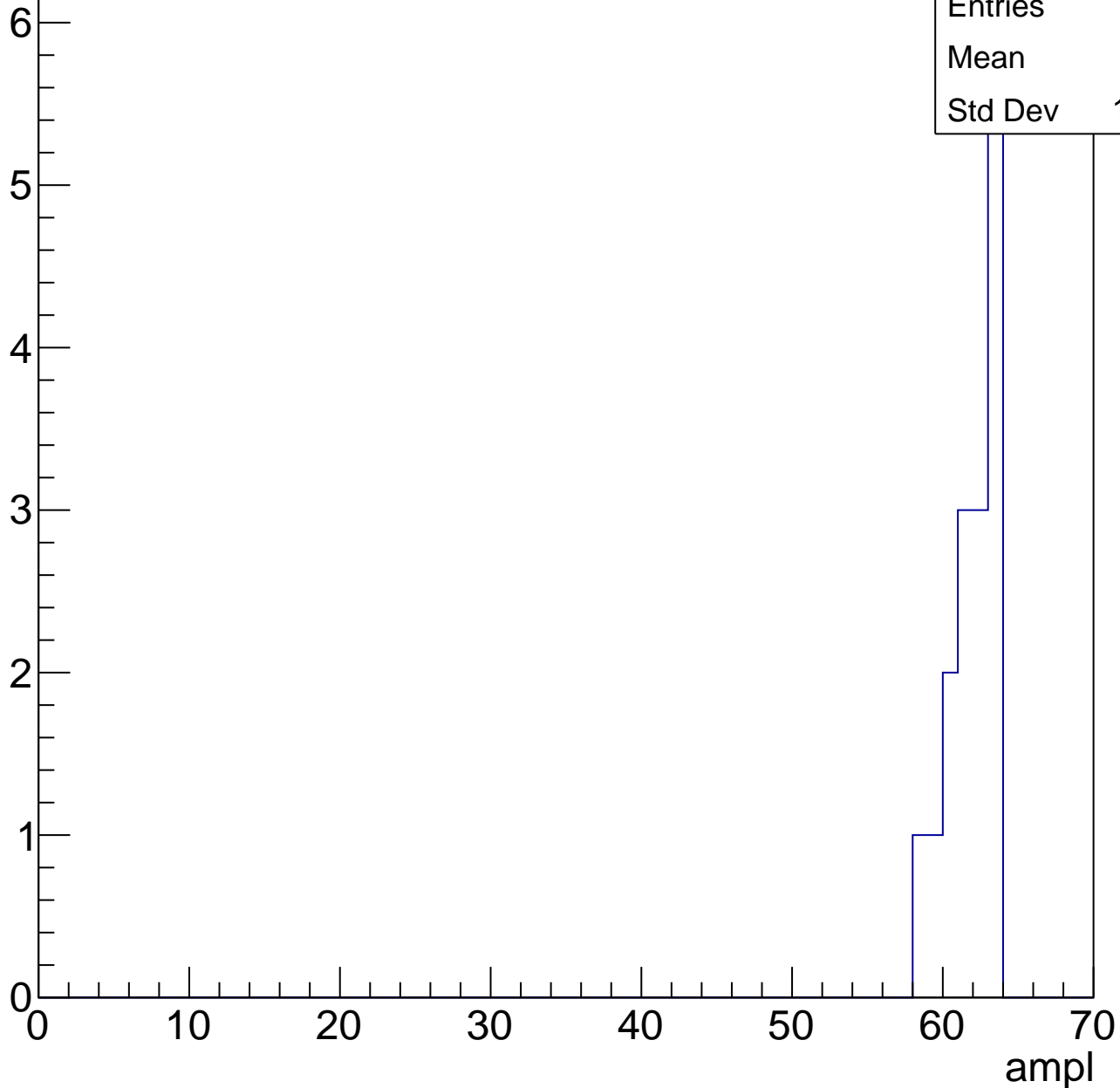
Entry



B1L100S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	16
Mean	61.5
Std Dev	1.541

B1L100S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch66, adc0

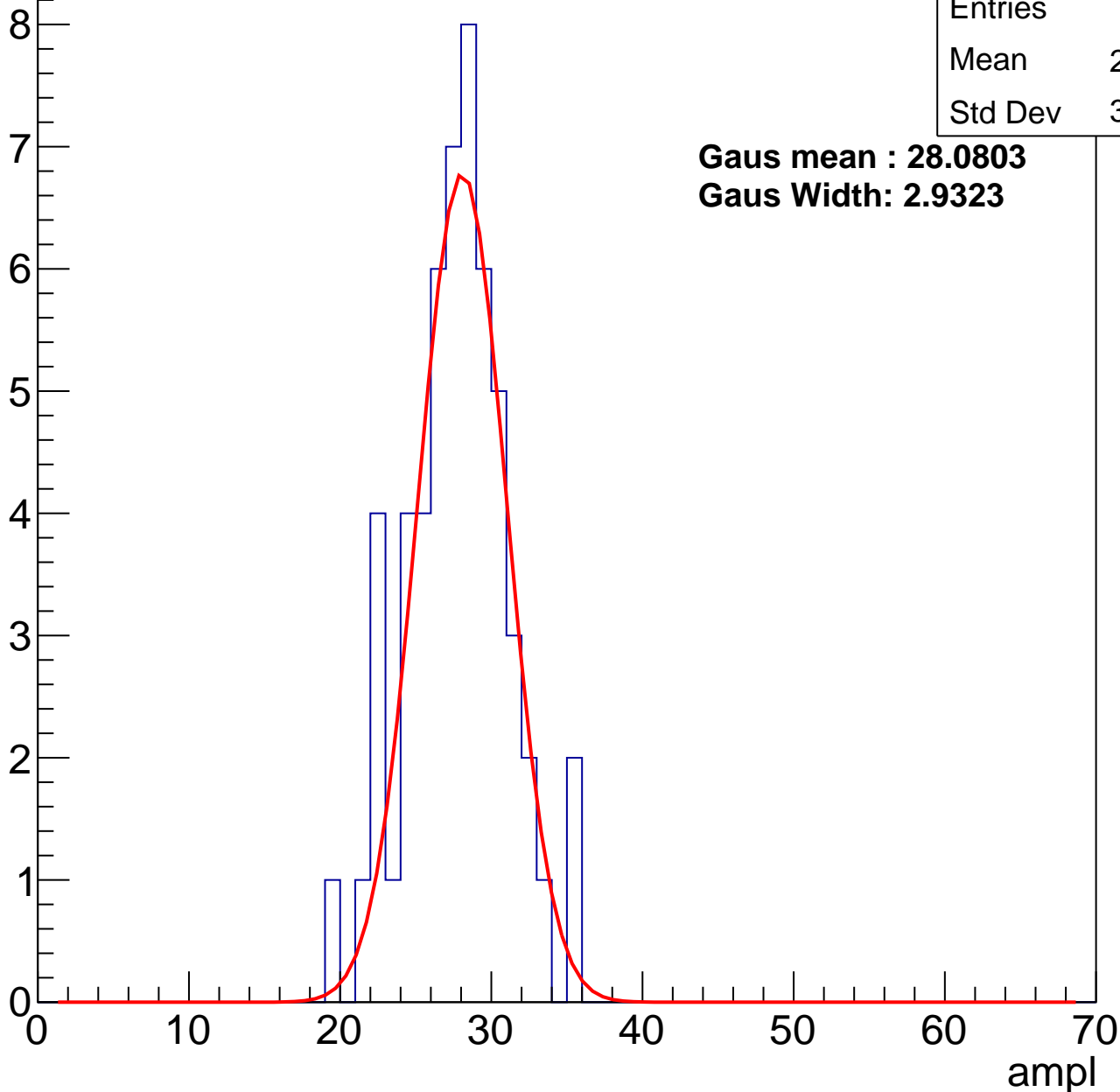
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	27.27
Std Dev	3.327

Gaus mean : 28.0803

Gaus Width: 2.9323



B1L100S, U14-ch66, adc1

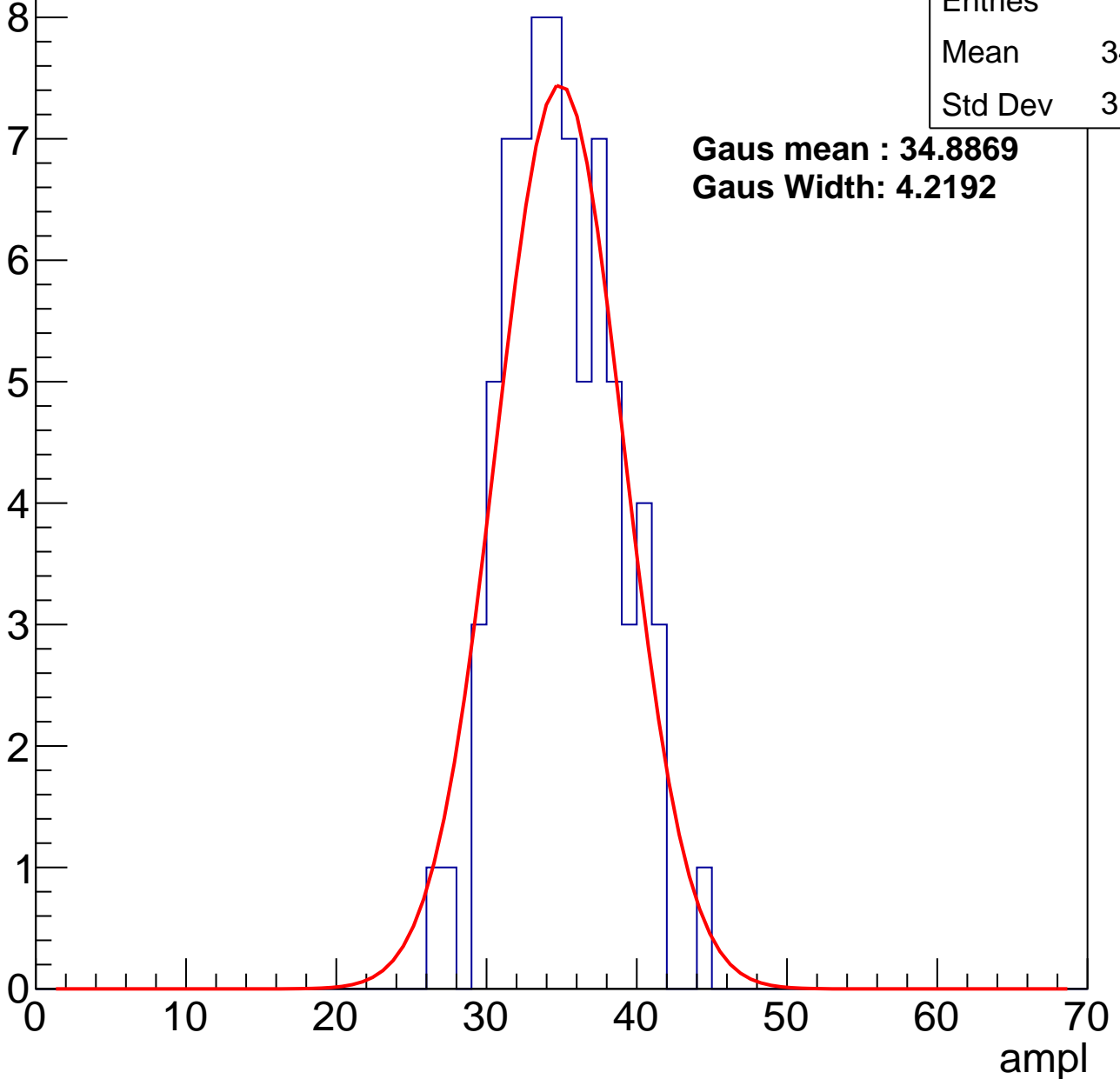
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	34.47
Std Dev	3.627

Gaus mean : 34.8869

Gaus Width: 4.2192



B1L100S, U14-ch66, adc2

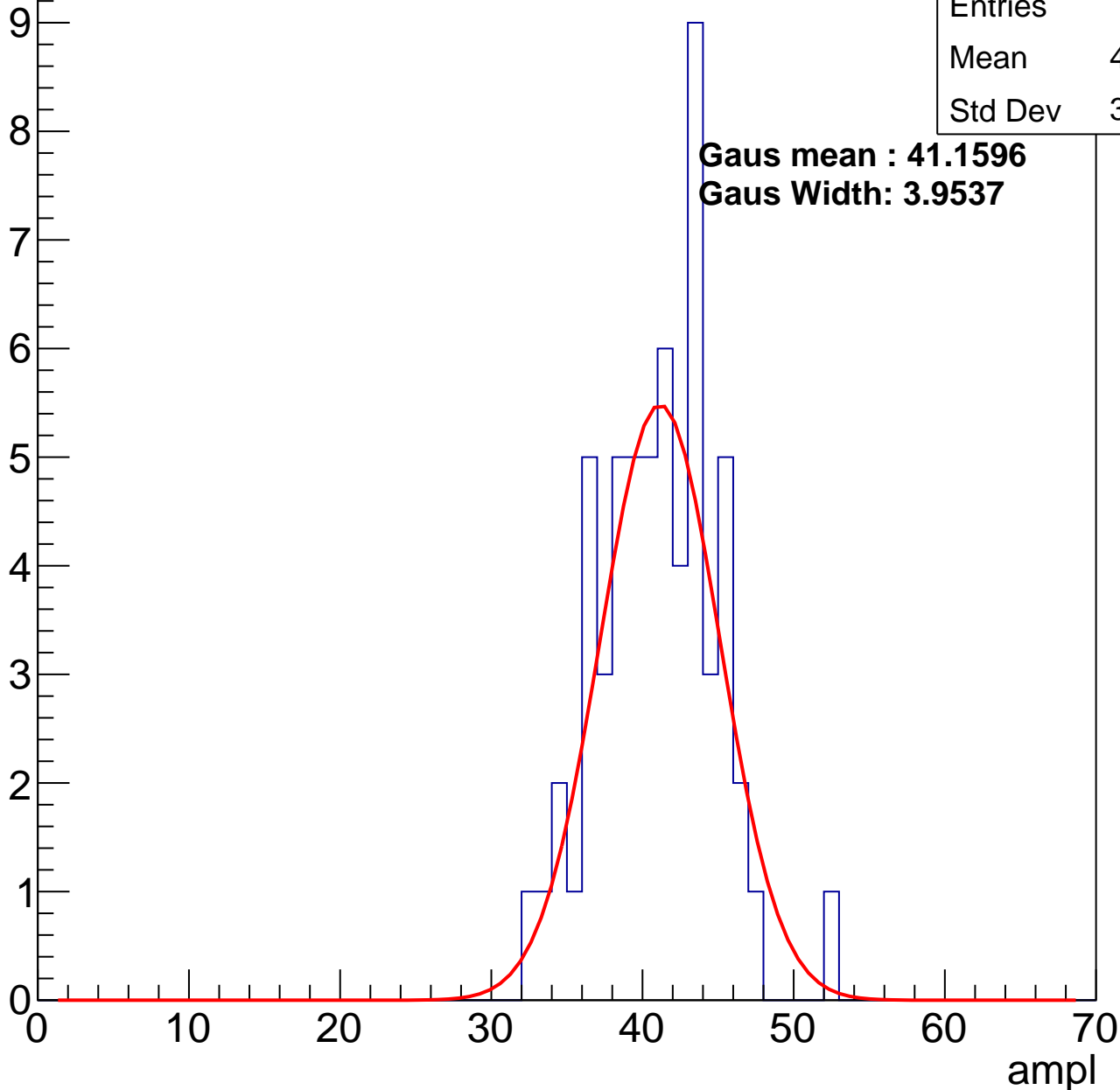
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	40.56
Std Dev	3.828

Gaus mean : 41.1596

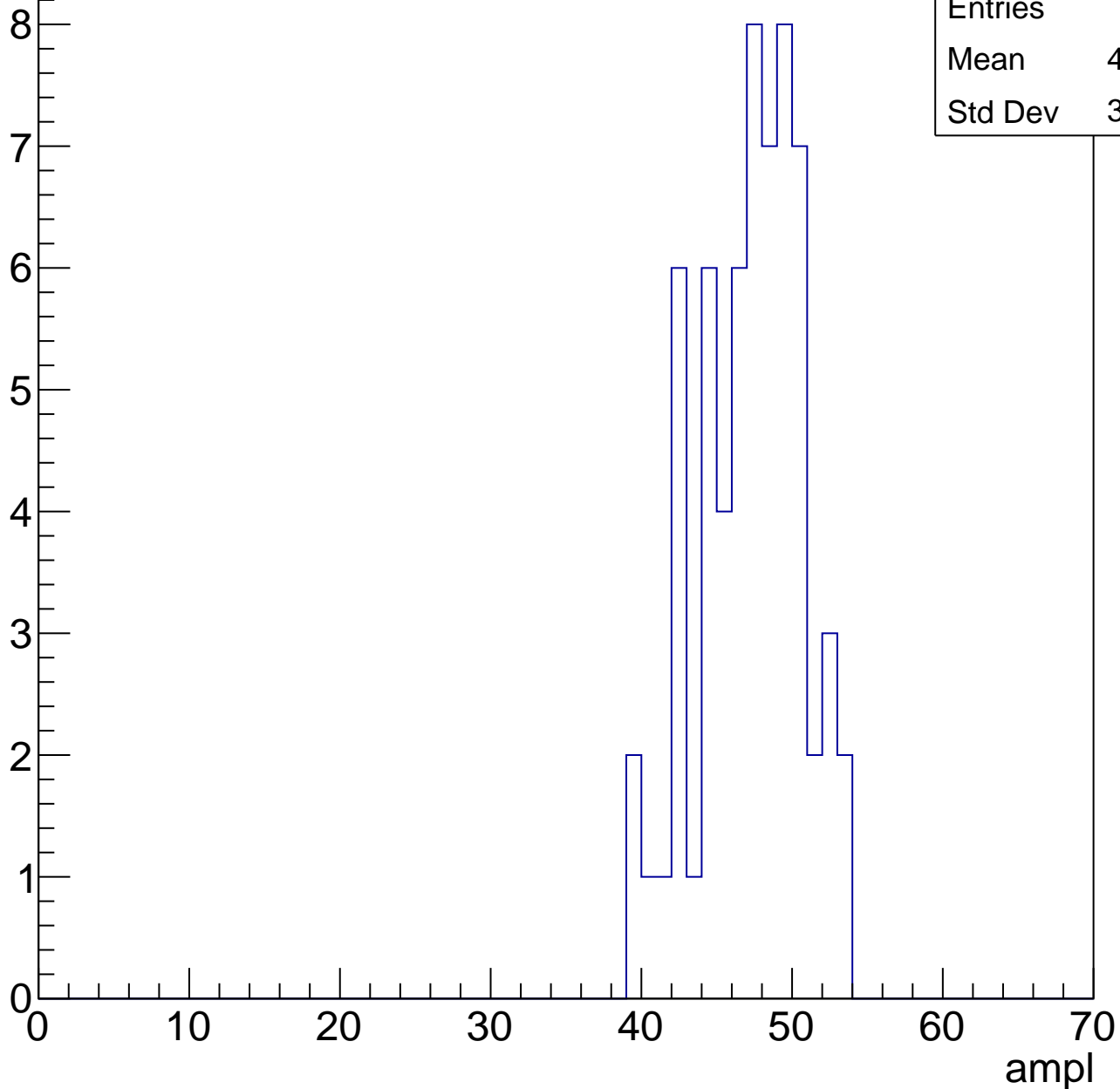
Gaus Width: 3.9537



B1L100S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



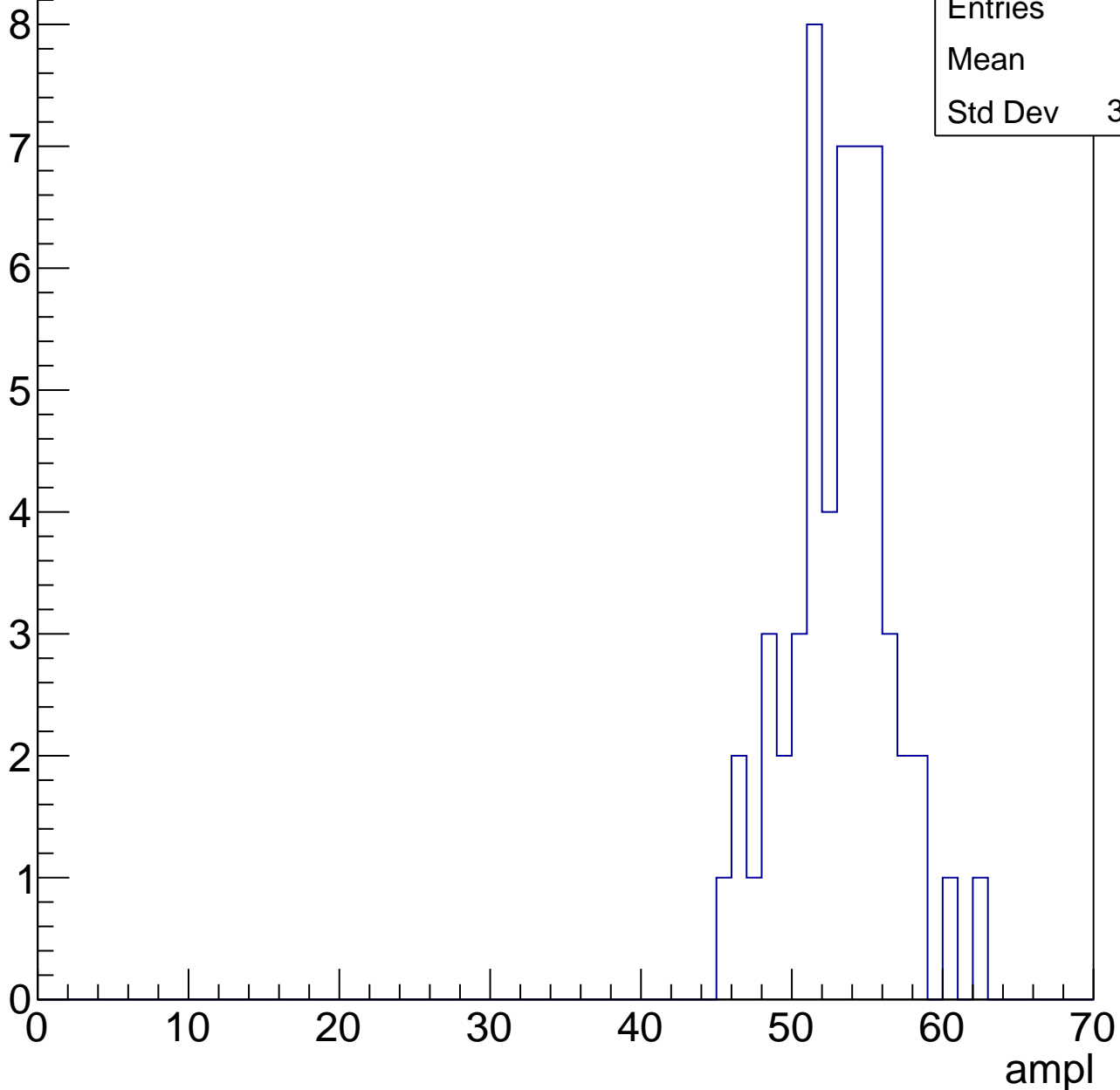
Entries	64
Mean	46.75
Std Dev	3.382

B1L100S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

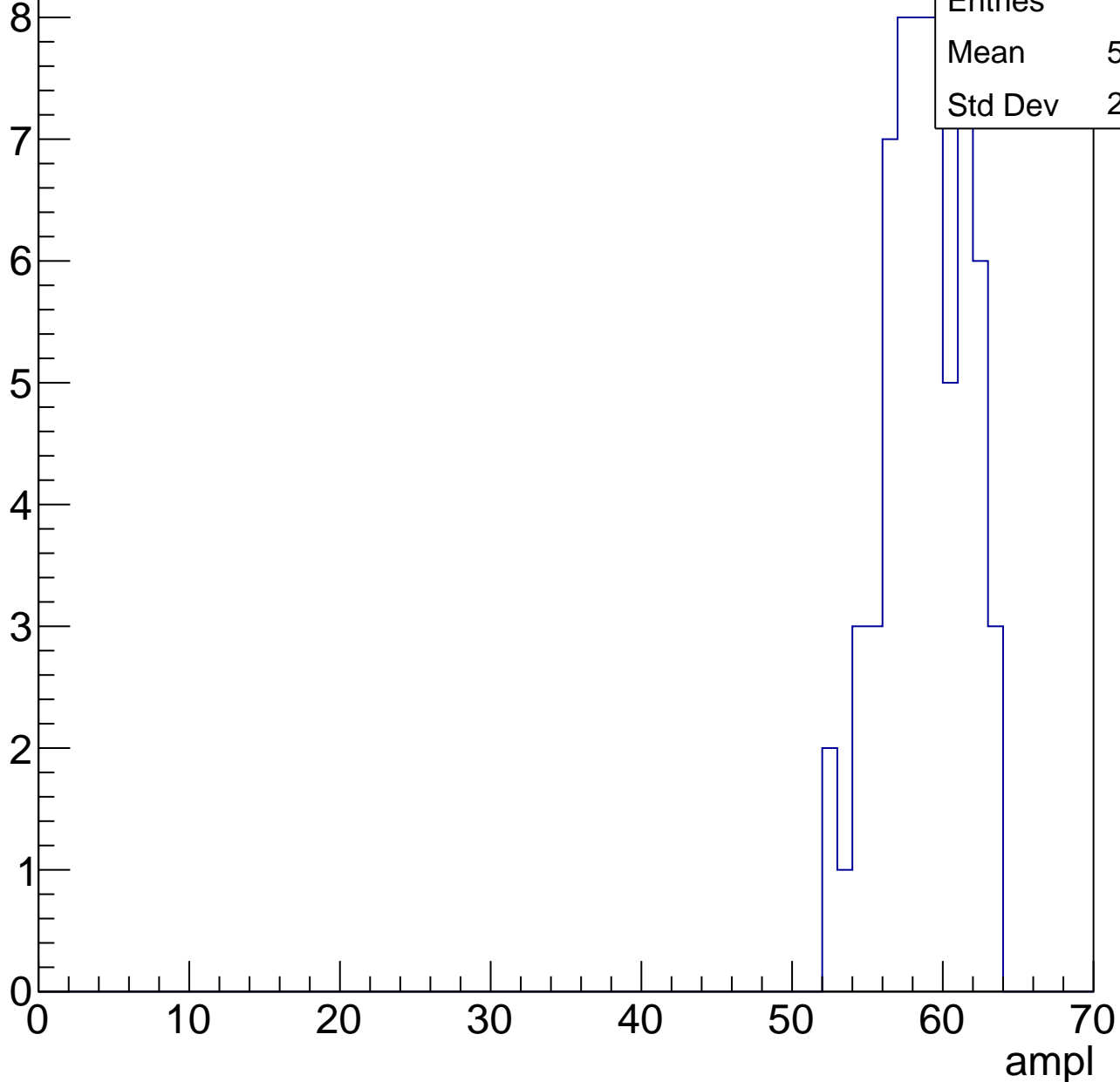
Entries	54
Mean	52.7
Std Dev	3.435



B1L100S, U14-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

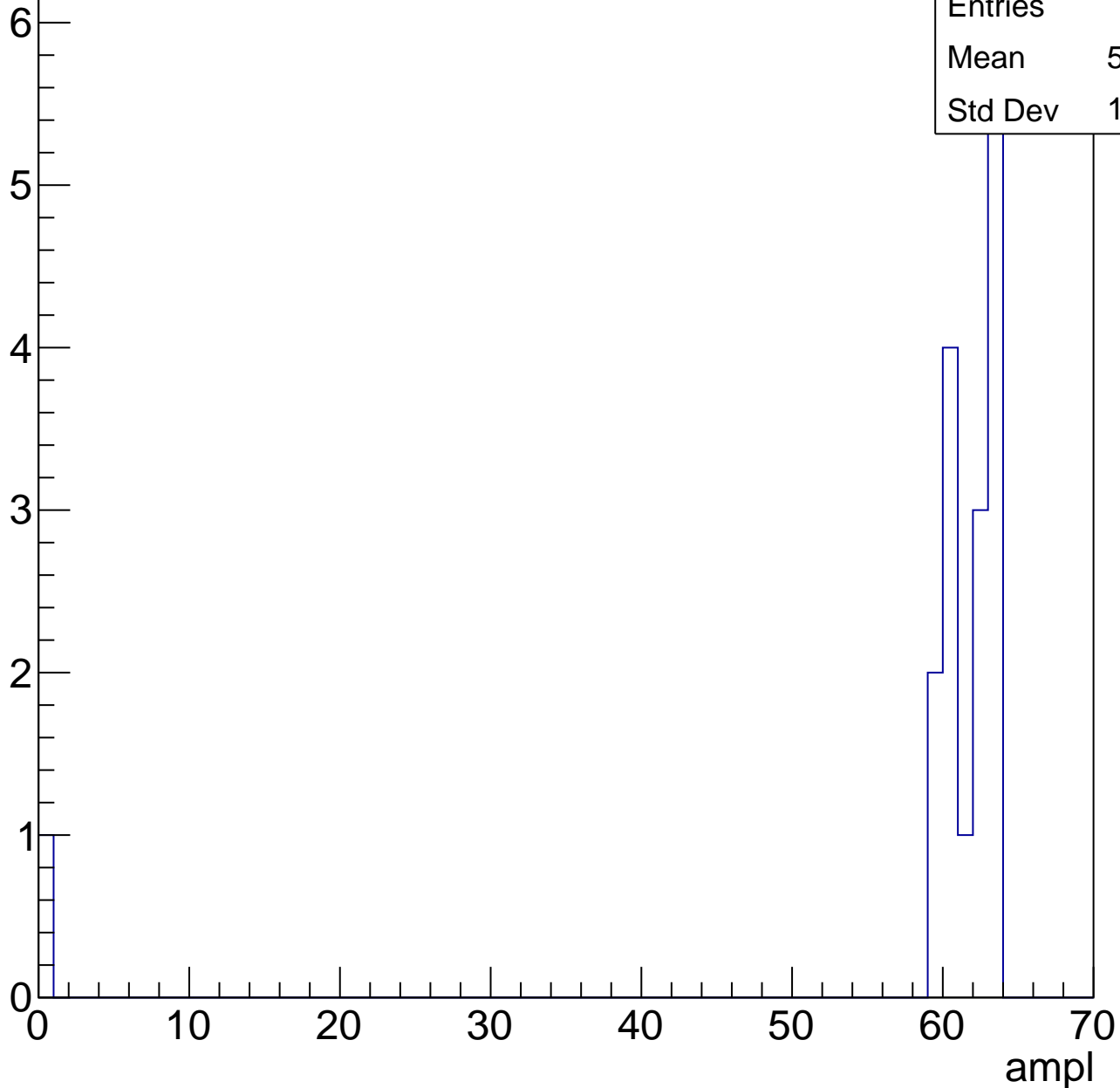
Entry



B1L100S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

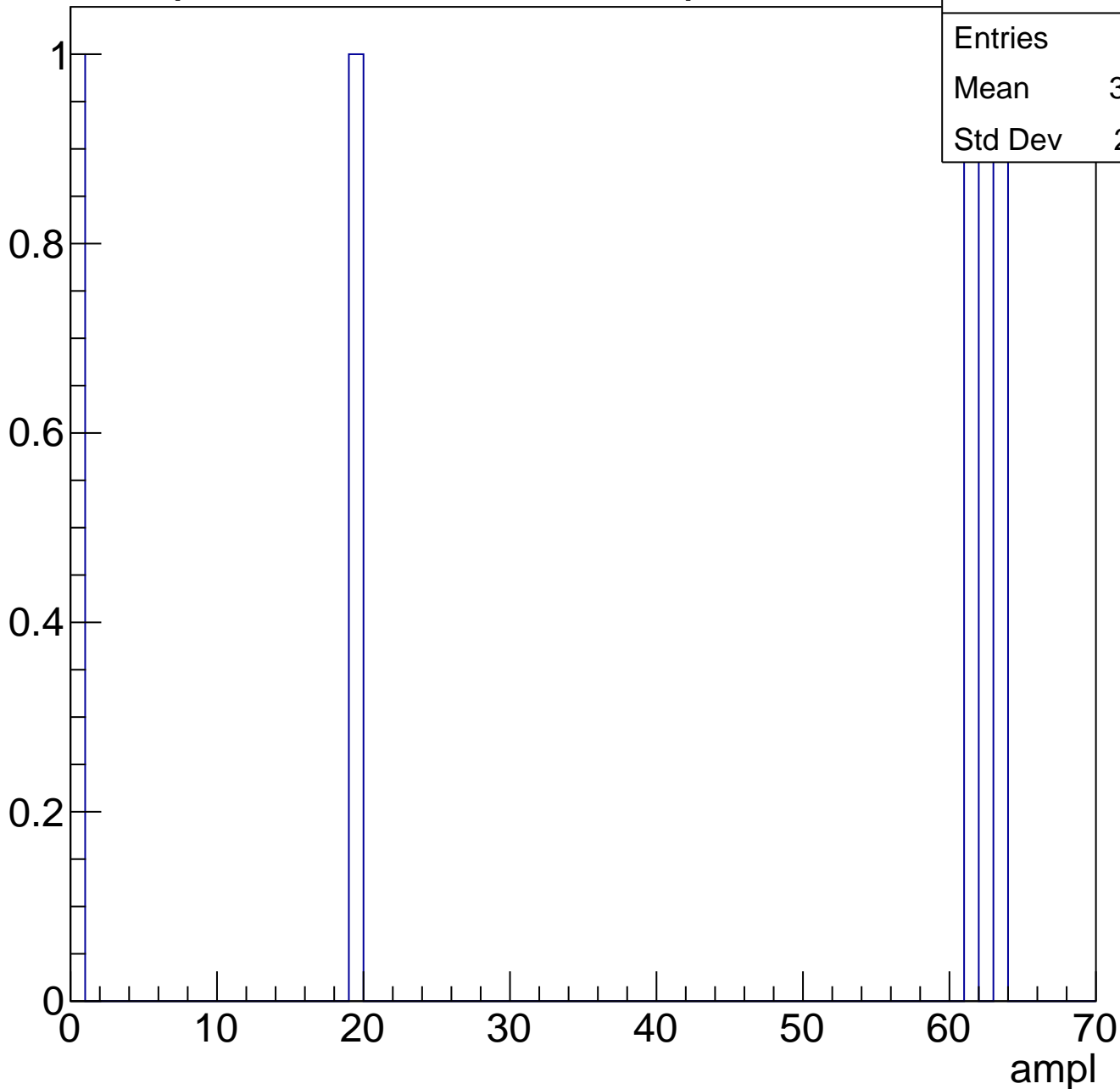
Entry



B1L100S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	35.75
Std Dev	27.11

B1L100S, U14-ch67, adc0

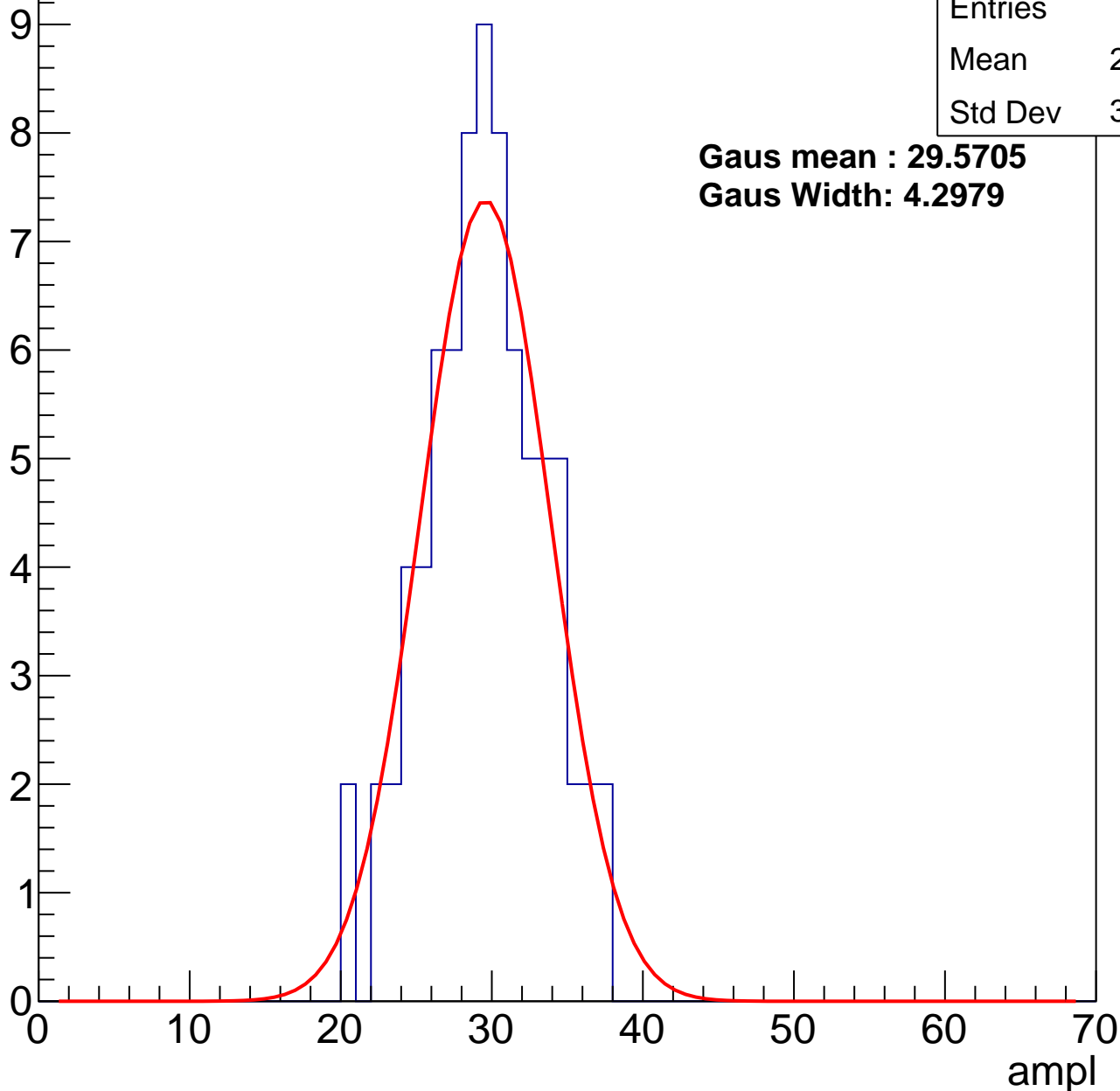
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.05
Std Dev	3.859

Gaus mean : 29.5705

Gaus Width: 4.2979



B1L100S, U14-ch67, adc1

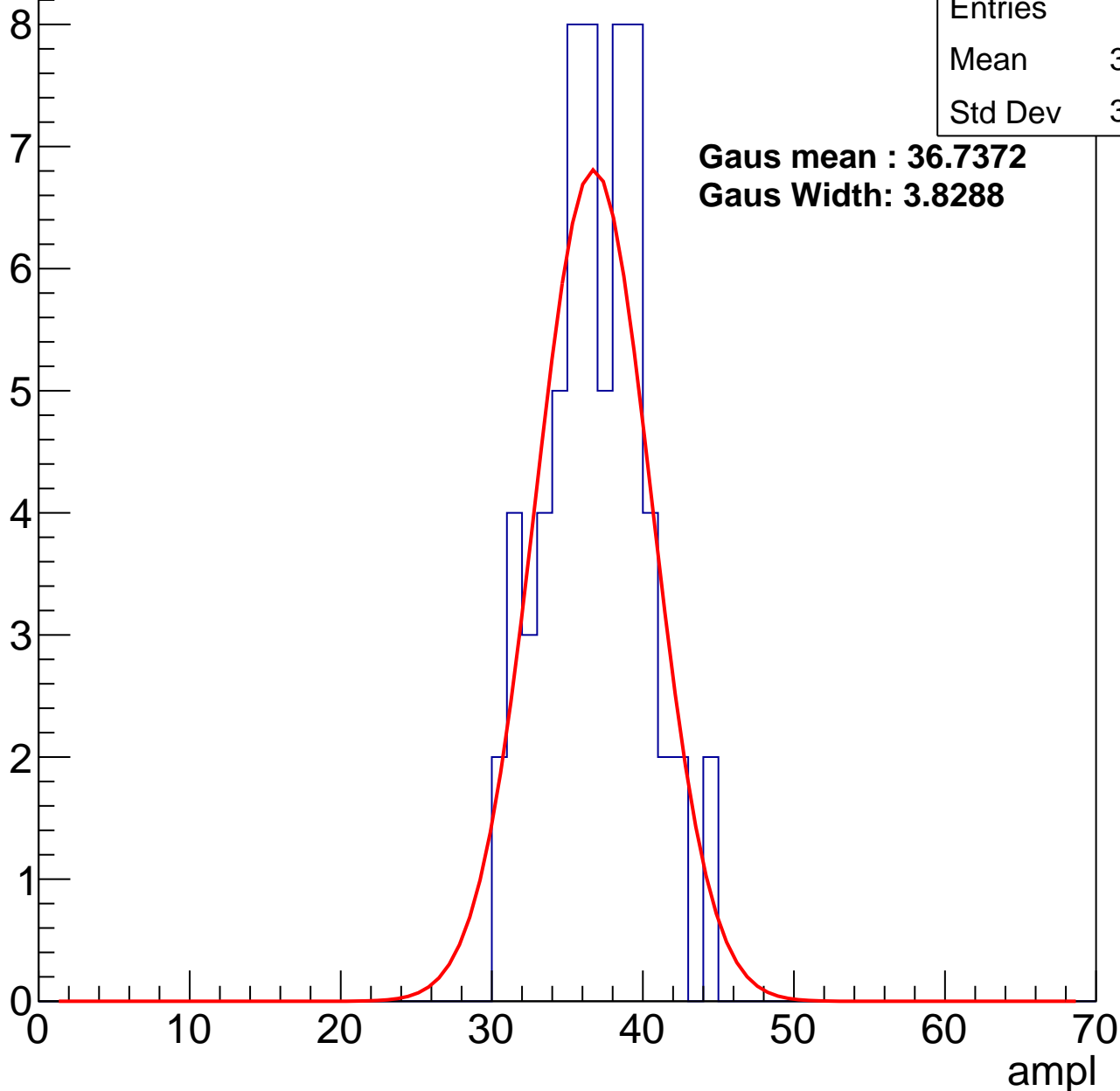
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.38
Std Dev	3.276

Gaus mean : 36.7372

Gaus Width: 3.8288



B1L100S, U14-ch67, adc2

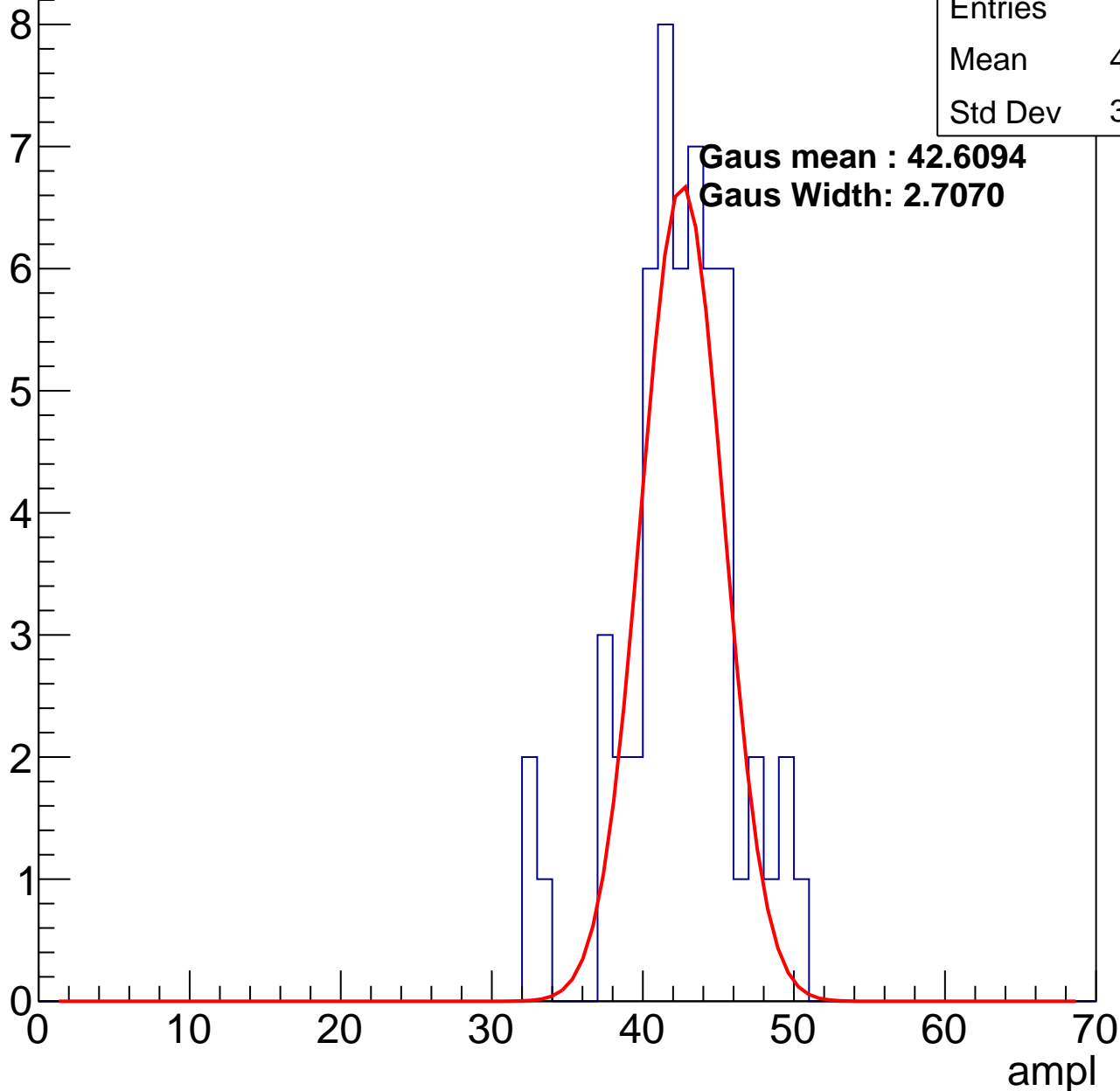
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.02
Std Dev	3.749

Gaus mean : 42.6094

Gaus Width: 2.7070

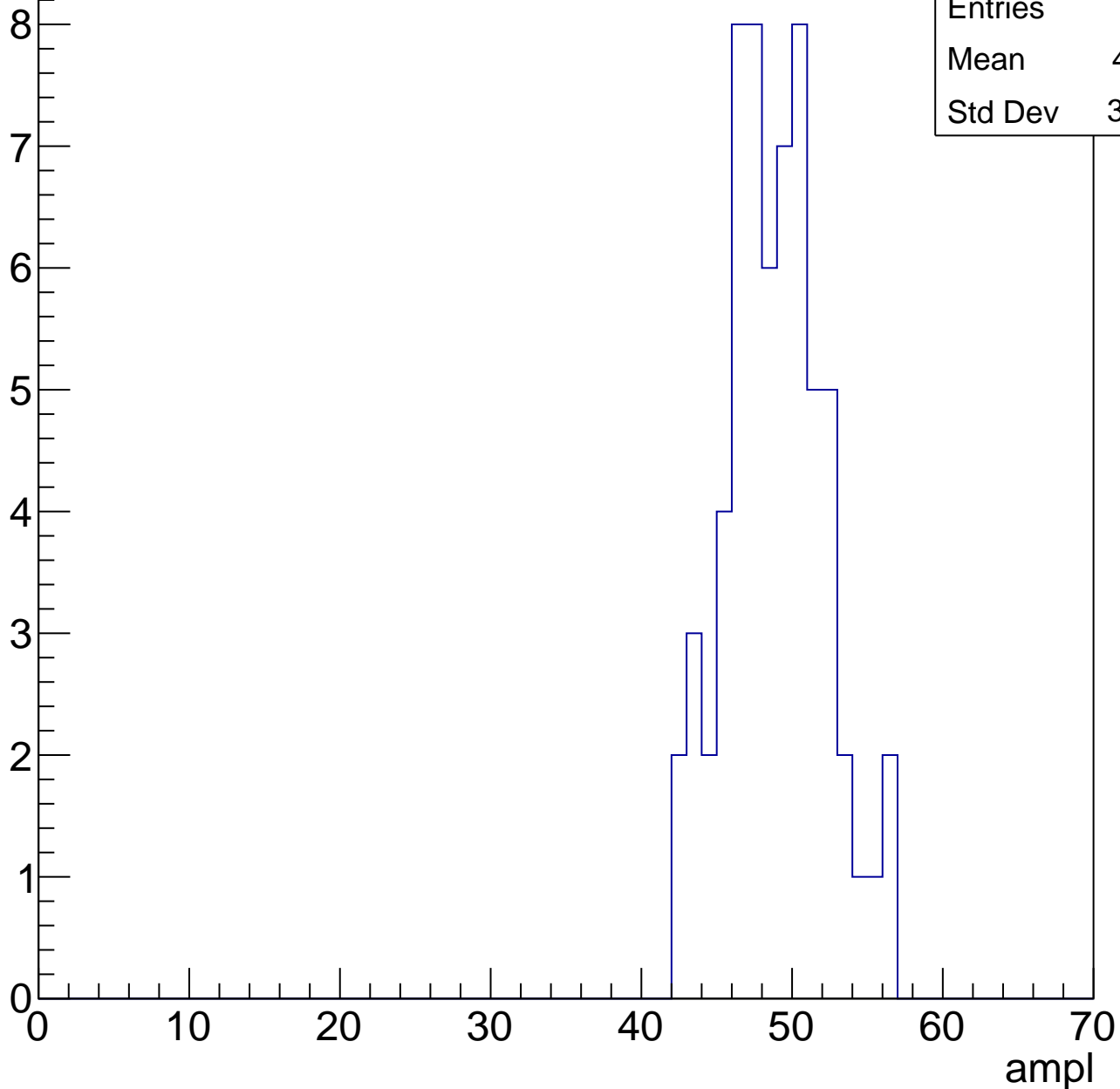


B1L100S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

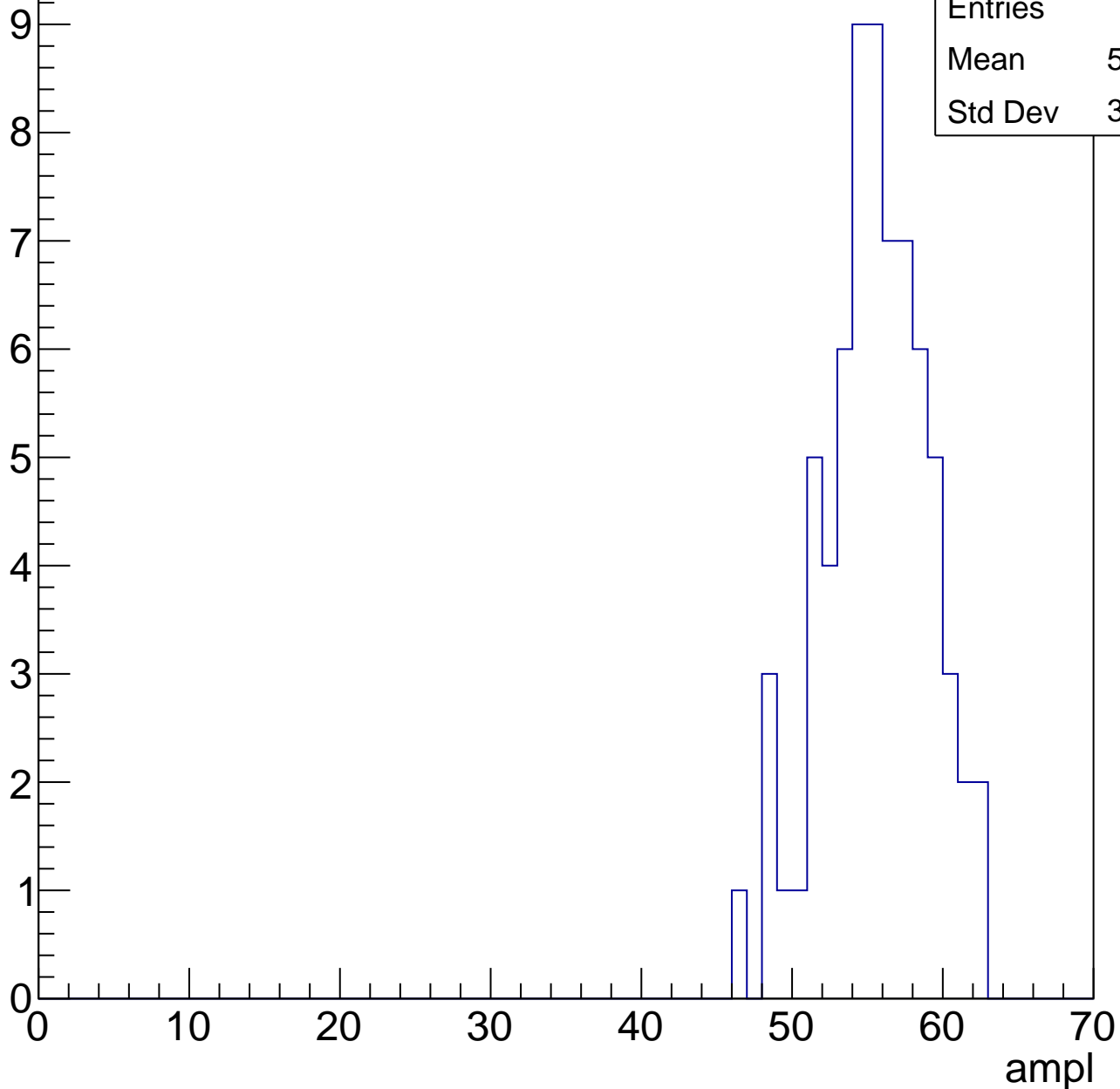
Entries	64
Mean	48.41
Std Dev	3.239



B1L100S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

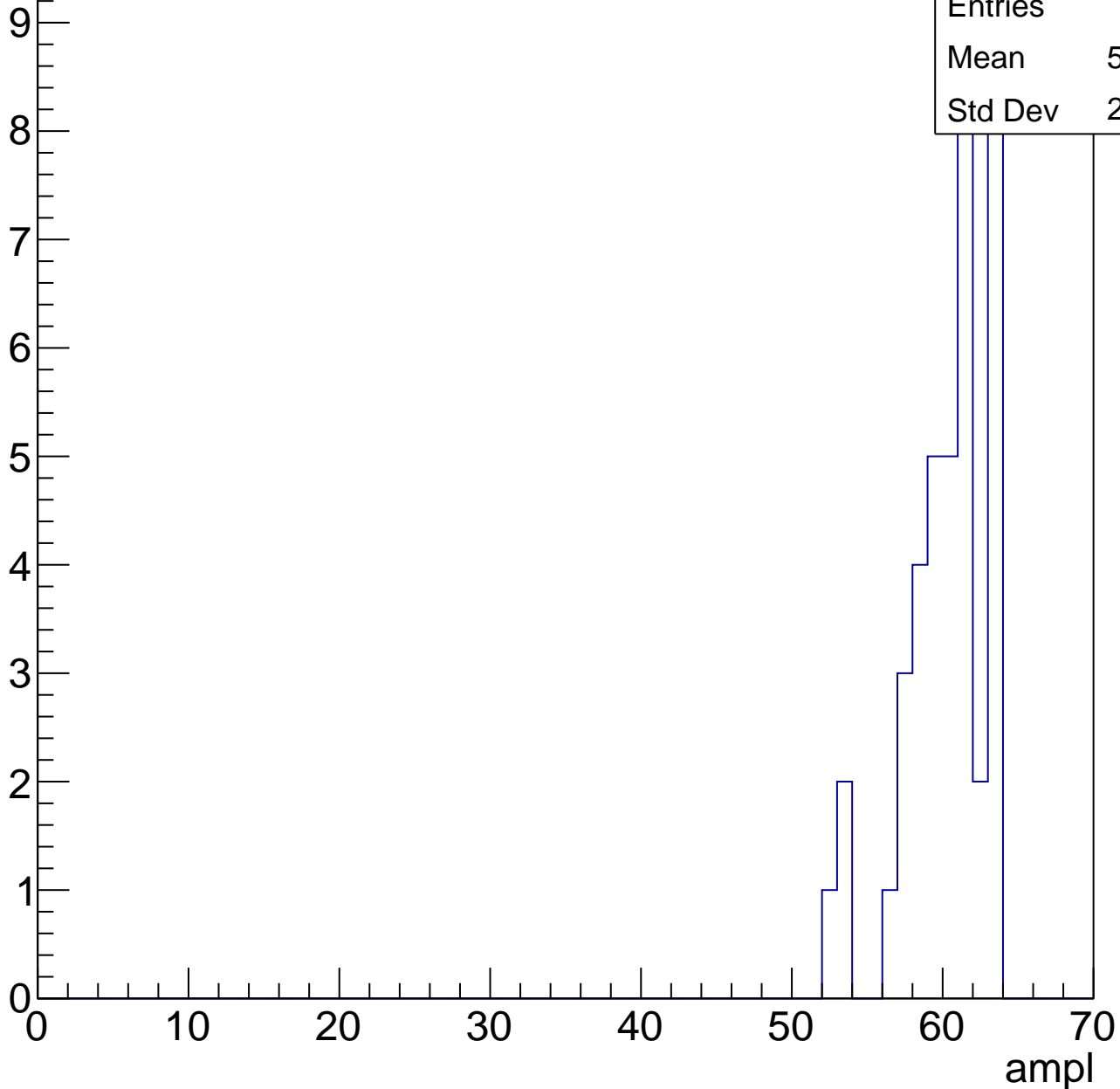


B1L100S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59.77
Std Dev	2.833

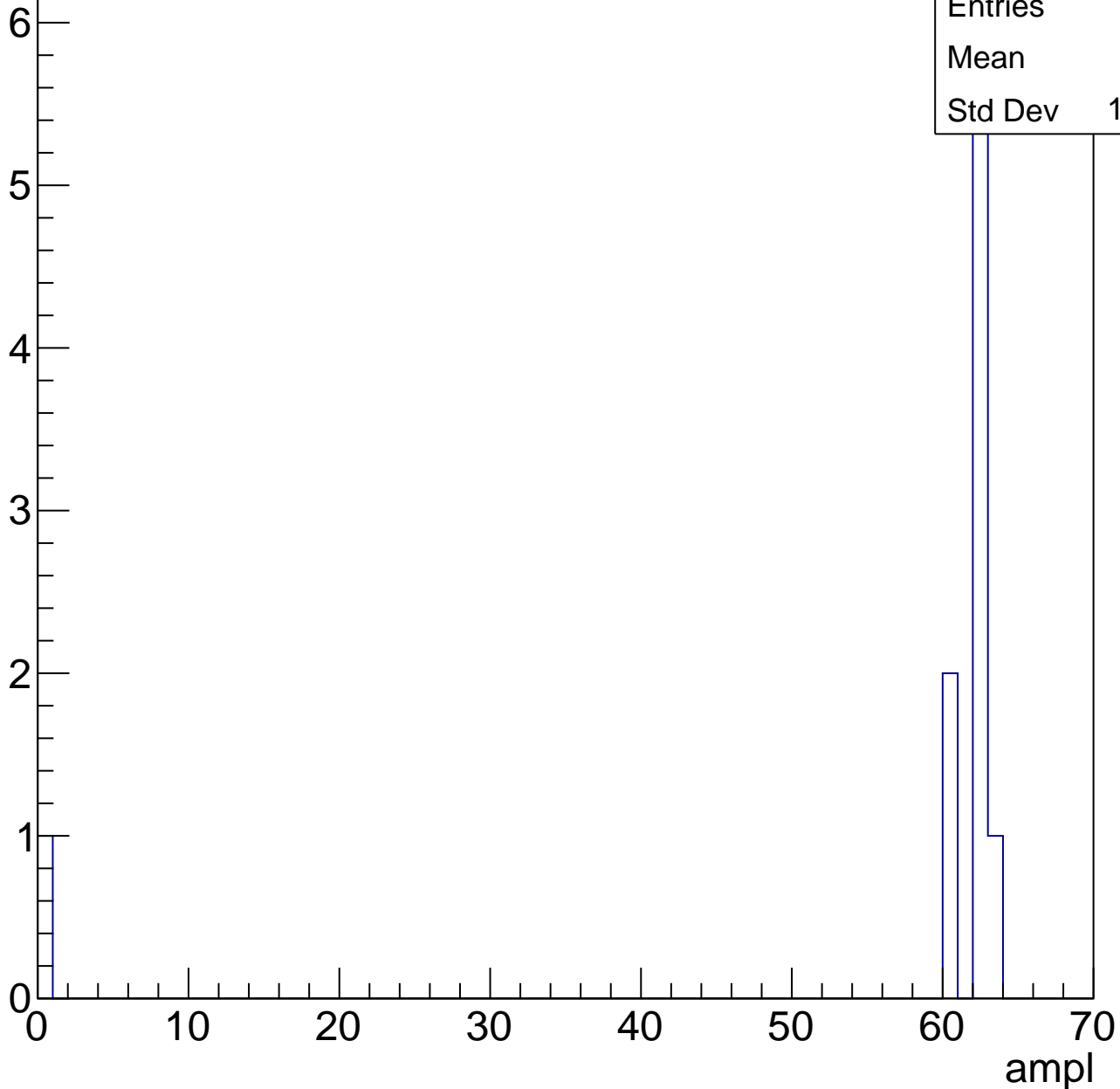


B1L100S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	55.5
Std Dev	18.52



B1L100S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch68, adc0

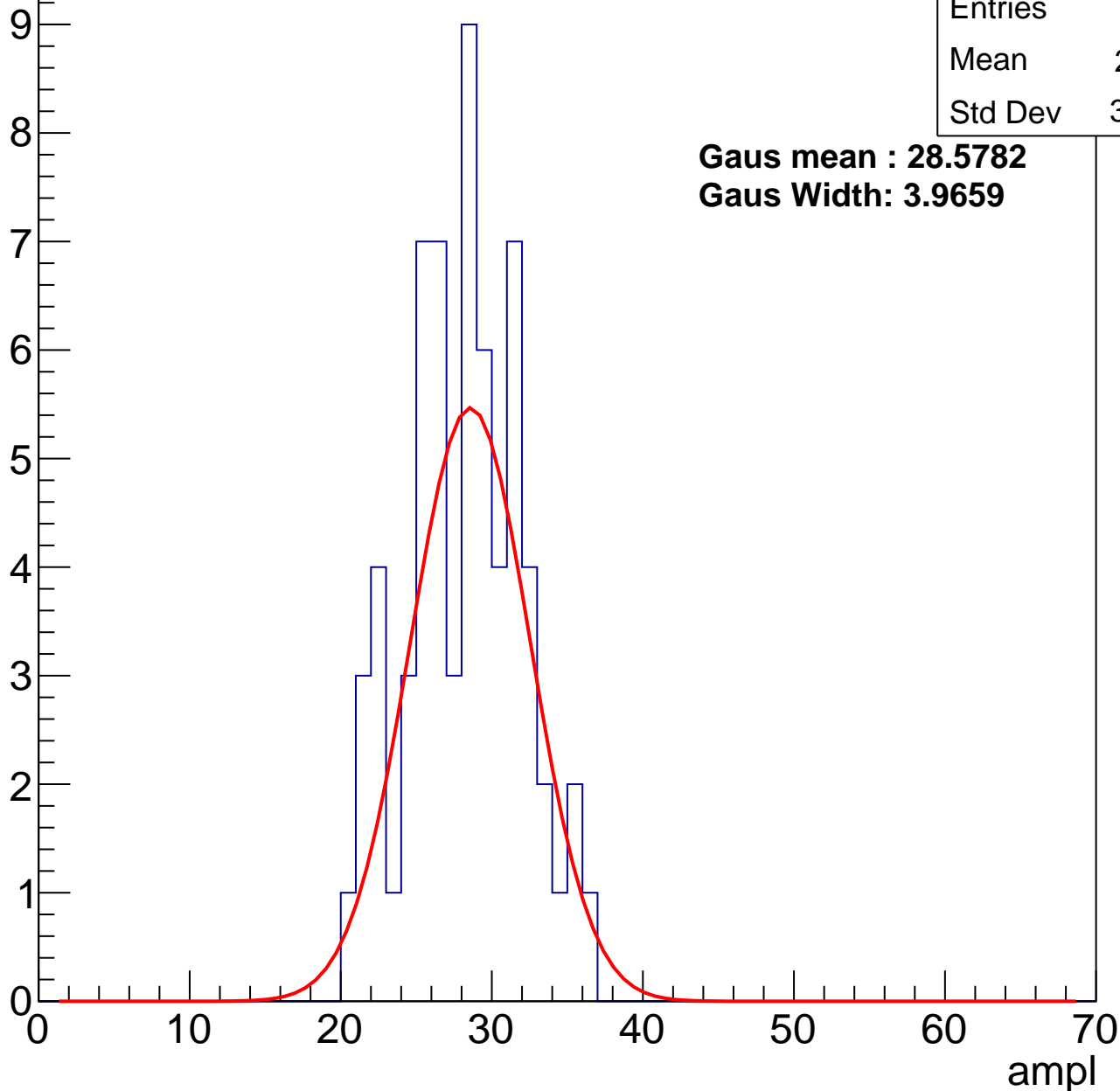
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.71
Std Dev	3.749

Gaus mean : 28.5782

Gaus Width: 3.9659



B1L100S, U14-ch68, adc1

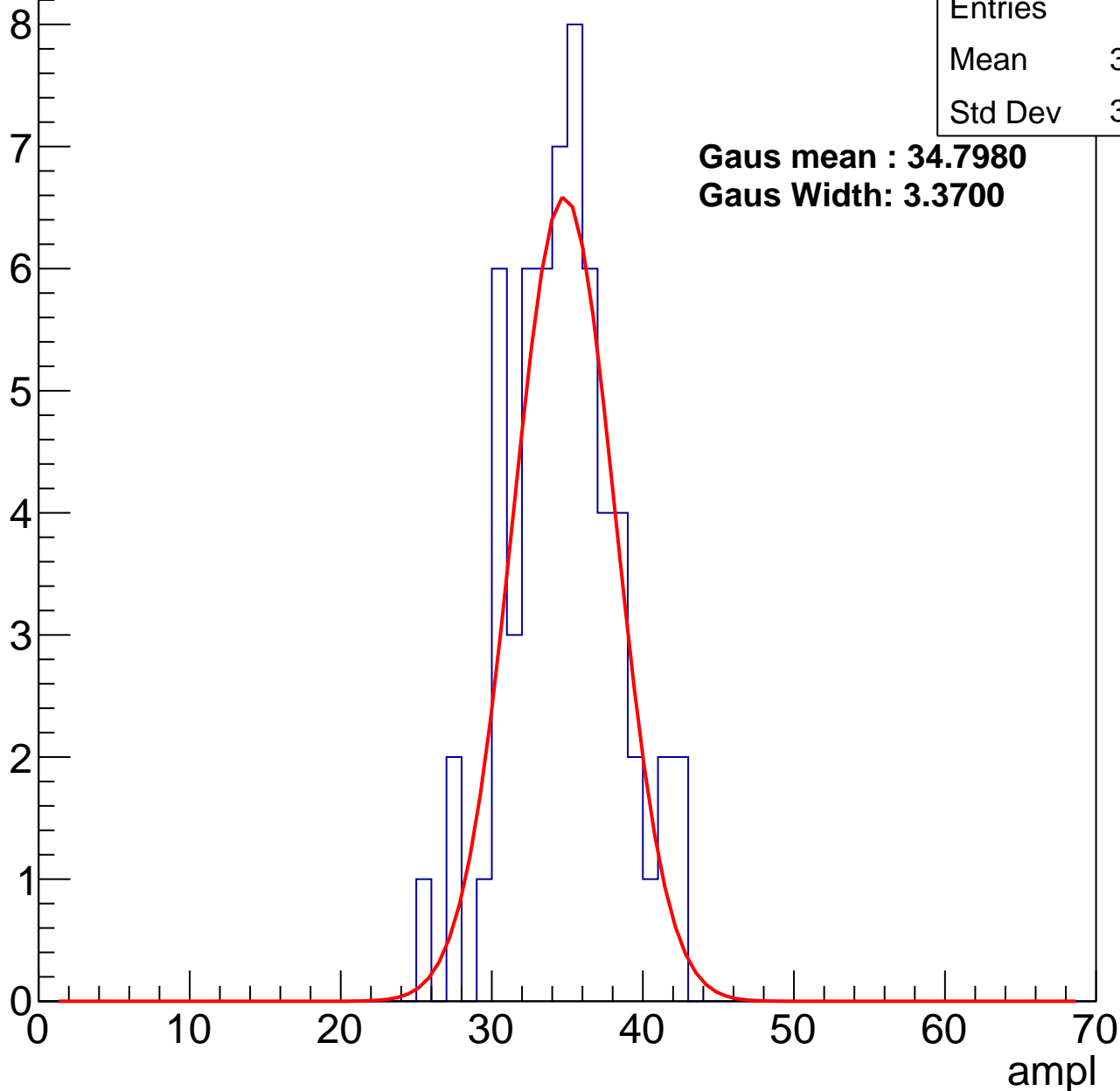
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	34.25
Std Dev	3.615

Gaus mean : 34.7980

Gaus Width: 3.3700



B1L100S, U14-ch68, adc2

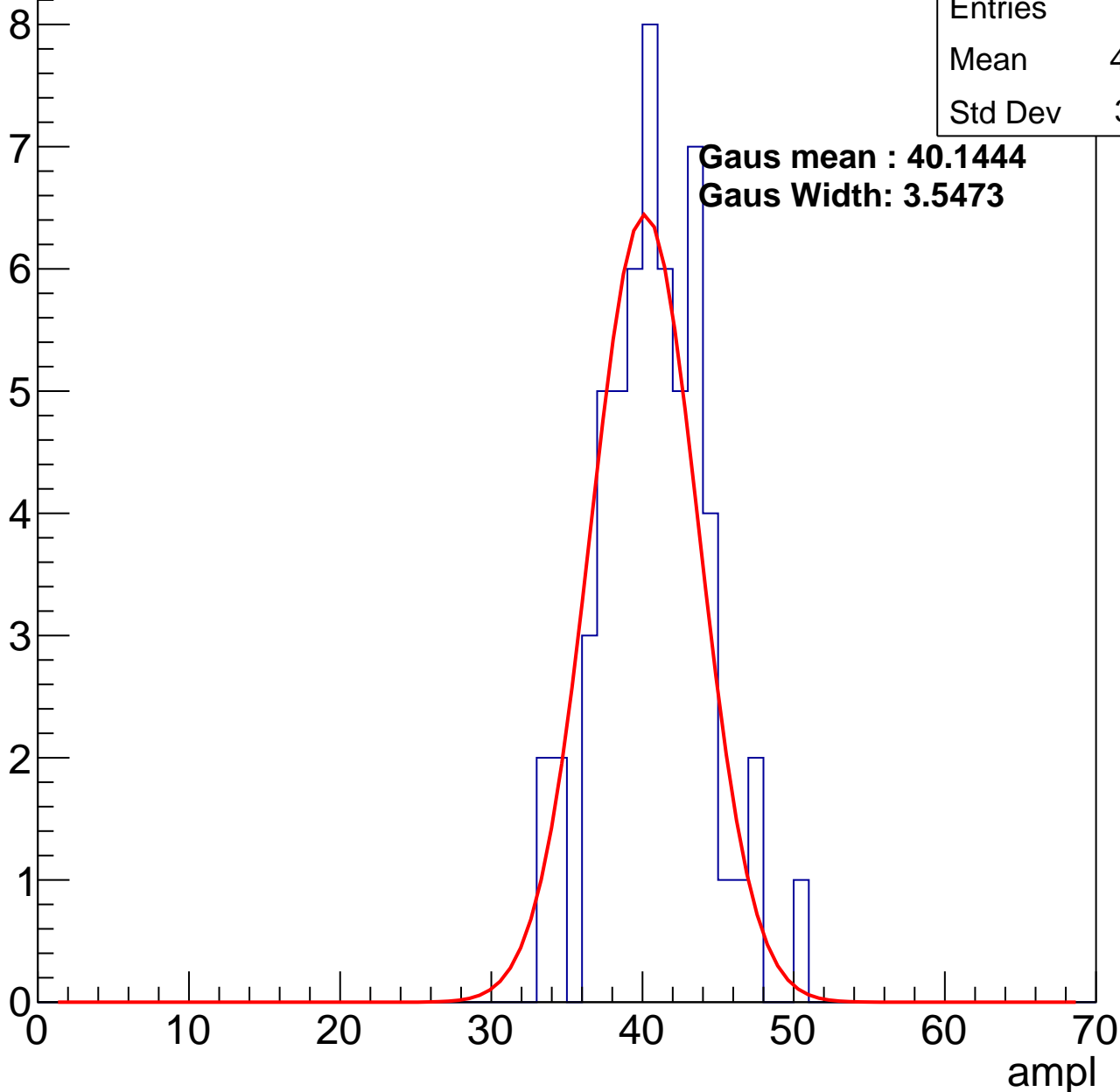
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	40.33
Std Dev	3.461

Gaus mean : 40.1444

Gaus Width: 3.5473

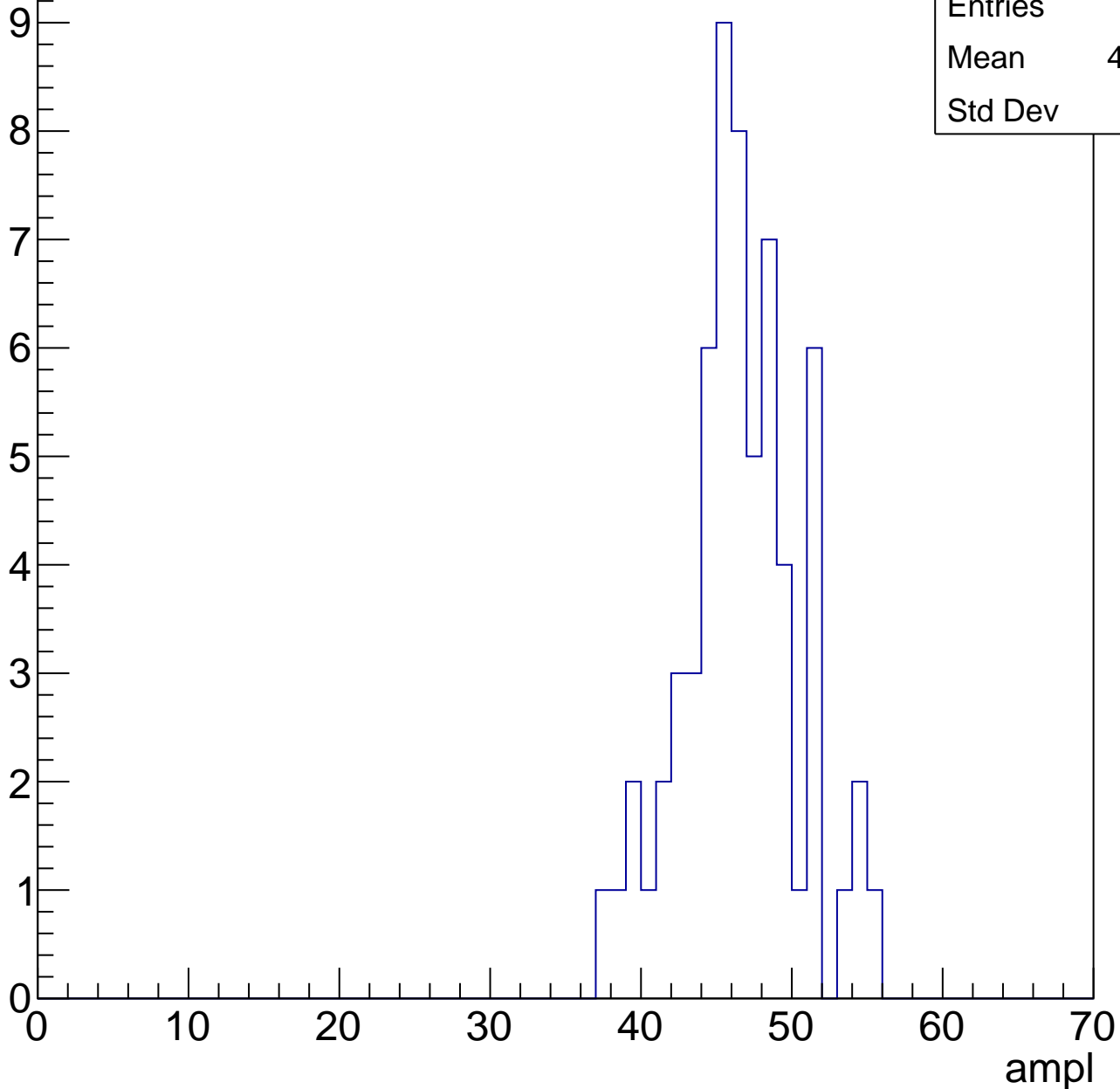


B1L100S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	46.13
Std Dev	3.84

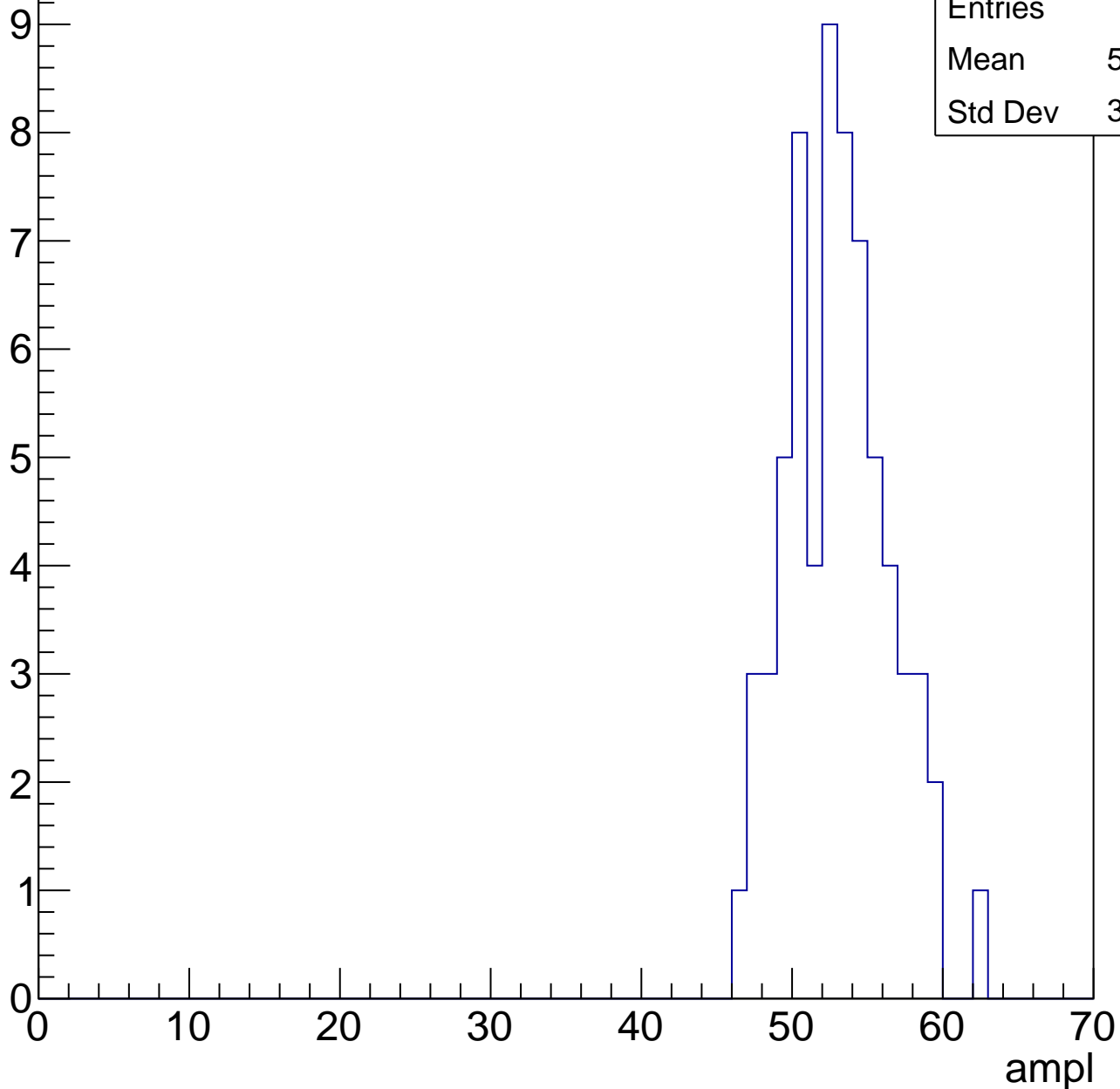


B1L100S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

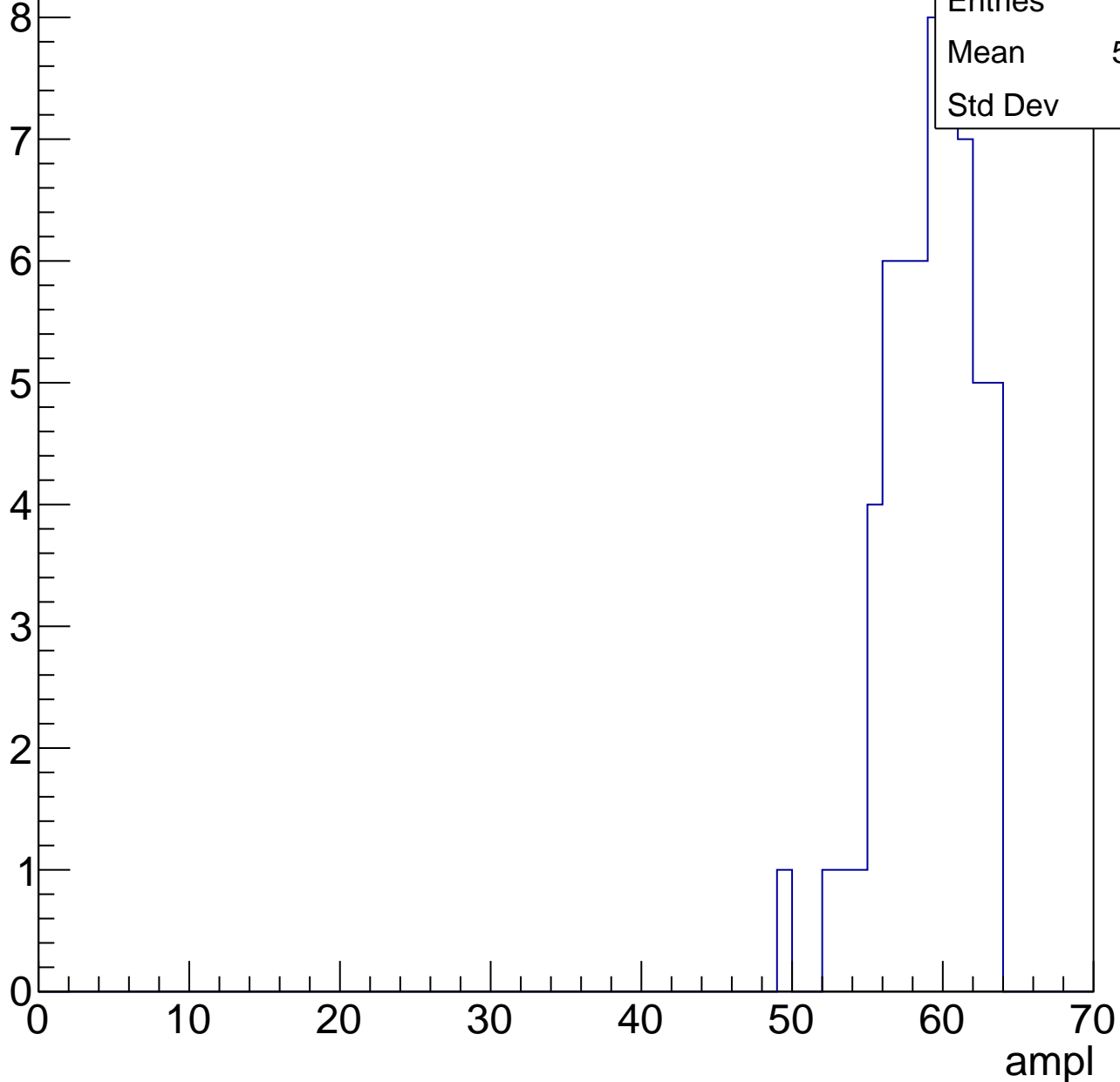
Entries	66
Mean	52.64
Std Dev	3.347



B1L100S, U14-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

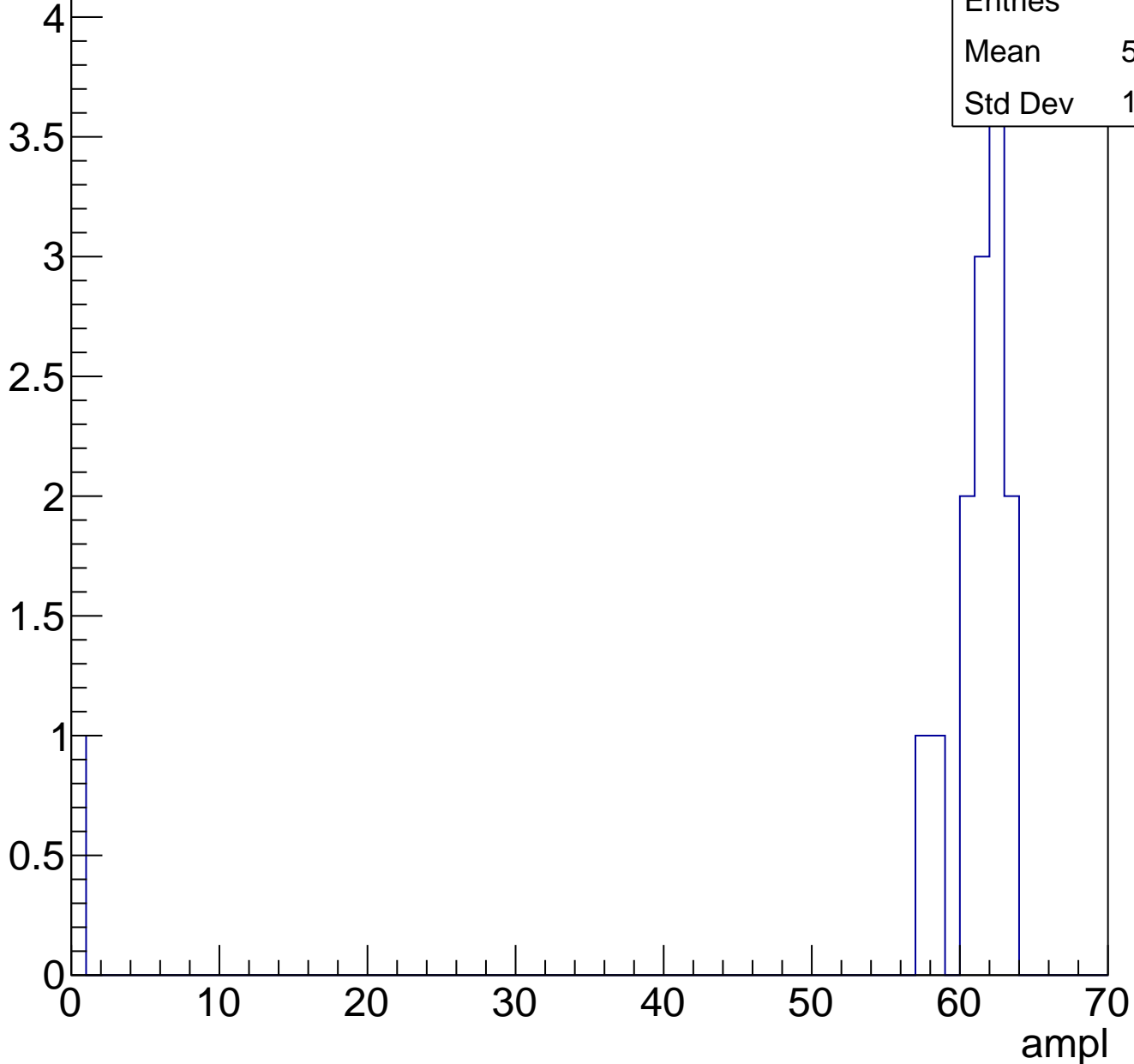
Entry



B1L100S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

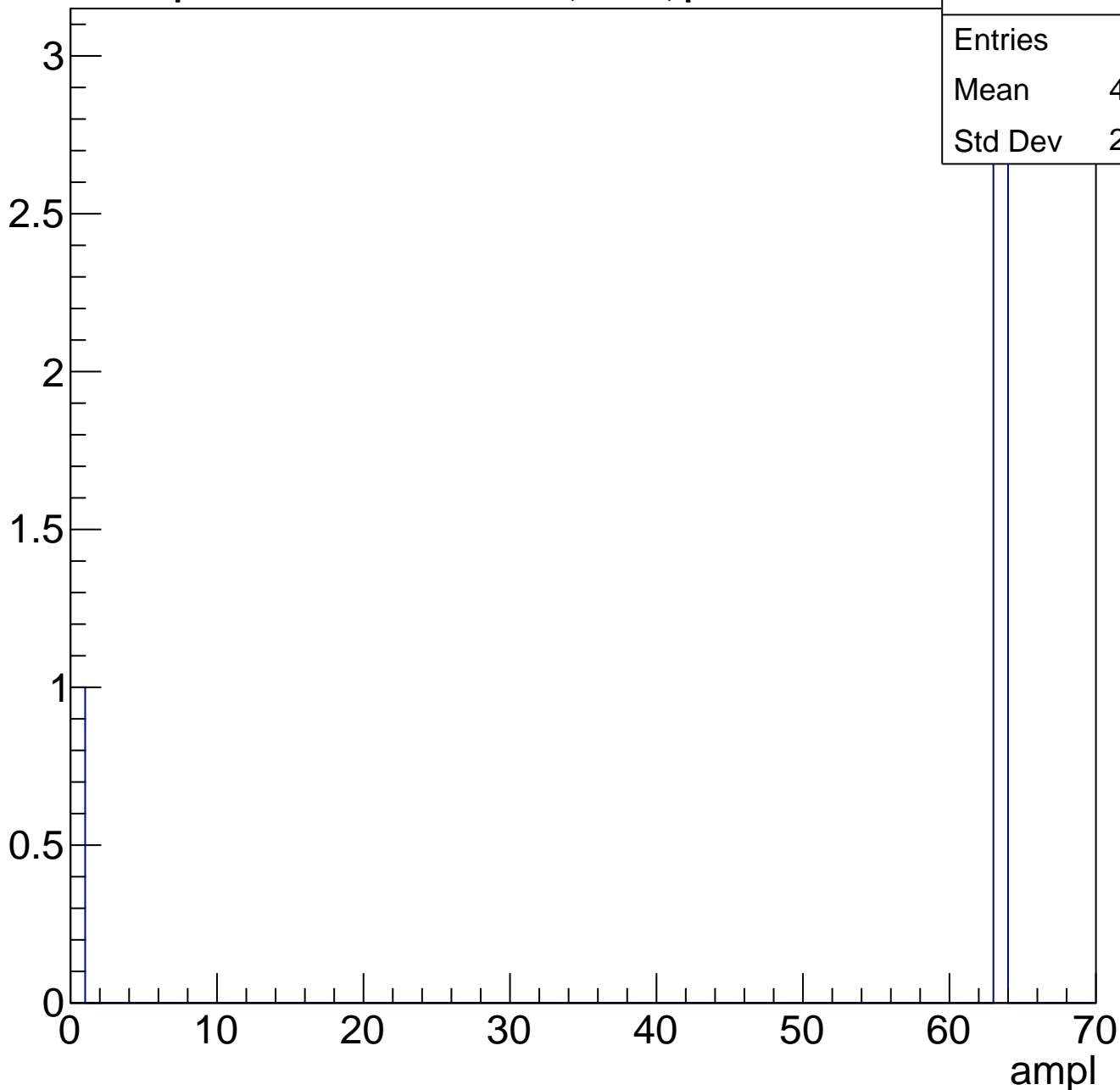
Entry



B1L100S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch69, adc0

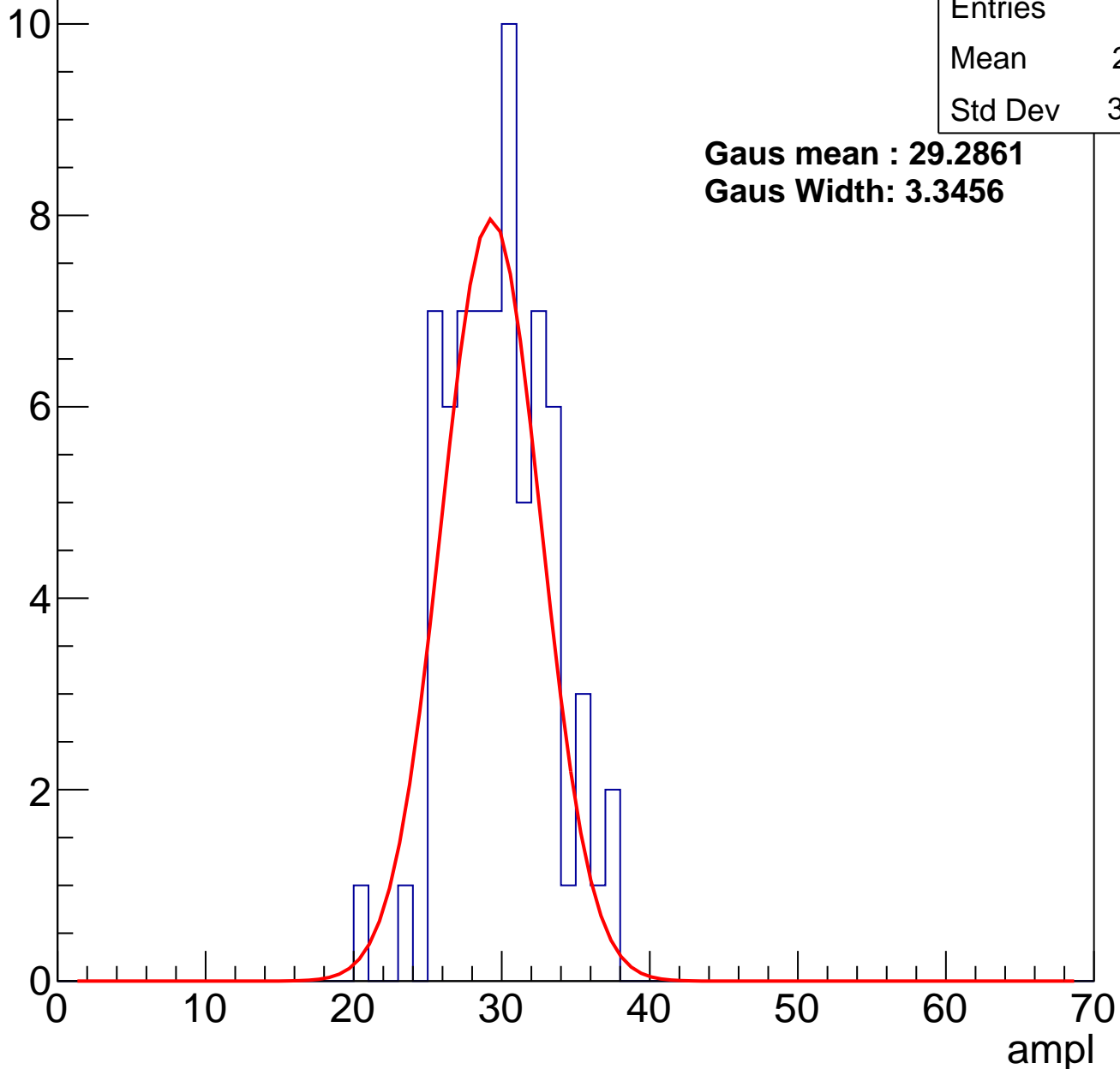
calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	29.41
Std Dev	3.368

Gaus mean : 29.2861

Gaus Width: 3.3456

Entry



B1L100S, U14-ch69, adc1

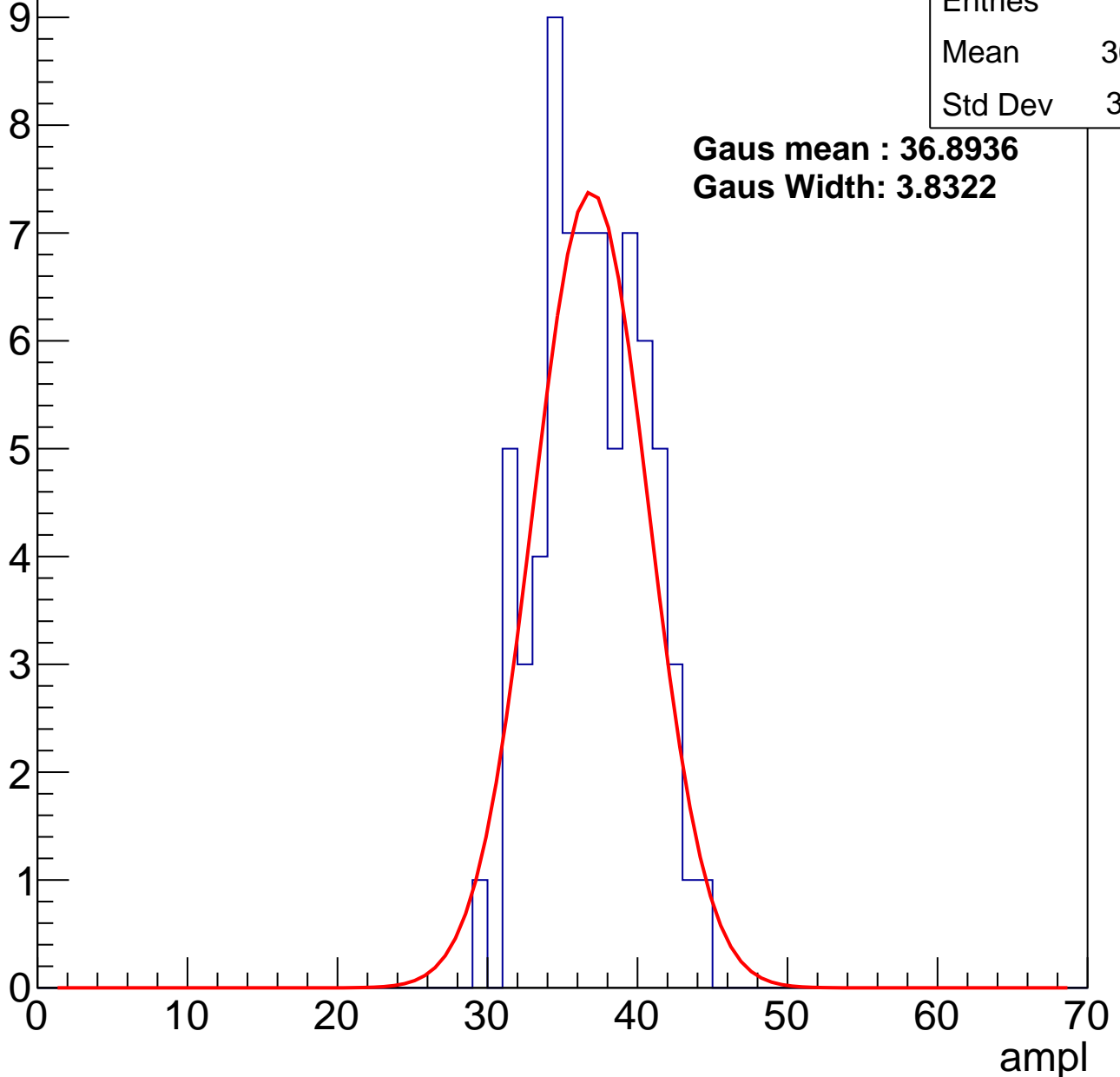
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.55
Std Dev	3.381

Gaus mean : 36.8936

Gaus Width: 3.8322



B1L100S, U14-ch69, adc2

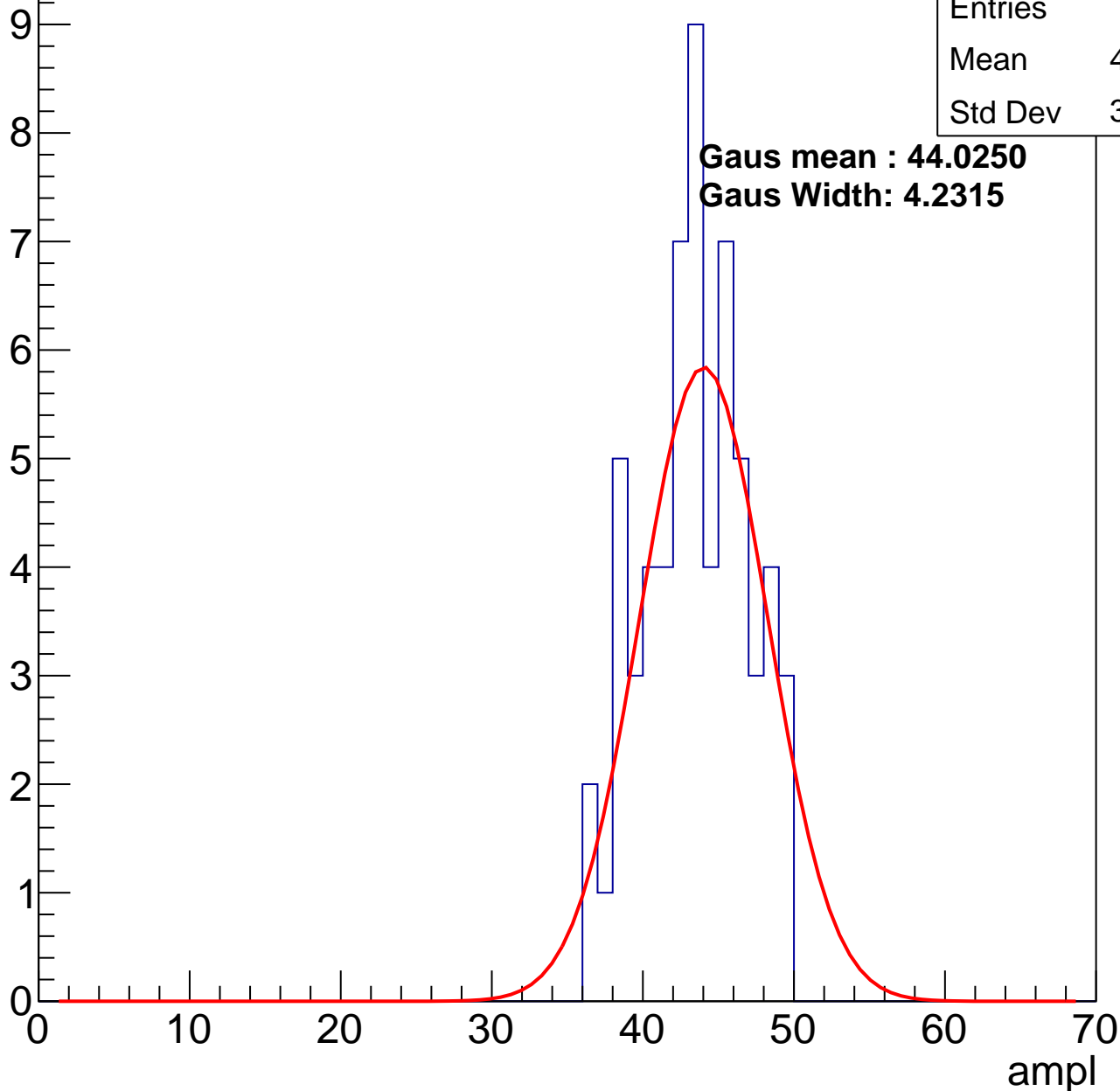
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.98
Std Dev	3.385

Gaus mean : 44.0250

Gaus Width: 4.2315

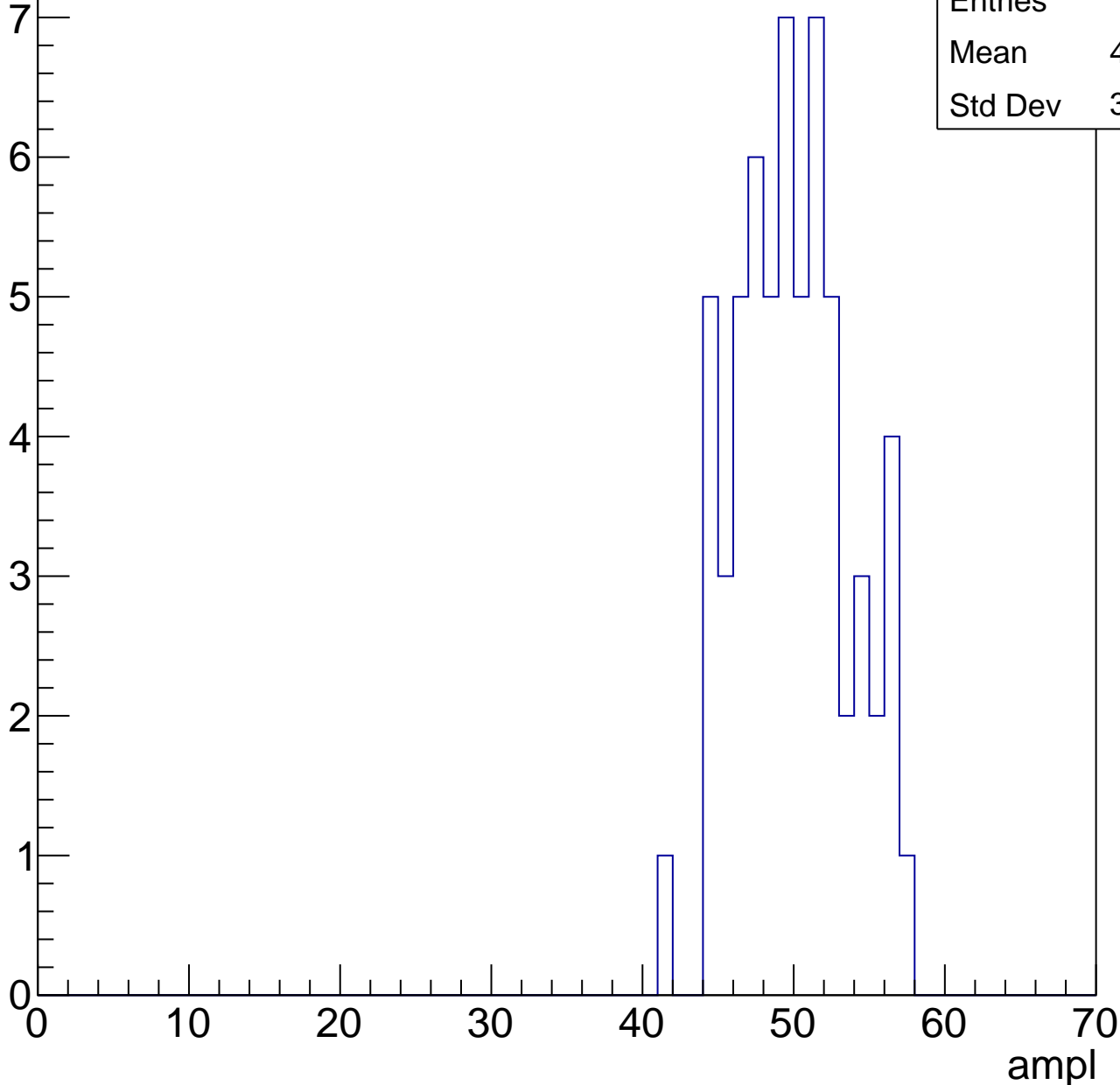


B1L100S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

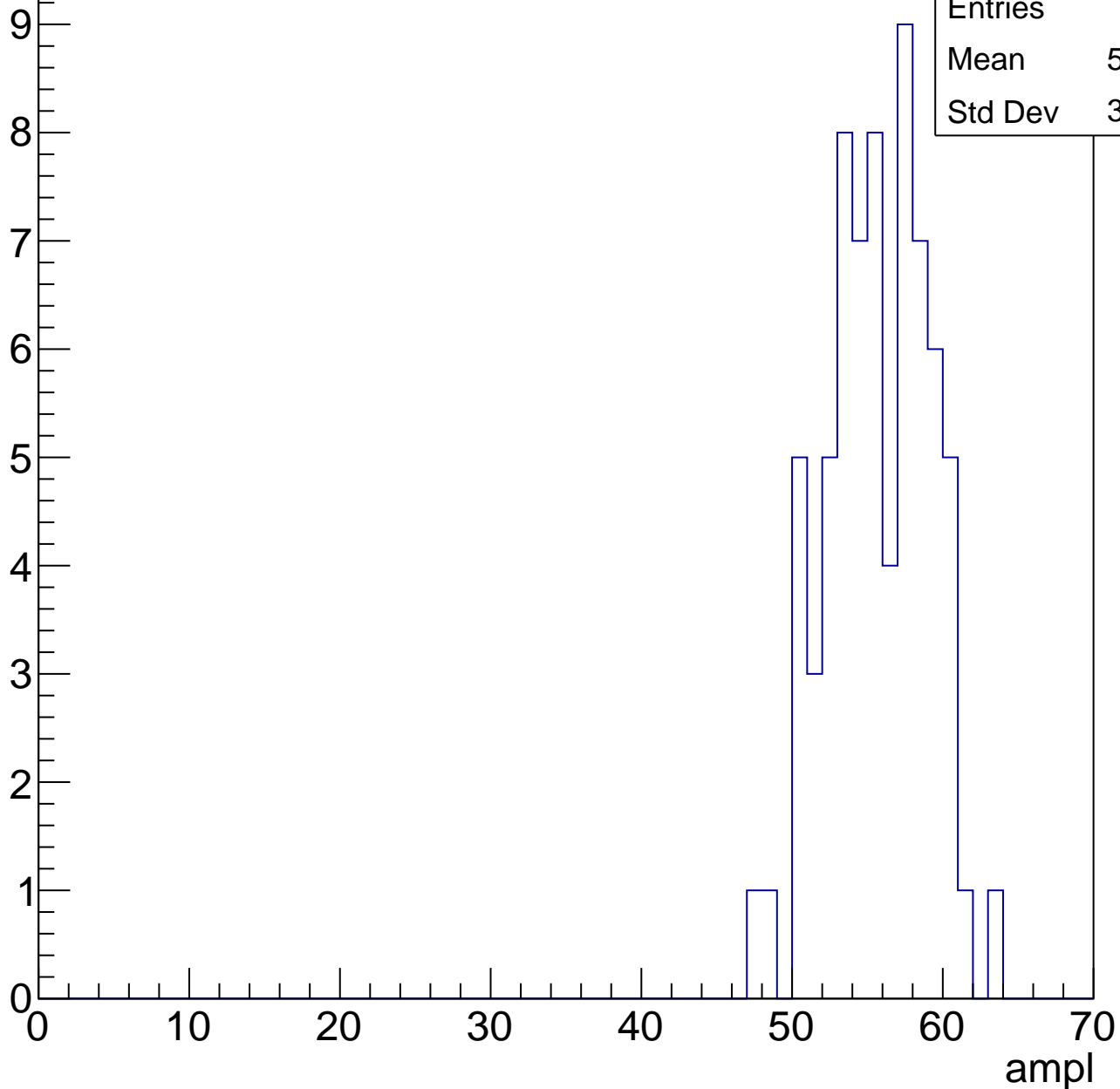
Entries	61
Mean	49.46
Std Dev	3.656



B1L100S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



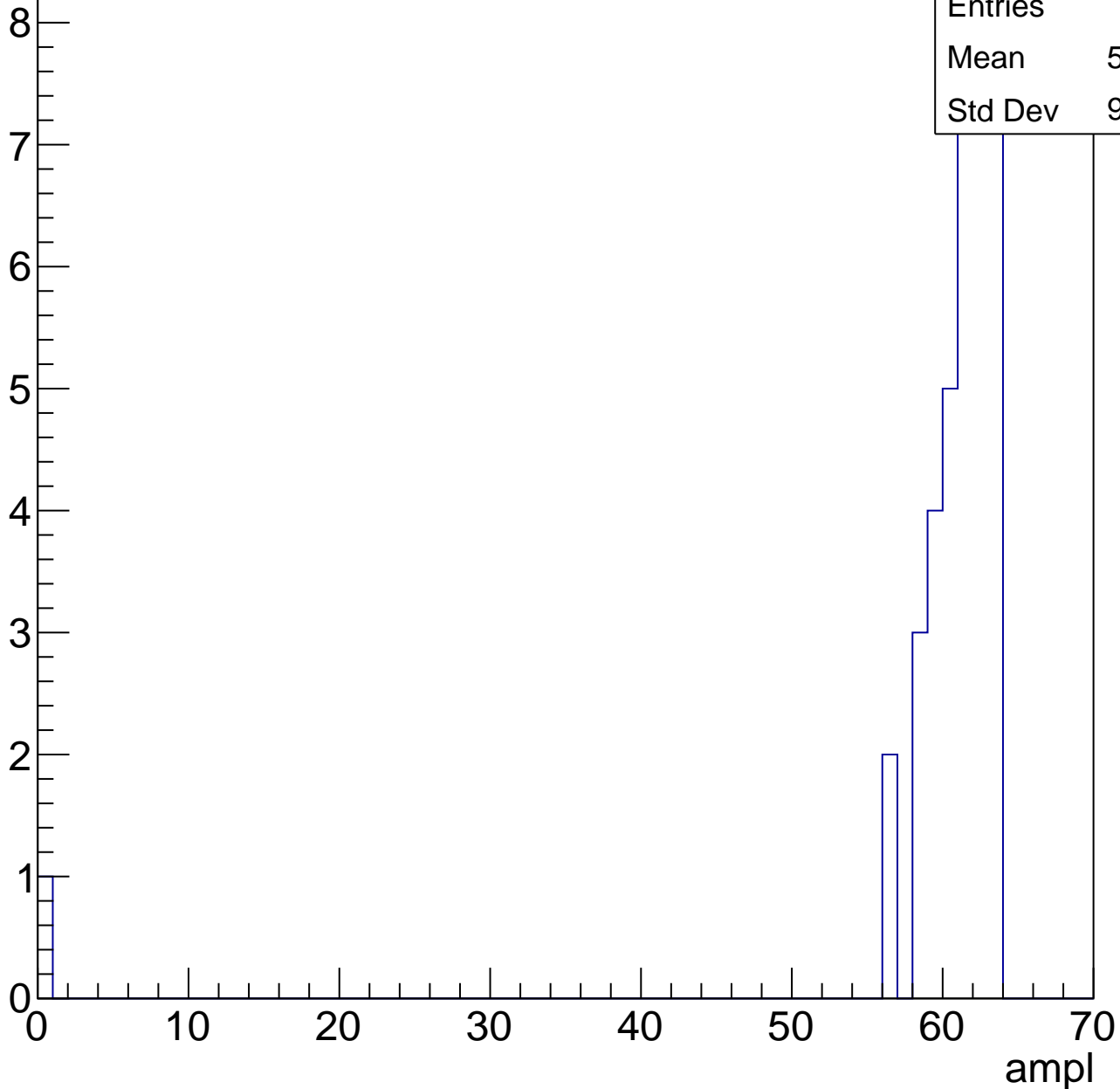
Entries	71
Mean	55.23
Std Dev	3.337

B1L100S, U14-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.23
Std Dev	9.789



B1L100S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B1L100S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch70, adc0

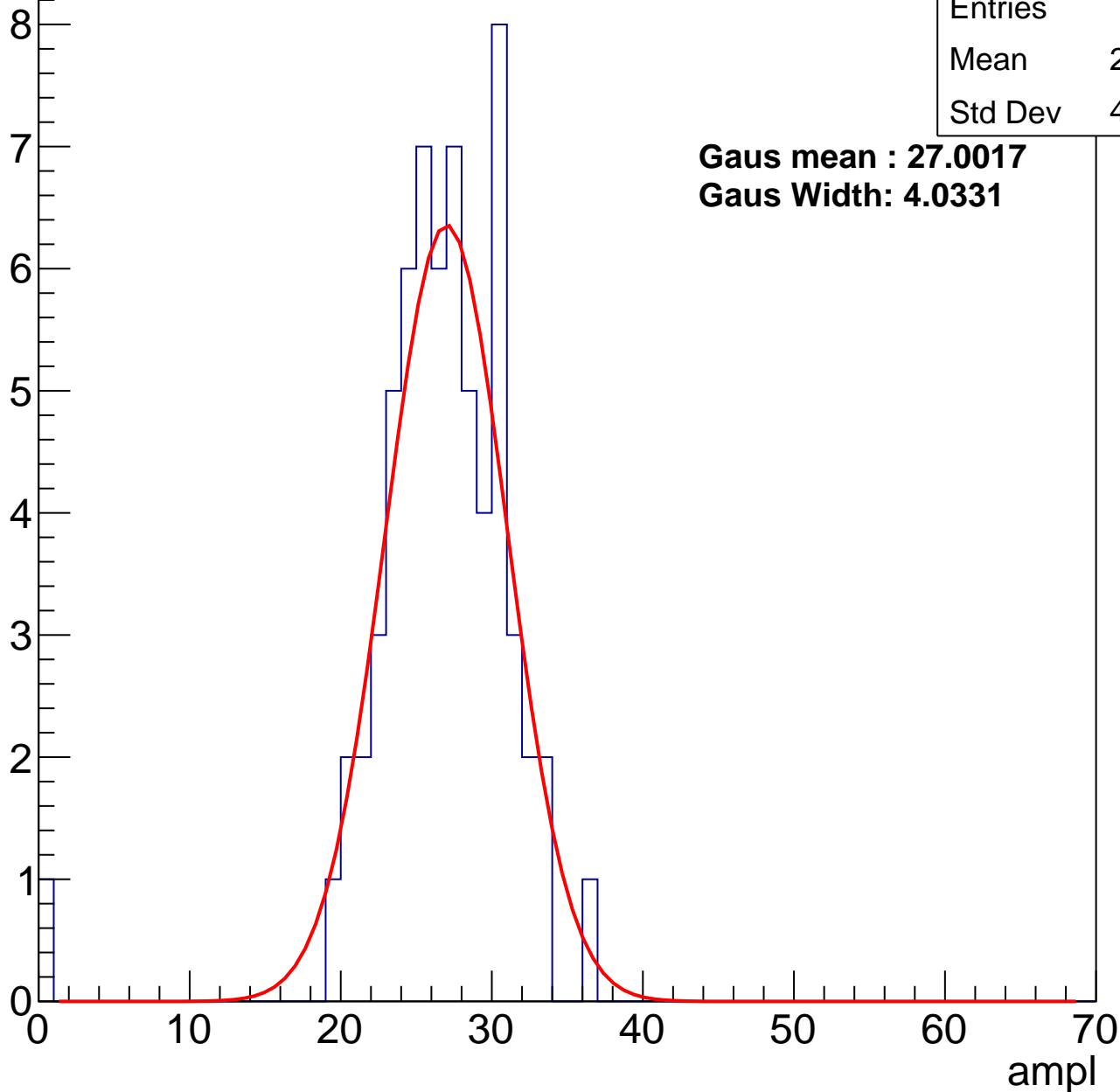
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	26.17
Std Dev	4.812

Gaus mean : 27.0017

Gaus Width: 4.0331



B1L100S, U14-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	33.03
Std Dev	3.9

Gaus mean : 33.5224

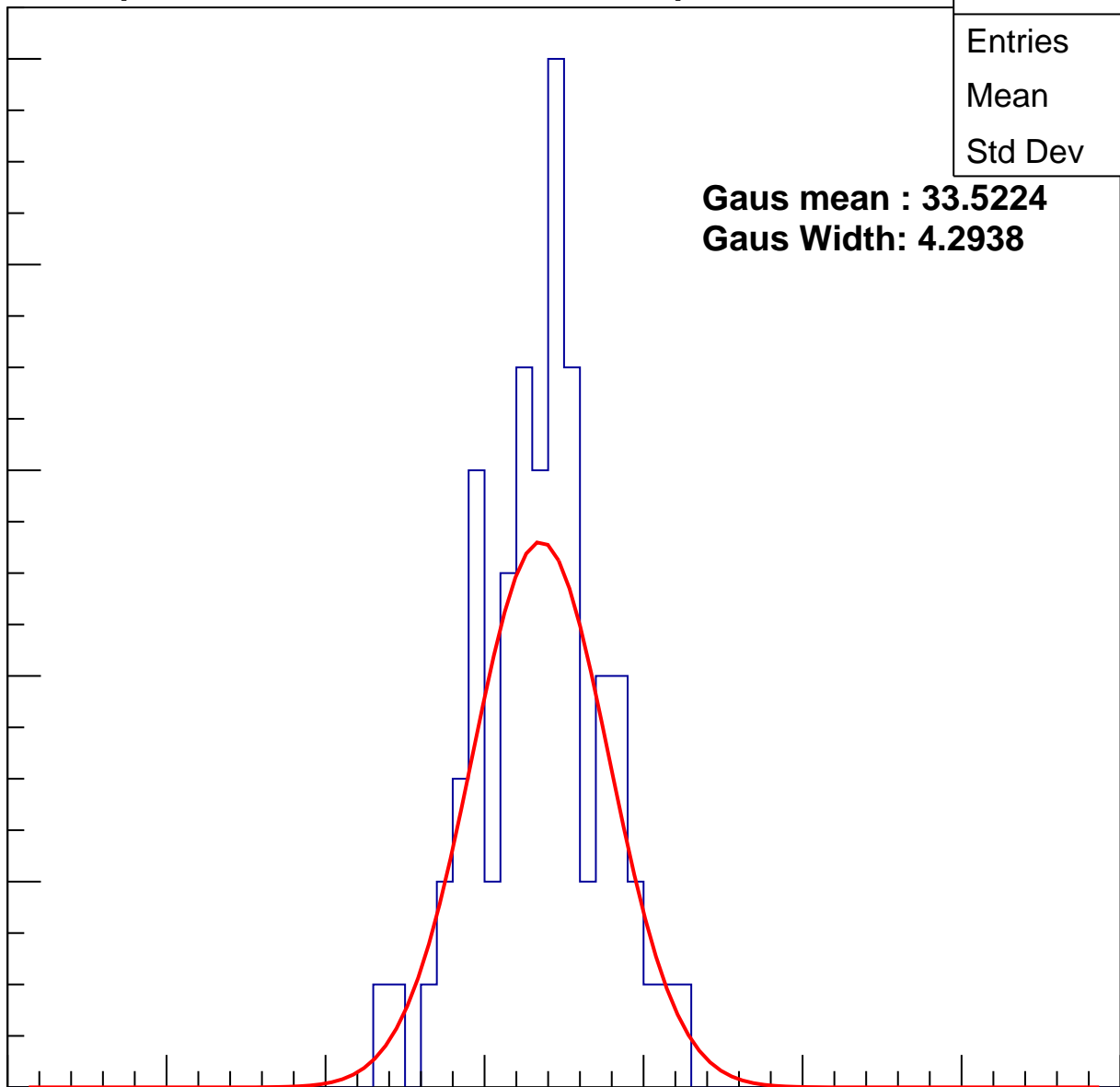
Gaus Width: 4.2938

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch70, adc2

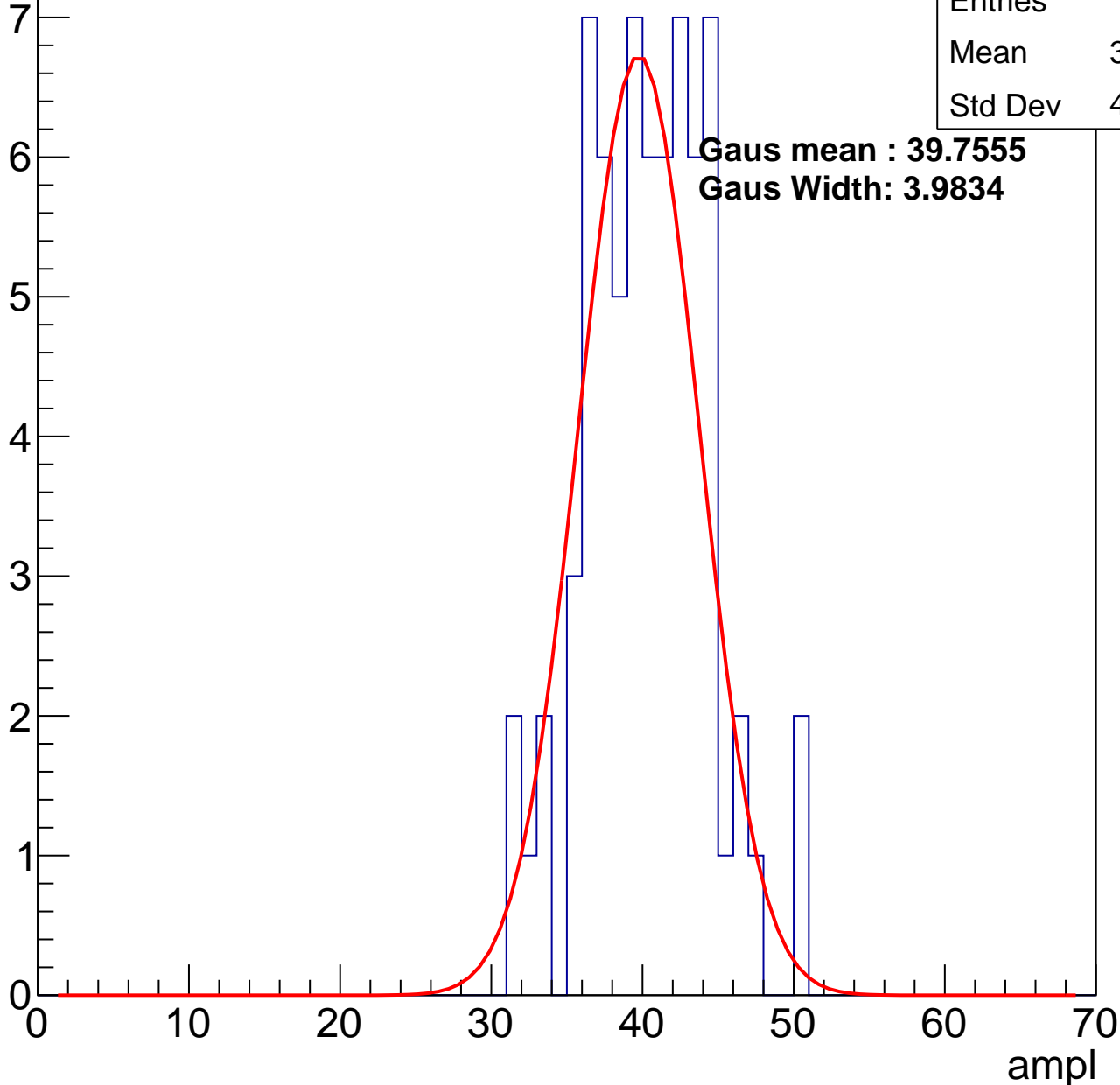
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	39.89
Std Dev	4.002

Gaus mean : 39.7555

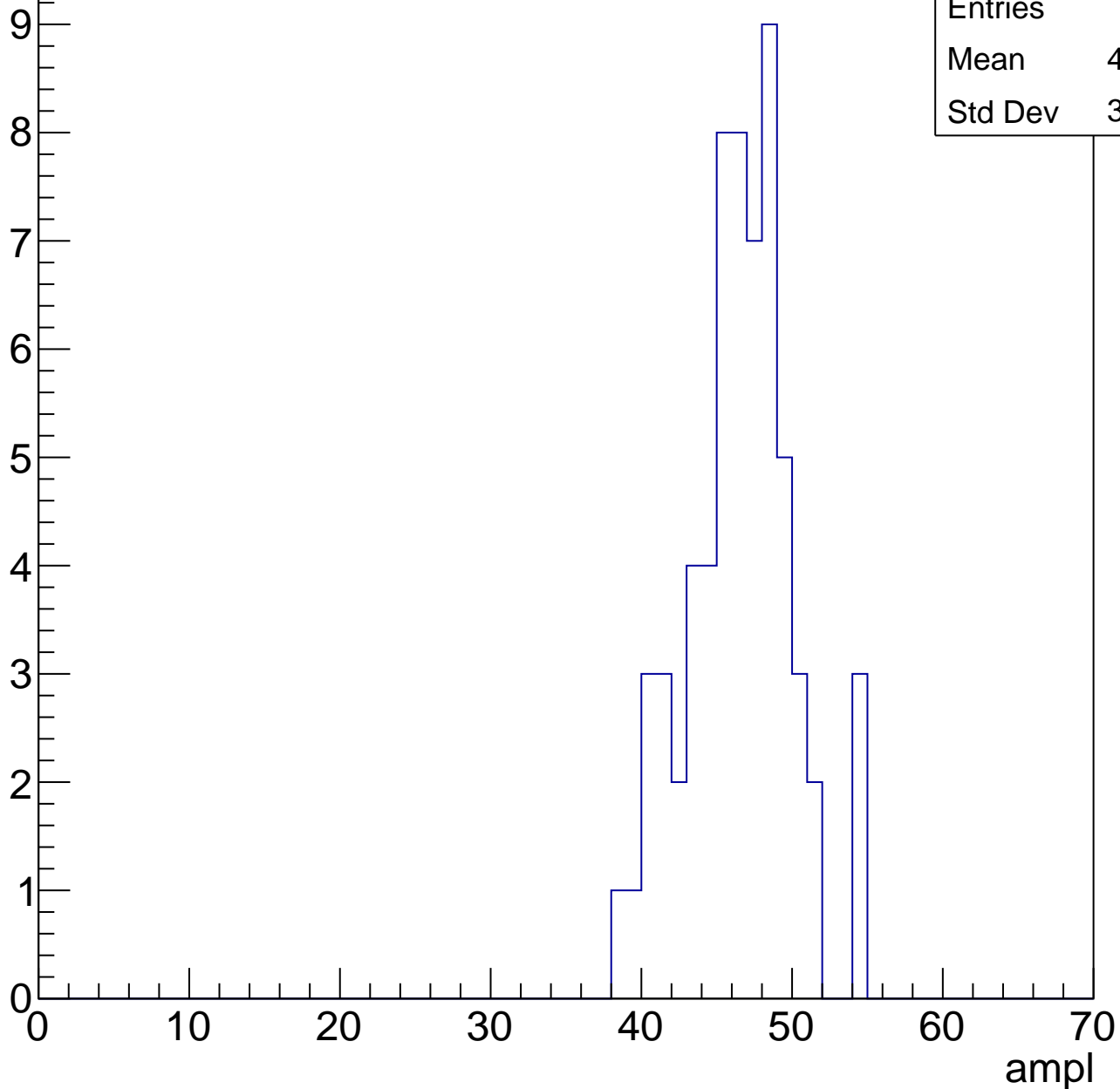
Gaus Width: 3.9834



B1L100S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

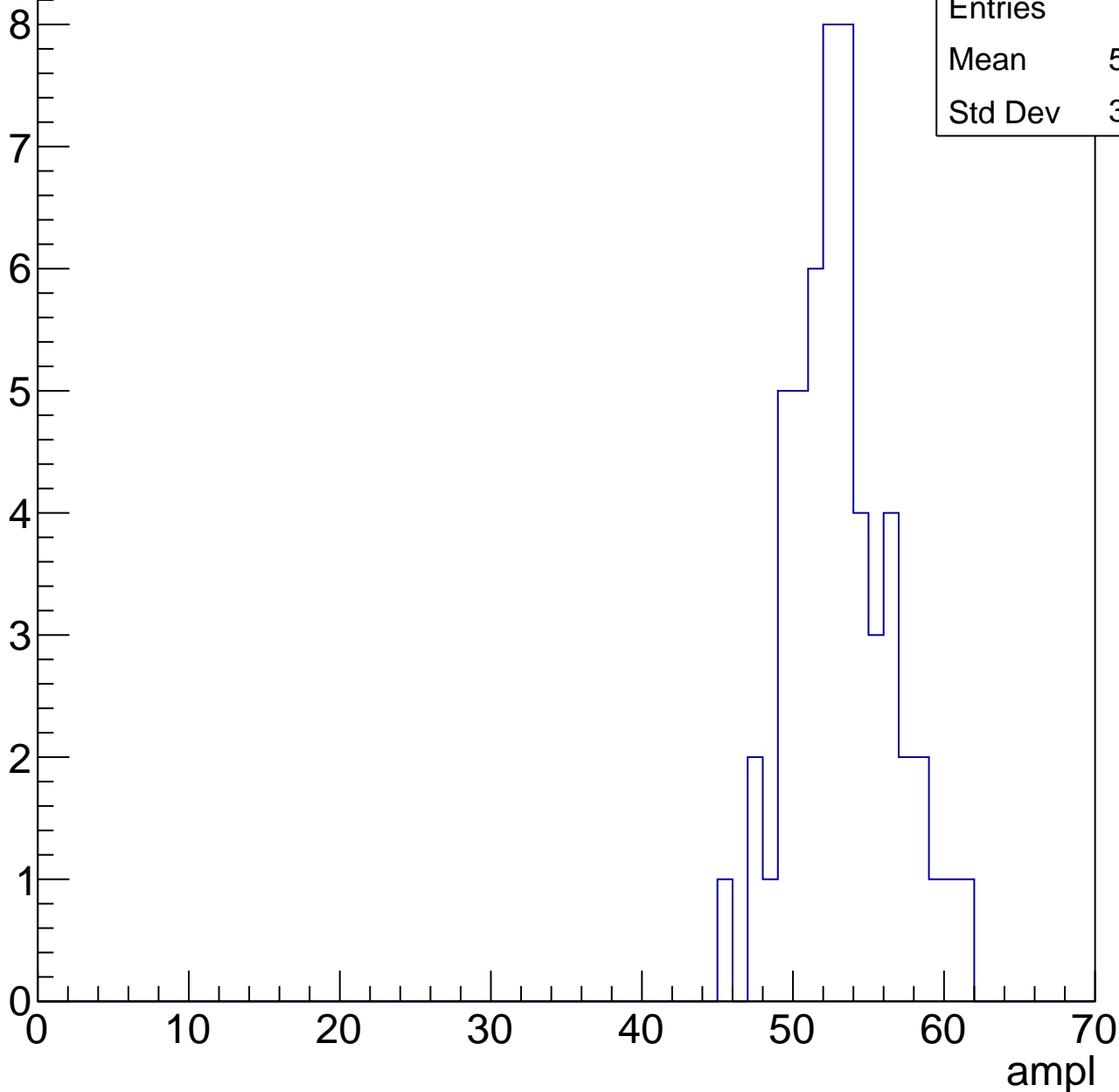


B1L100S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	52.65
Std Dev	3.318



B1L100S, U14-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 62

Mean 57.73

Std Dev 3.086

ampl

0

10

20

30

40

50

60

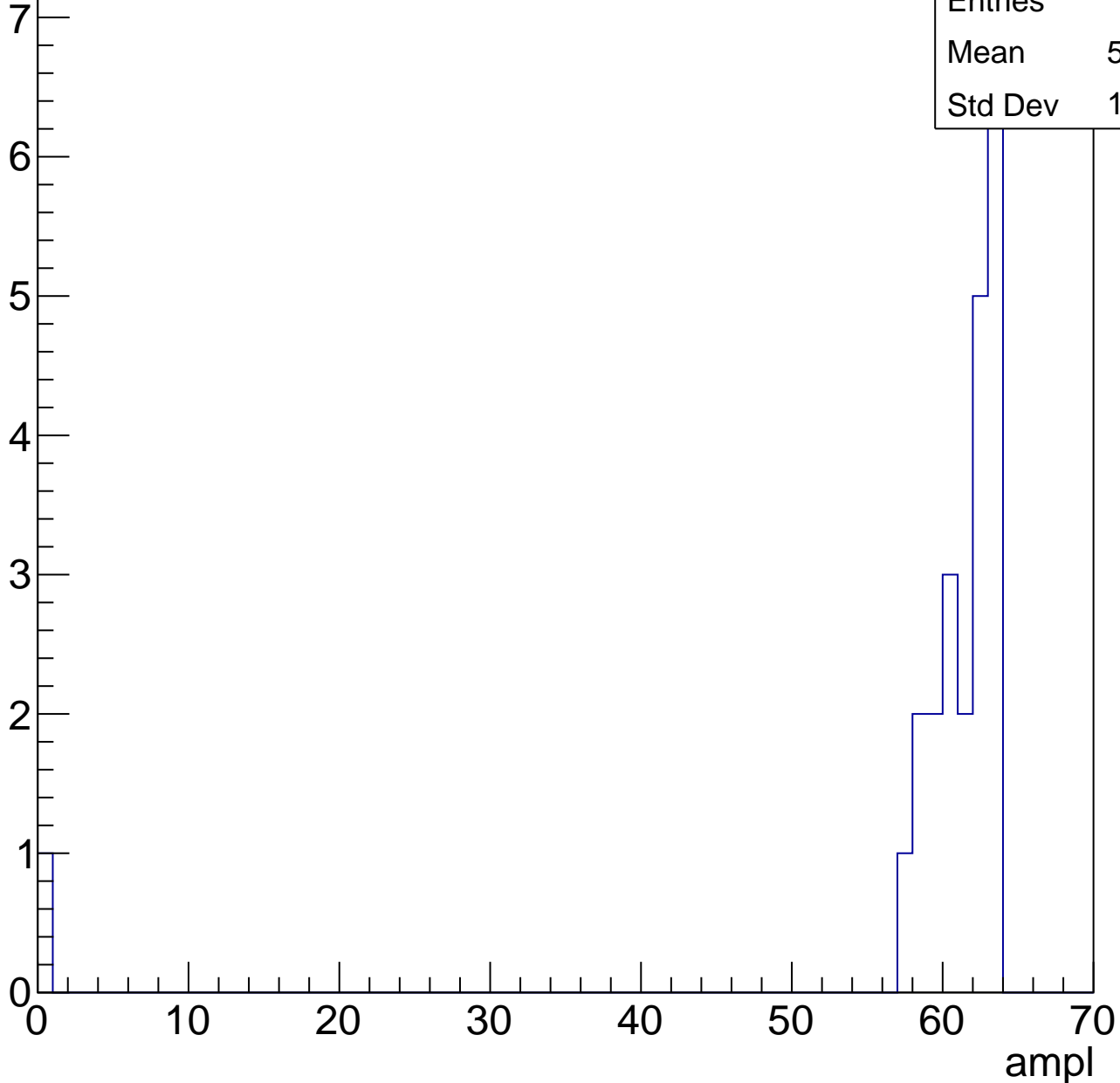
70

B1L100S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

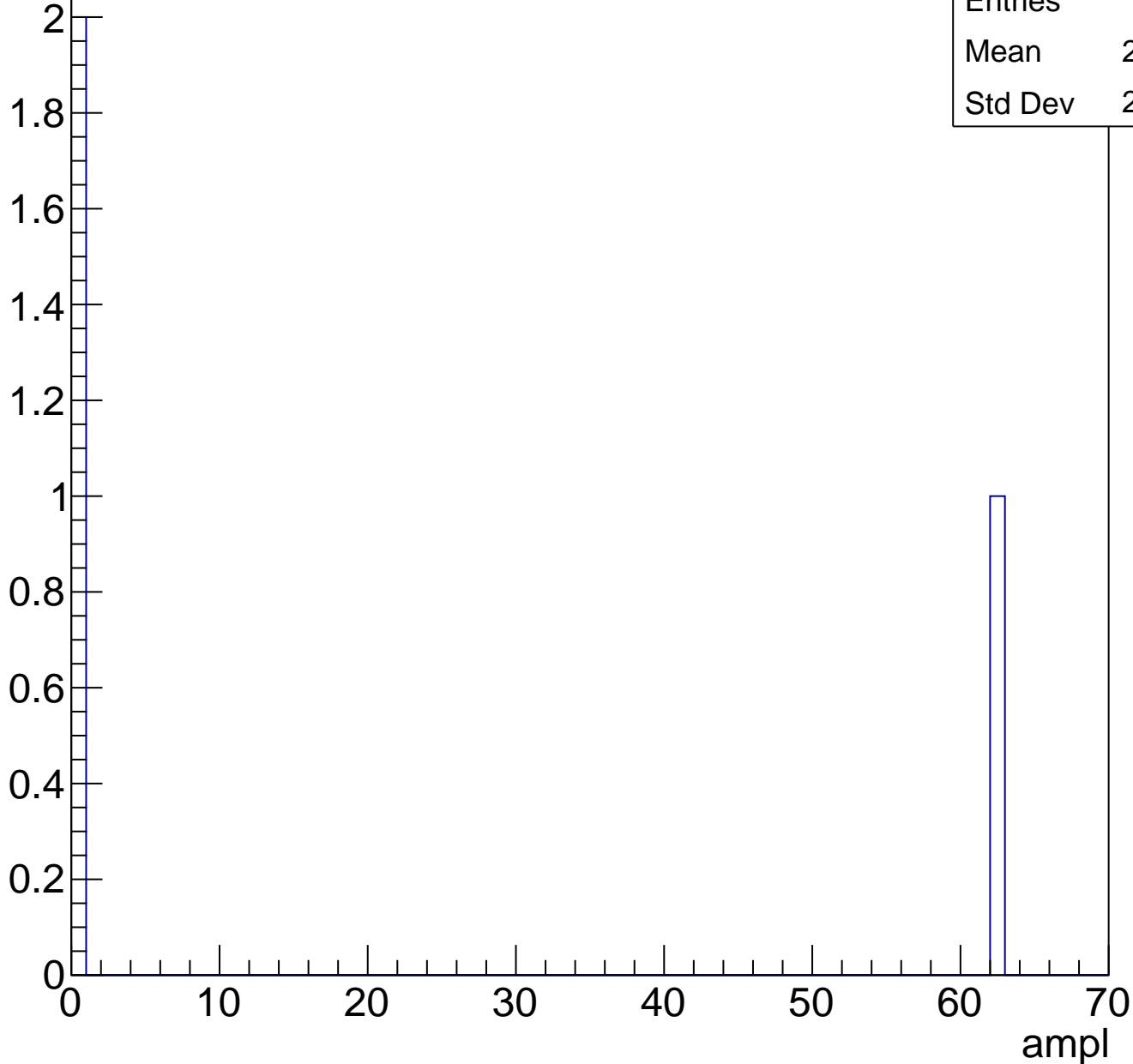
Entries	23
Mean	58.43
Std Dev	12.59



B1L100S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L100S, U14-ch71, adc0

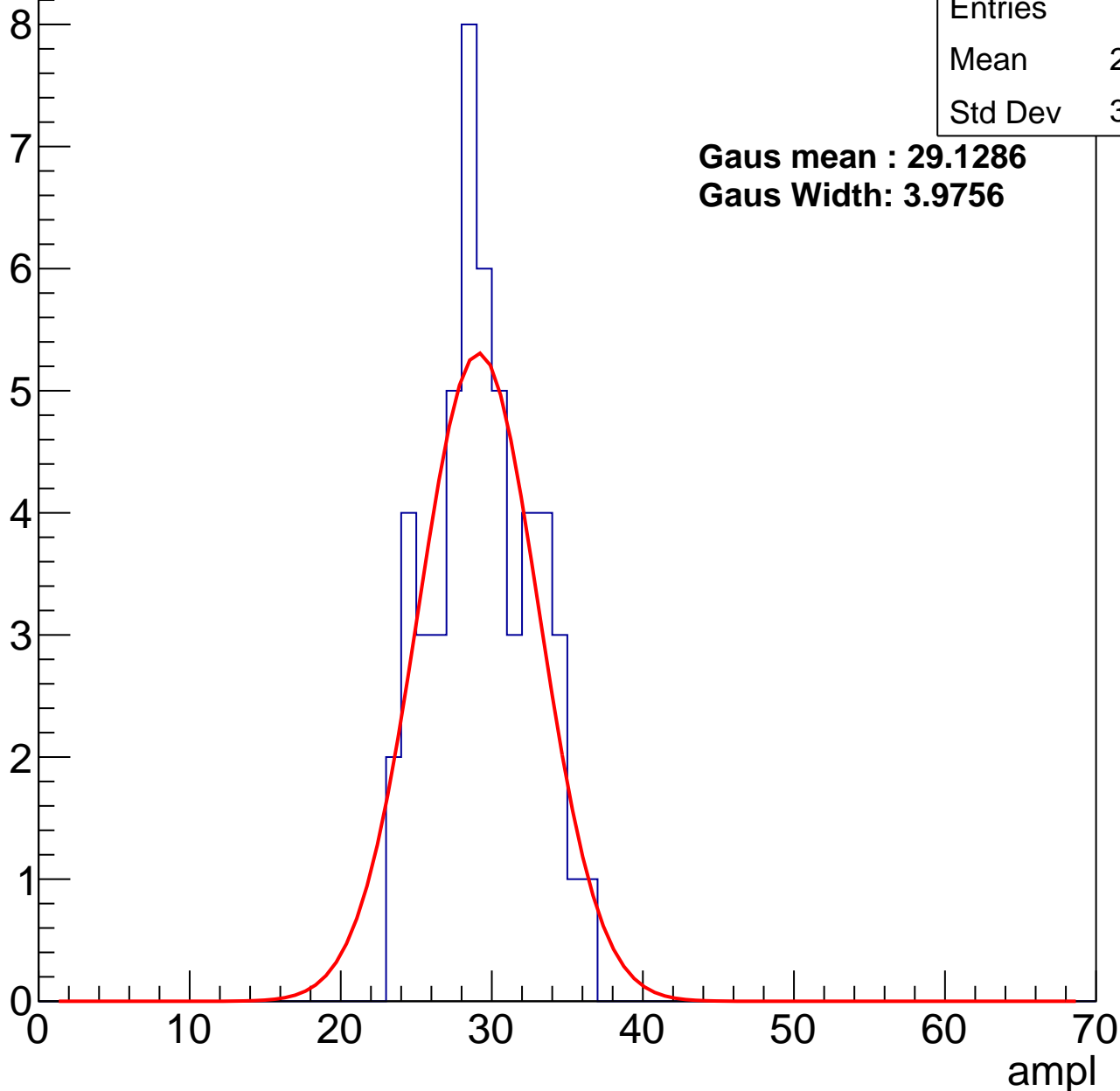
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	28.92
Std Dev	3.257

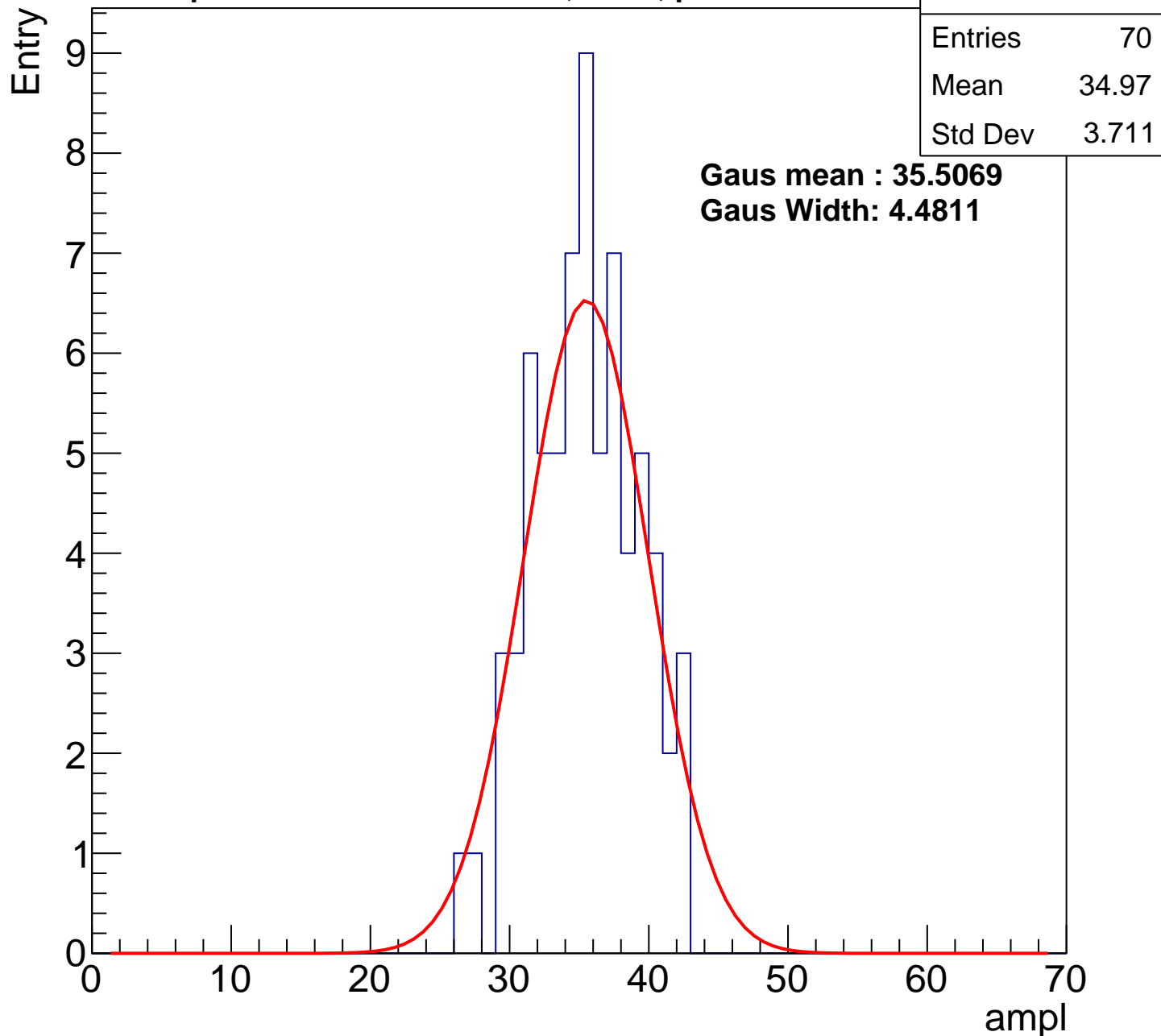
Gaus mean : 29.1286

Gaus Width: 3.9756



B1L100S, U14-ch71, adc1

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch71, adc2

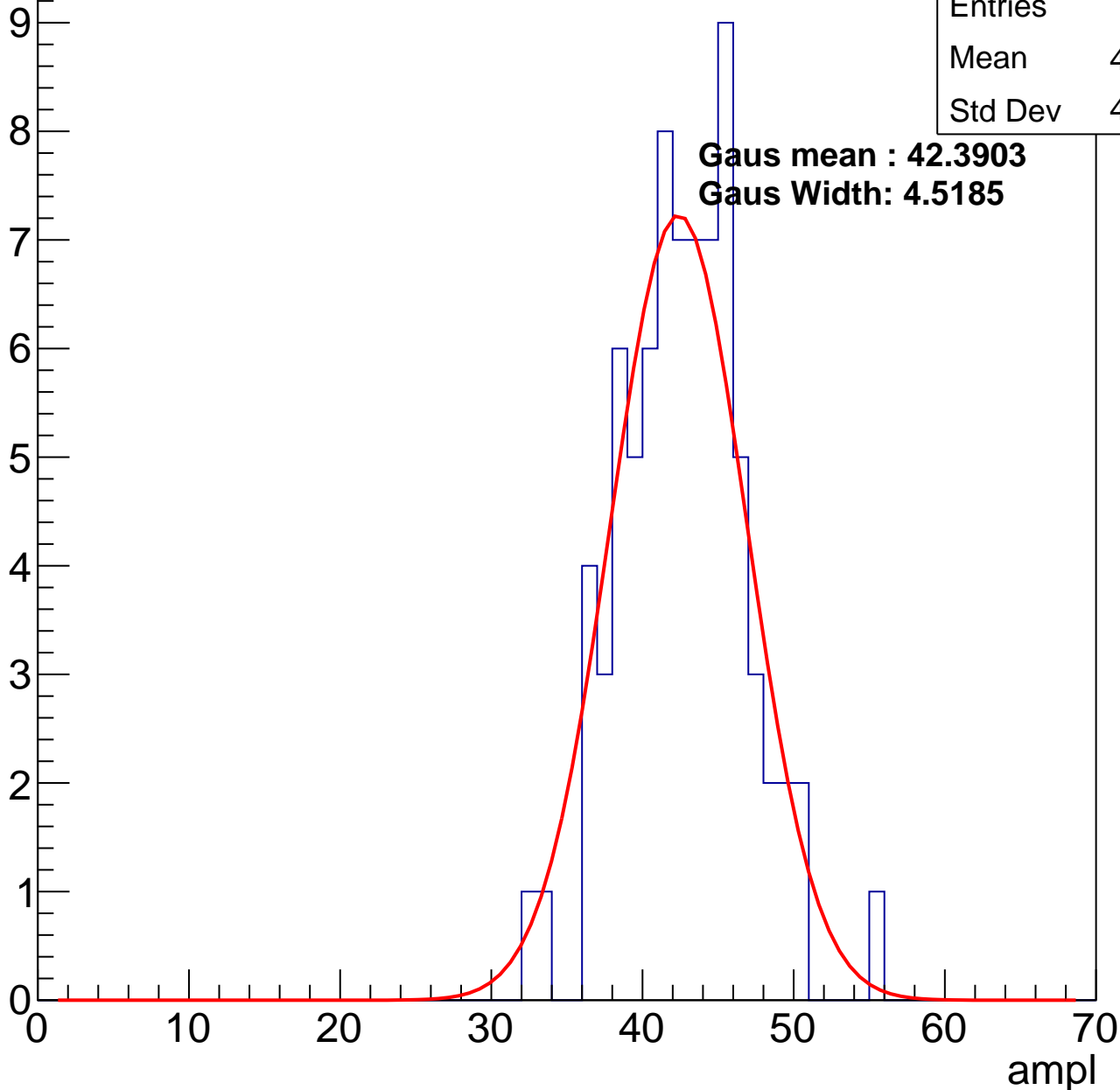
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	42.27
Std Dev	4.065

Gaus mean : 42.3903

Gaus Width: 4.5185

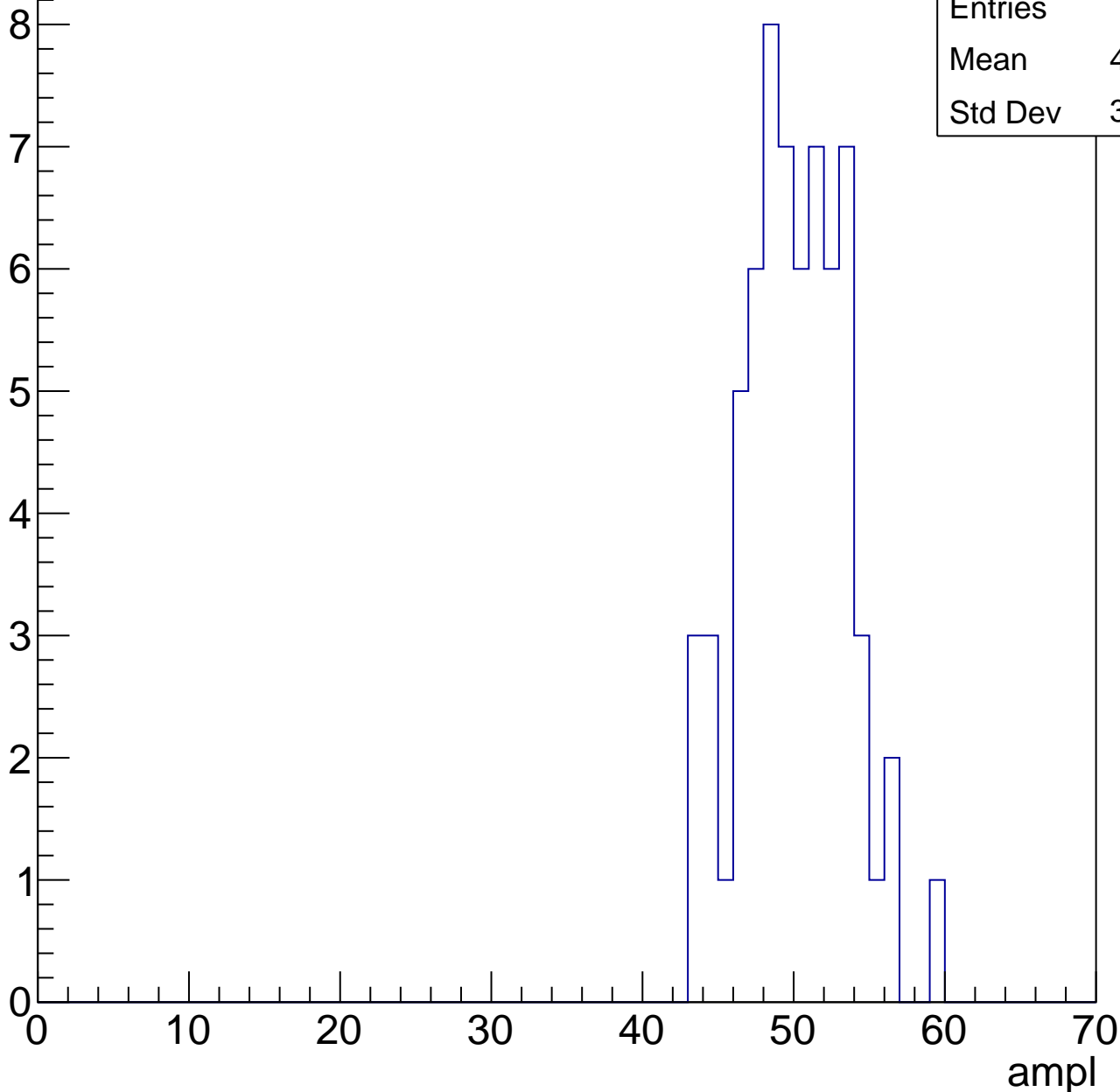


B1L100S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.59
Std Dev	3.398

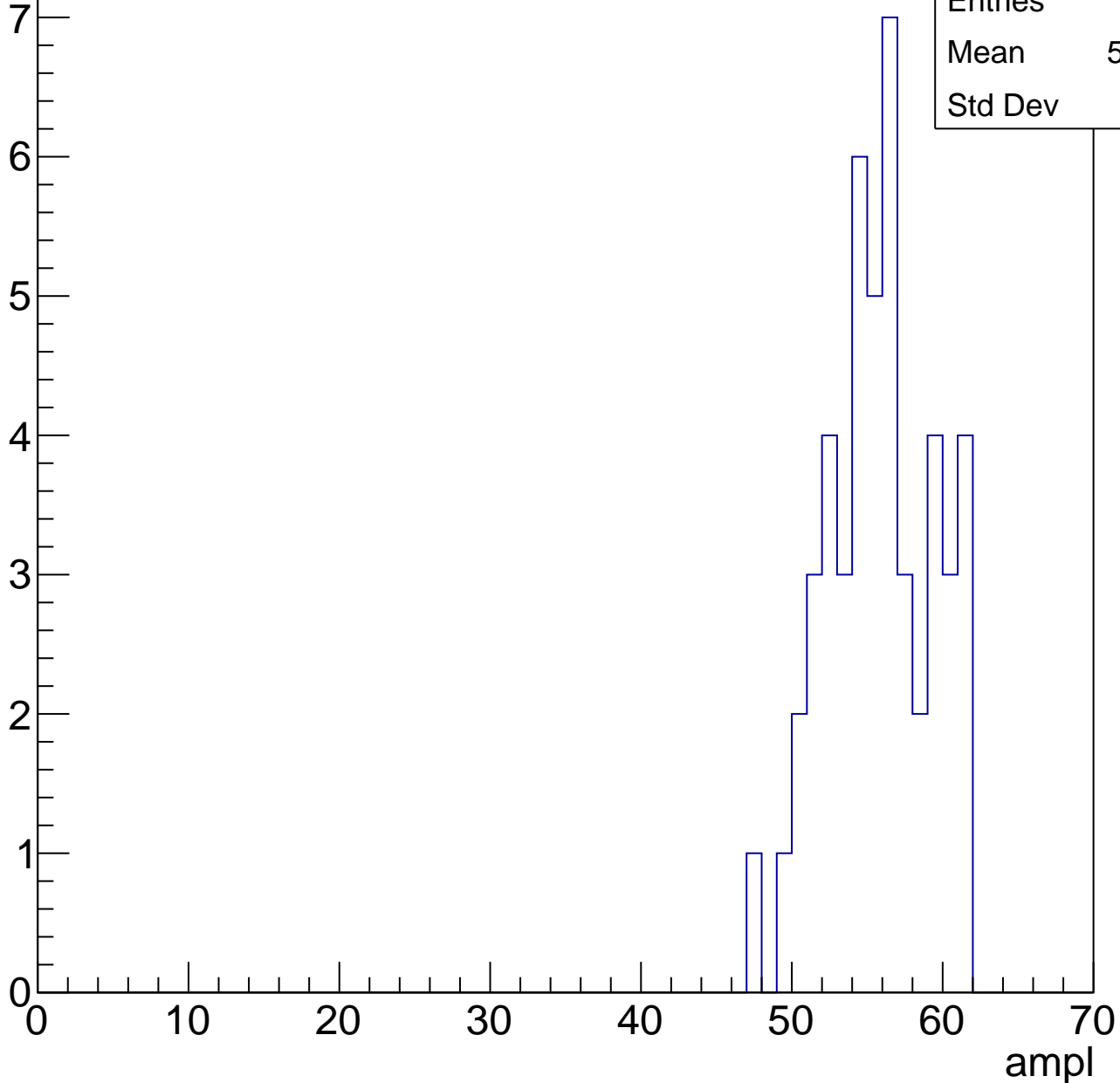


B1L100S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

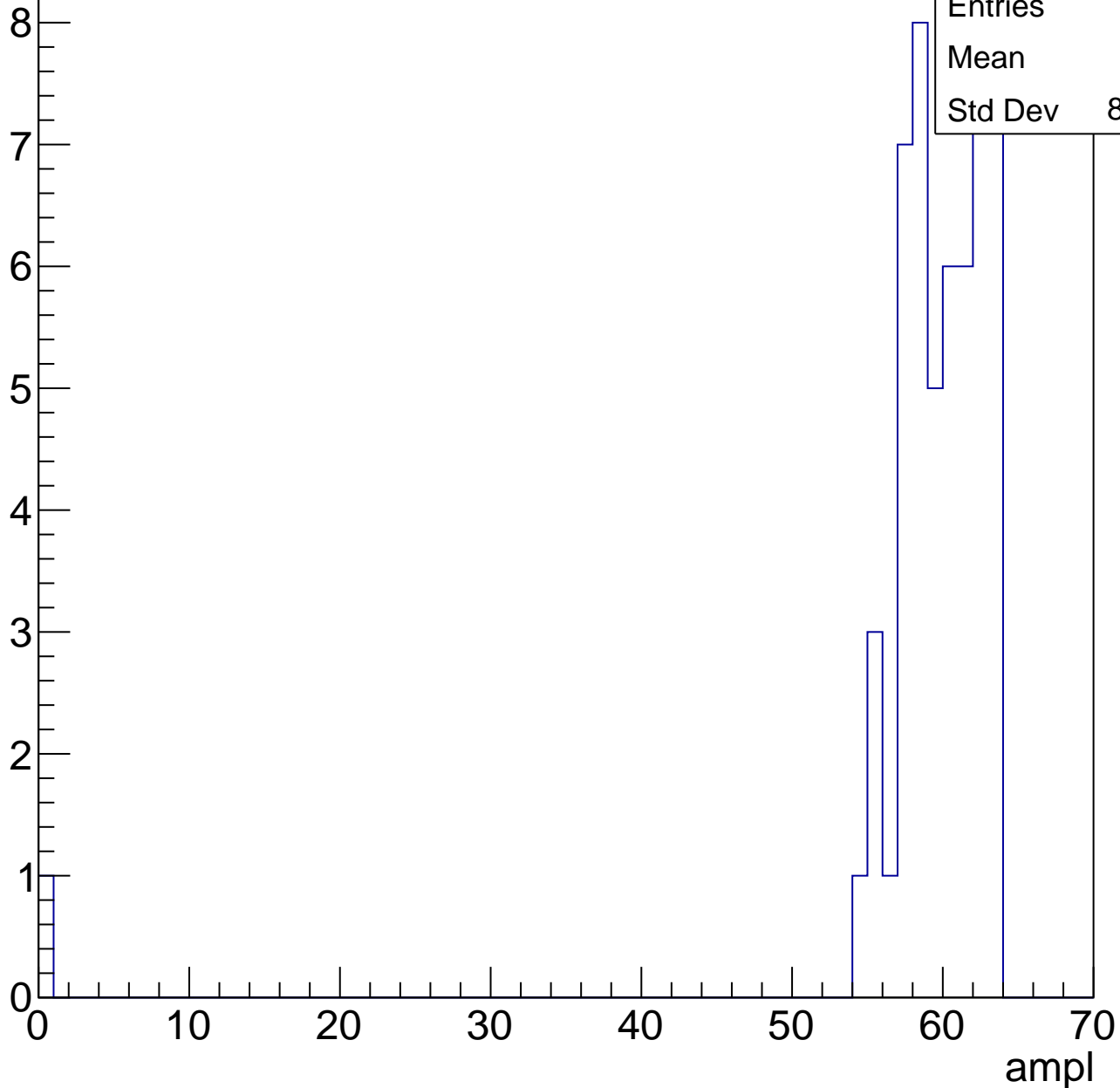
Entries	48
Mean	55.29
Std Dev	3.44



B1L100S, U14-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

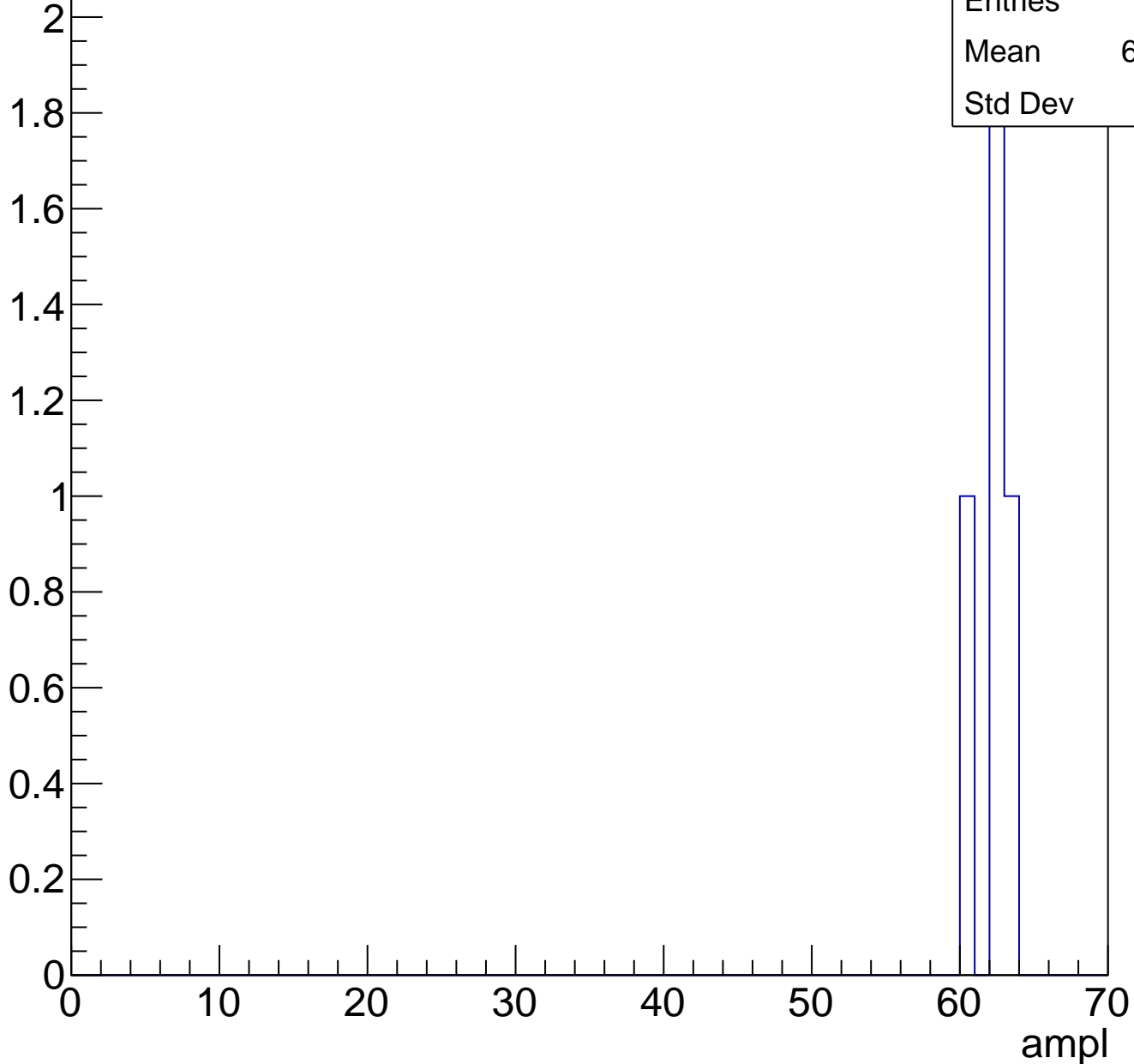
Entry



B1L100S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch71, adc7

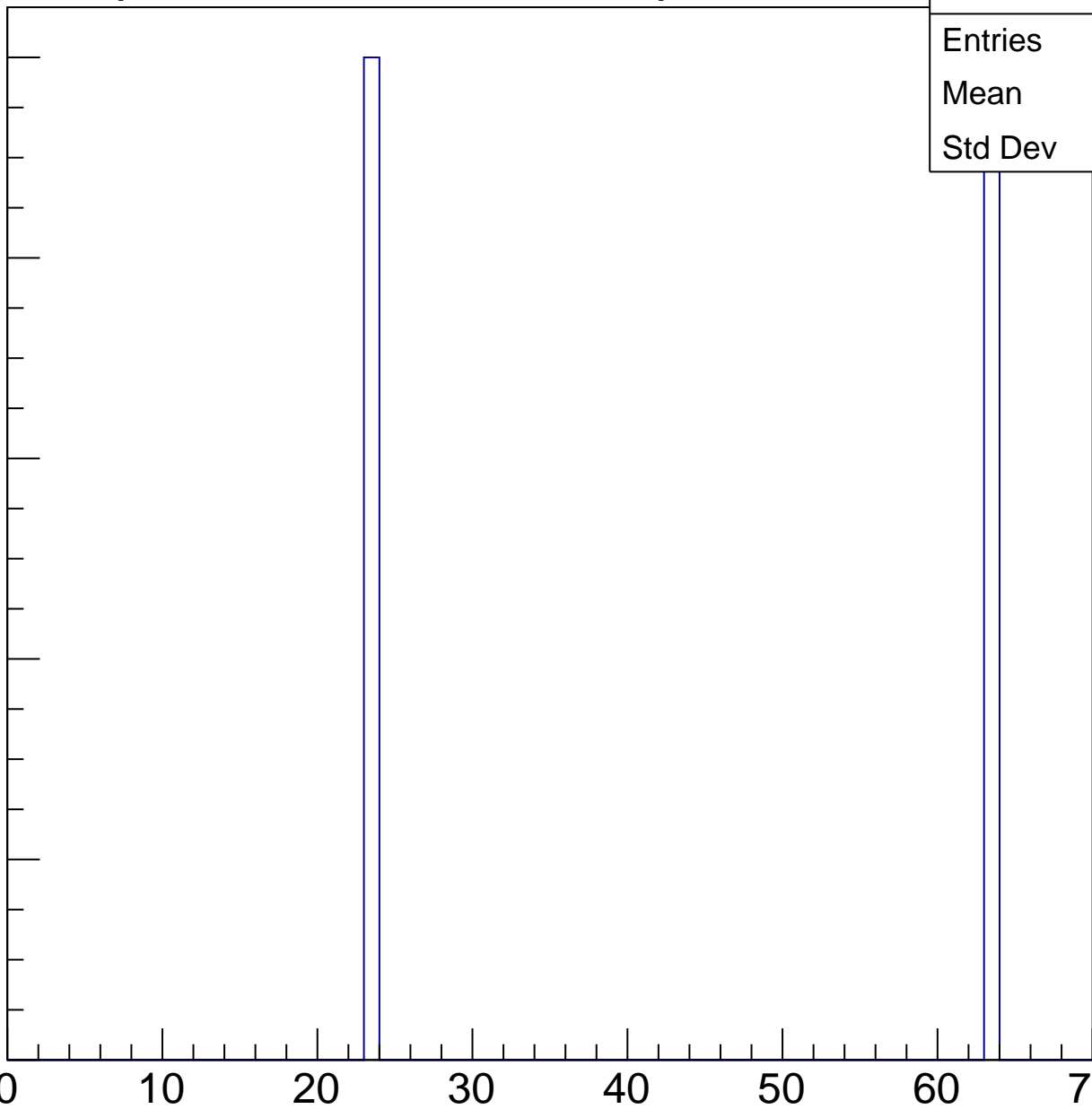
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl



B1L100S, U14-ch72, adc0

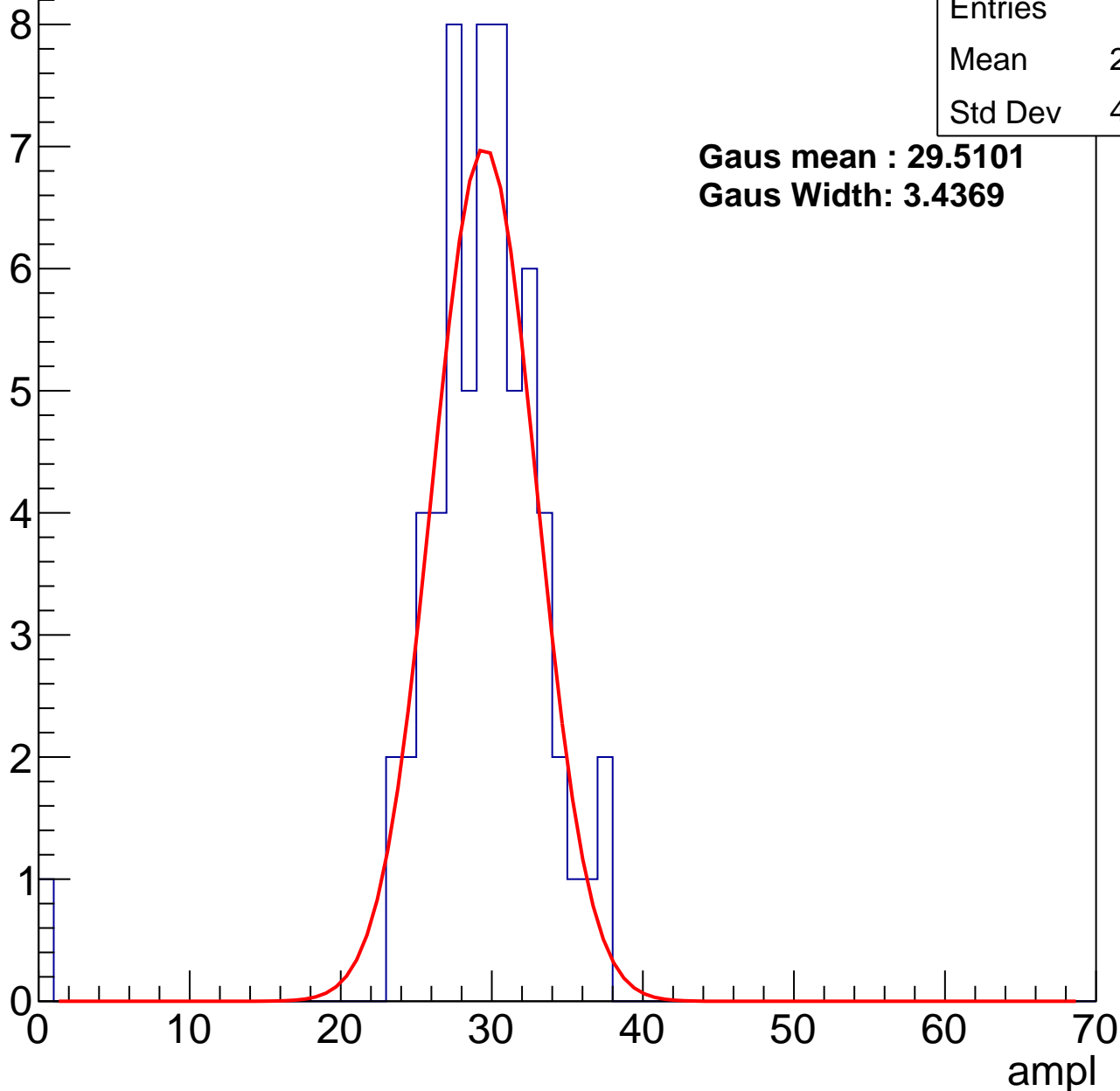
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.86
Std Dev	4.892

Gaus mean : 29.5101

Gaus Width: 3.4369



B1L100S, U14-ch72, adc1

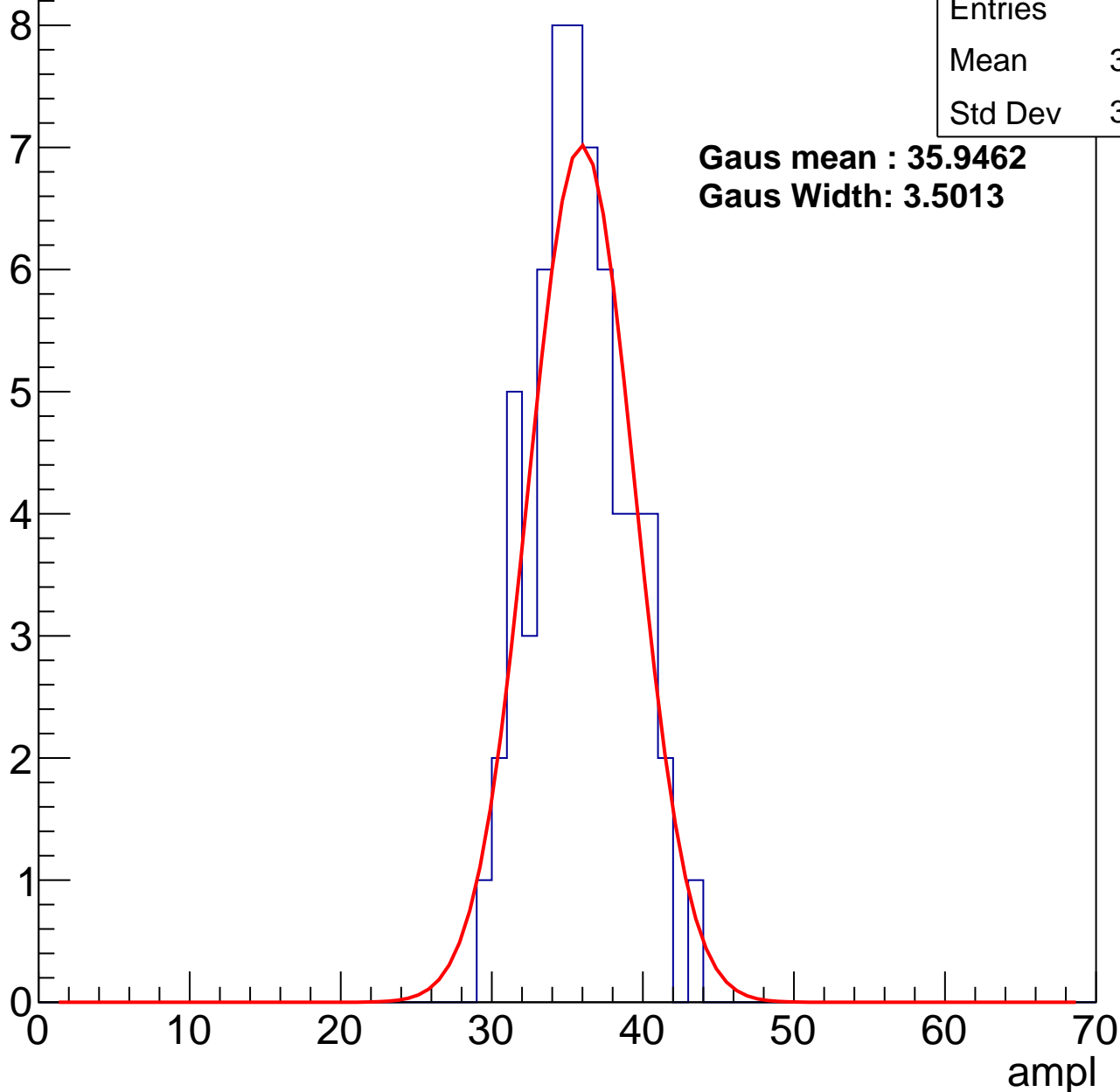
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.36
Std Dev	3.094

Gaus mean : 35.9462

Gaus Width: 3.5013



B1L100S, U14-ch72, adc2

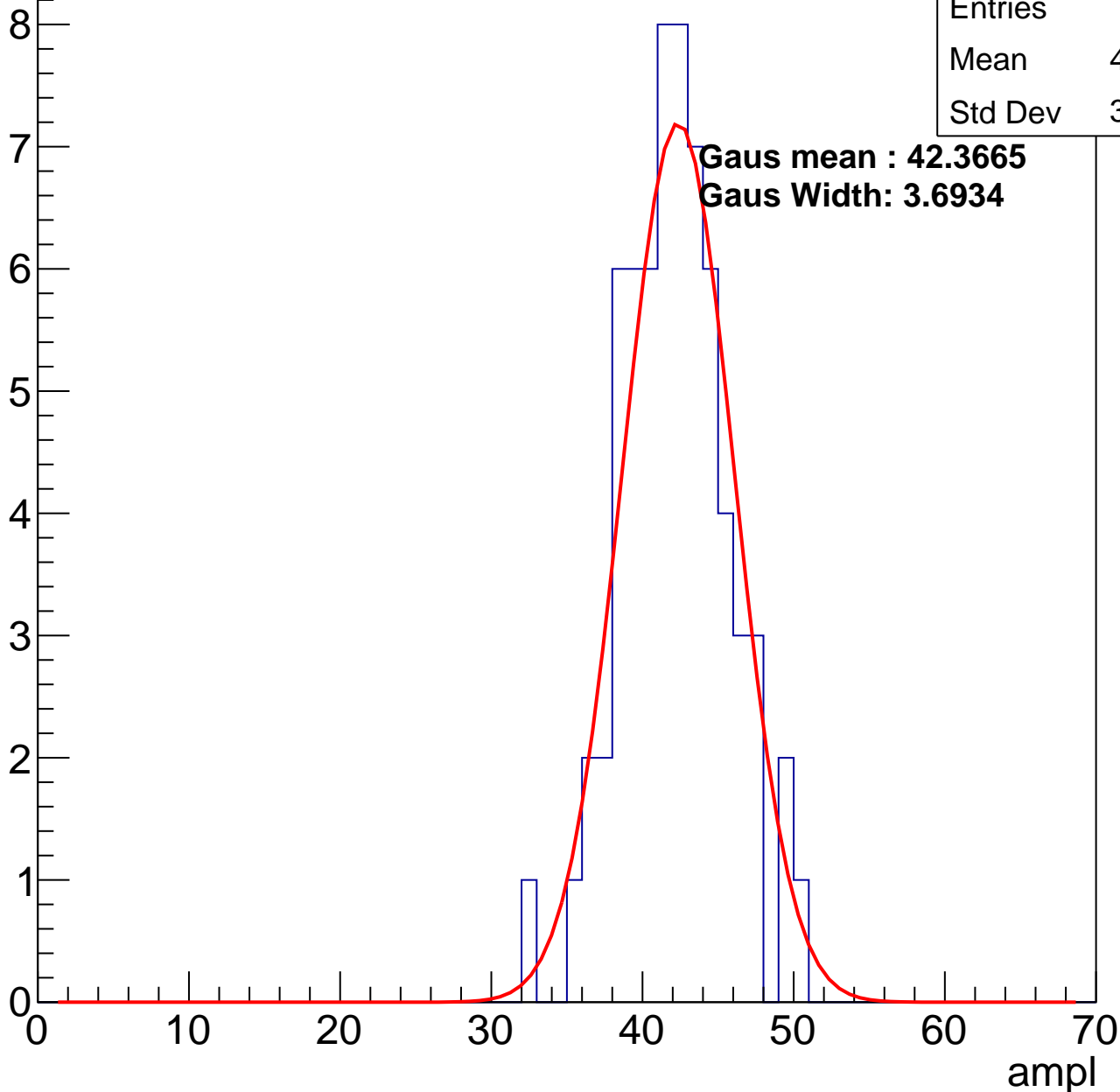
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.68
Std Dev	3.478

Gaus mean : 42.3665

Gaus Width: 3.6934



B1L100S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	48.44
Std Dev	3.487

Entry

10

8

6

4

2

0

0

10

20

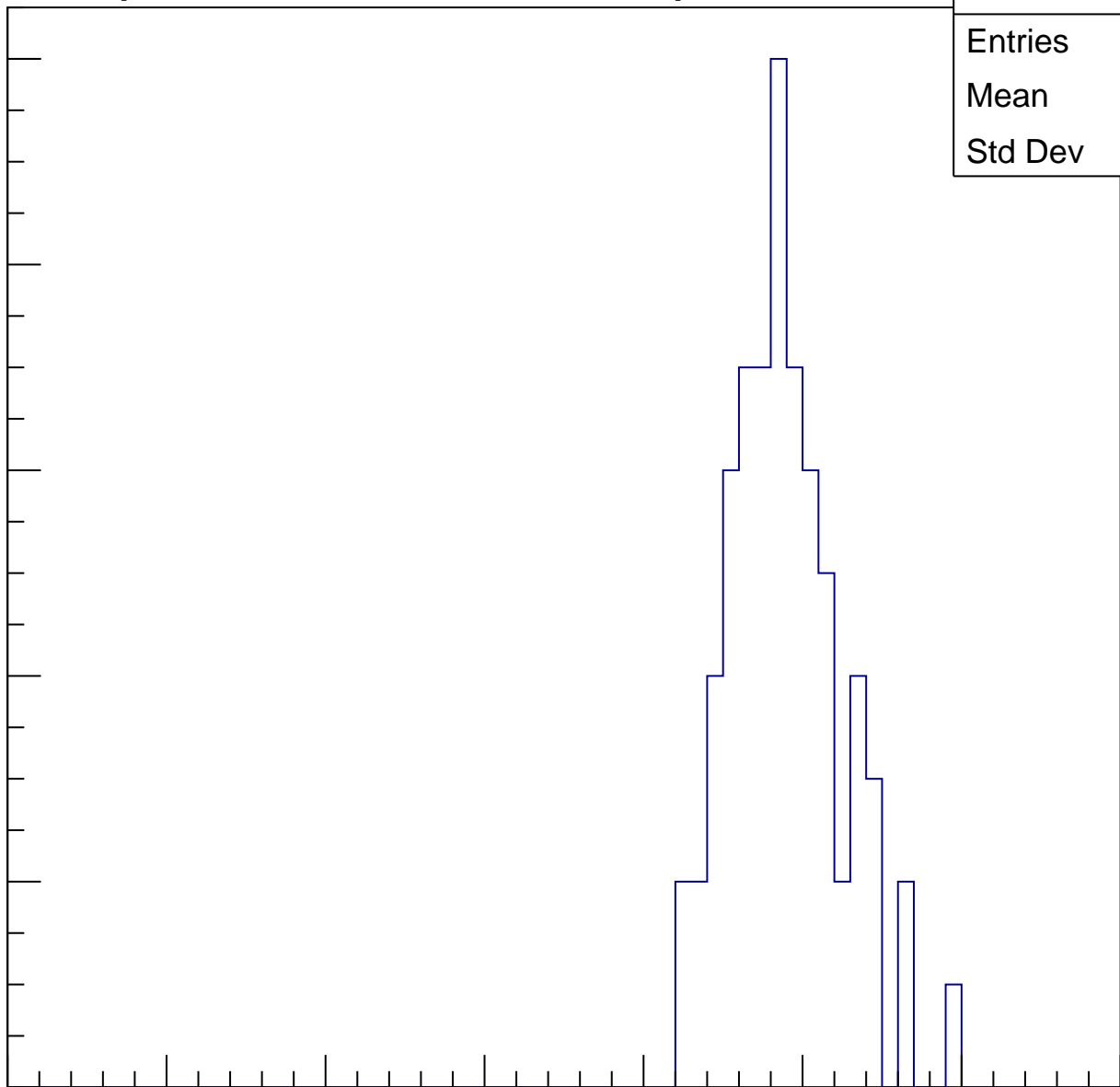
30

40

50

60

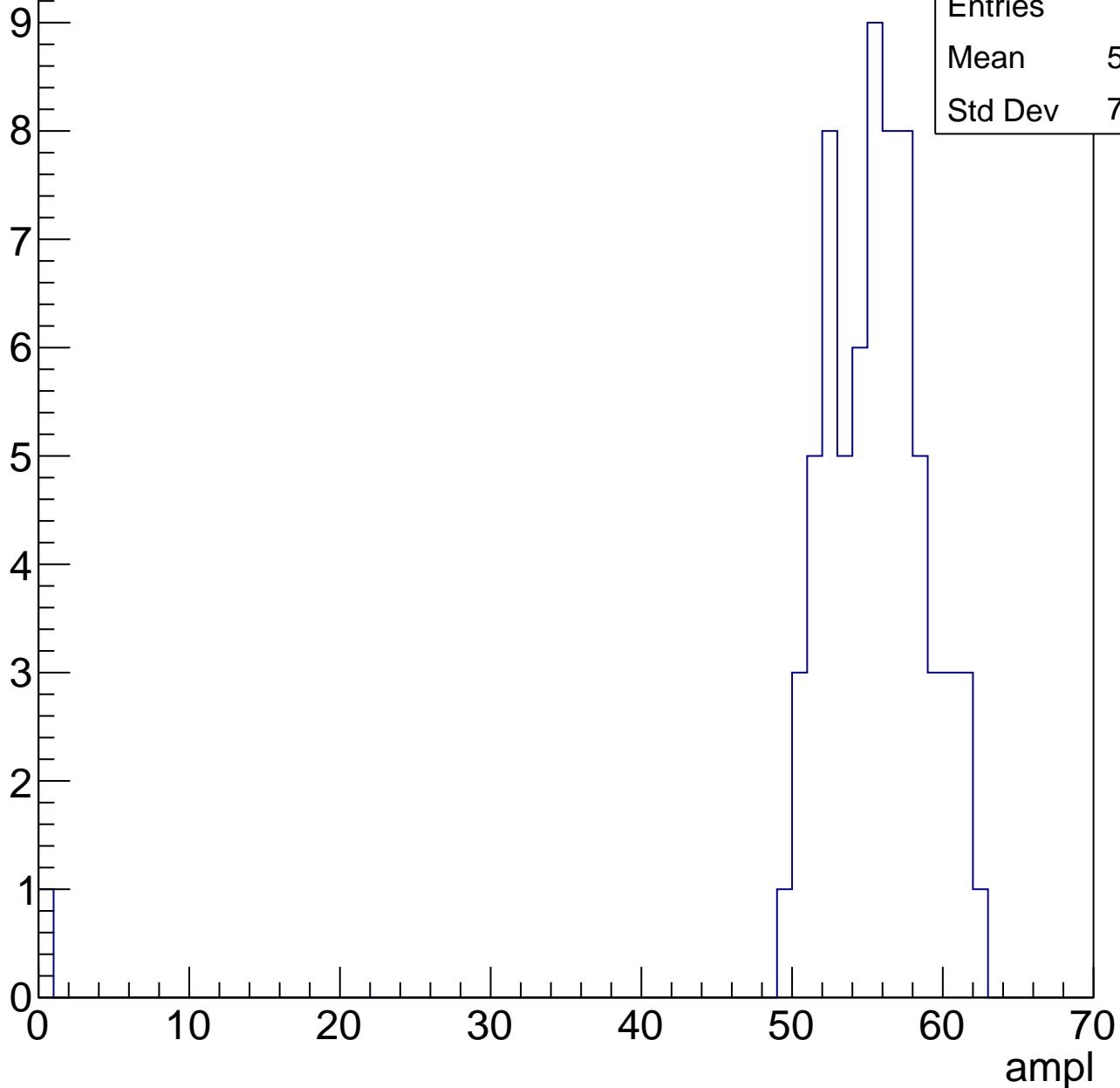
ampl



B1L100S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

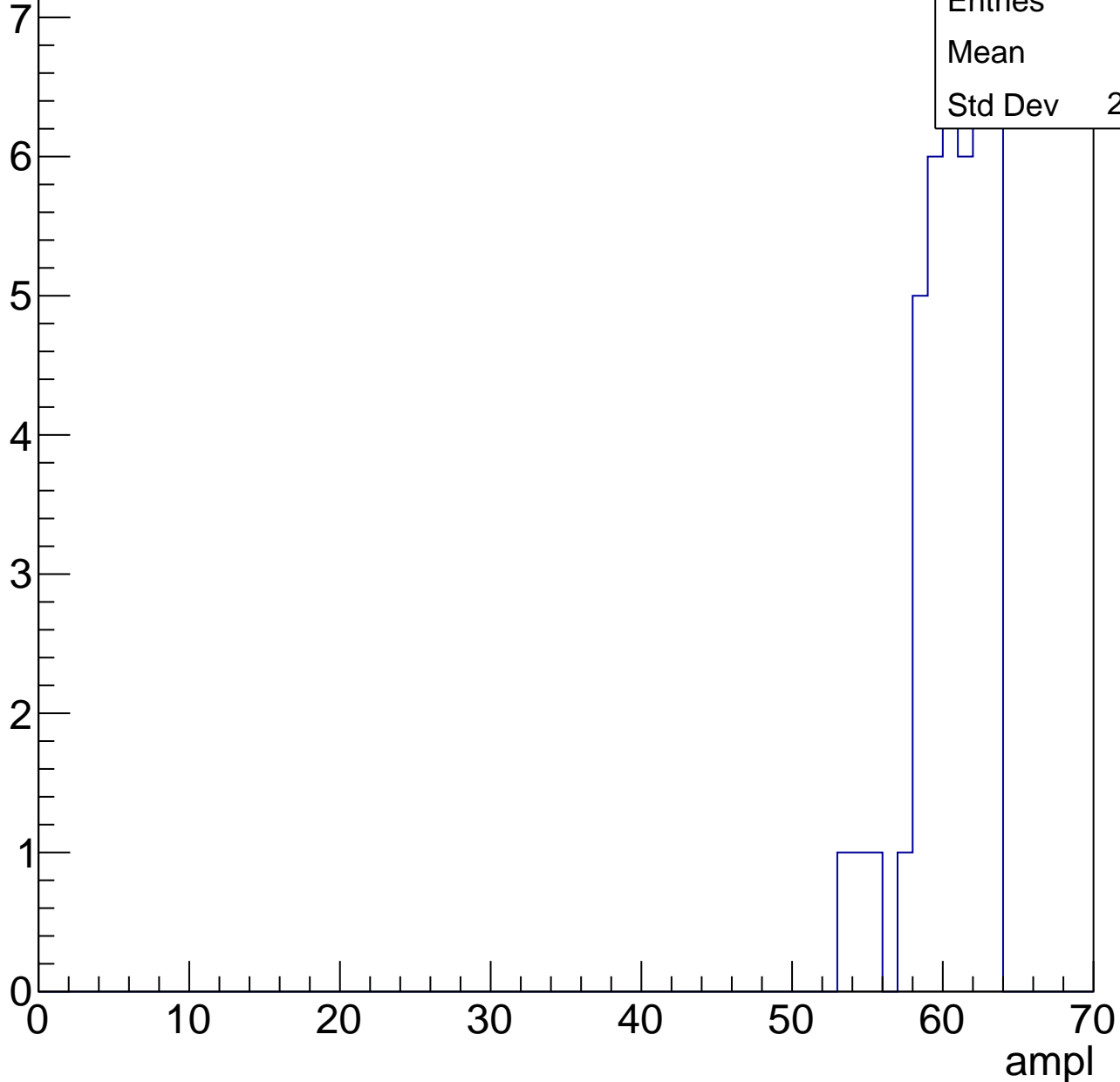
Entry



B1L100S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

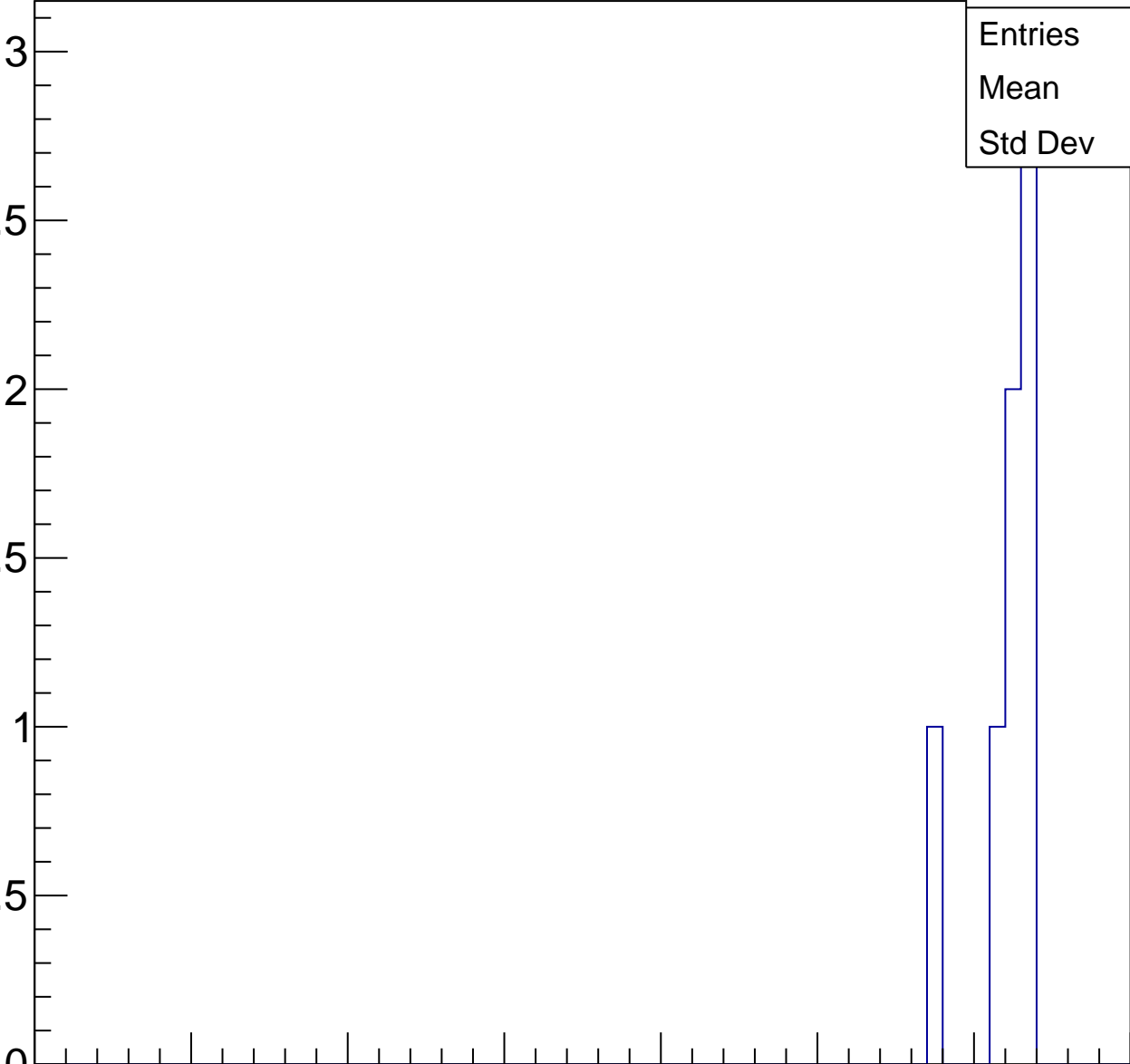
Entry

3
2.5
2
1.5
1
0.5
0

Entries	7
Mean	61.57
Std Dev	1.99

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch73, adc0

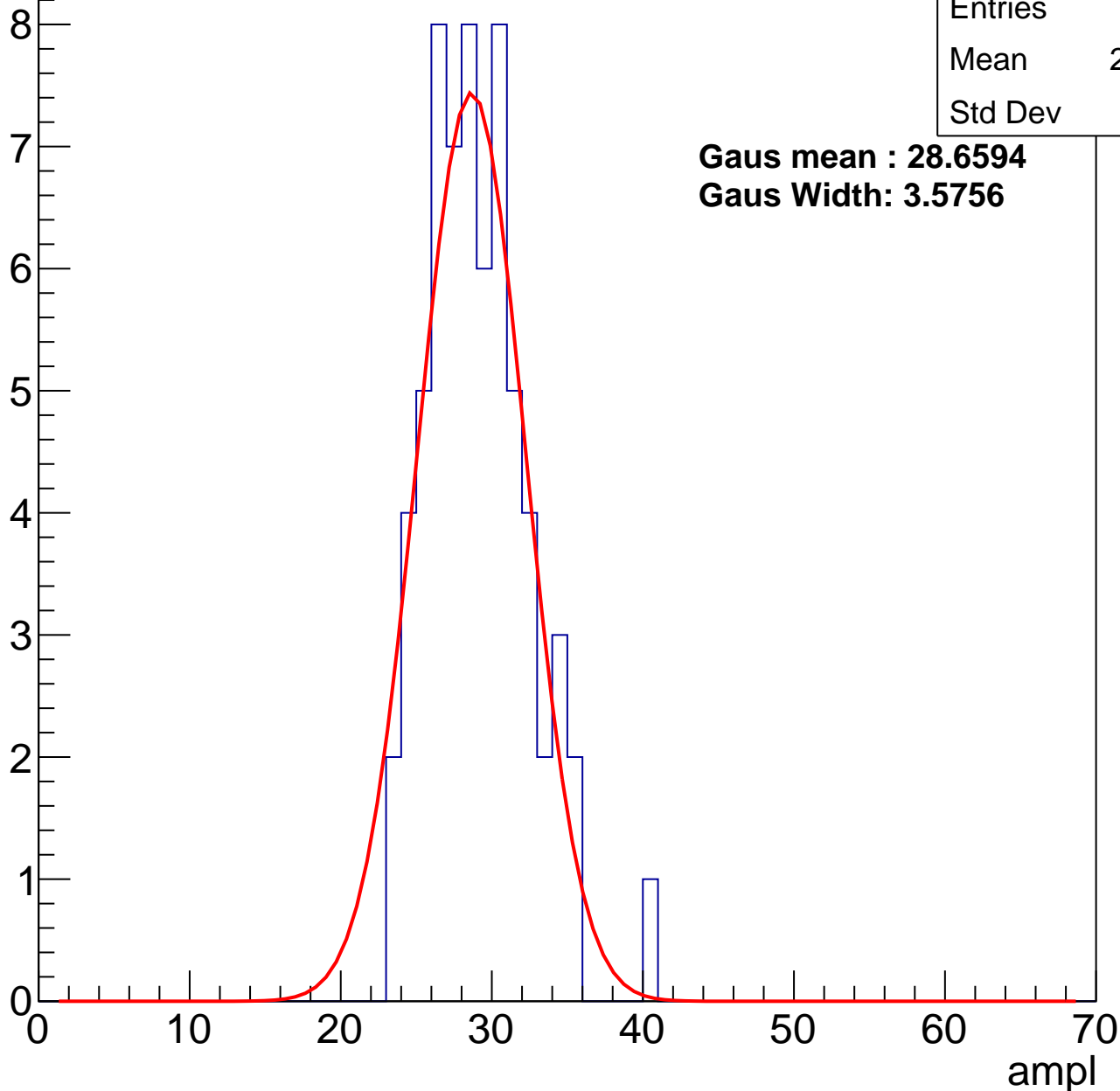
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.66
Std Dev	3.32

Gaus mean : 28.6594

Gaus Width: 3.5756



B1L100S, U14-ch73, adc1

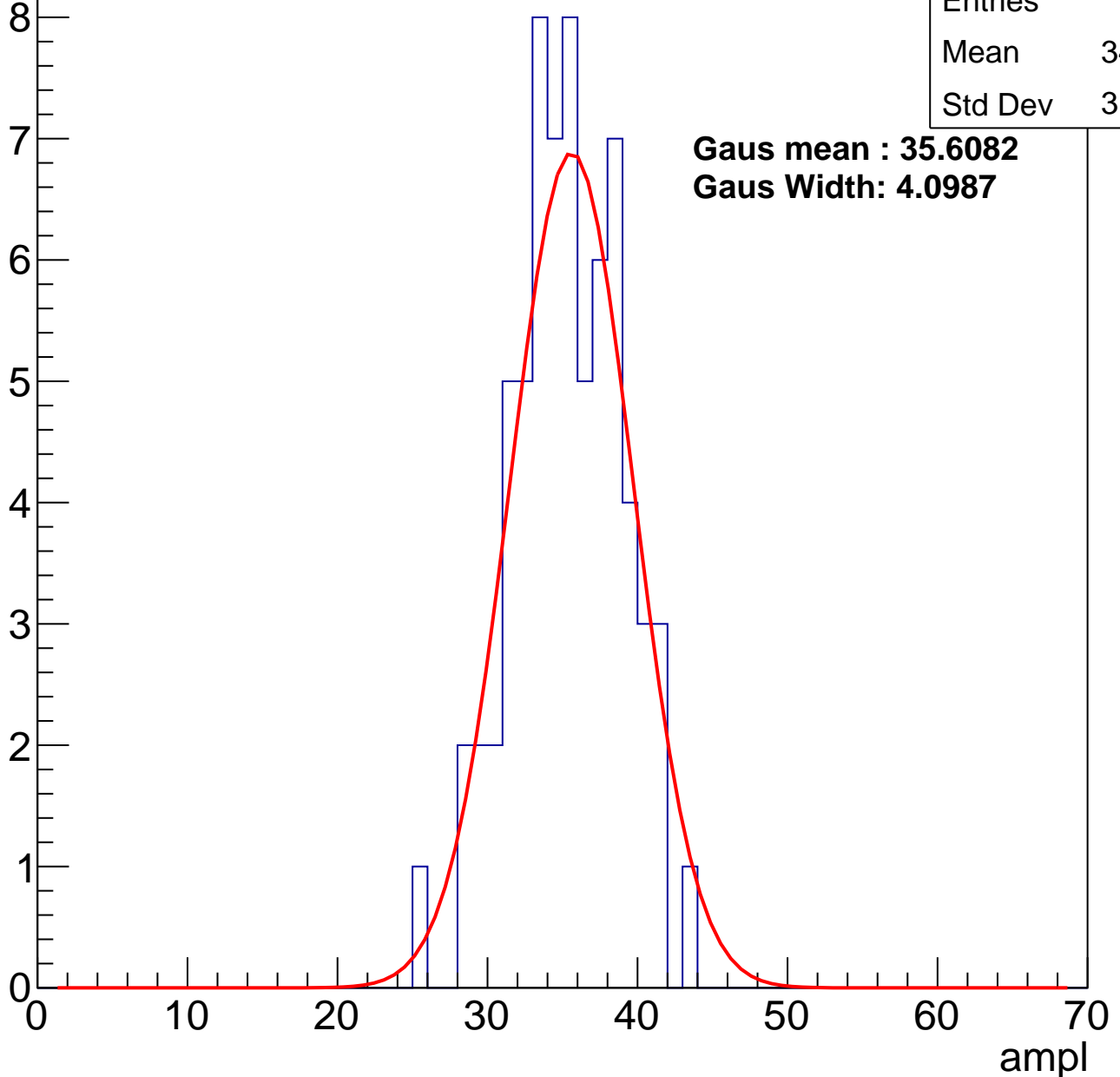
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.87
Std Dev	3.583

Gaus mean : 35.6082

Gaus Width: 4.0987



B1L100S, U14-ch73, adc2

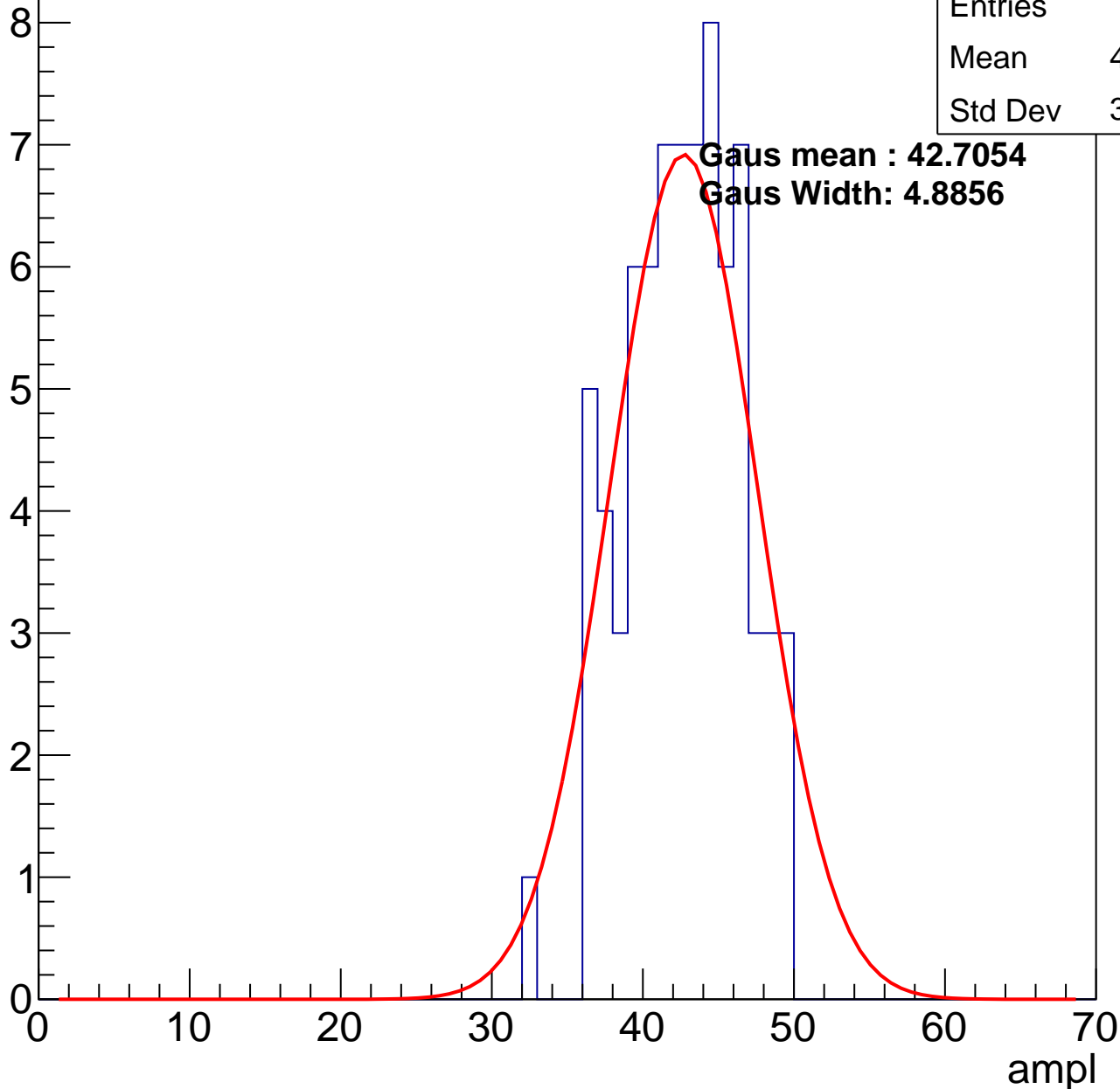
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.18
Std Dev	3.709

Gaus mean : 42.7054

Gaus Width: 4.8856

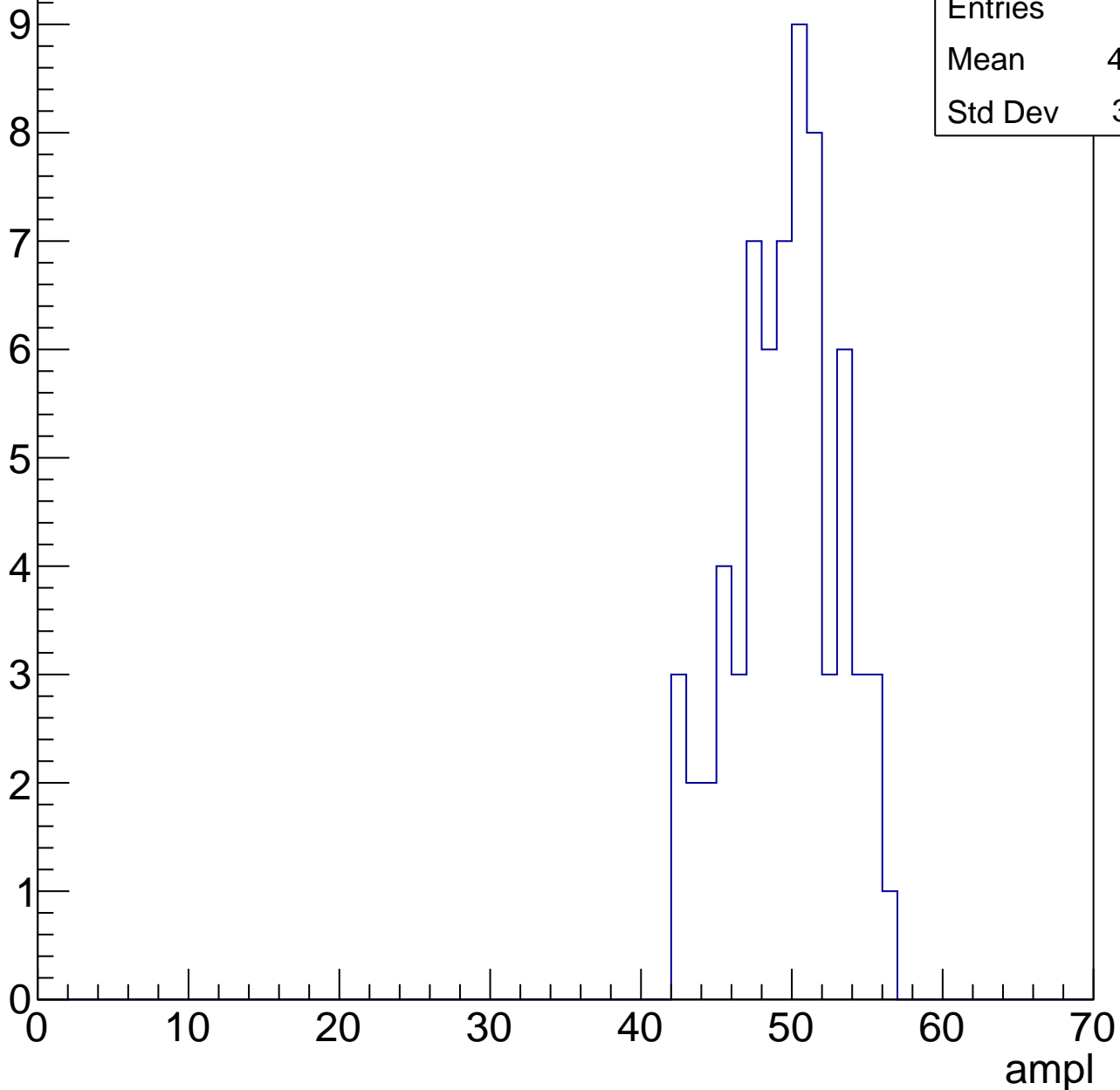


B1L100S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	49.15
Std Dev	3.431

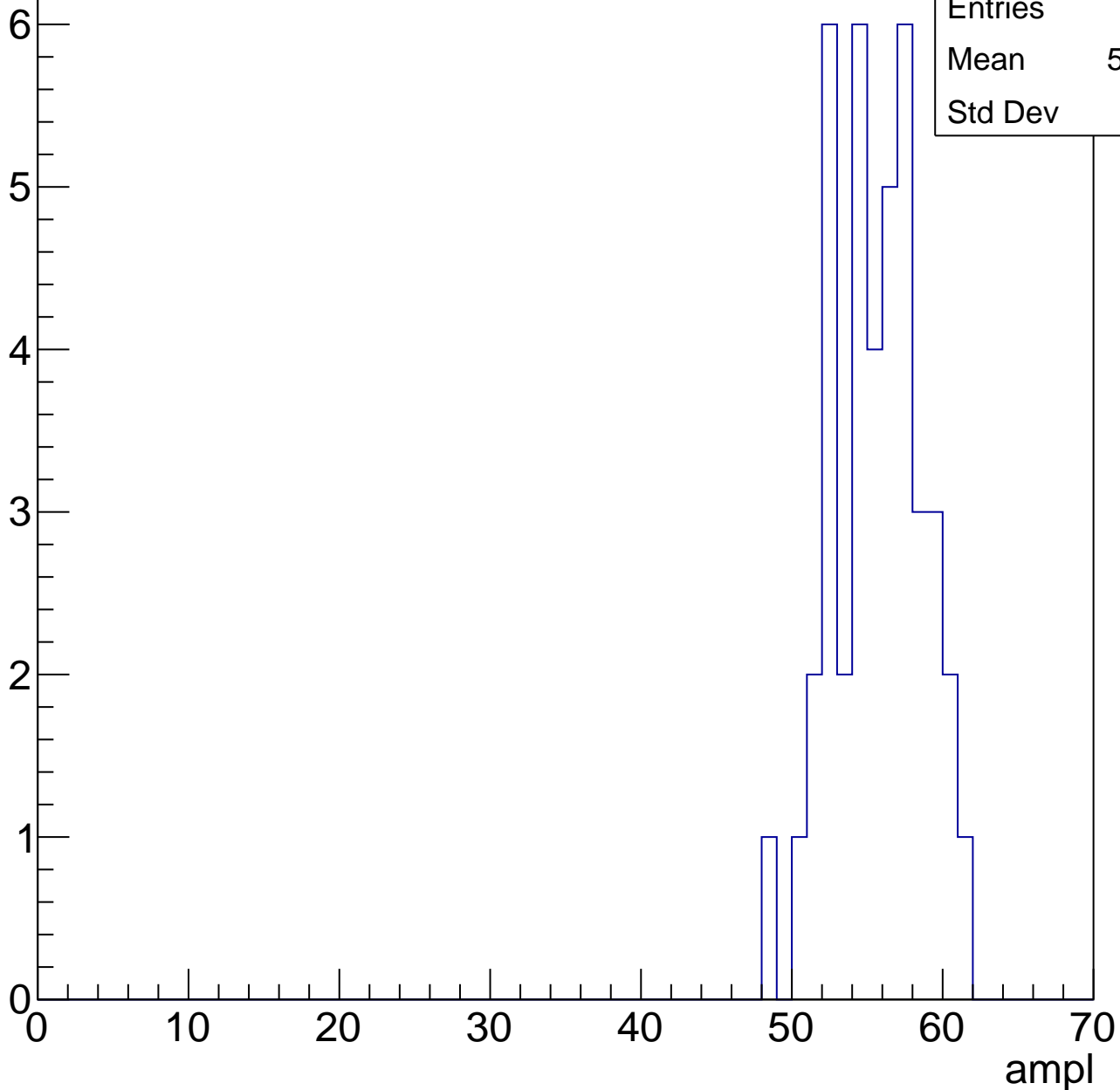


B1L100S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	55.14
Std Dev	2.94

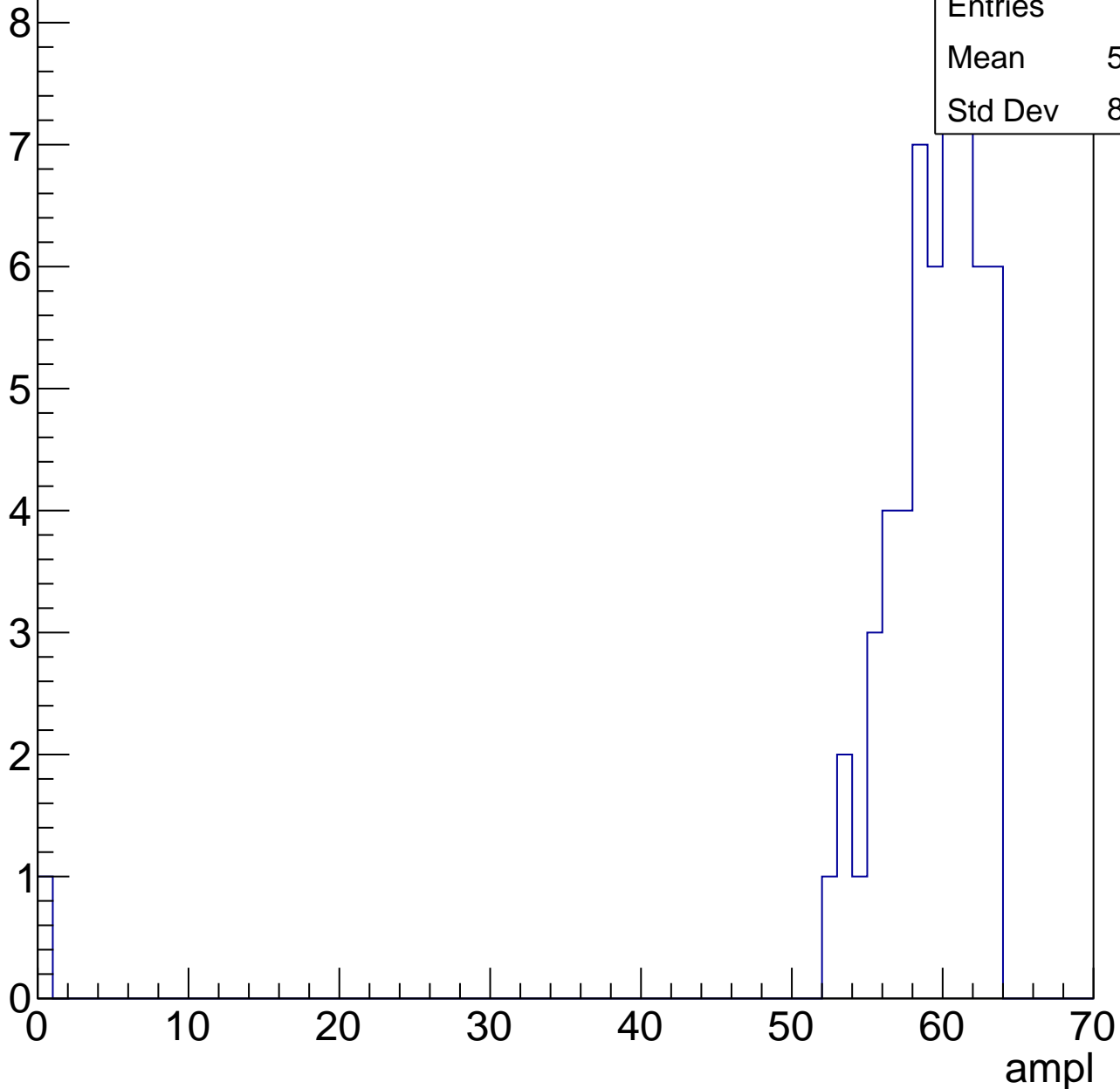


B1L100S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

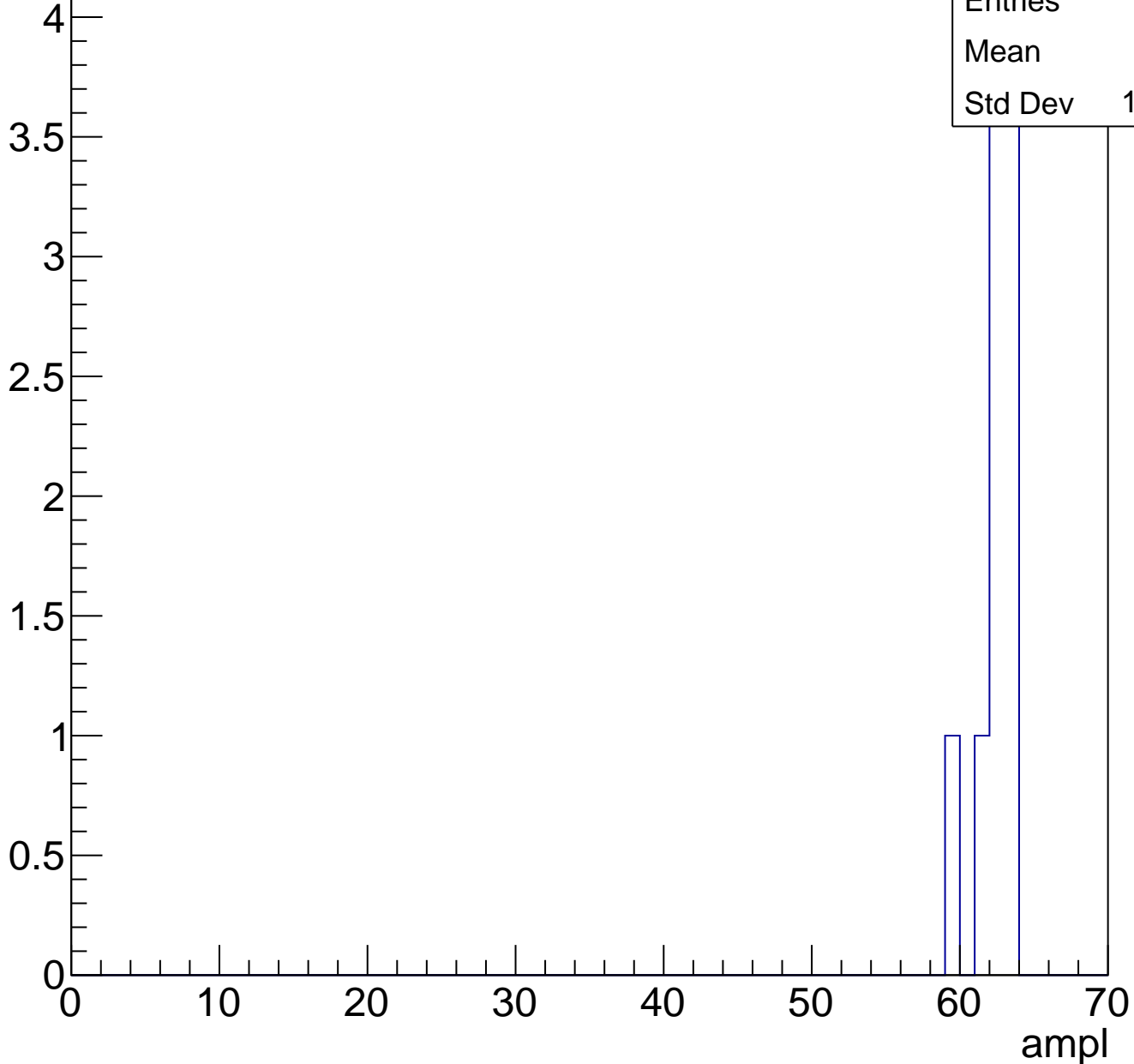
Entries	57
Mean	58.02
Std Dev	8.239



B1L100S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U14-ch74, adc0

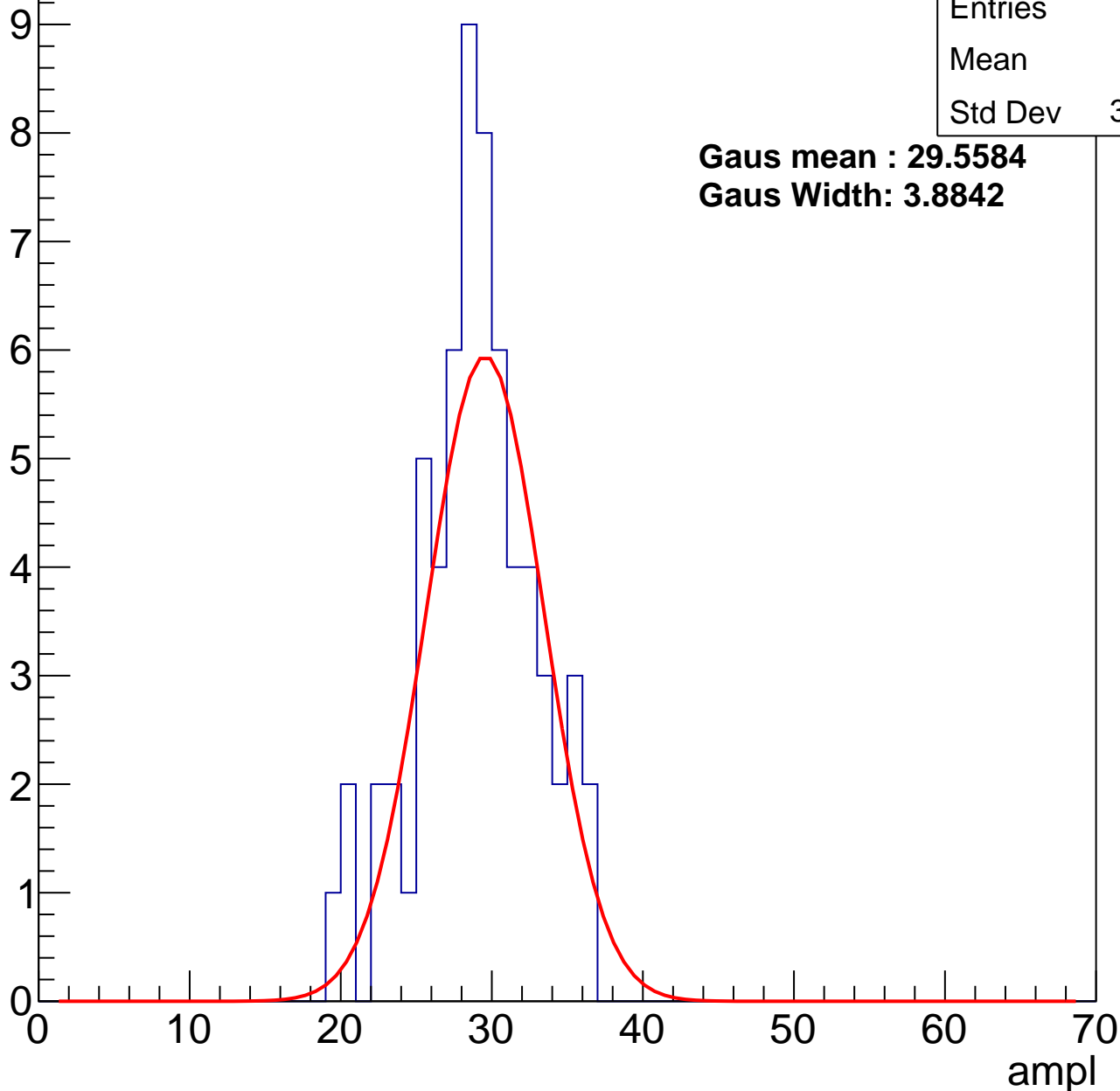
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.5
Std Dev	3.857

Gaus mean : 29.5584

Gaus Width: 3.8842



B1L100S, U14-ch74, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	35.61
Std Dev	3.798

Gaus mean : 35.8791

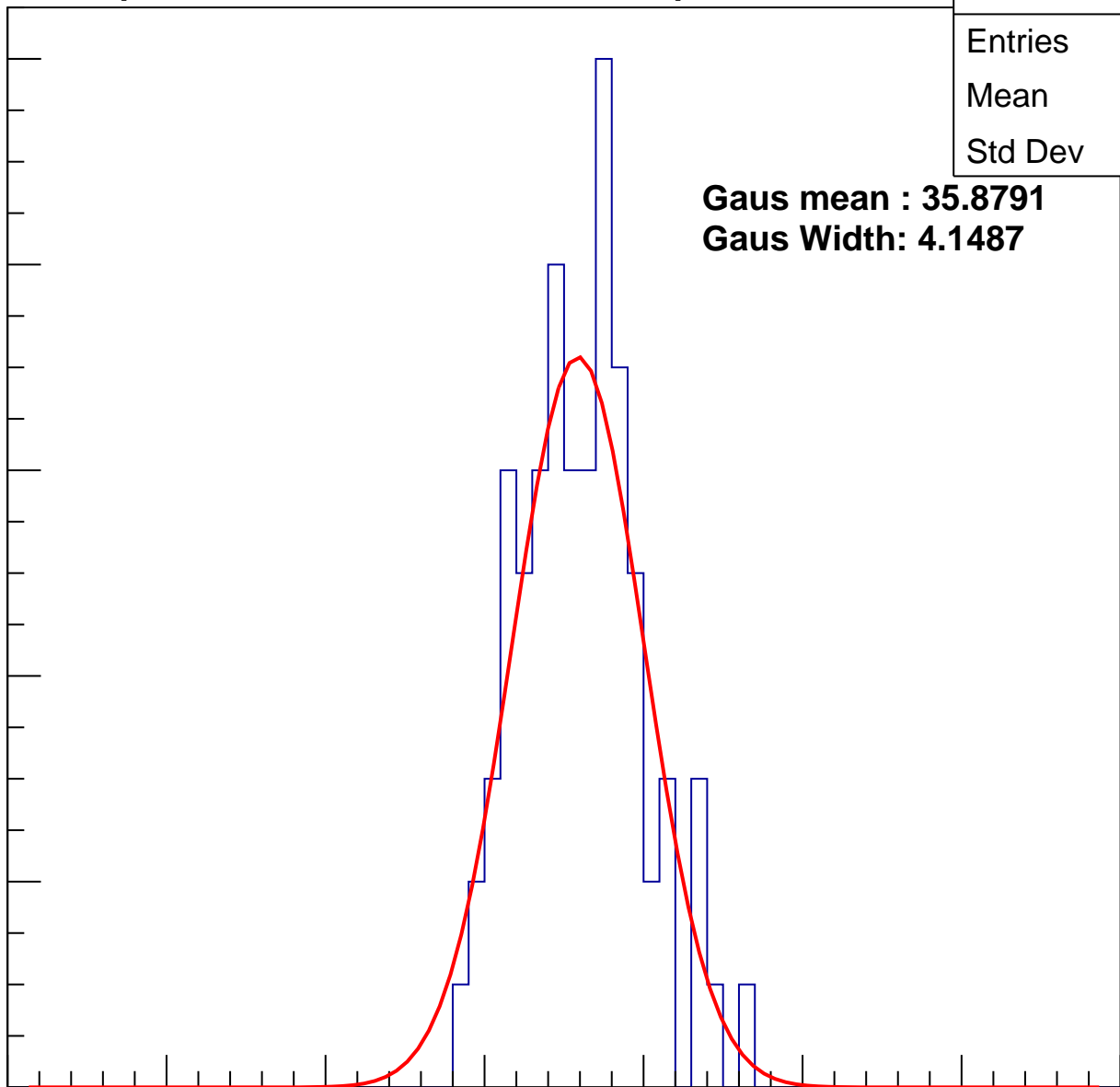
Gaus Width: 4.1487

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch74, adc2

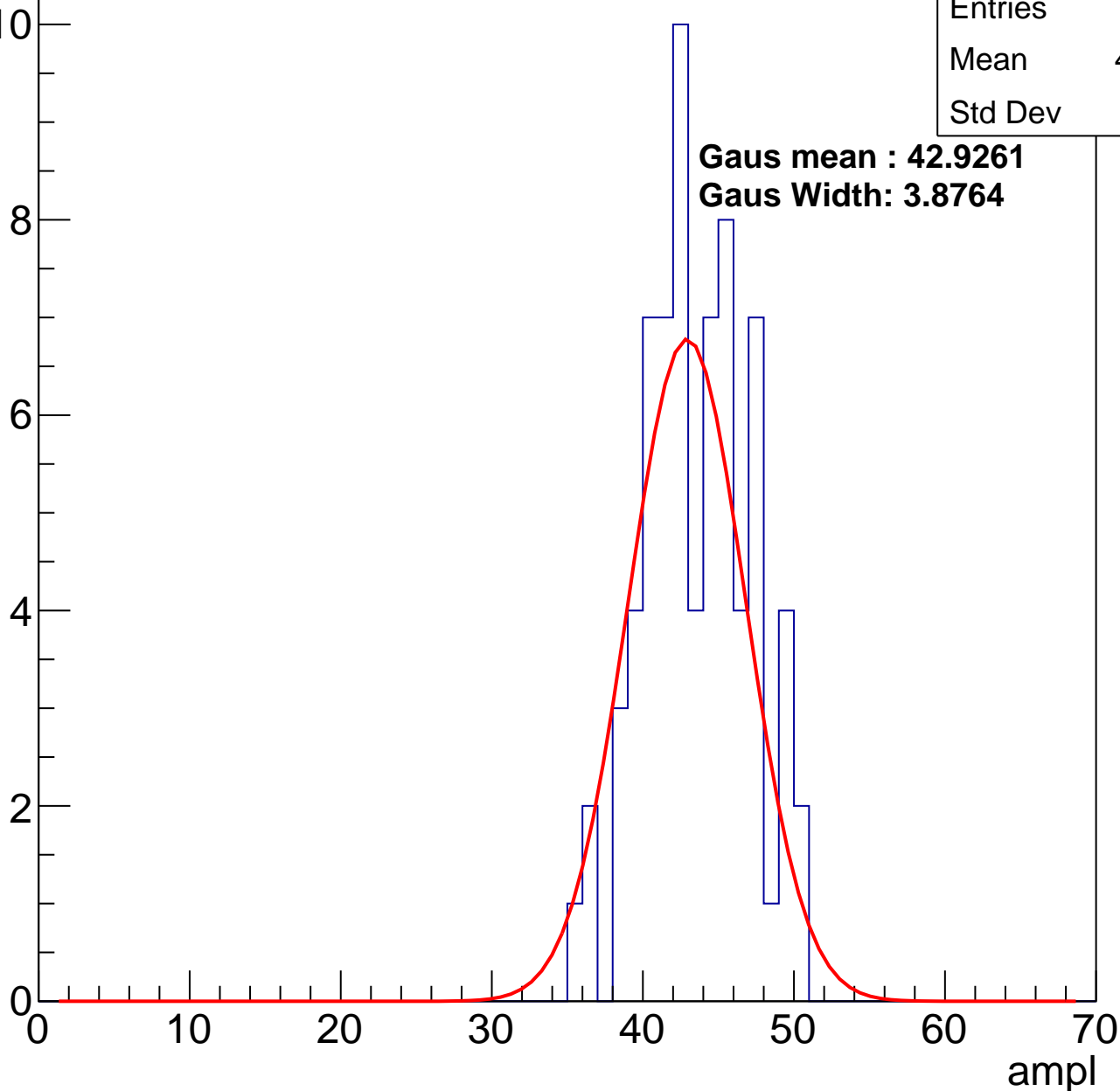
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.11
Std Dev	3.47

Gaus mean : 42.9261

Gaus Width: 3.8764

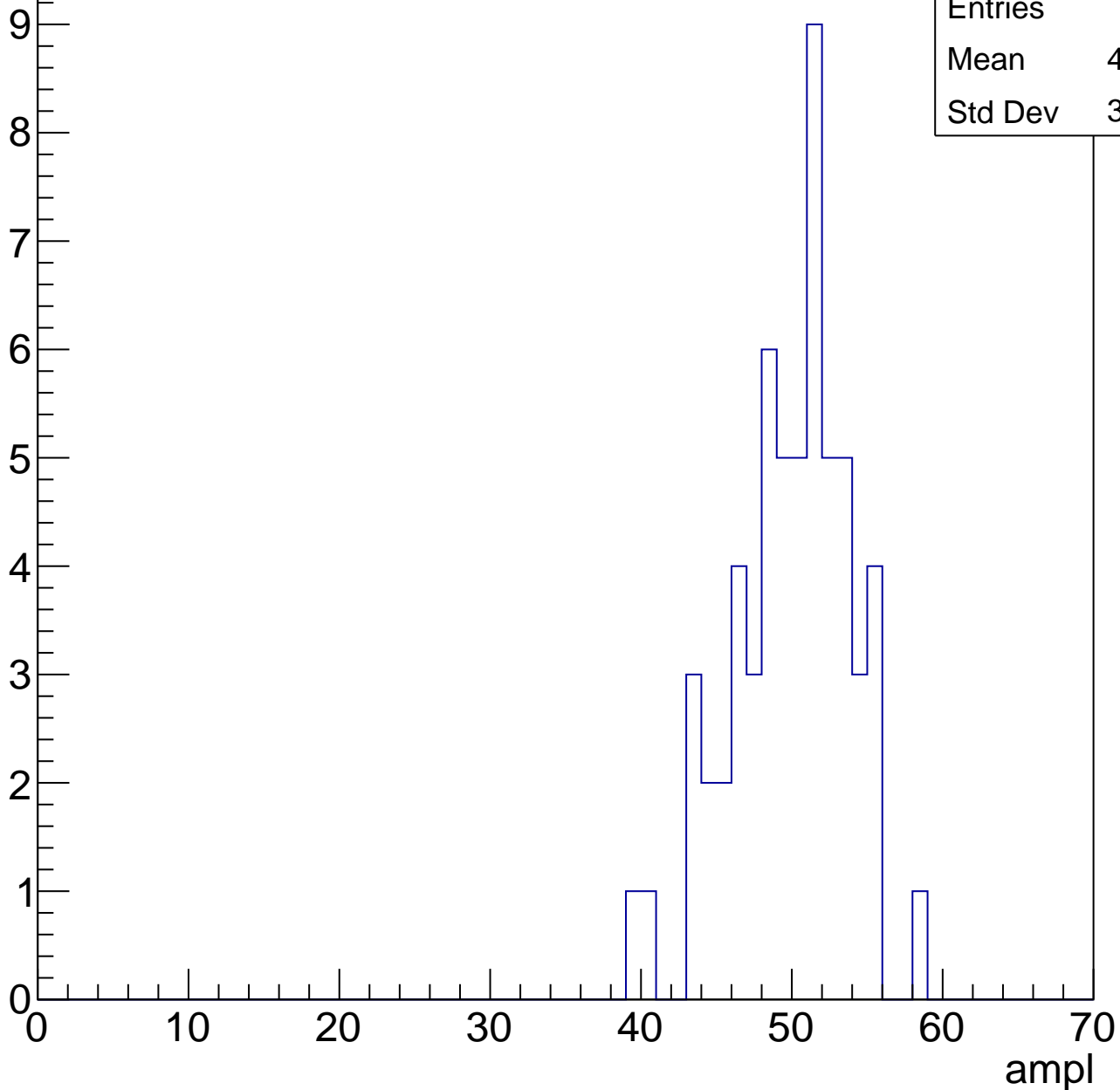


B1L100S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.46
Std Dev	3.859

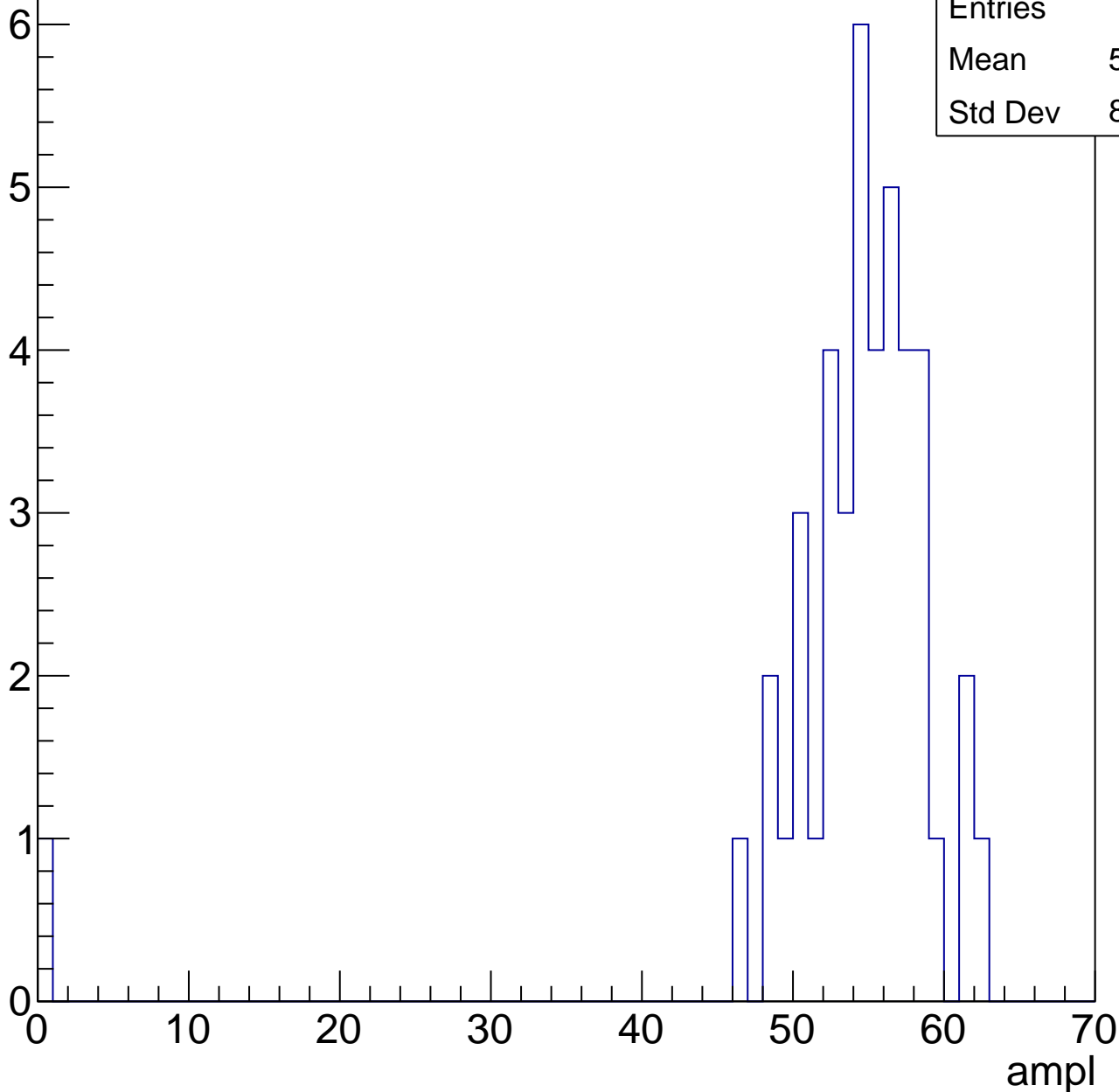


B1L100S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	53.16
Std Dev	8.936

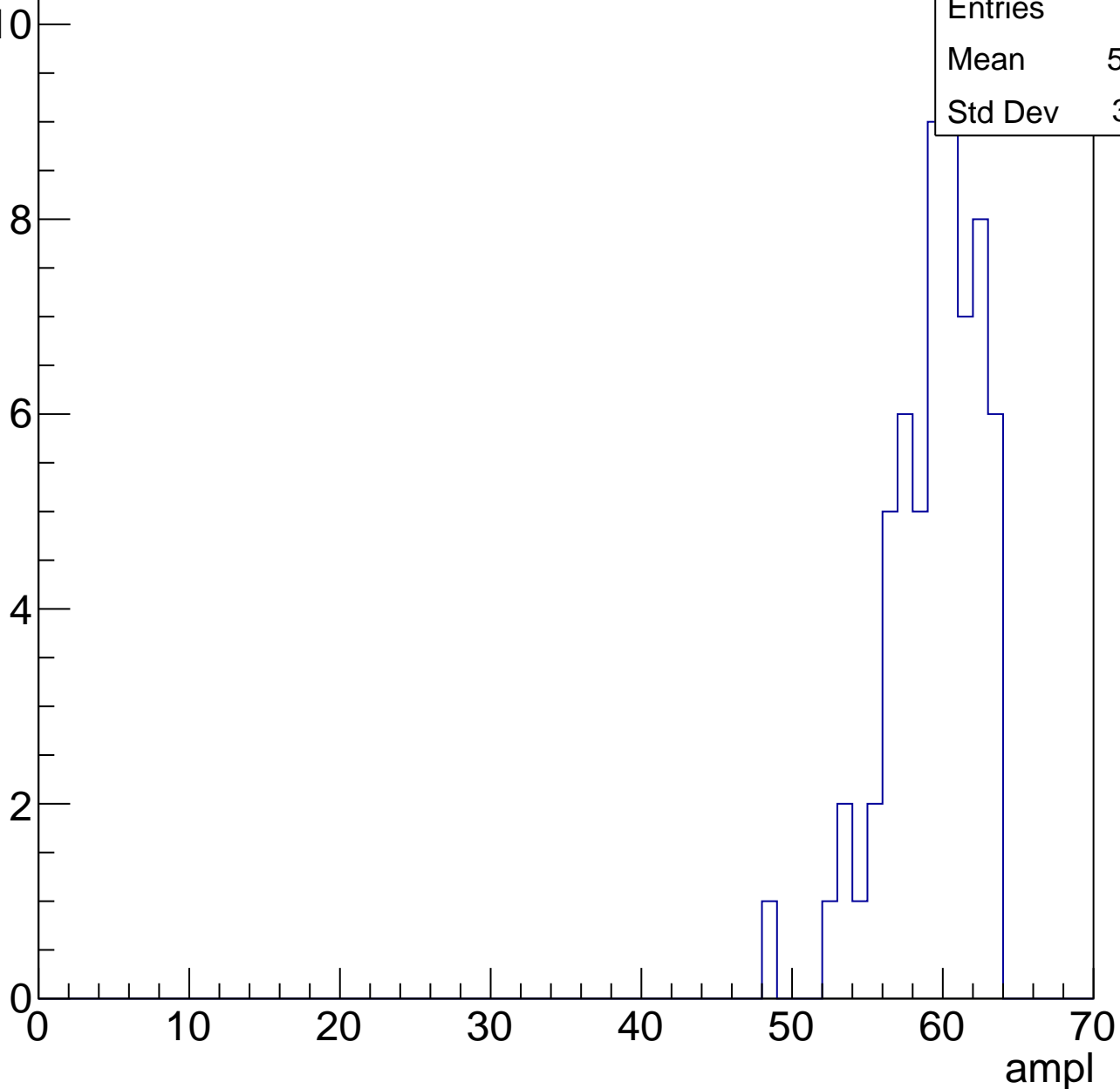


B1L100S, U14-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	58.95
Std Dev	3.031



B1L100S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch75, adc0

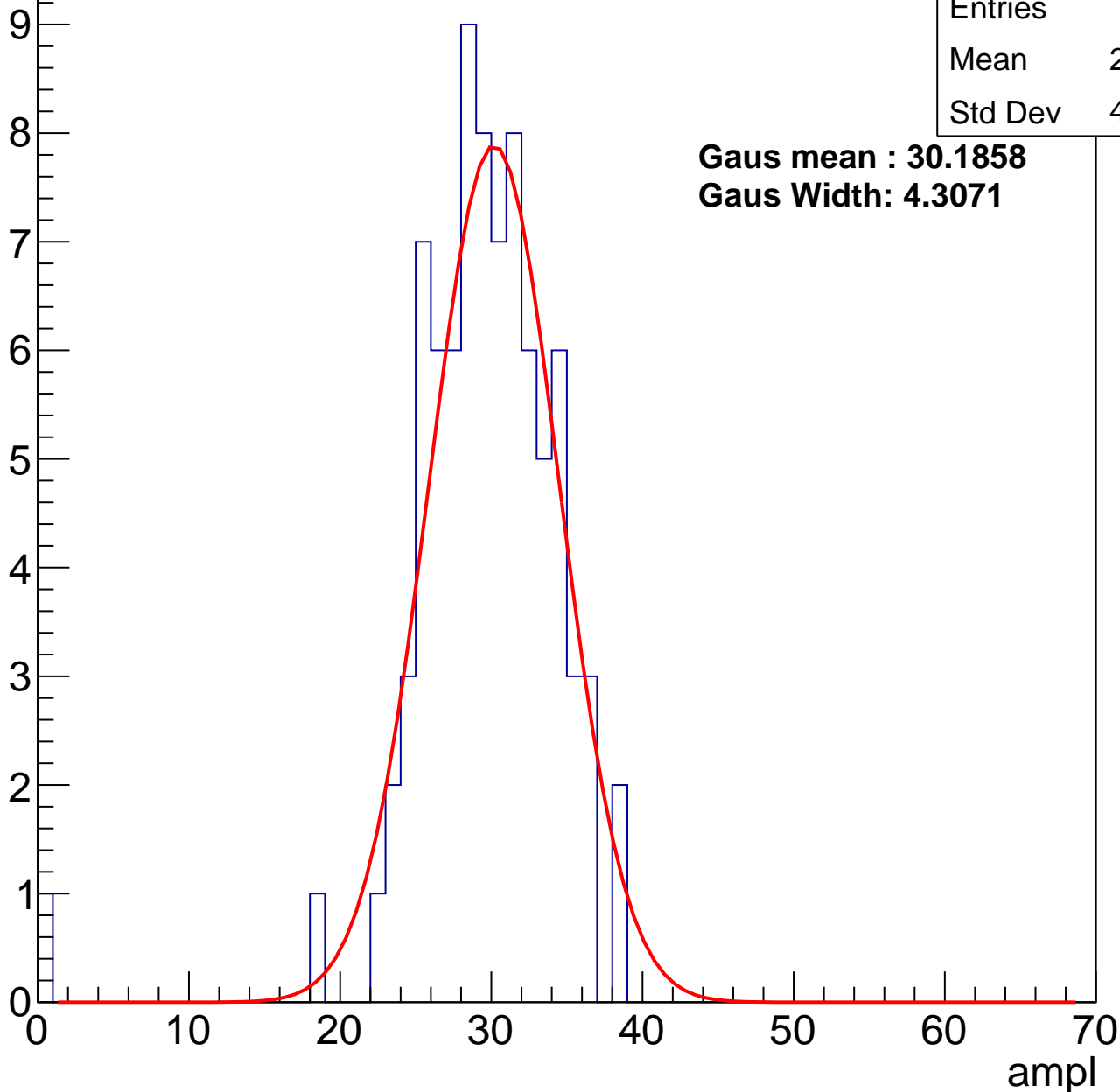
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	29.08
Std Dev	4.996

Gaus mean : 30.1858

Gaus Width: 4.3071



B1L100S, U14-ch75, adc1

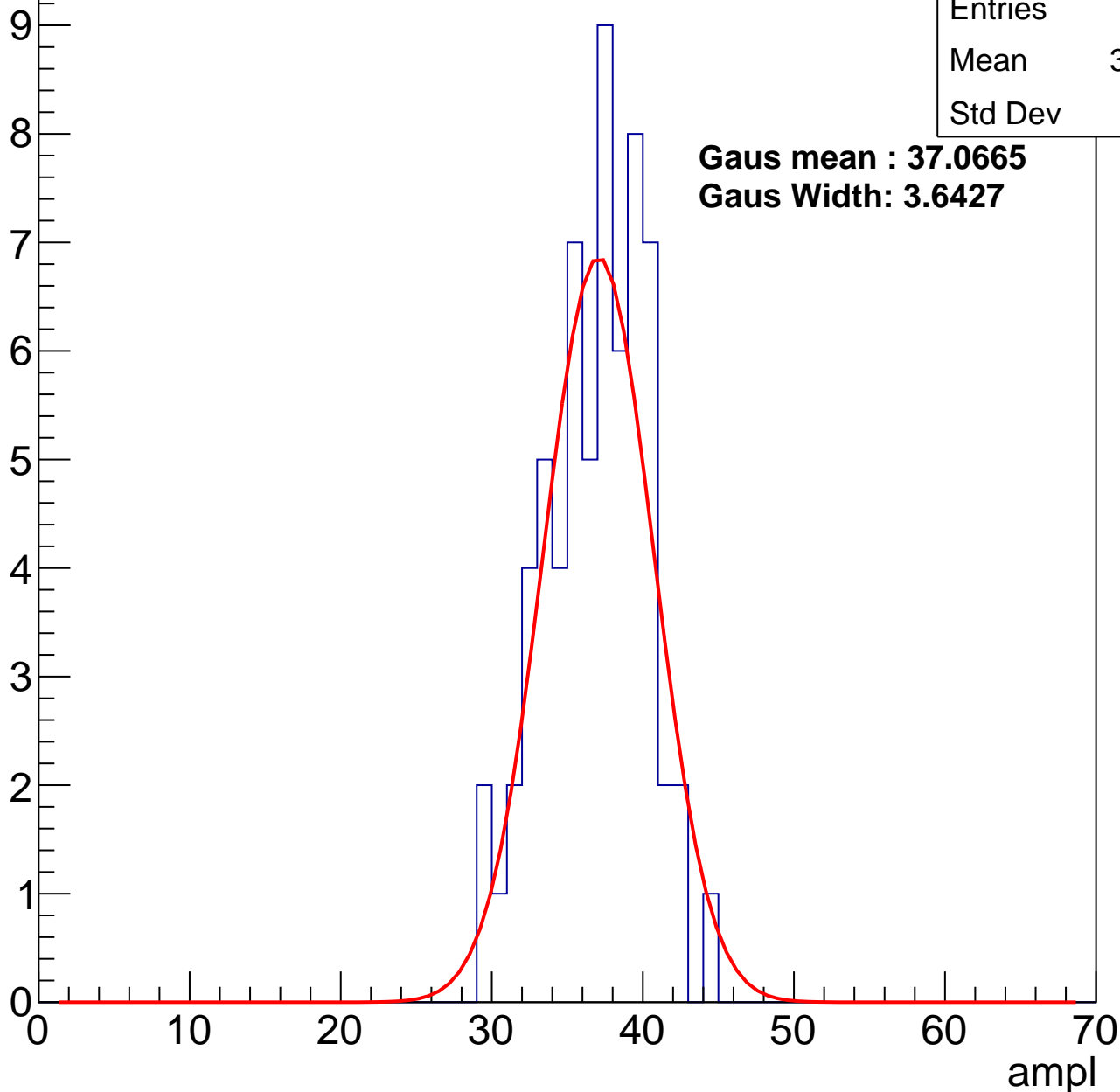
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.42
Std Dev	3.3

Gaus mean : 37.0665

Gaus Width: 3.6427



B1L100S, U14-ch75, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.74
Std Dev	3.329

Gaus mean : 44.2832

Gaus Width: 3.2862

10

8

6

4

2

0

0

10

20

30

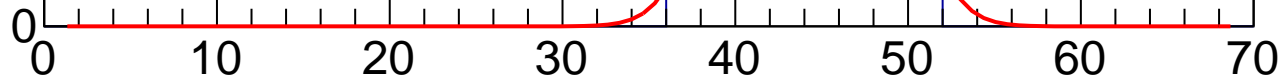
40

50

60

70

ampl

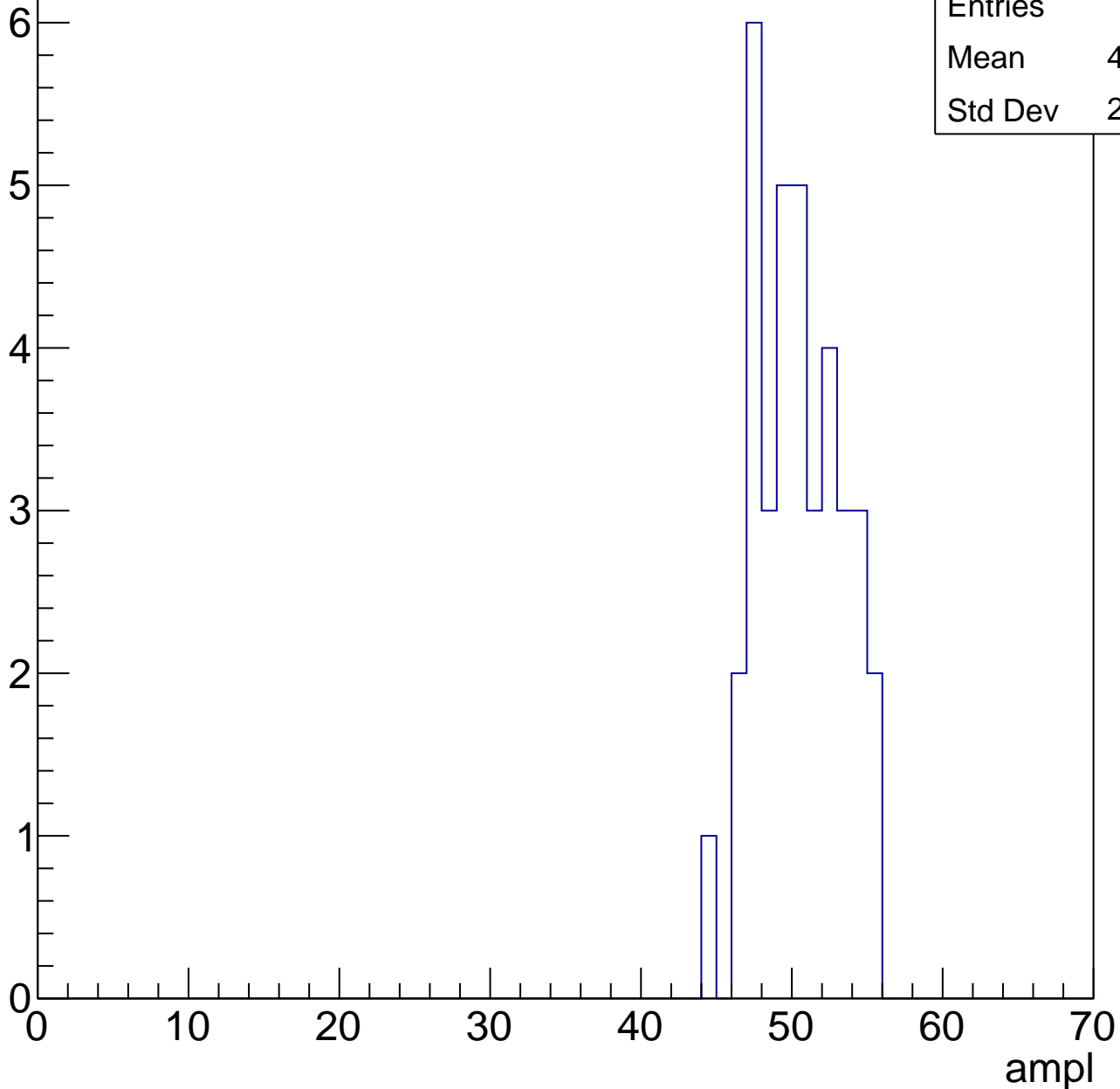


B1L100S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

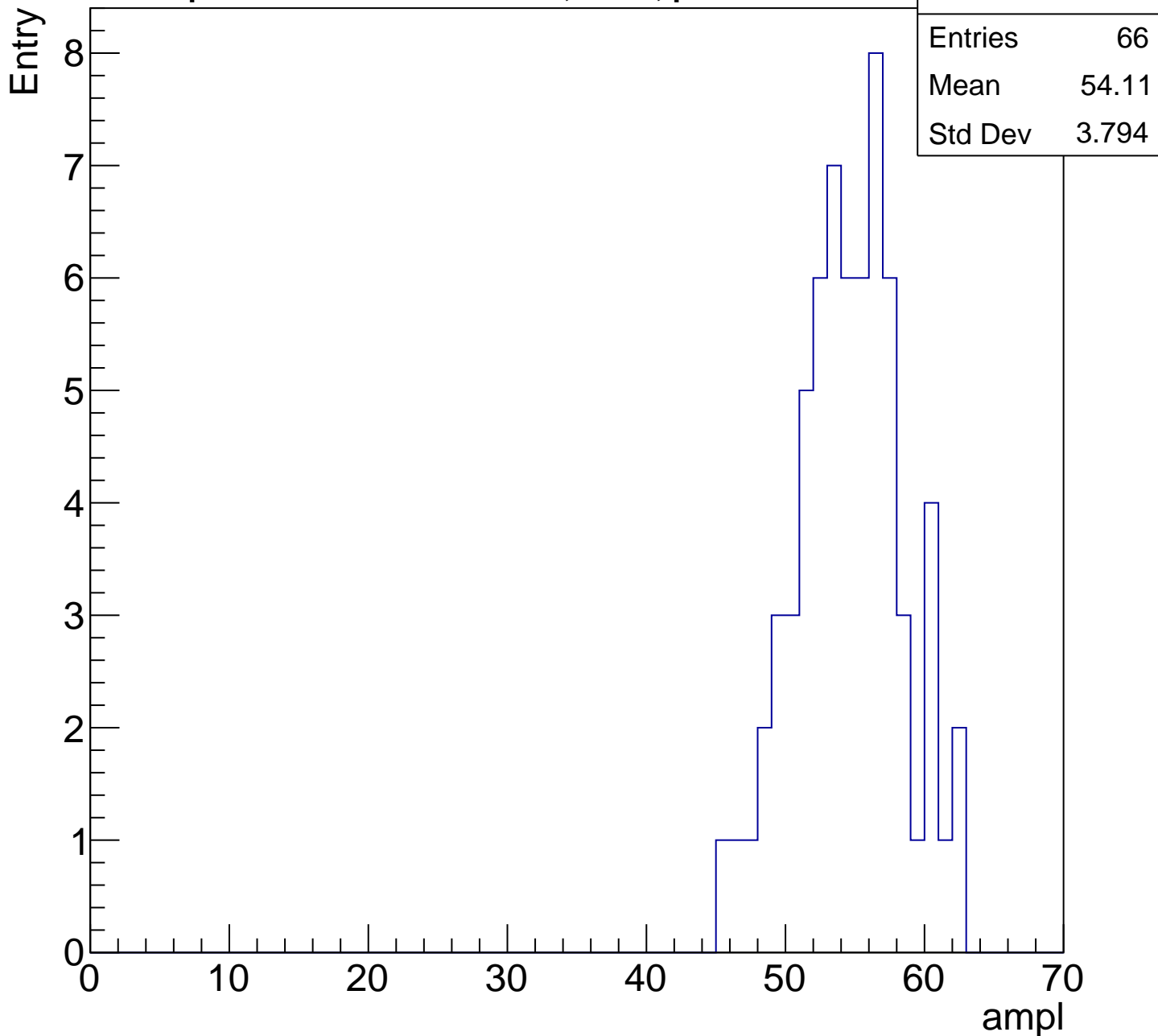
Entry

Entries	37
Mean	49.97
Std Dev	2.765



B1L100S, U14-ch75, adc4

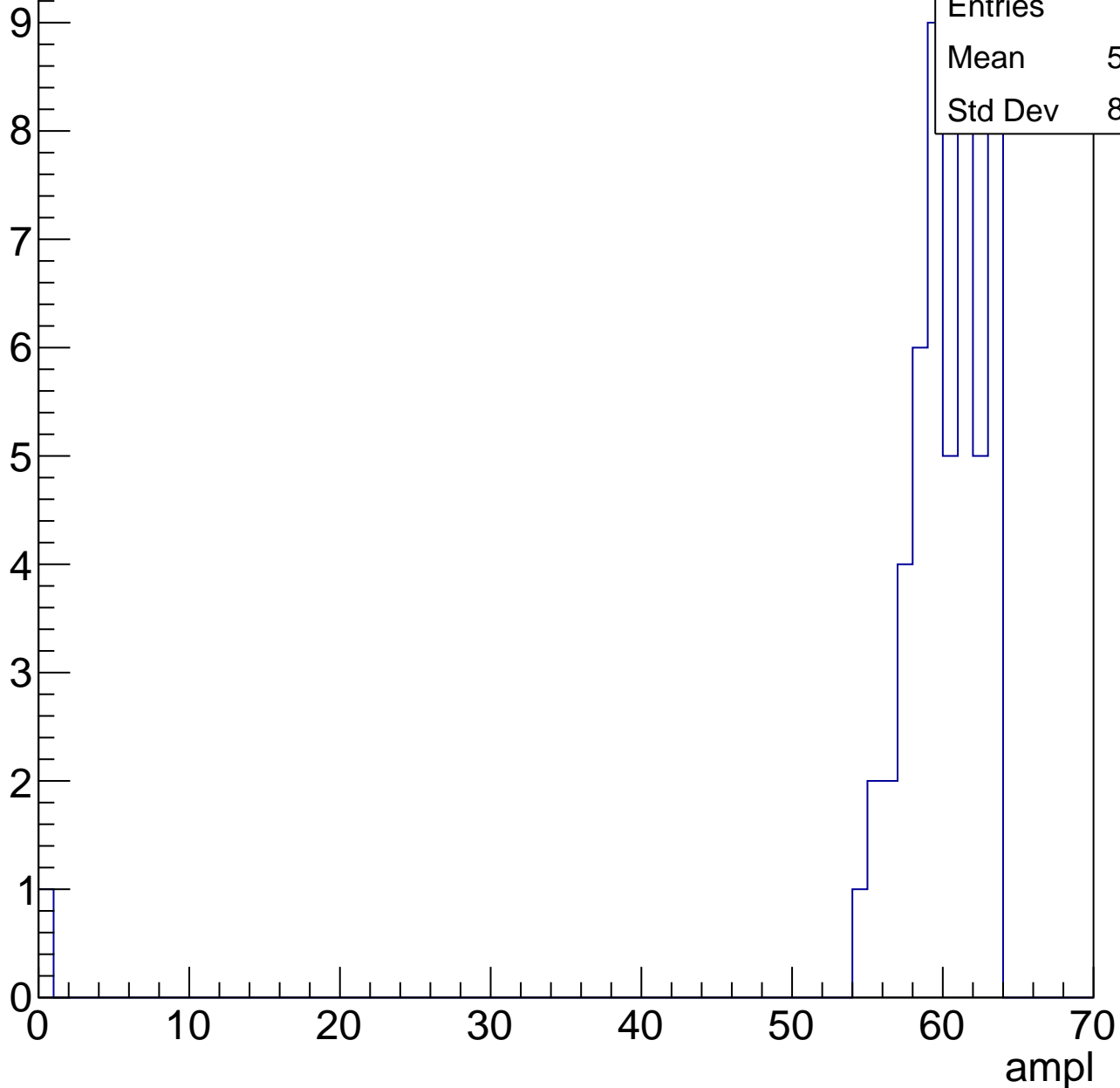
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

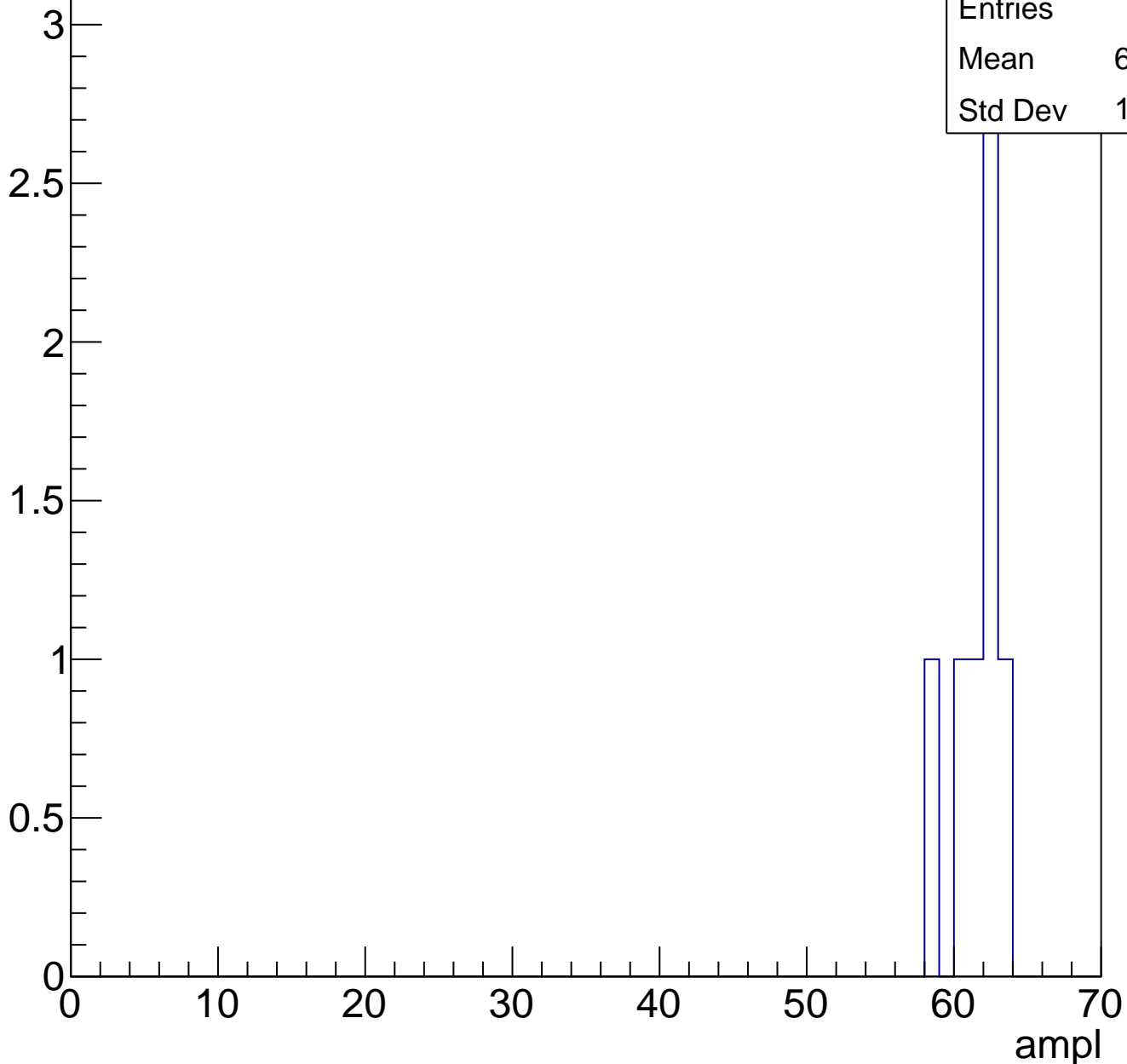
Entry



B1L100S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch76, adc0

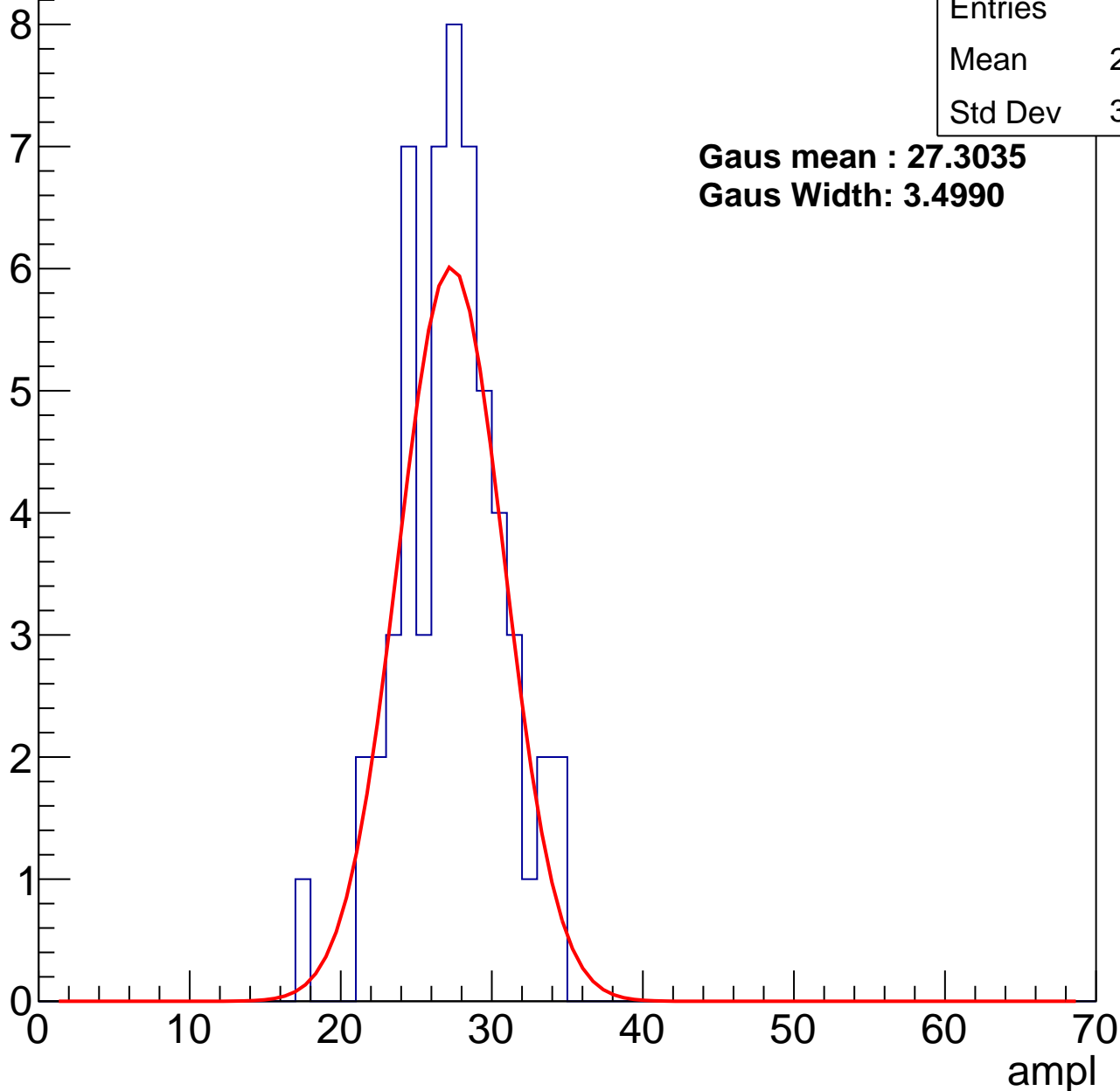
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	26.89
Std Dev	3.396

Gaus mean : 27.3035

Gaus Width: 3.4990



B1L100S, U14-ch76, adc1

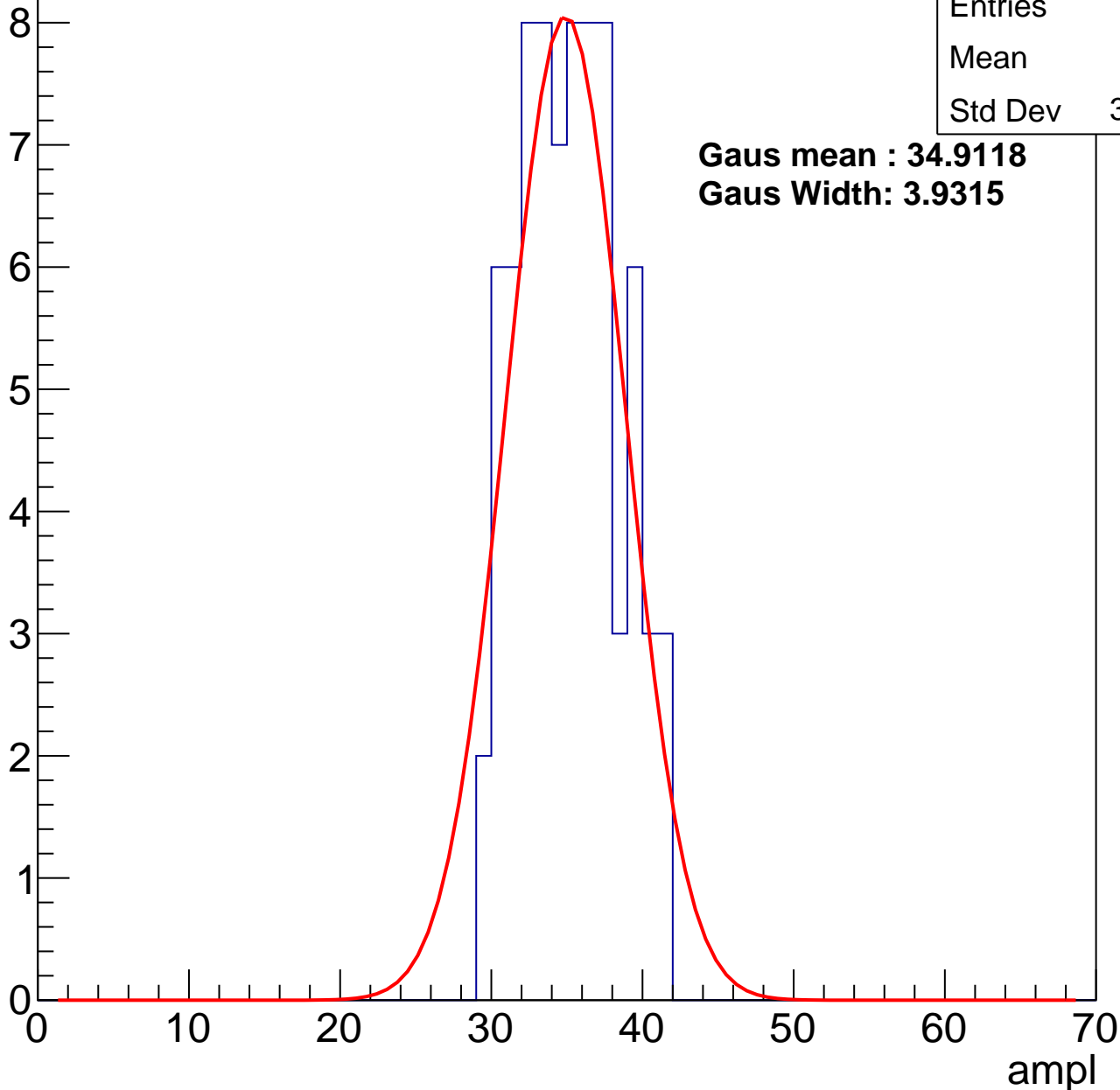
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	34.7
Std Dev	3.179

Gaus mean : 34.9118

Gaus Width: 3.9315



B1L100S, U14-ch76, adc2

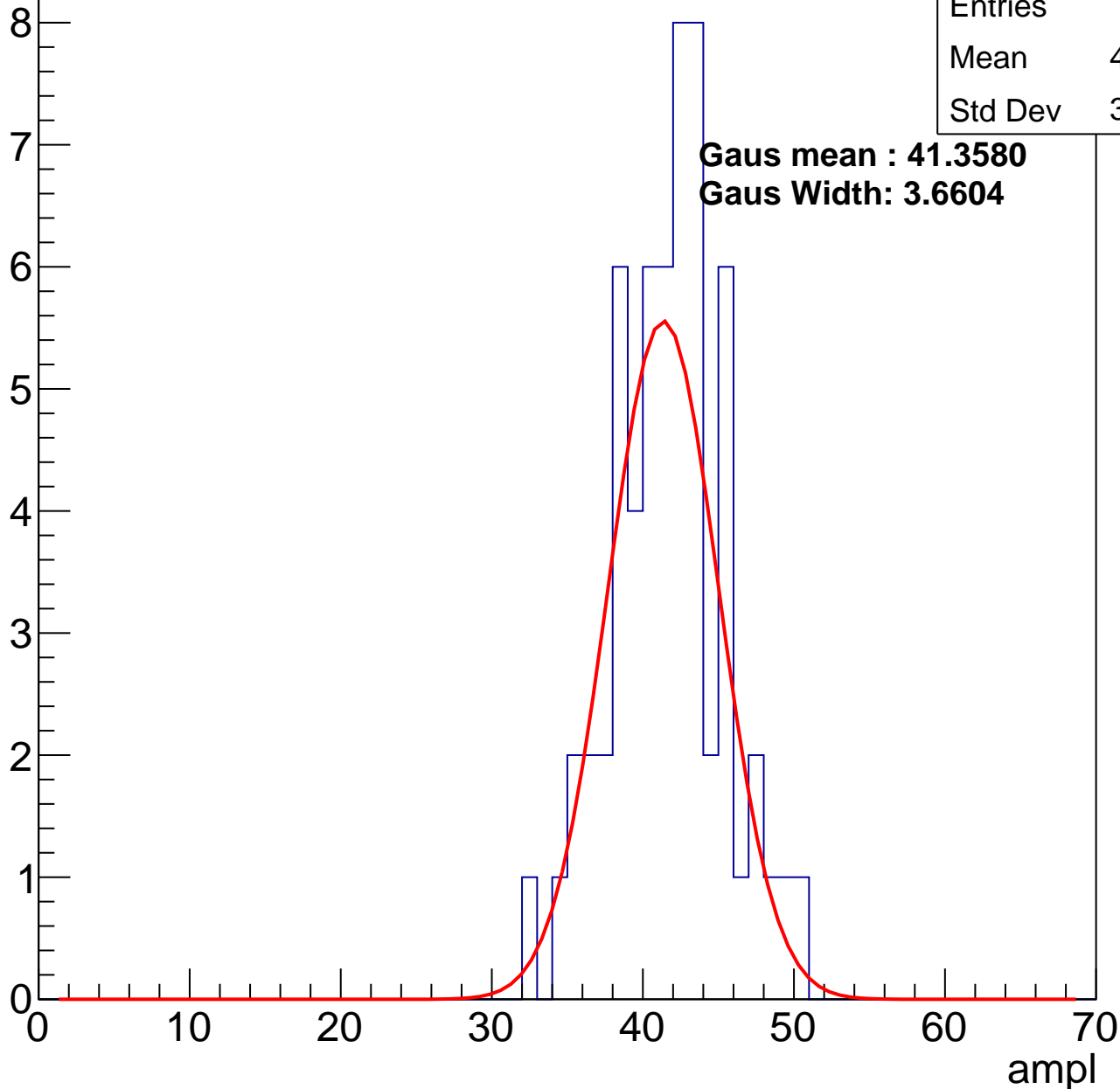
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.28
Std Dev	3.652

Gaus mean : 41.3580

Gaus Width: 3.6604

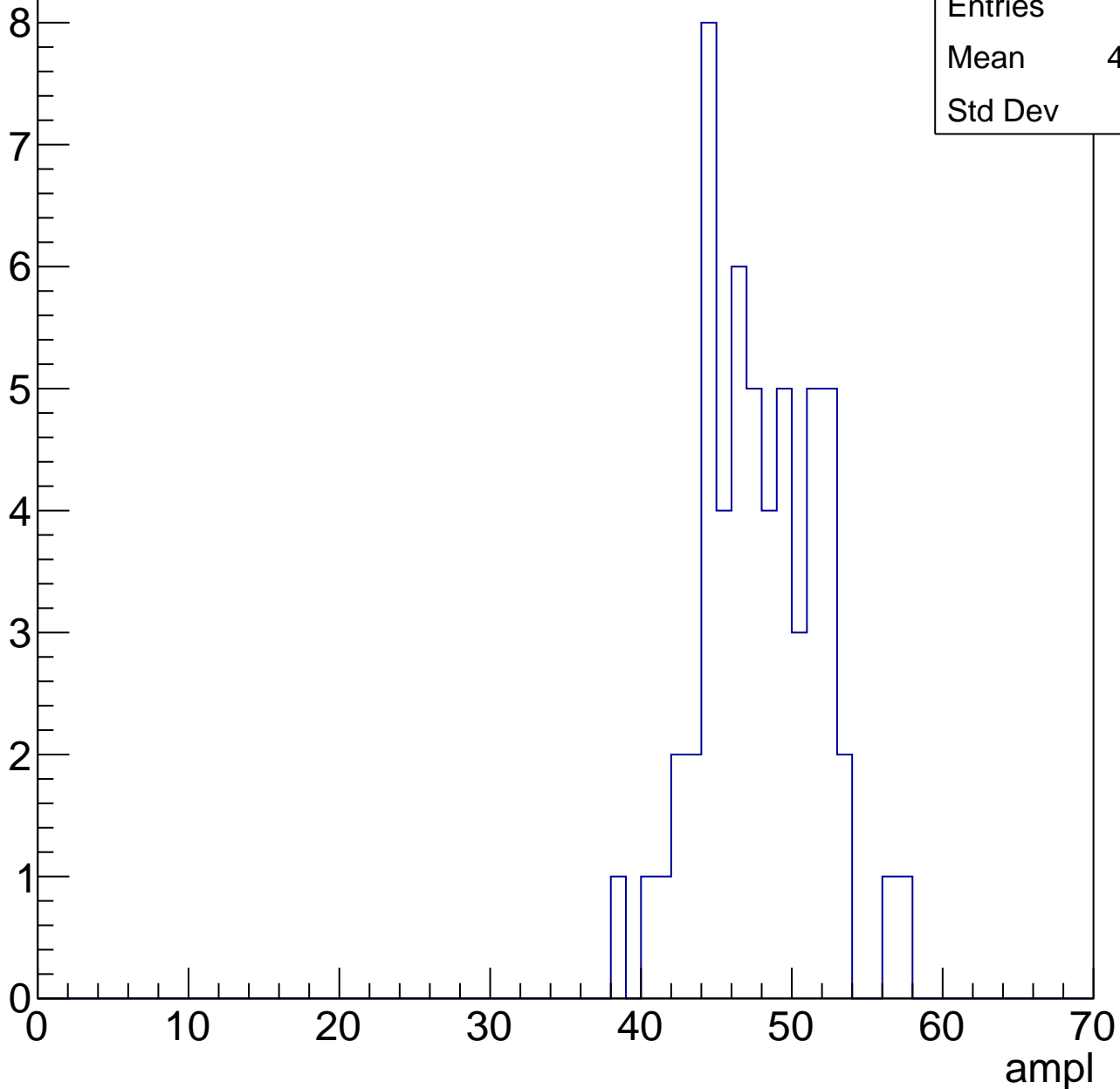


B1L100S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

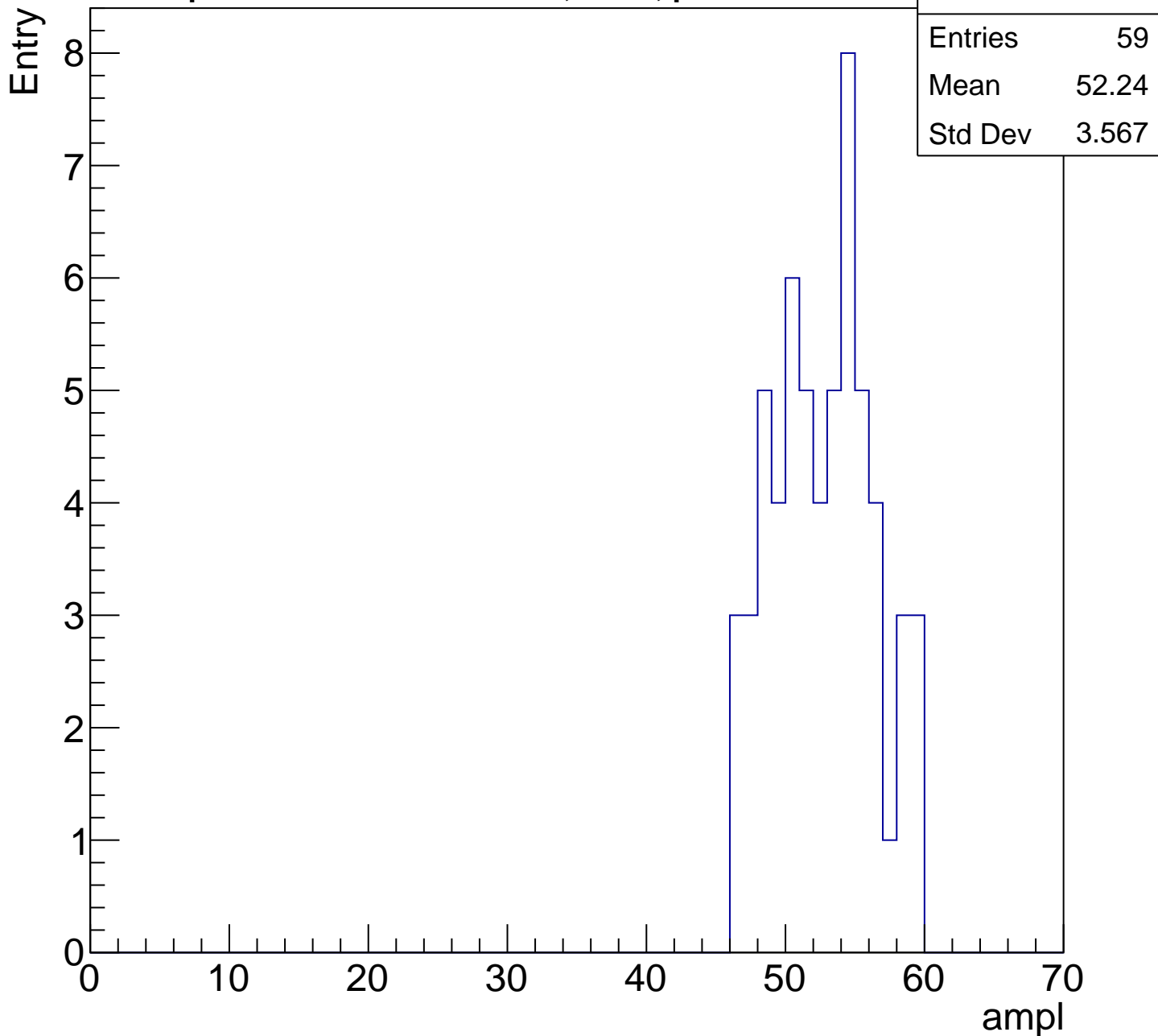
Entry

Entries	56
Mean	47.38
Std Dev	3.88



B1L100S, U14-ch76, adc4

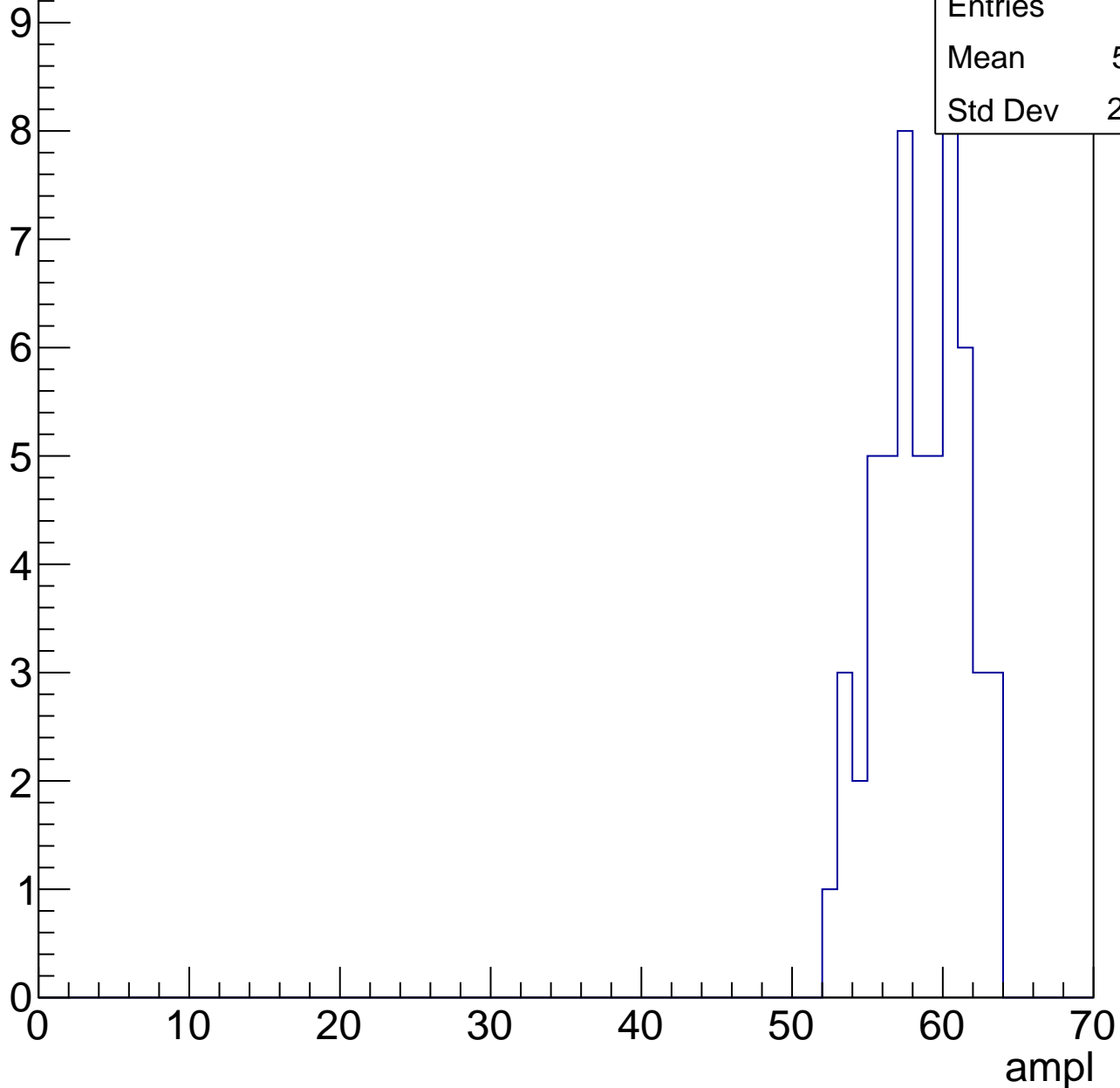
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U14-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



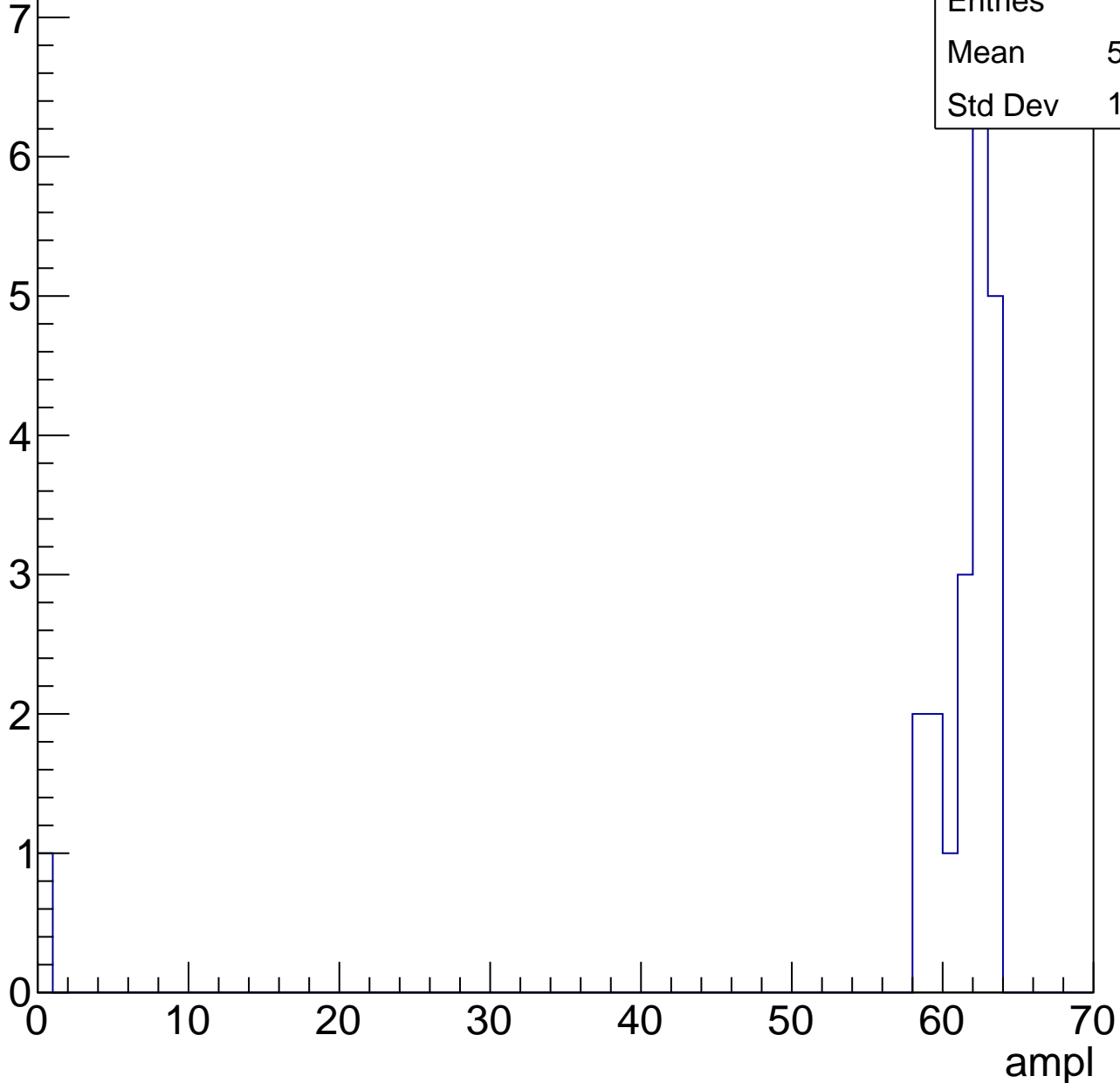
Entries	55
Mean	58.11
Std Dev	2.807

B1L100S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

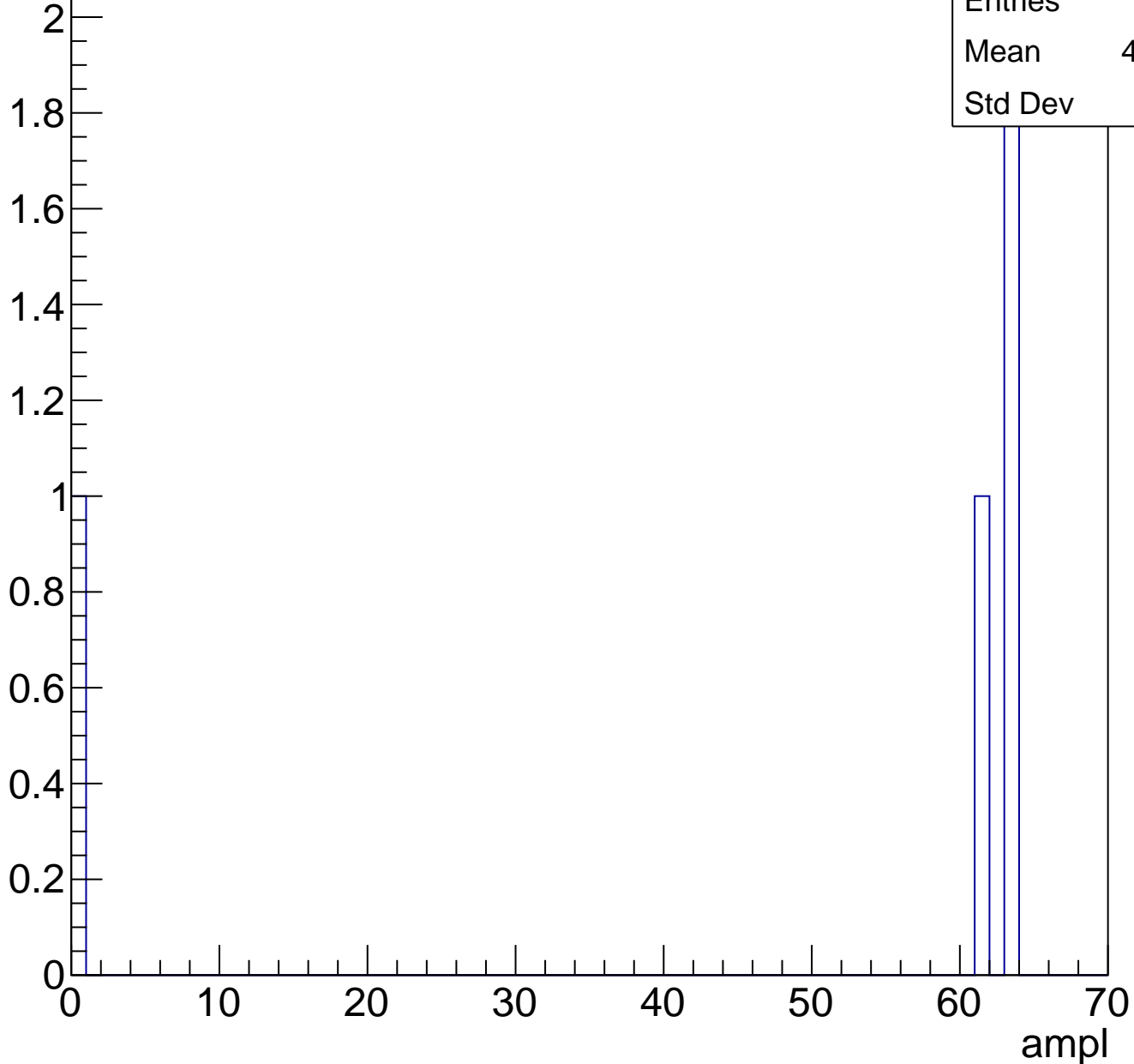
Entries	21
Mean	58.38
Std Dev	13.15



B1L100S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch77, adc0

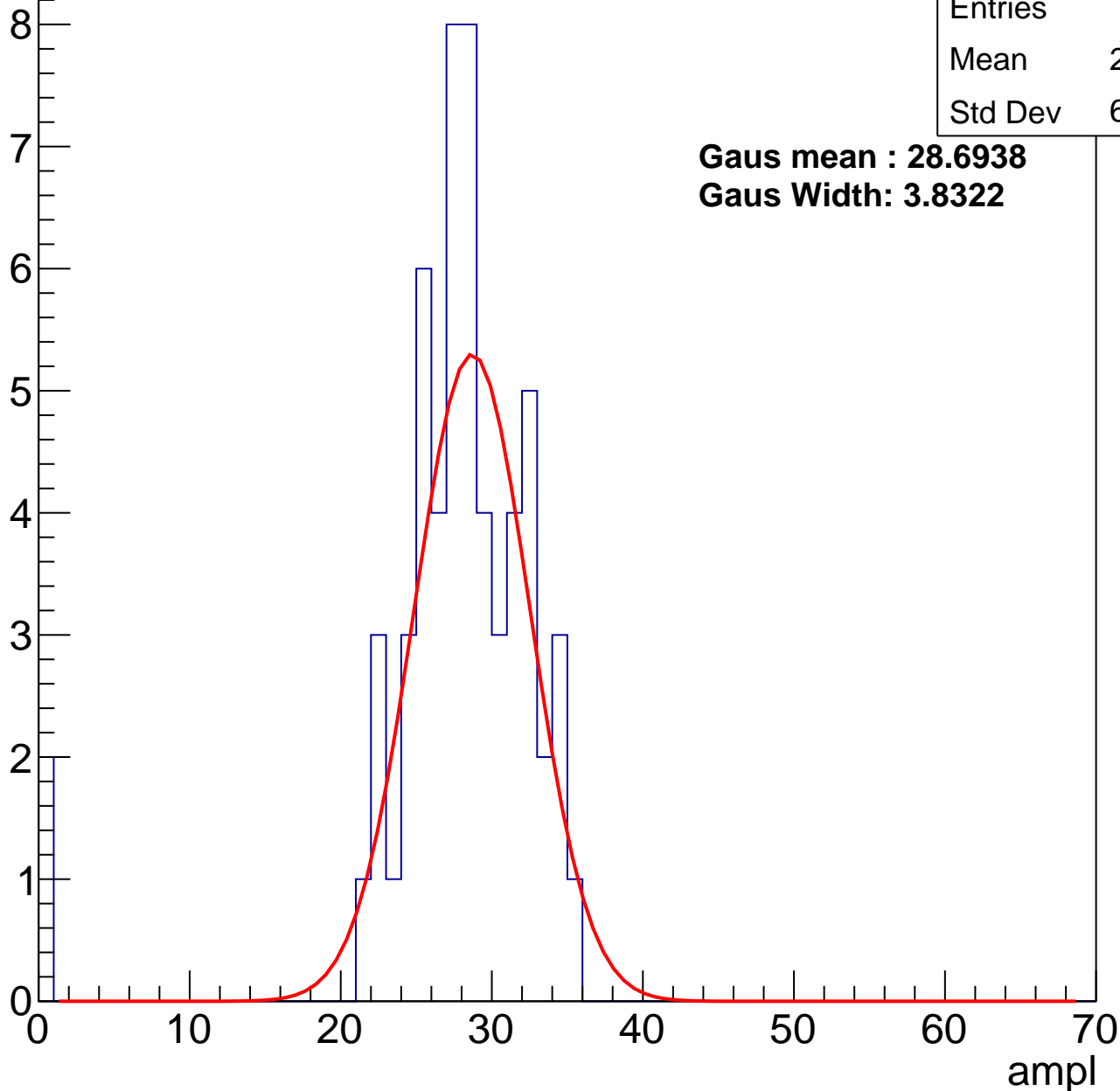
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	27.05
Std Dev	6.107

Gaus mean : 28.6938

Gaus Width: 3.8322



B1L100S, U14-ch77, adc1

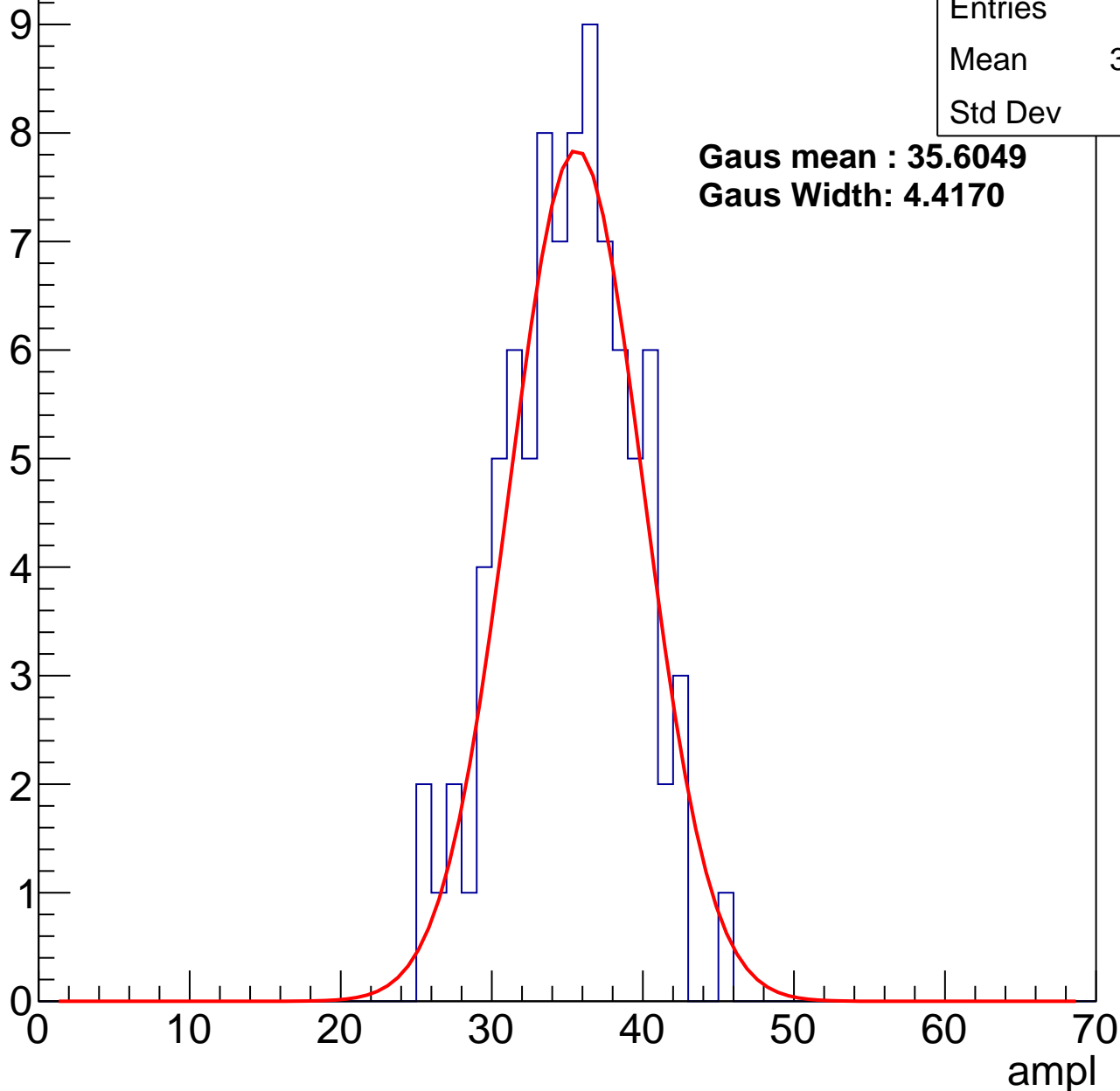
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	34.67
Std Dev	4.18

Gaus mean : 35.6049

Gaus Width: 4.4170



B1L100S, U14-ch77, adc2

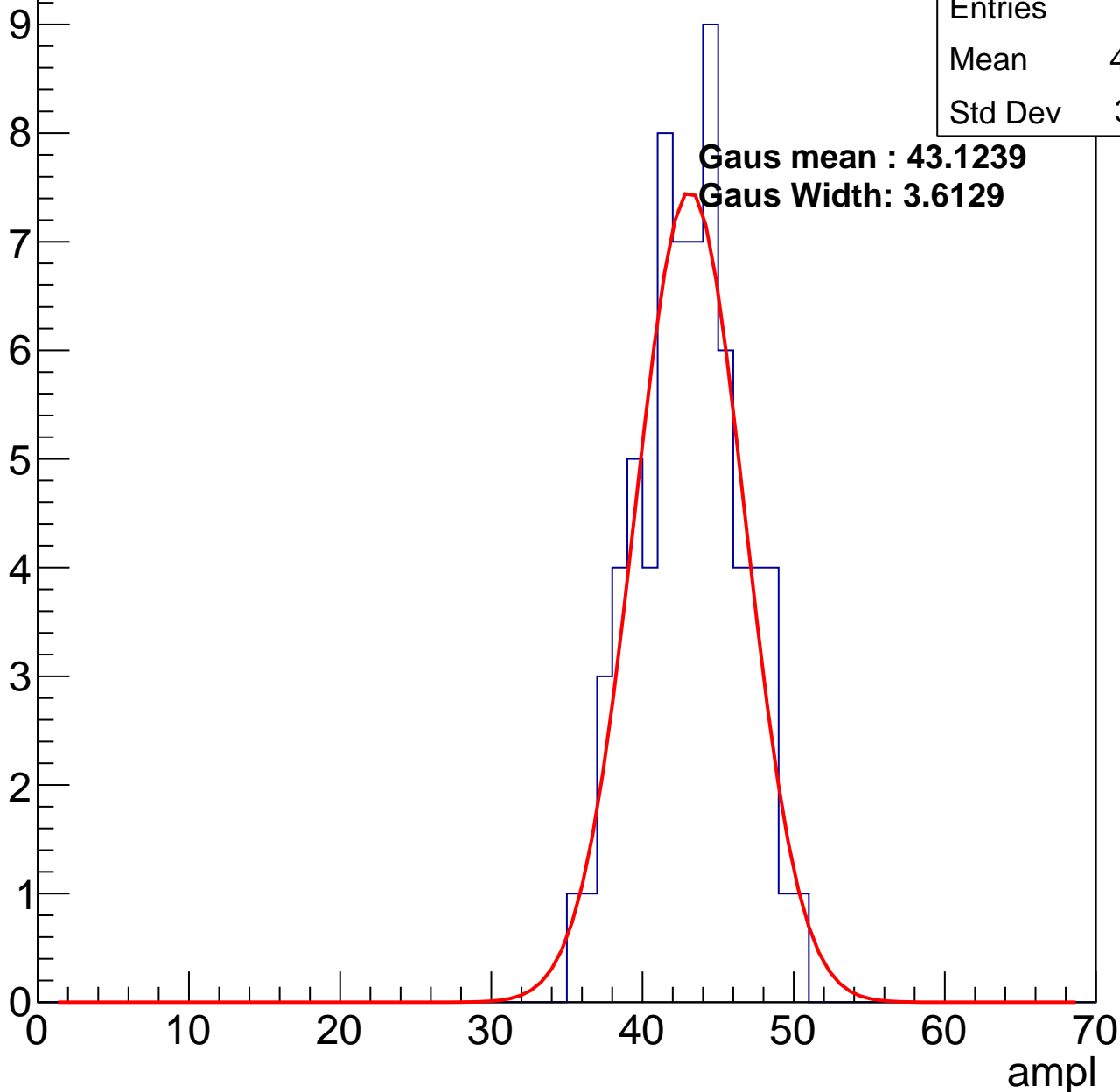
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.62
Std Dev	3.371

Gaus mean : 43.1239

Gaus Width: 3.6129

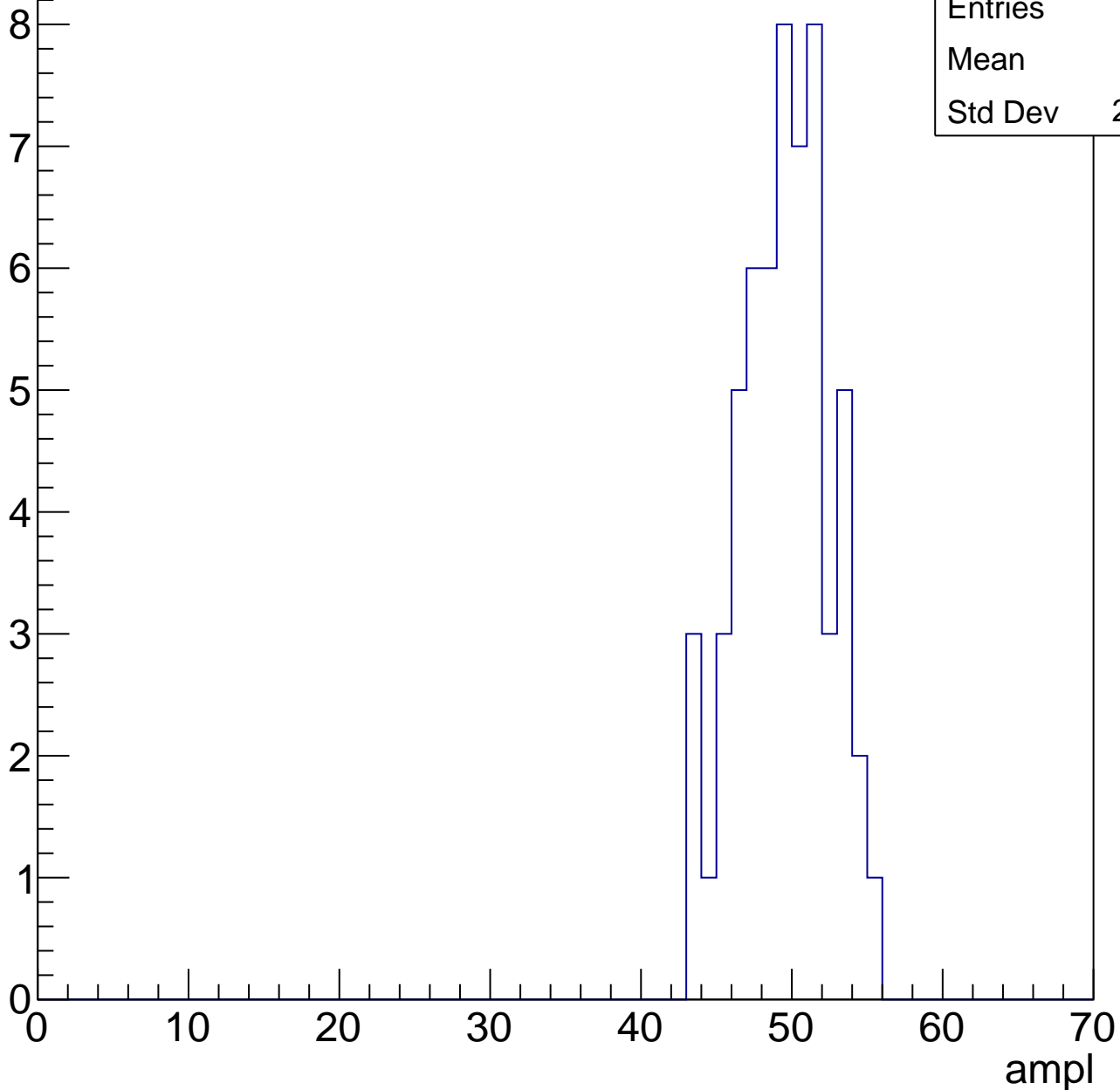


B1L100S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49
Std Dev	2.901

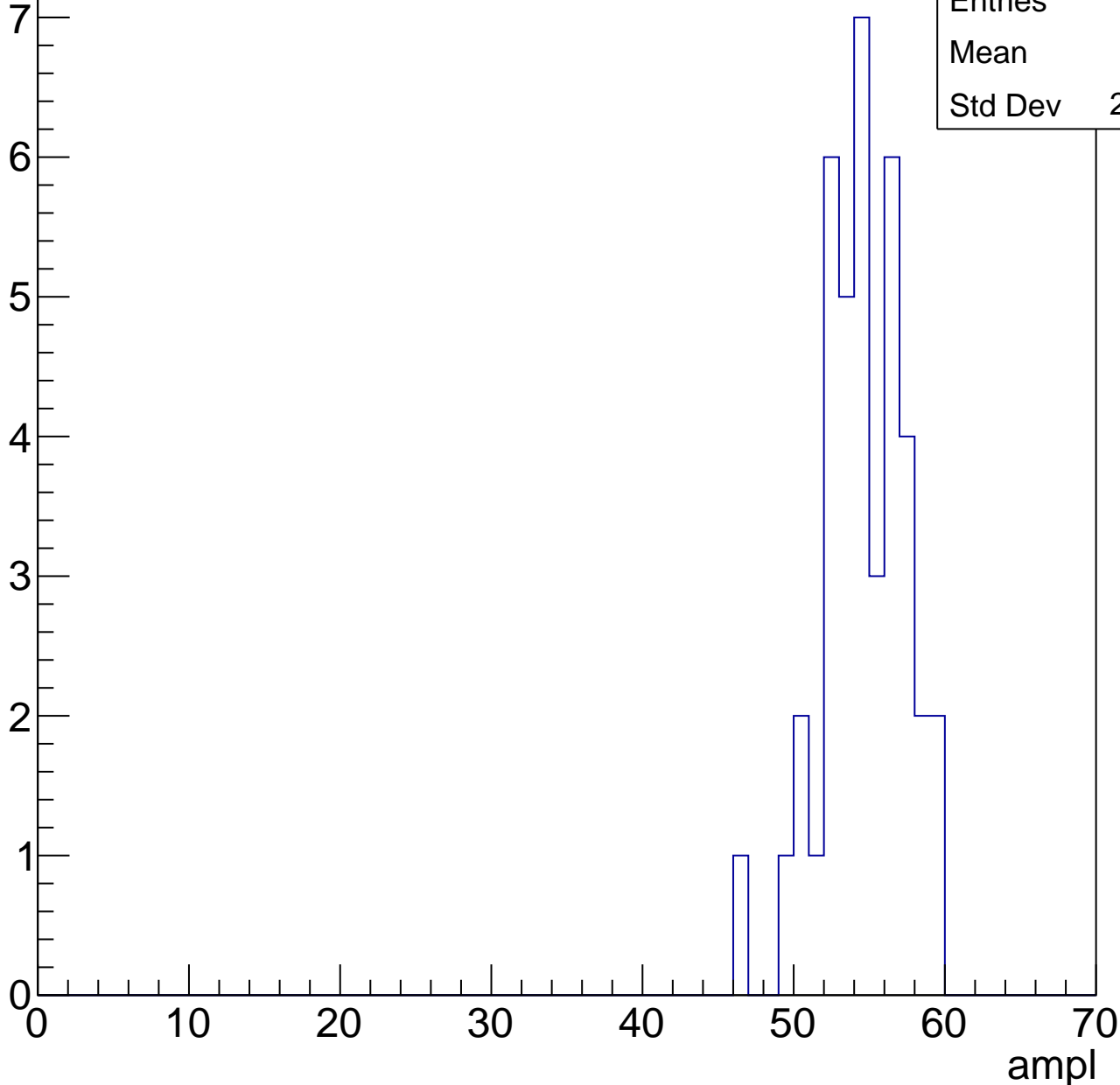


B1L100S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	54.1
Std Dev	2.755



B1L100S, U14-ch77, adc5

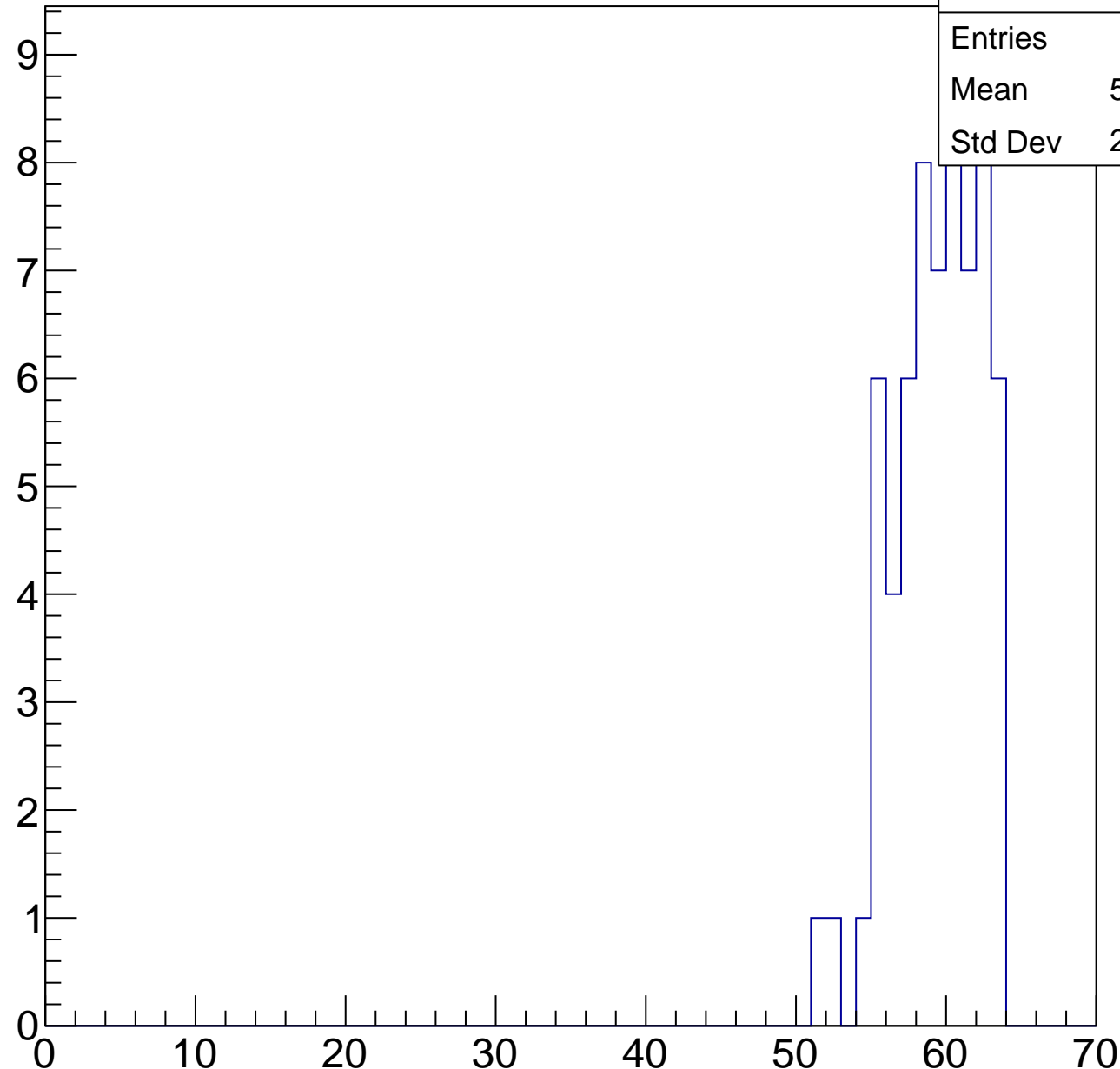
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	58.97
Std Dev	2.817

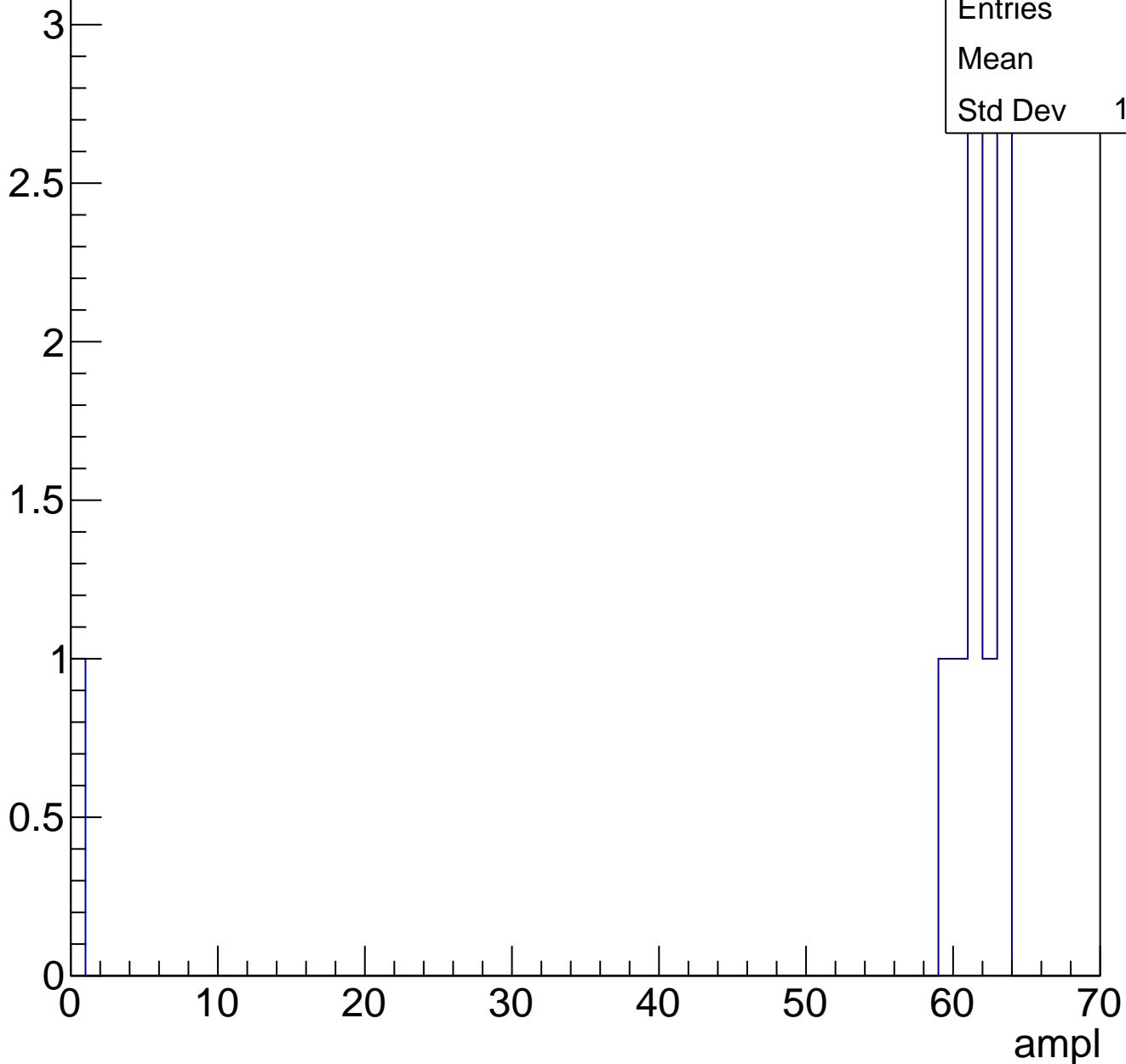
ampl



B1L100S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch78, adc0

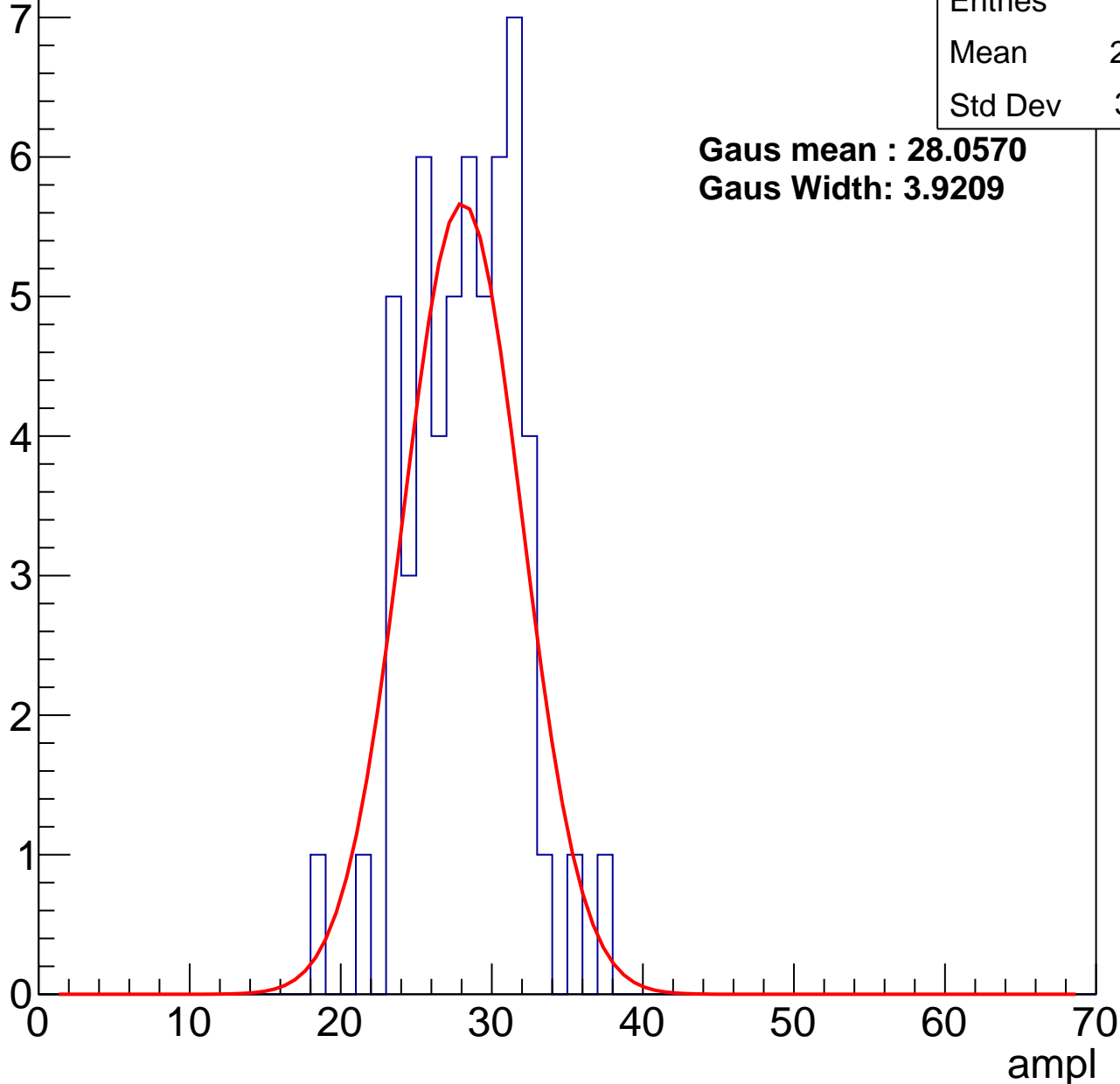
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	27.82
Std Dev	3.551

Gaus mean : 28.0570

Gaus Width: 3.9209



B1L100S, U14-ch78, adc1

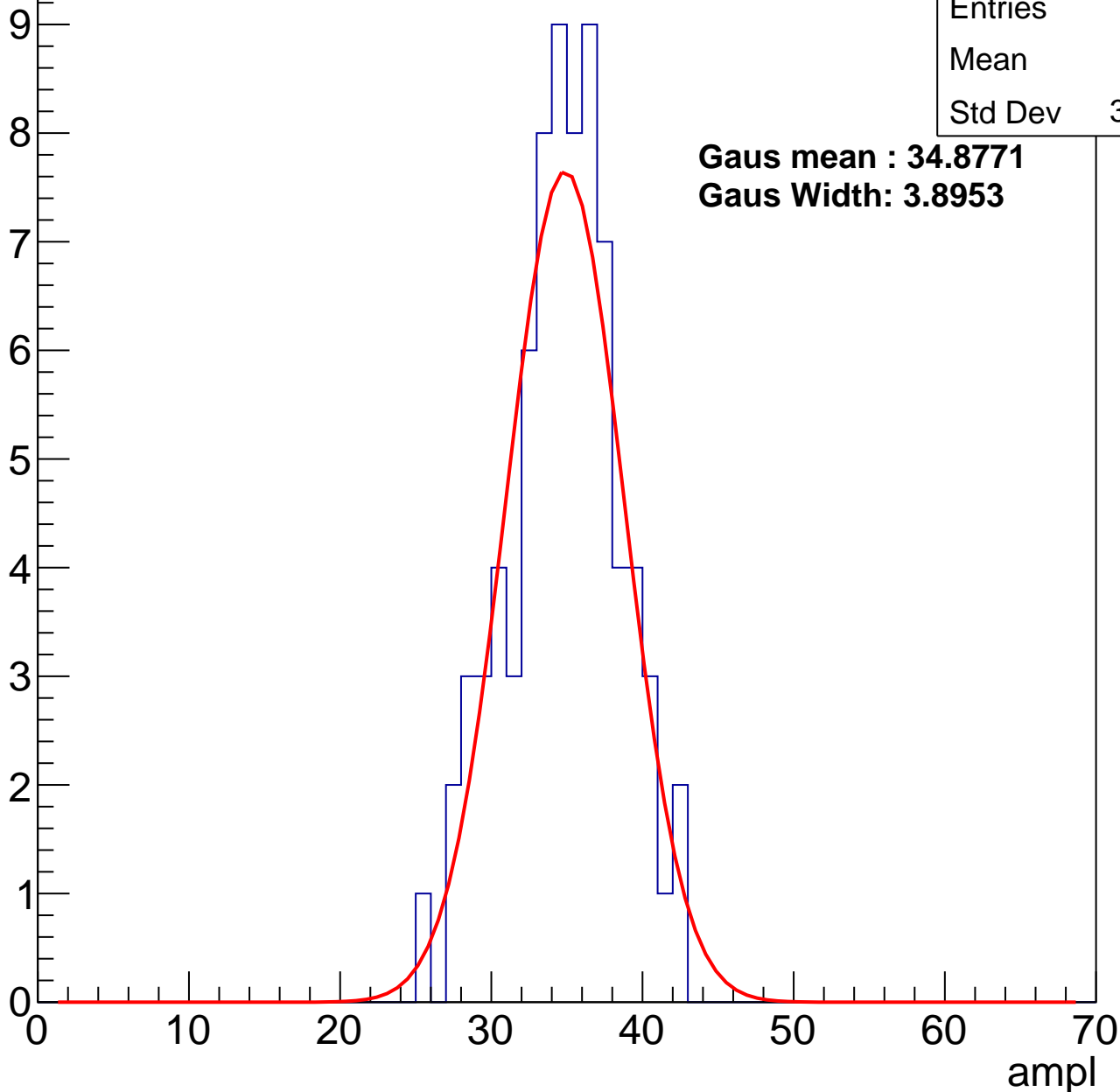
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	34.3
Std Dev	3.665

Gaus mean : 34.8771

Gaus Width: 3.8953



B1L100S, U14-ch78, adc2

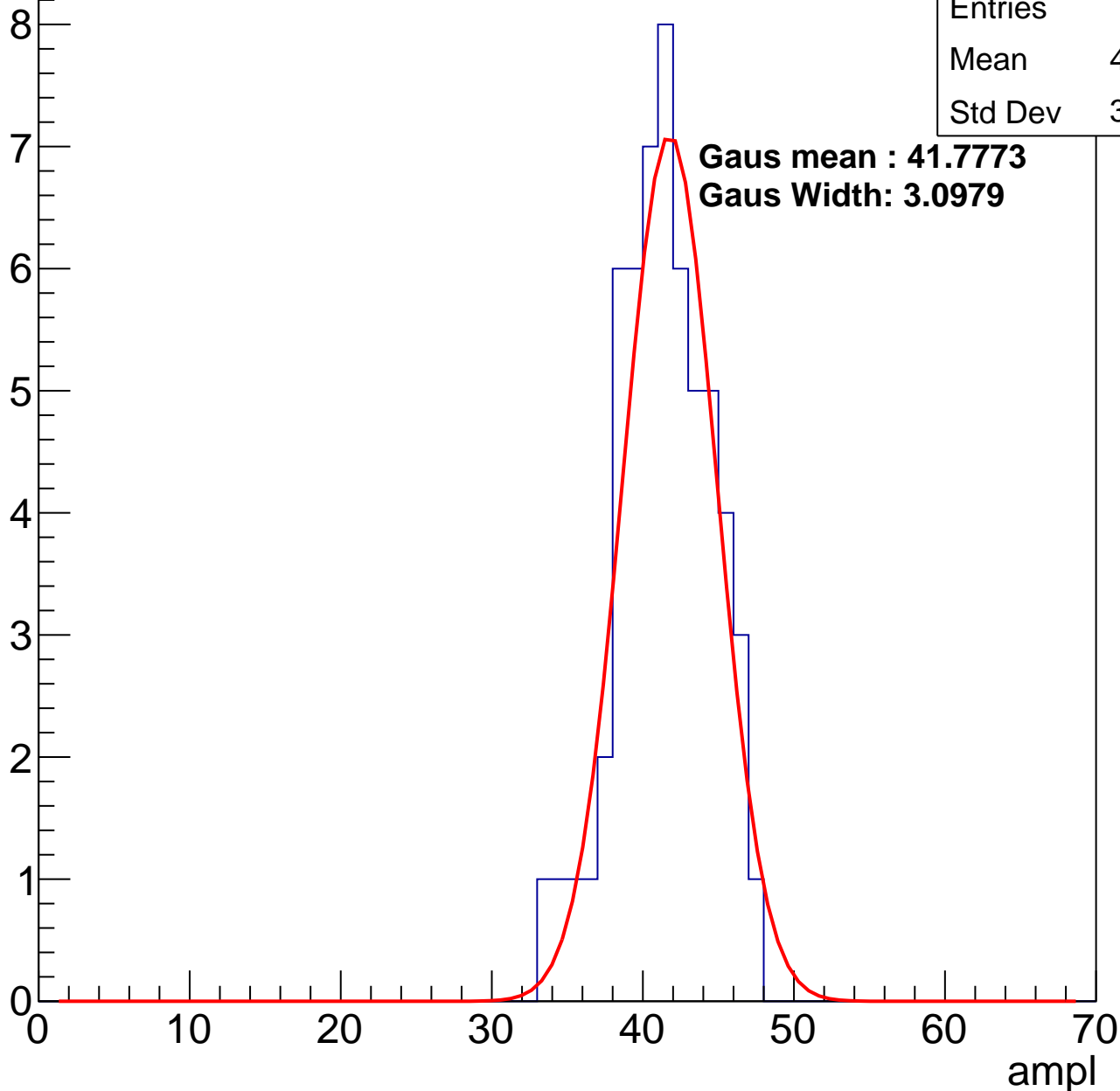
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	40.95
Std Dev	3.069

Gaus mean : 41.7773

Gaus Width: 3.0979



B1L100S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

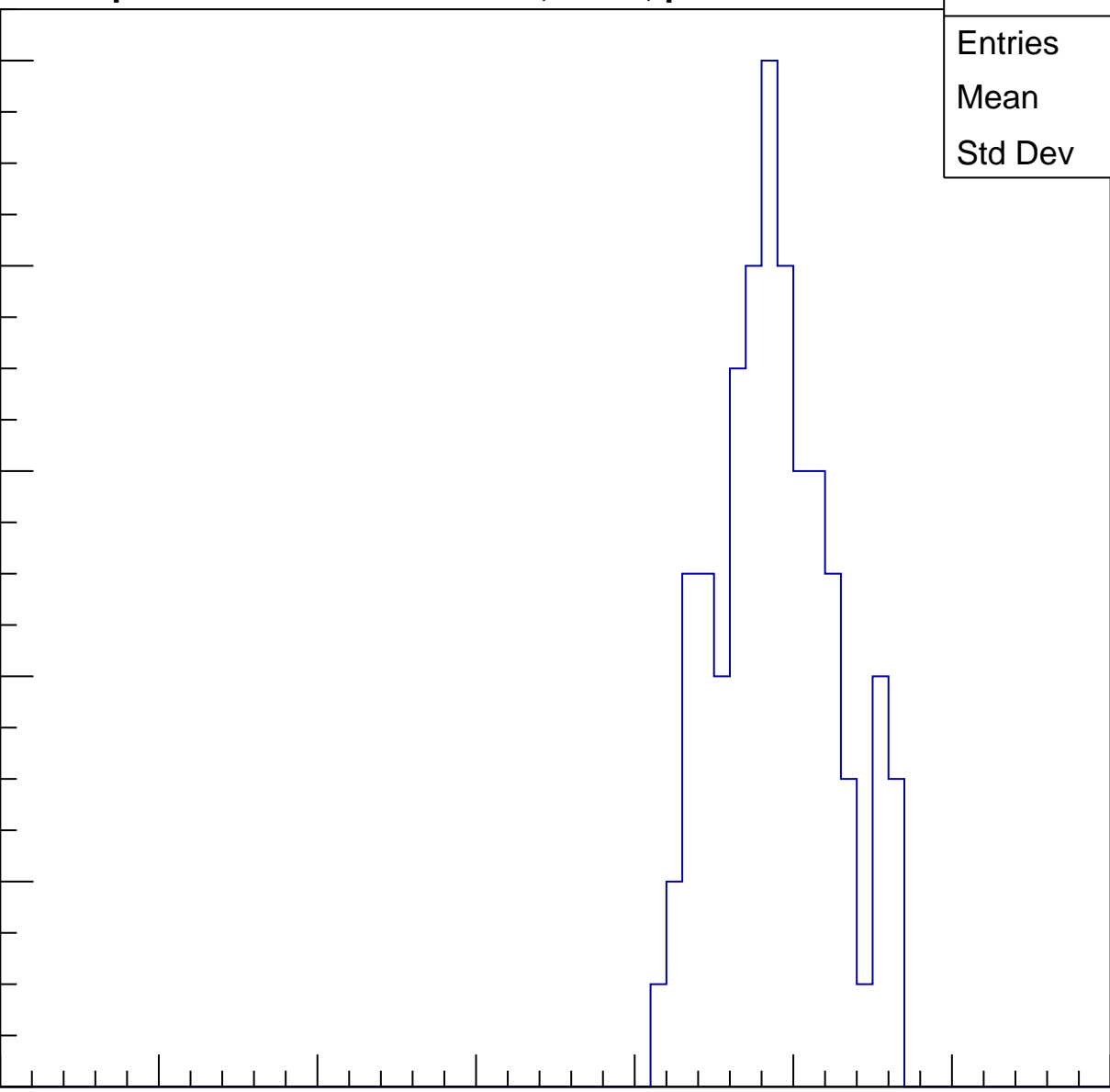
Entries	78
Mean	48.42
Std Dev	3.667

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

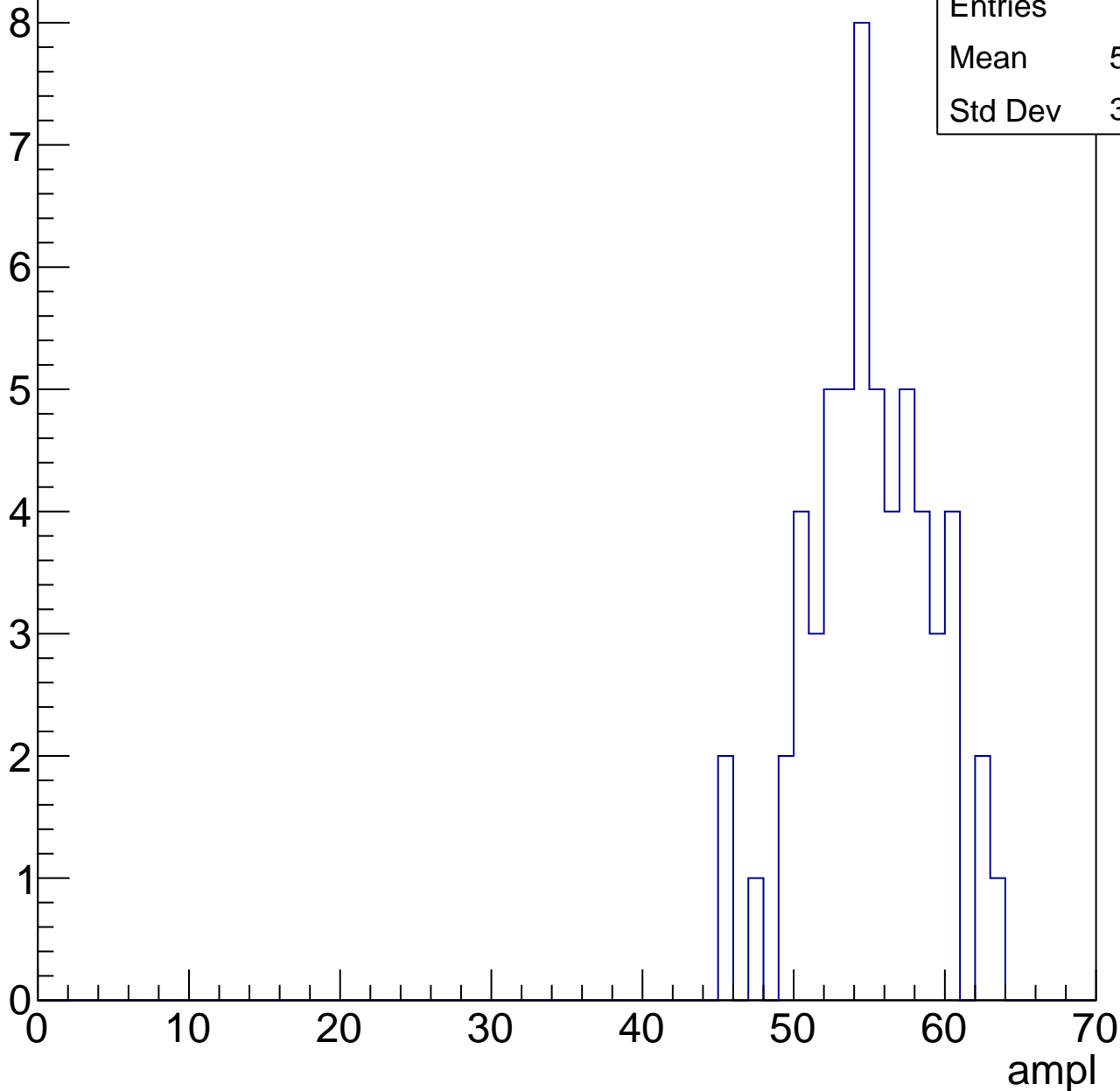


B1L100S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.57
Std Dev	3.975

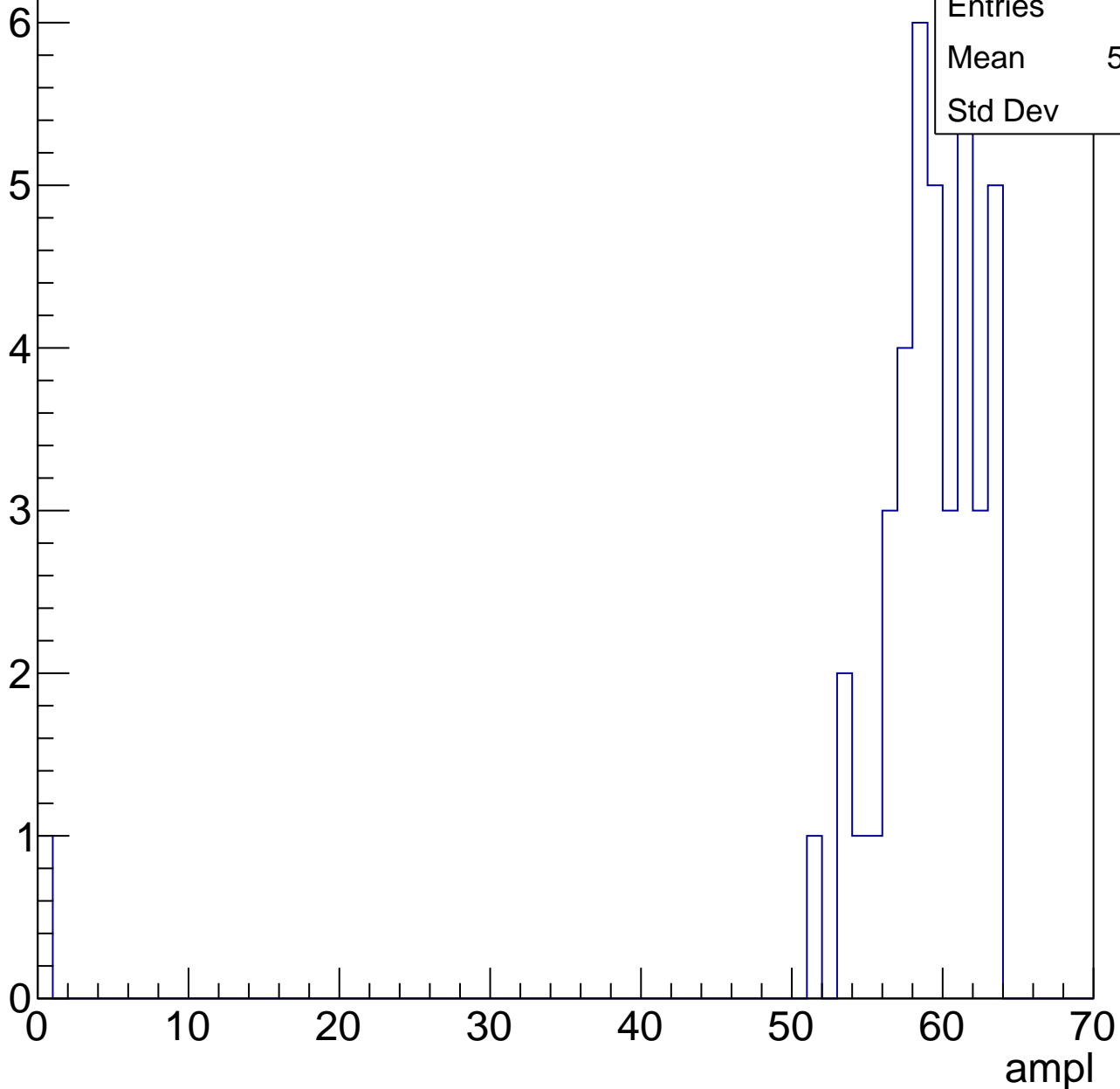


B1L100S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	57.37
Std Dev	9.54

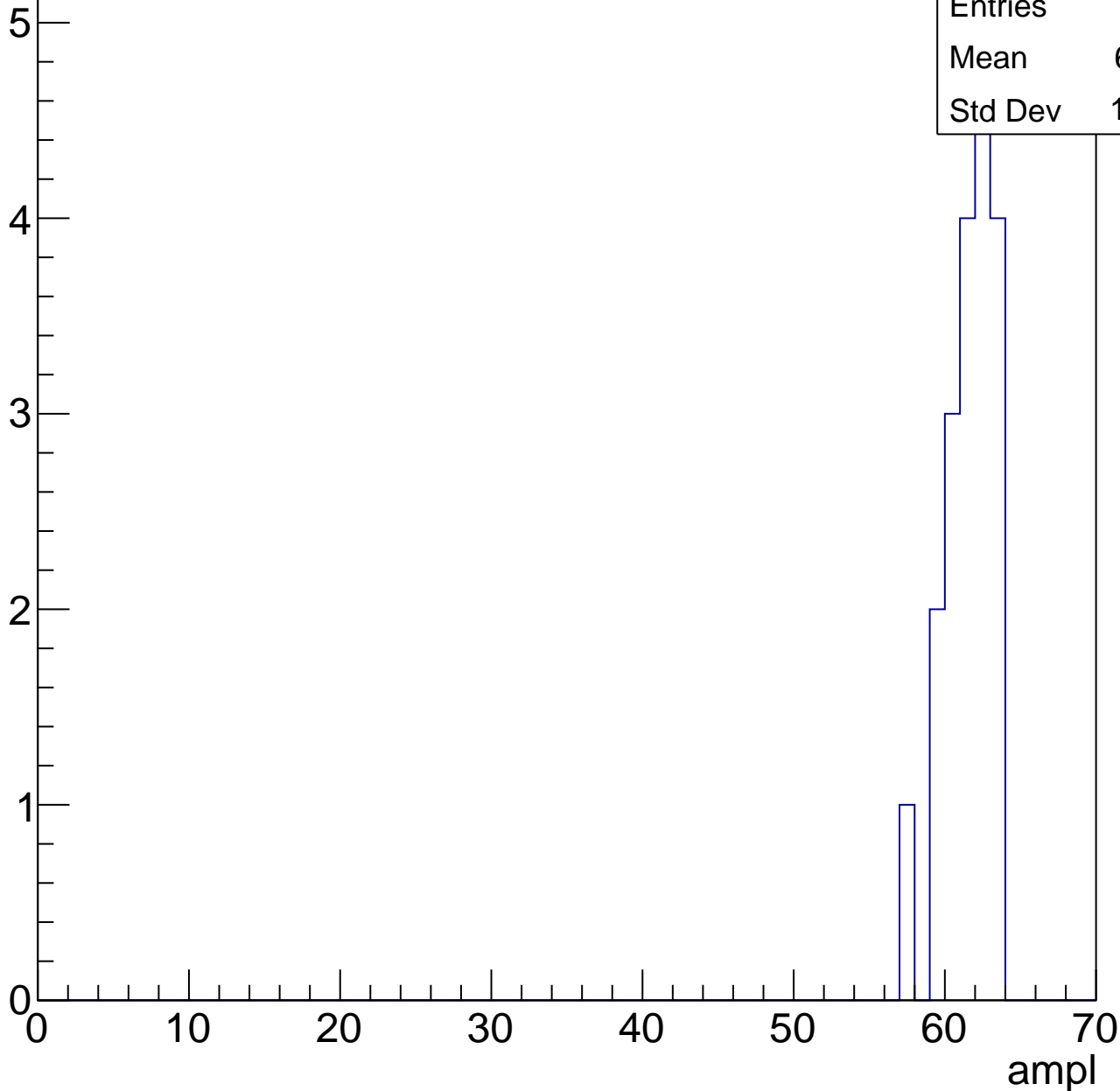


B1L100S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	61.11
Std Dev	1.586



B1L100S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch79, adc0

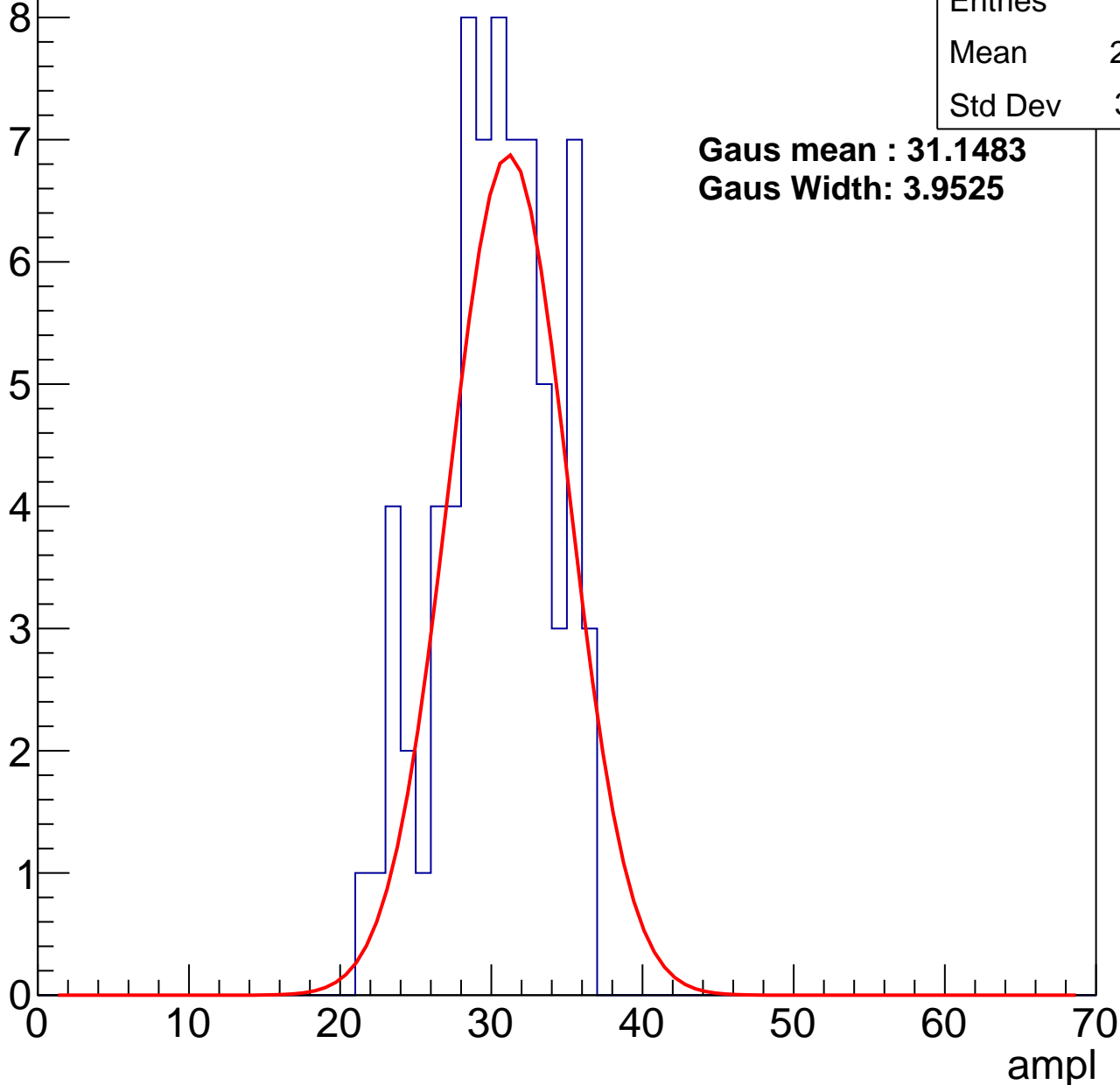
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.83
Std Dev	3.701

Gaus mean : 31.1483

Gaus Width: 3.9525



B1L100S, U14-ch79, adc1

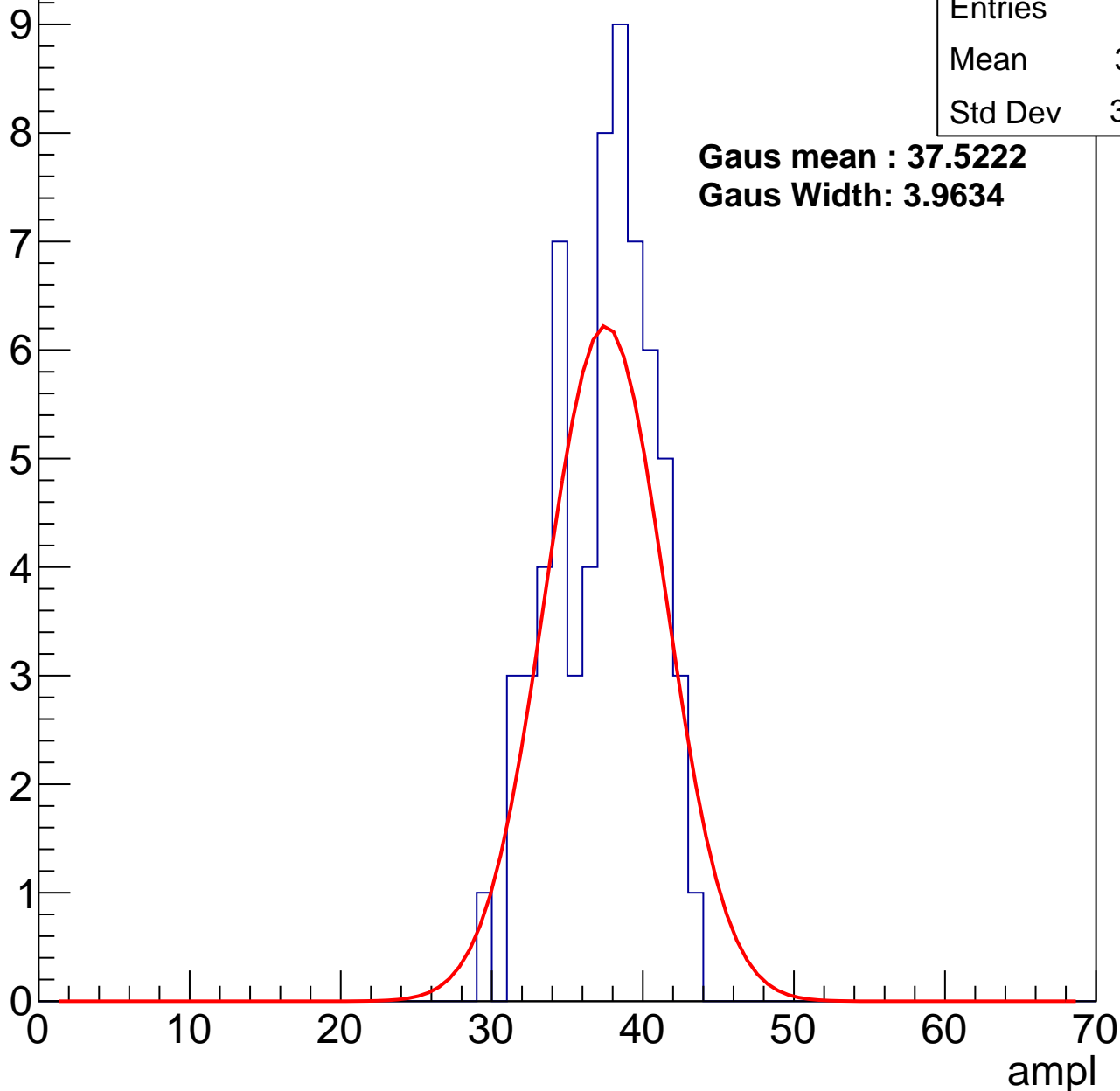
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.91
Std Dev	3.239

Gaus mean : 37.5222

Gaus Width: 3.9634



B1L100S, U14-ch79, adc2

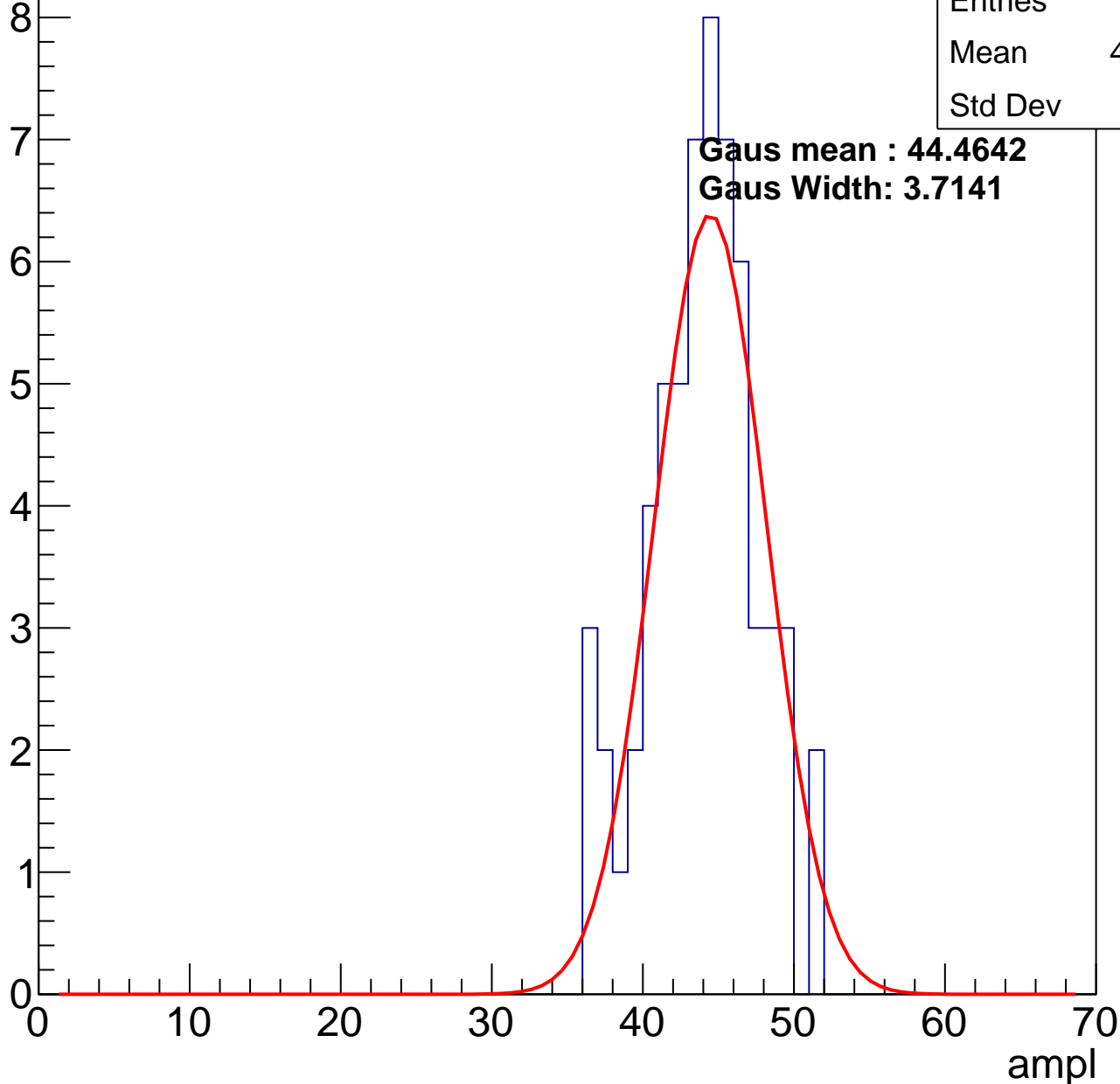
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.46
Std Dev	3.56

Gaus mean : 44.4642

Gaus Width: 3.7141

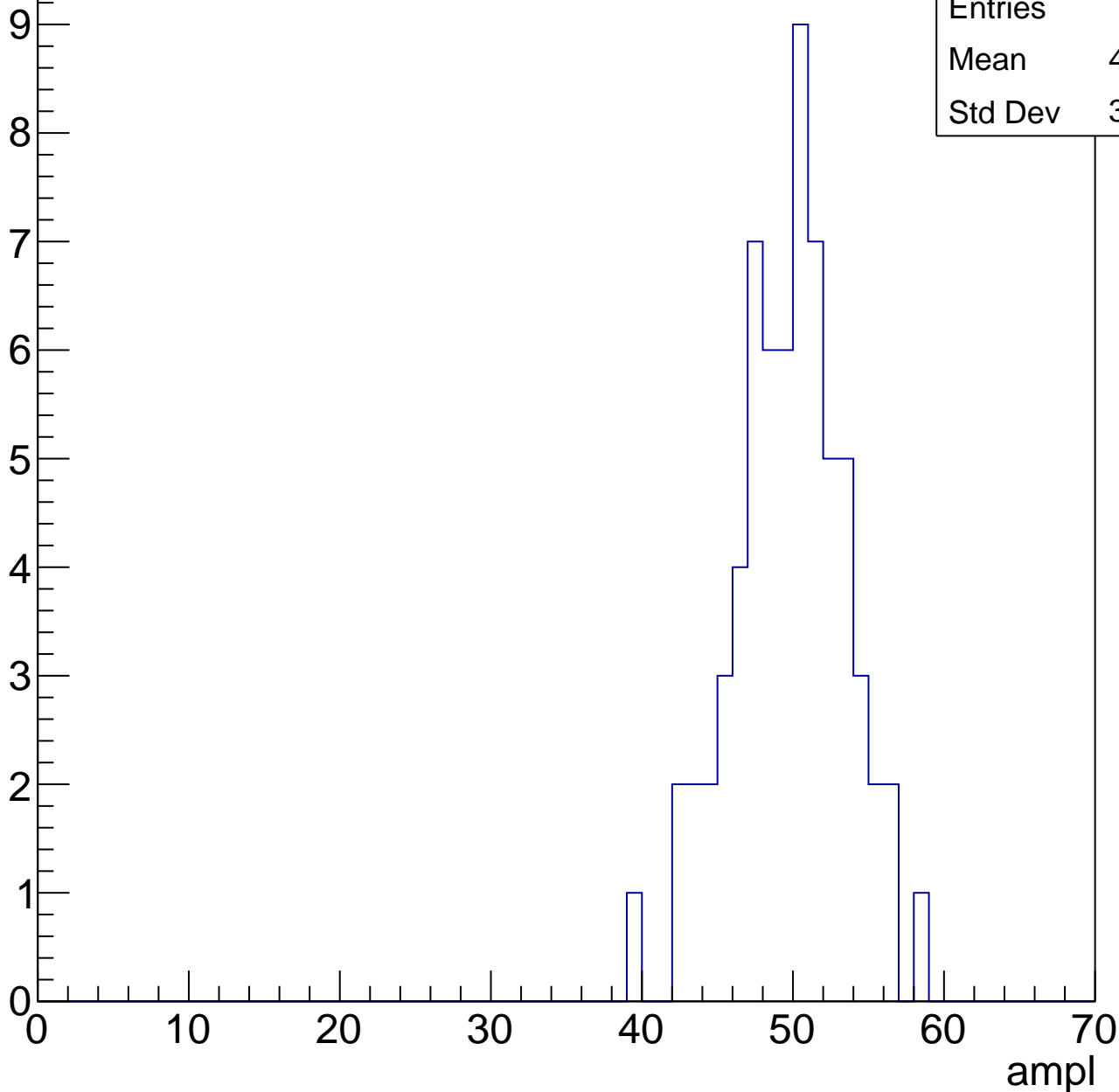


B1L100S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

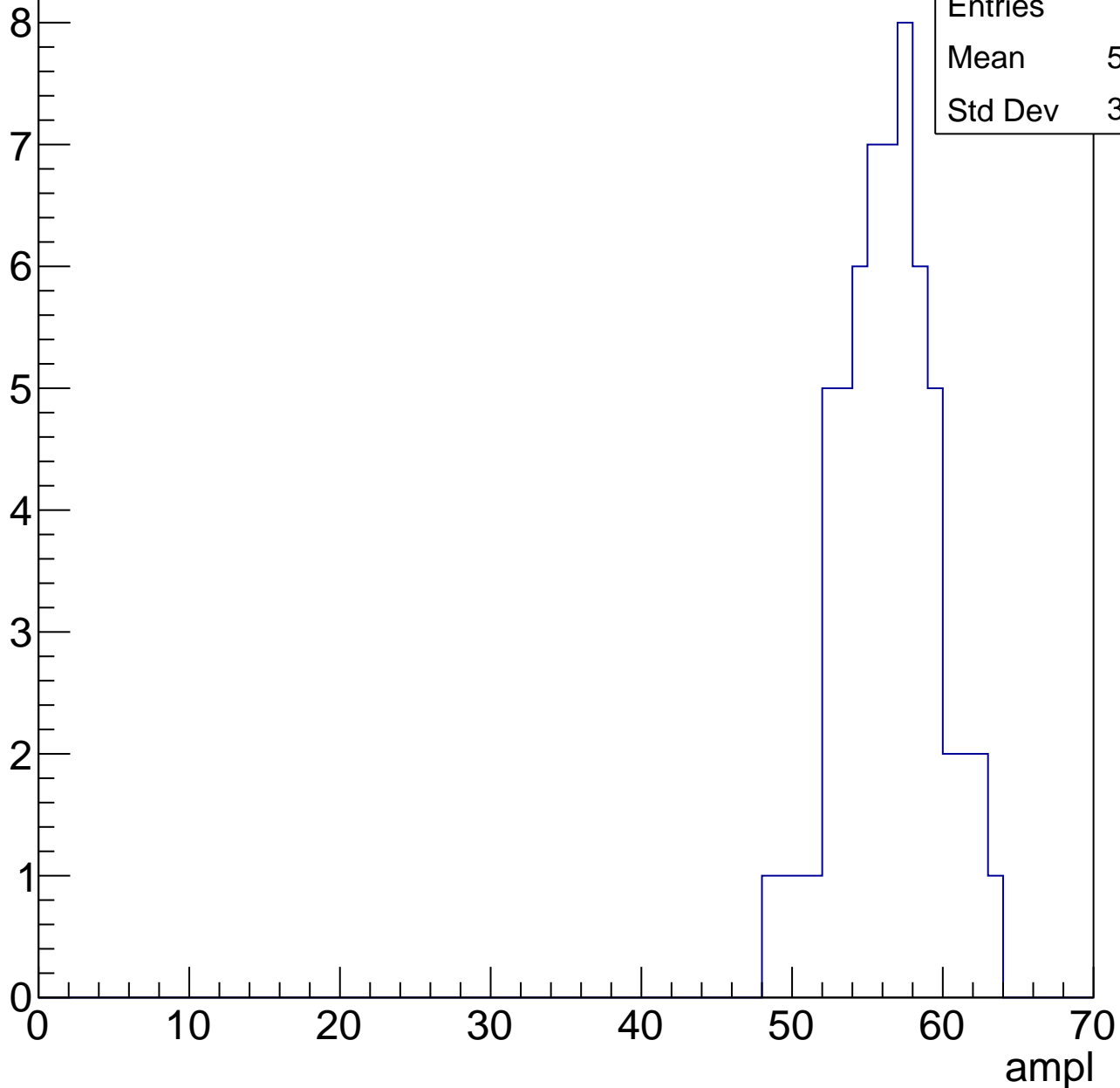
Entries	67
Mean	49.27
Std Dev	3.708



B1L100S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

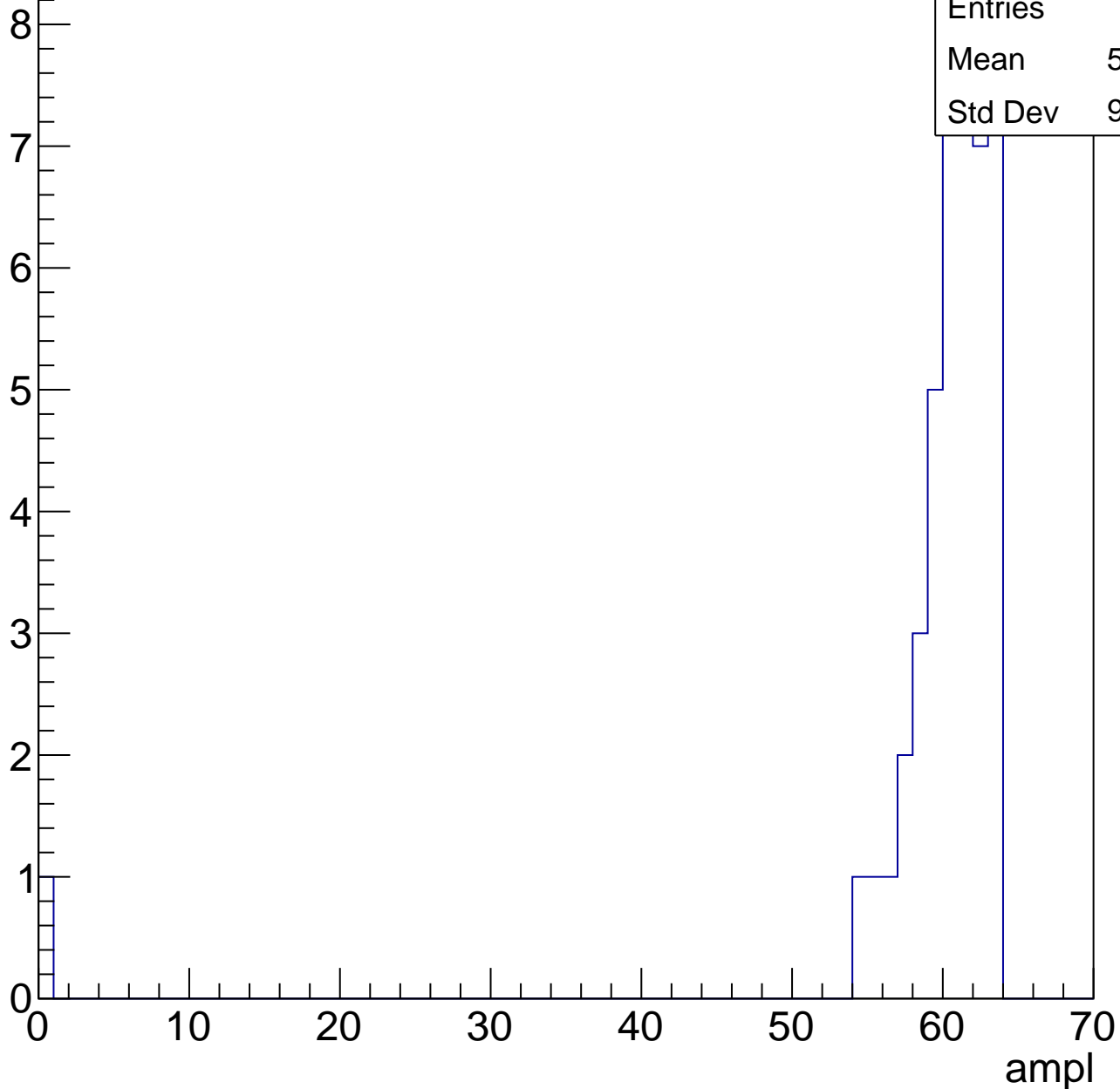
Entry



B1L100S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	45
Mean	58.98
Std Dev	9.157

B1L100S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U14-ch80, adc0

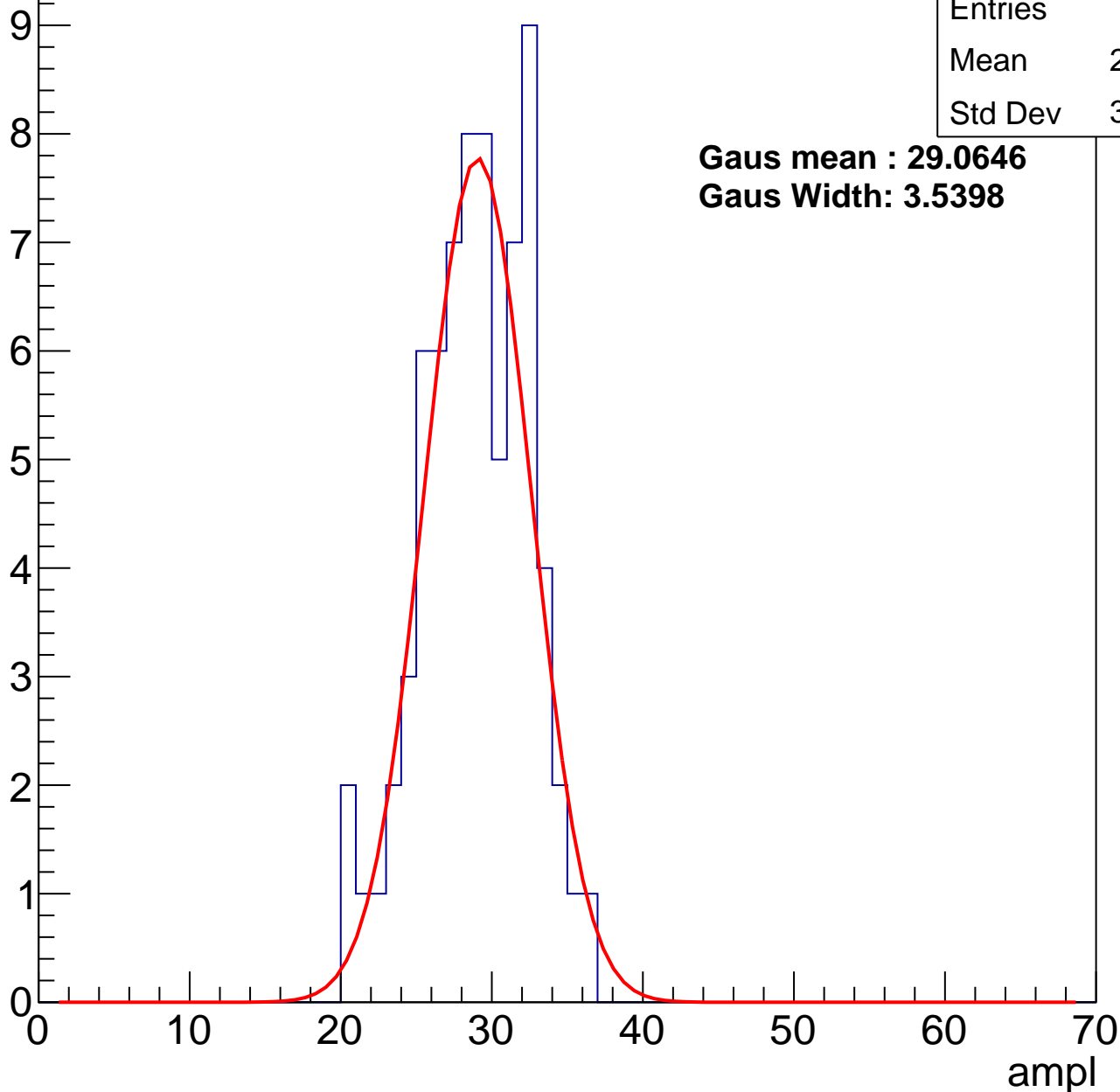
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.47
Std Dev	3.512

Gaus mean : 29.0646

Gaus Width: 3.5398



B1L100S, U14-ch80, adc1

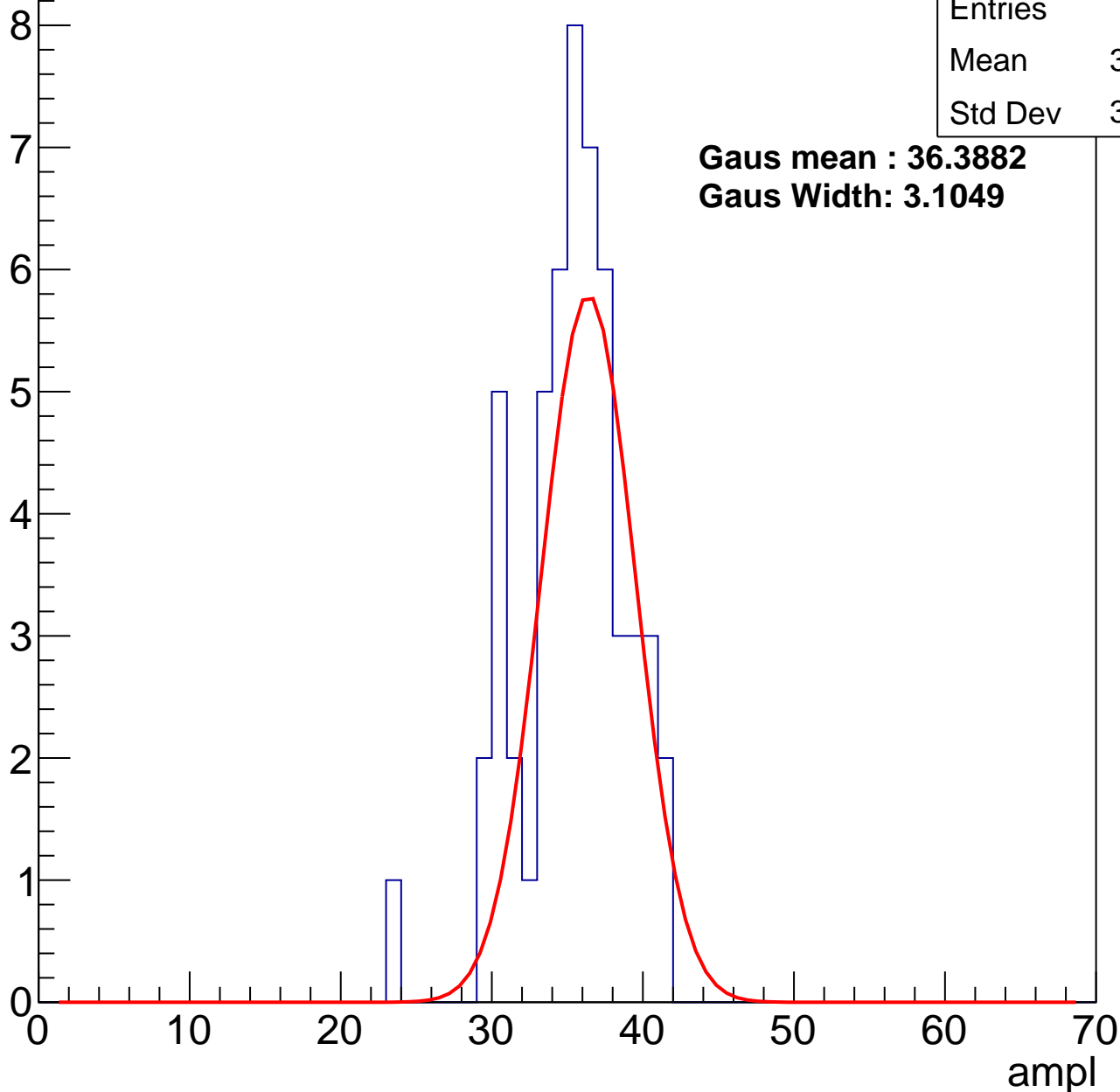
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	34.83
Std Dev	3.495

Gaus mean : 36.3882

Gaus Width: 3.1049



B1L100S, U14-ch80, adc2

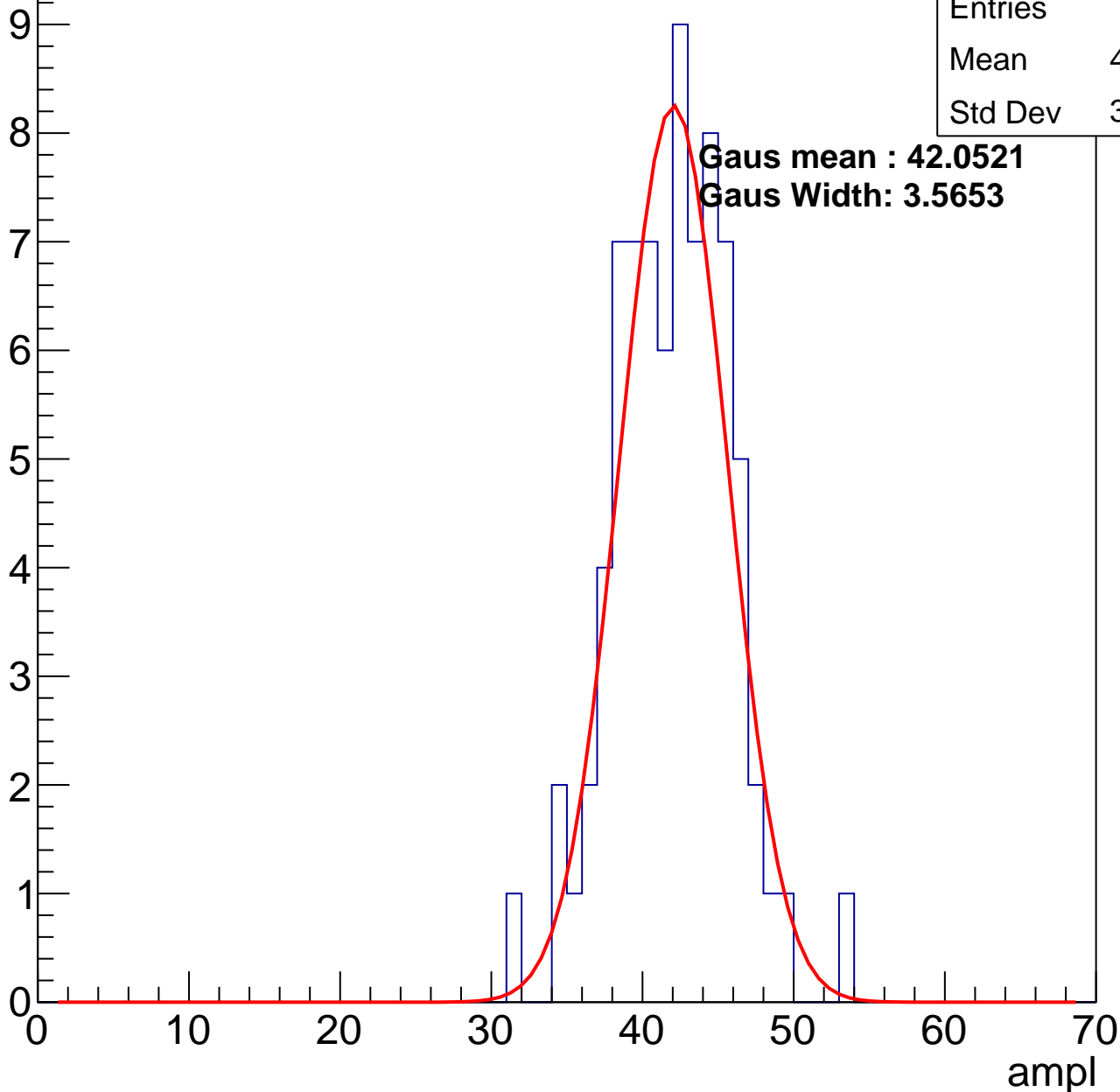
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	41.53
Std Dev	3.754

Gaus mean : 42.0521

Gaus Width: 3.5653

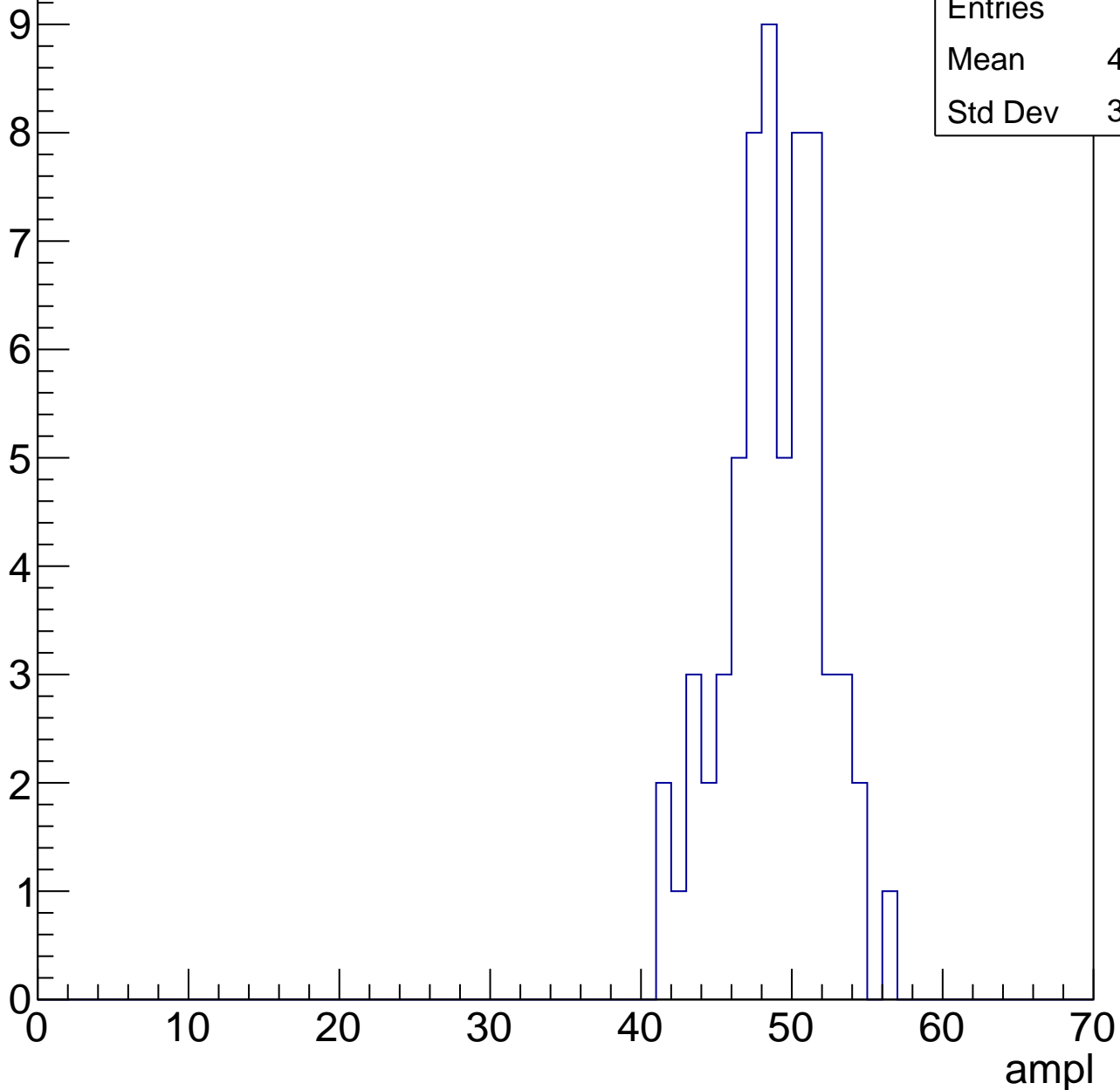


B1L100S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

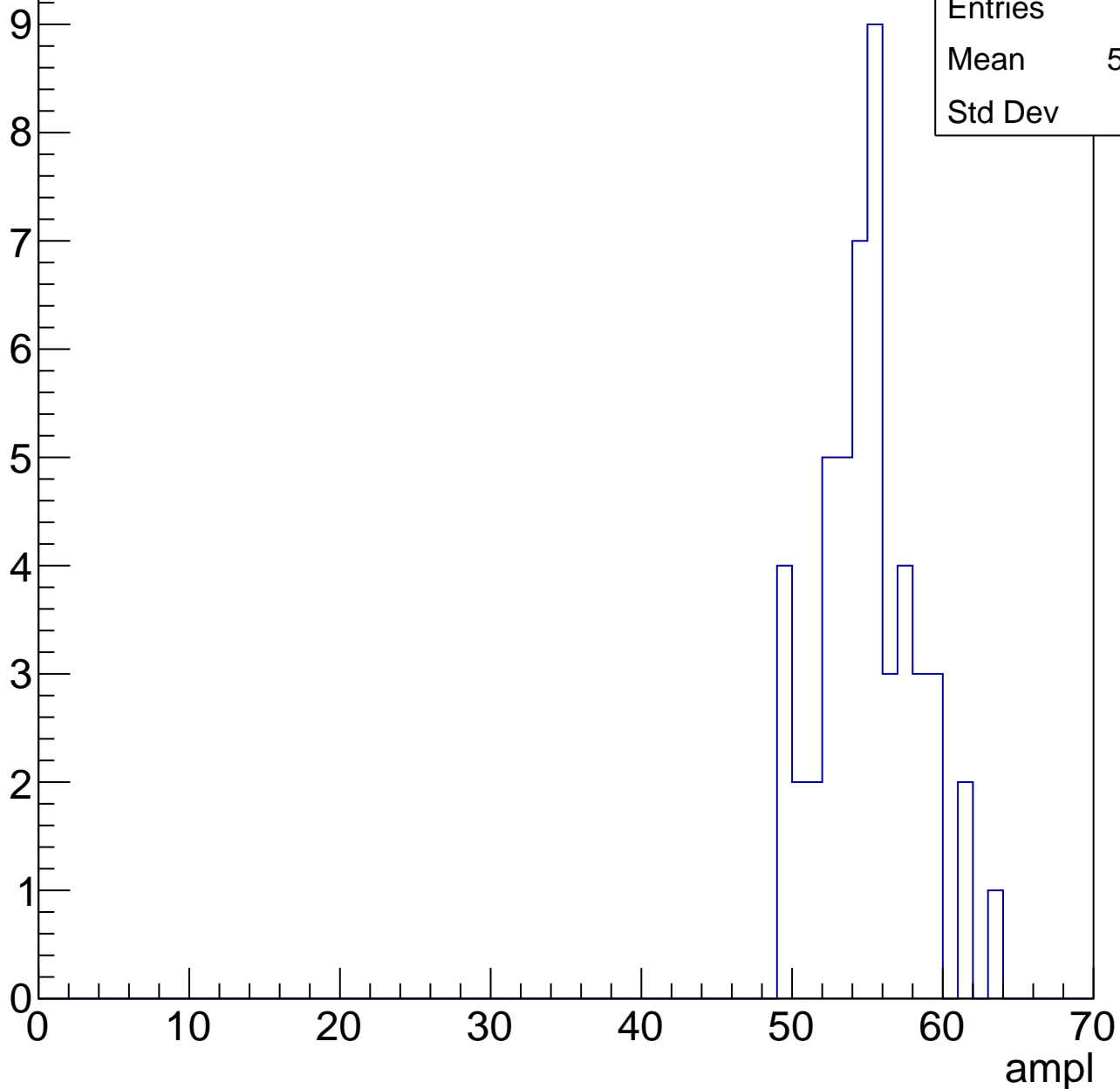
Entries	63
Mean	48.35
Std Dev	3.228



B1L100S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



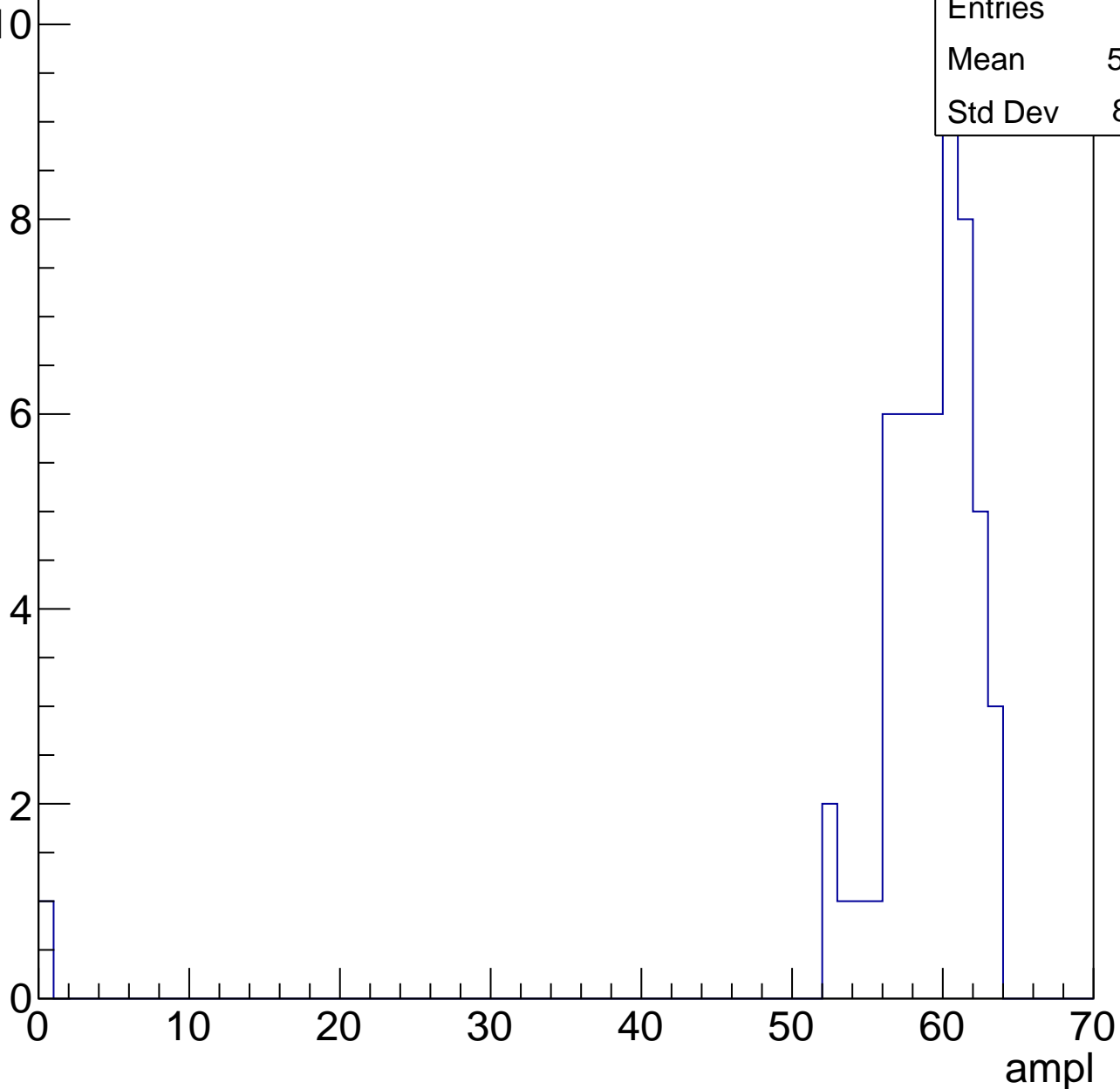
Entries	50
Mean	54.56
Std Dev	3.22

B1L100S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

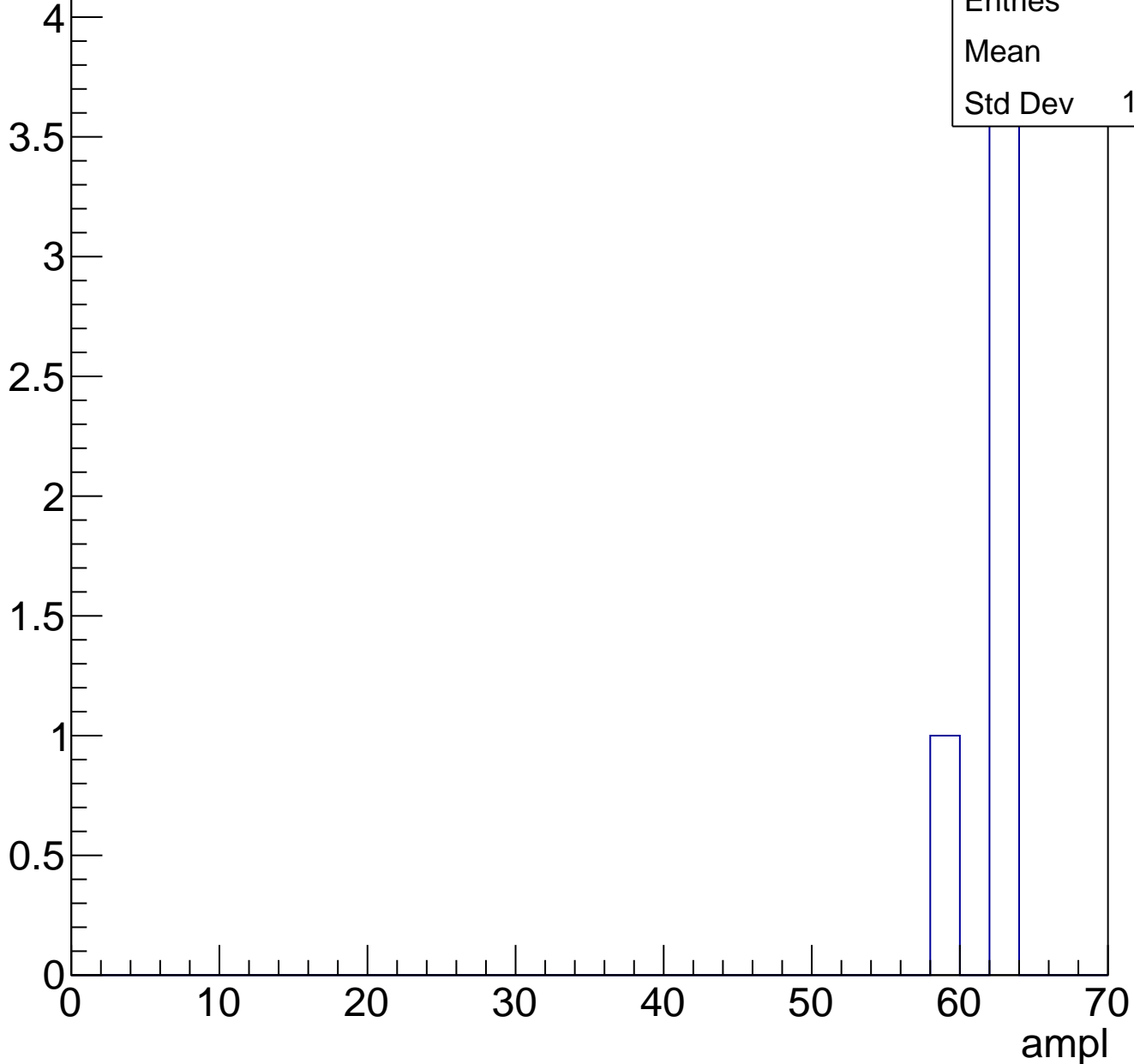
Entries	56
Mean	57.73
Std Dev	8.221



B1L100S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch80, adc7

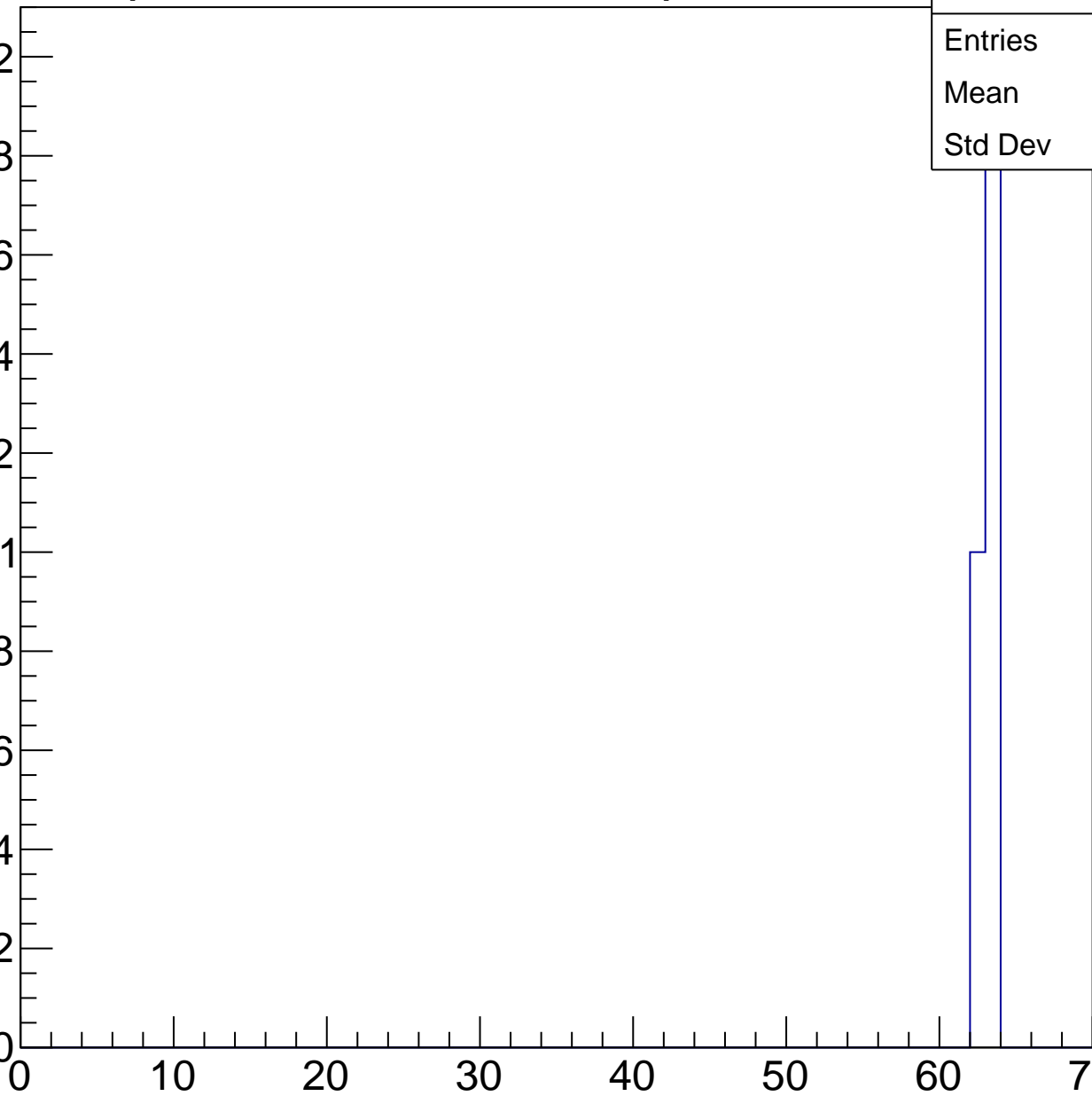
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L100S, U14-ch81, adc0

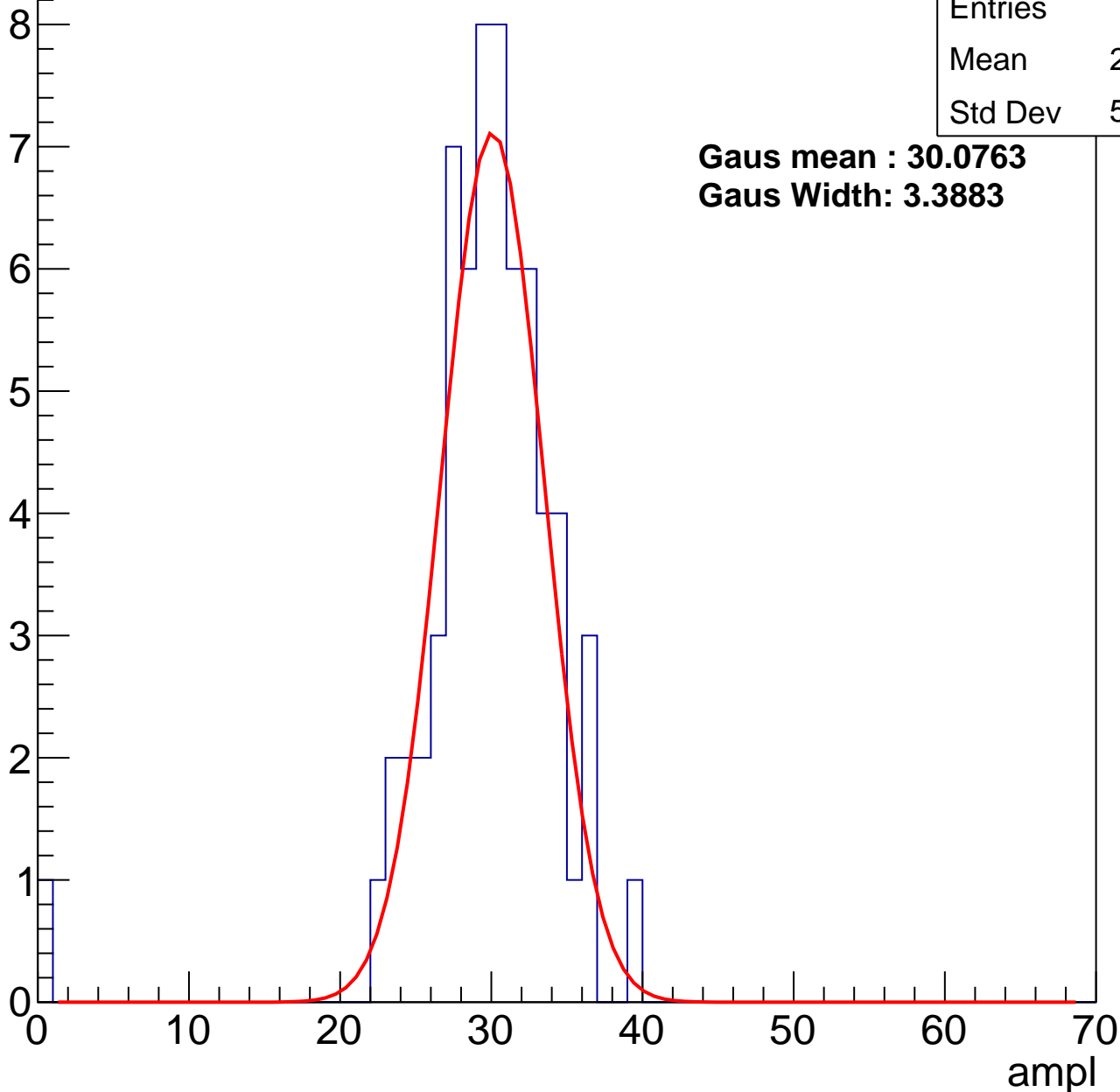
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.25
Std Dev	5.014

Gaus mean : 30.0763

Gaus Width: 3.3883



B1L100S, U14-ch81, adc1

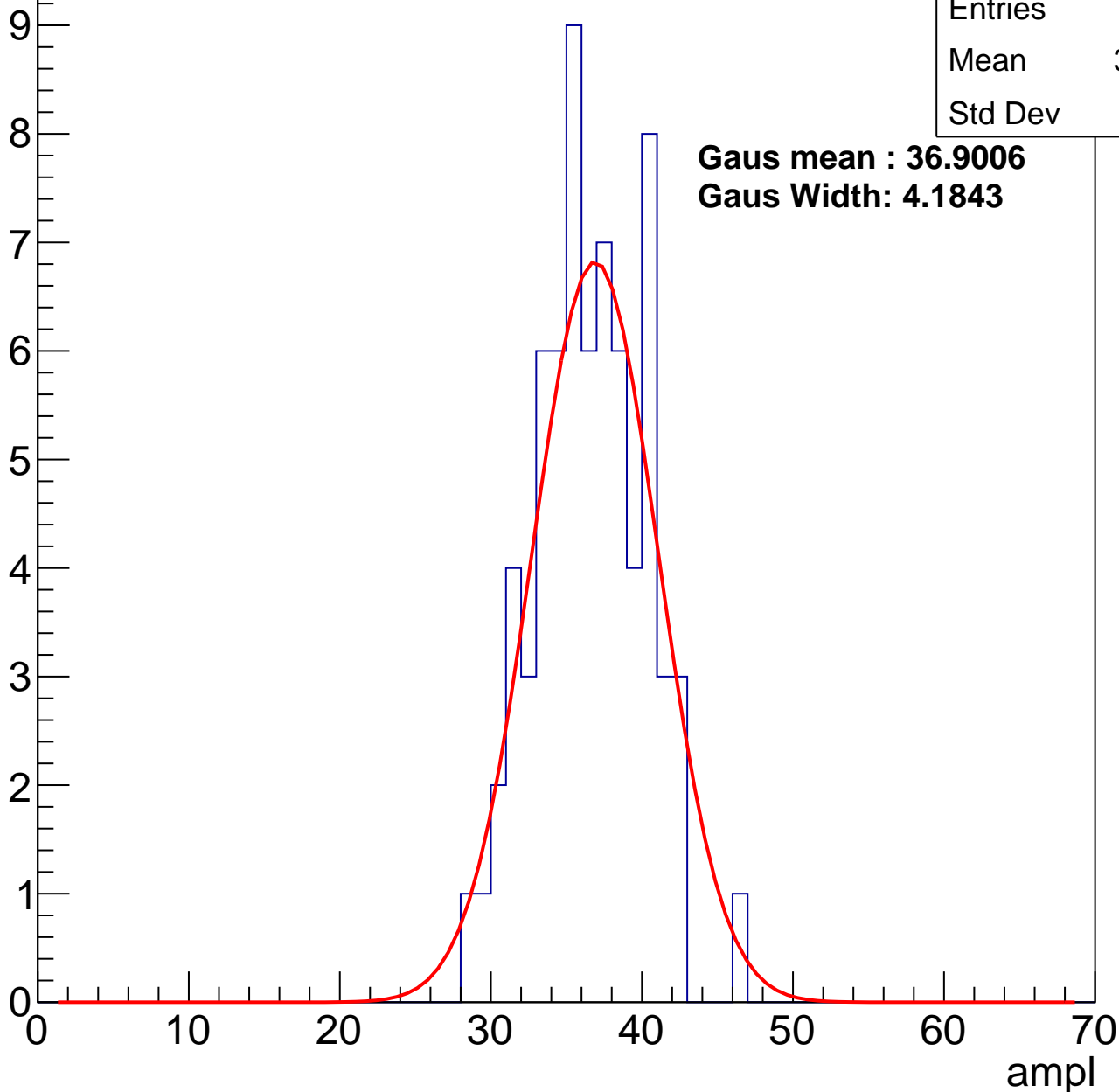
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.11
Std Dev	3.58

Gaus mean : 36.9006

Gaus Width: 4.1843

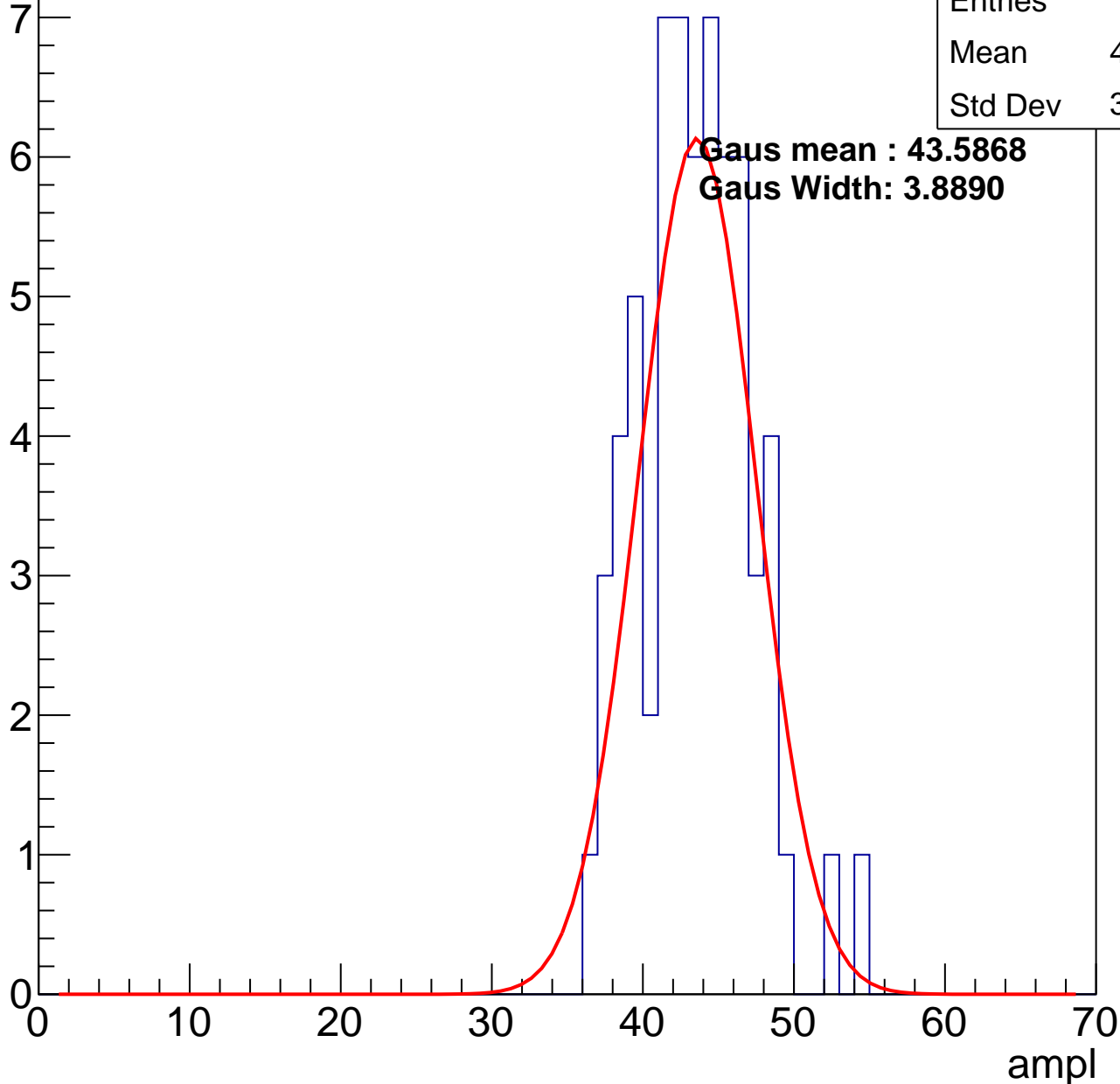


B1L100S, U14-ch81, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.05
Std Dev	3.668

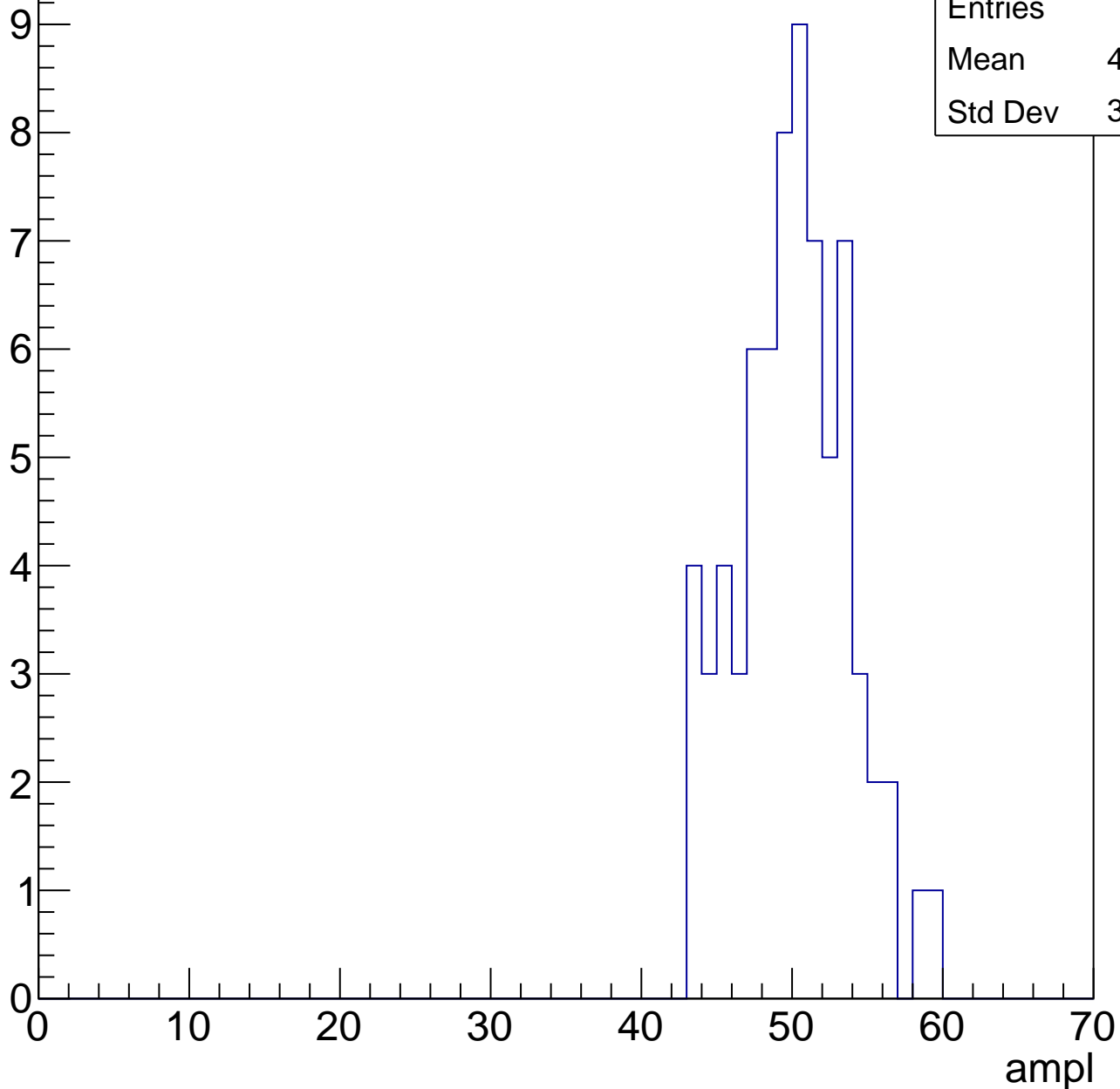


B1L100S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

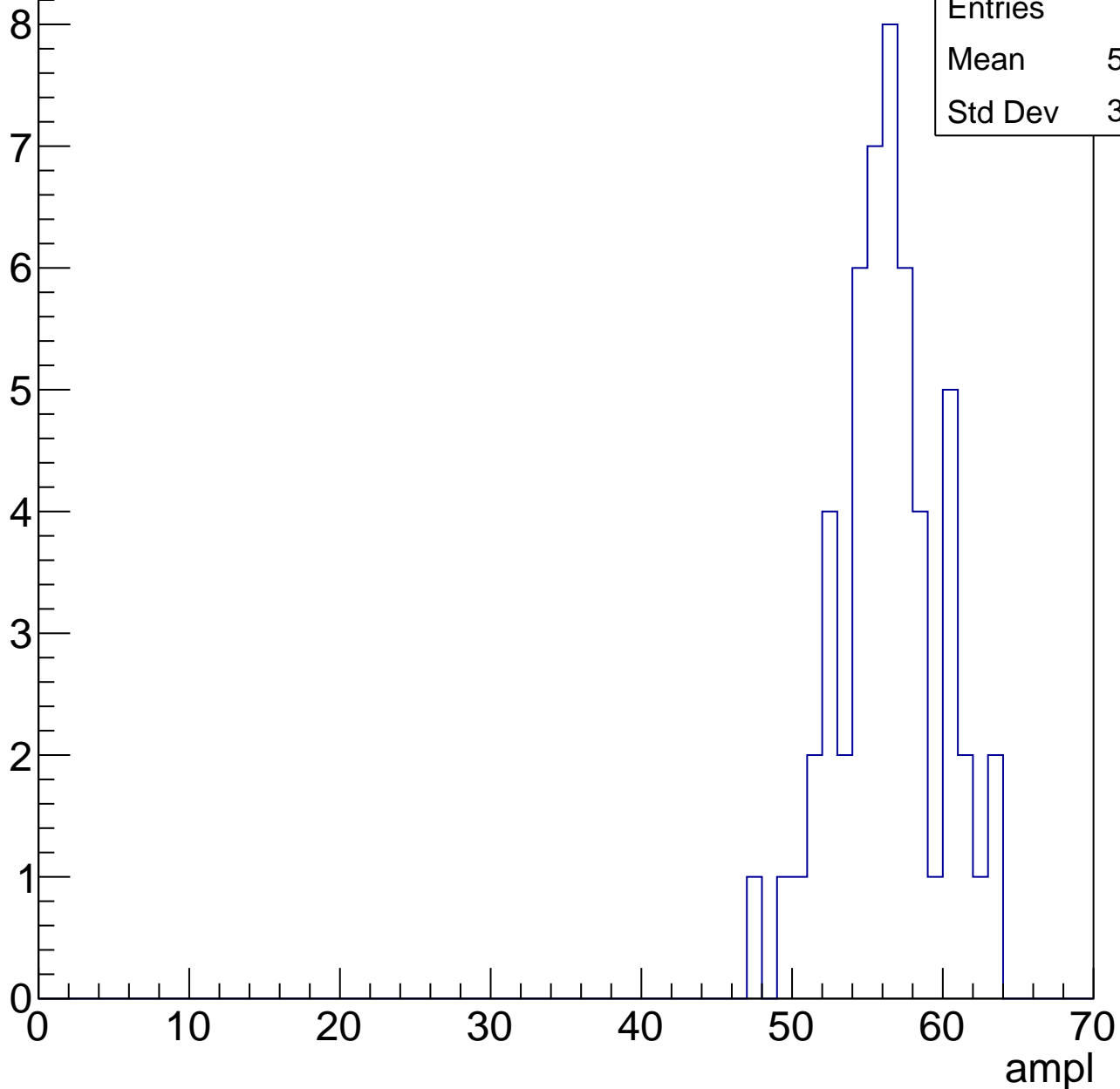
Entries	71
Mean	49.62
Std Dev	3.625



B1L100S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

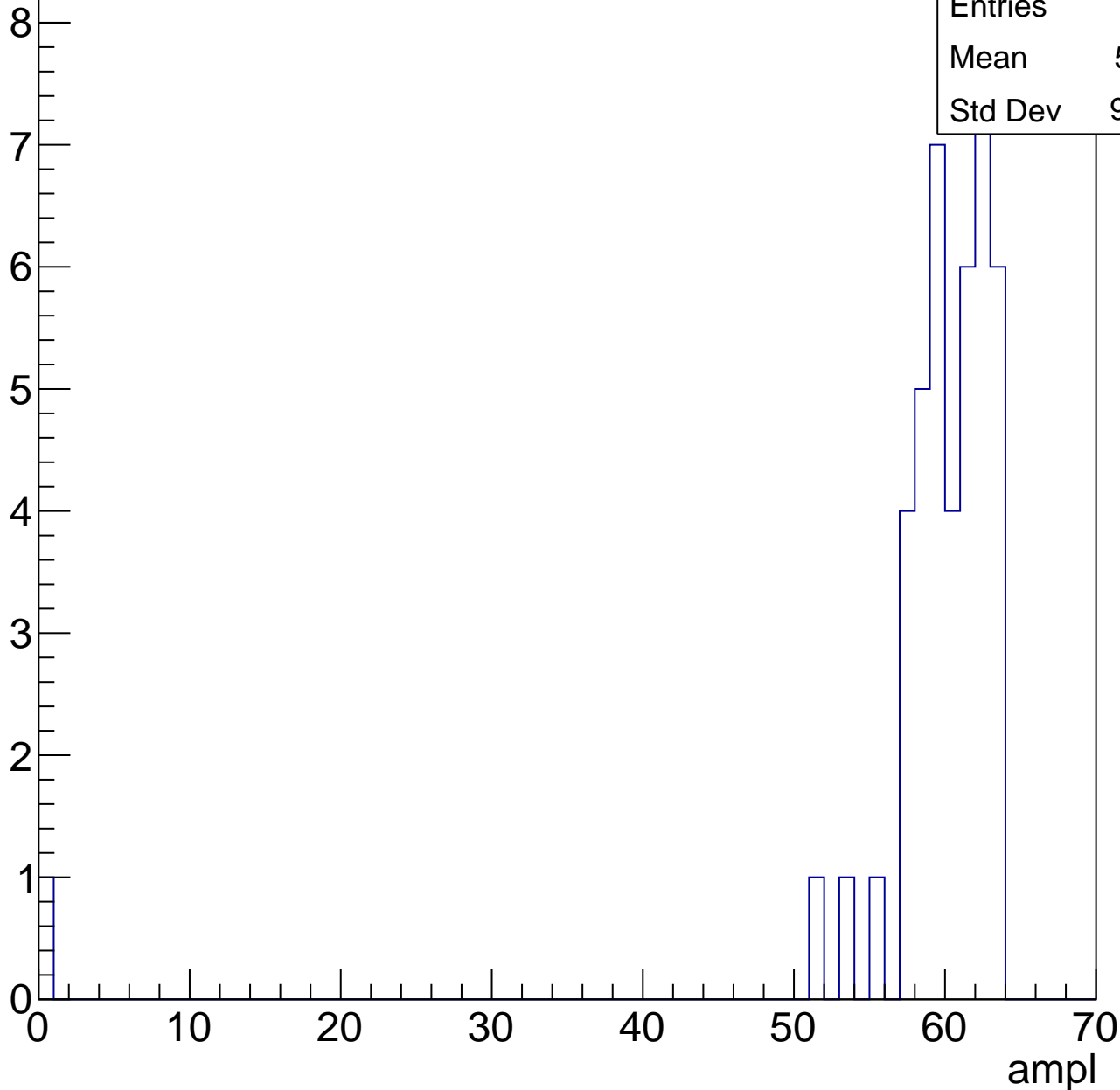


B1L100S, U14-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.41
Std Dev	9.292



B1L100S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

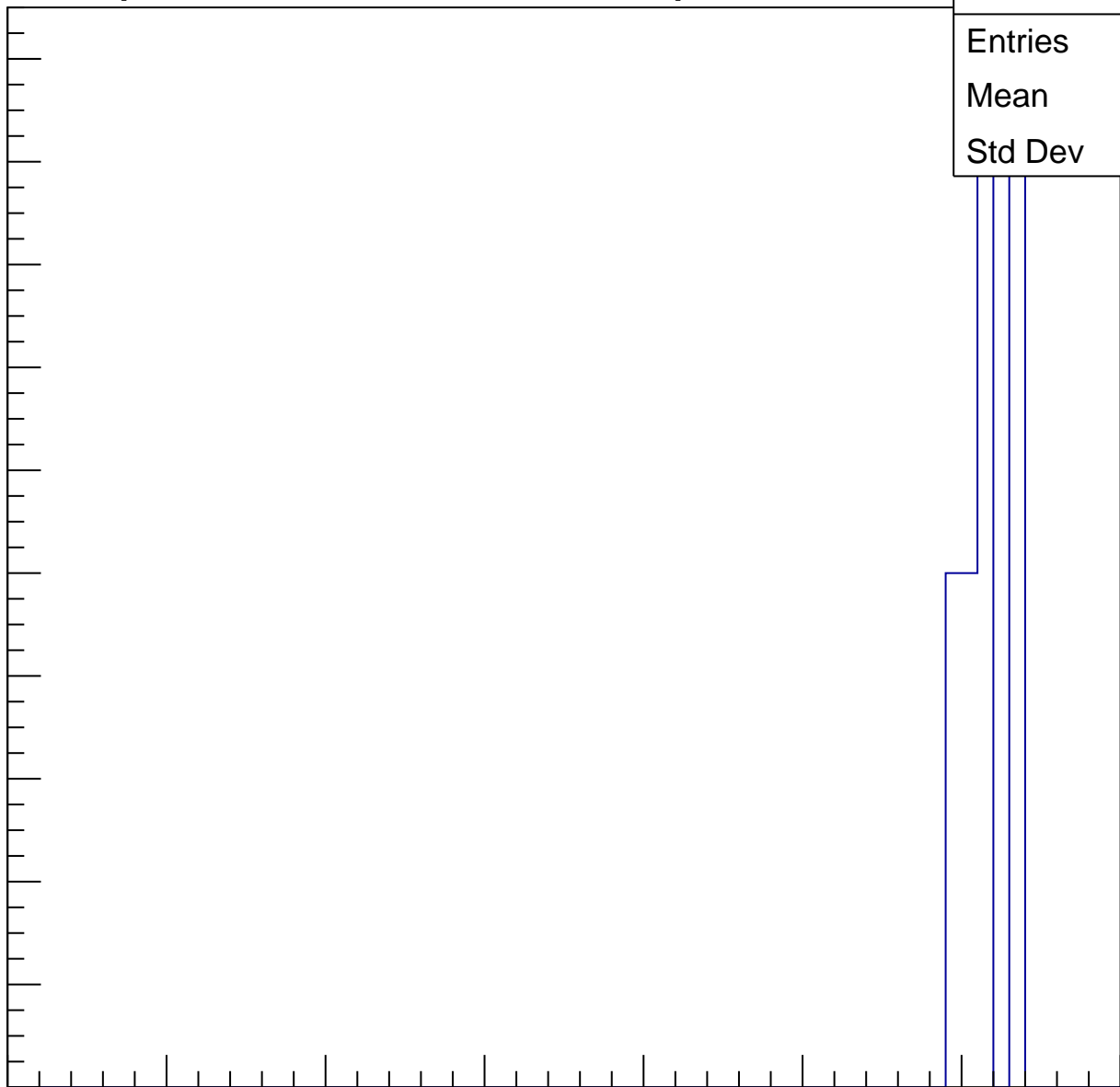
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.462

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch82, adc0

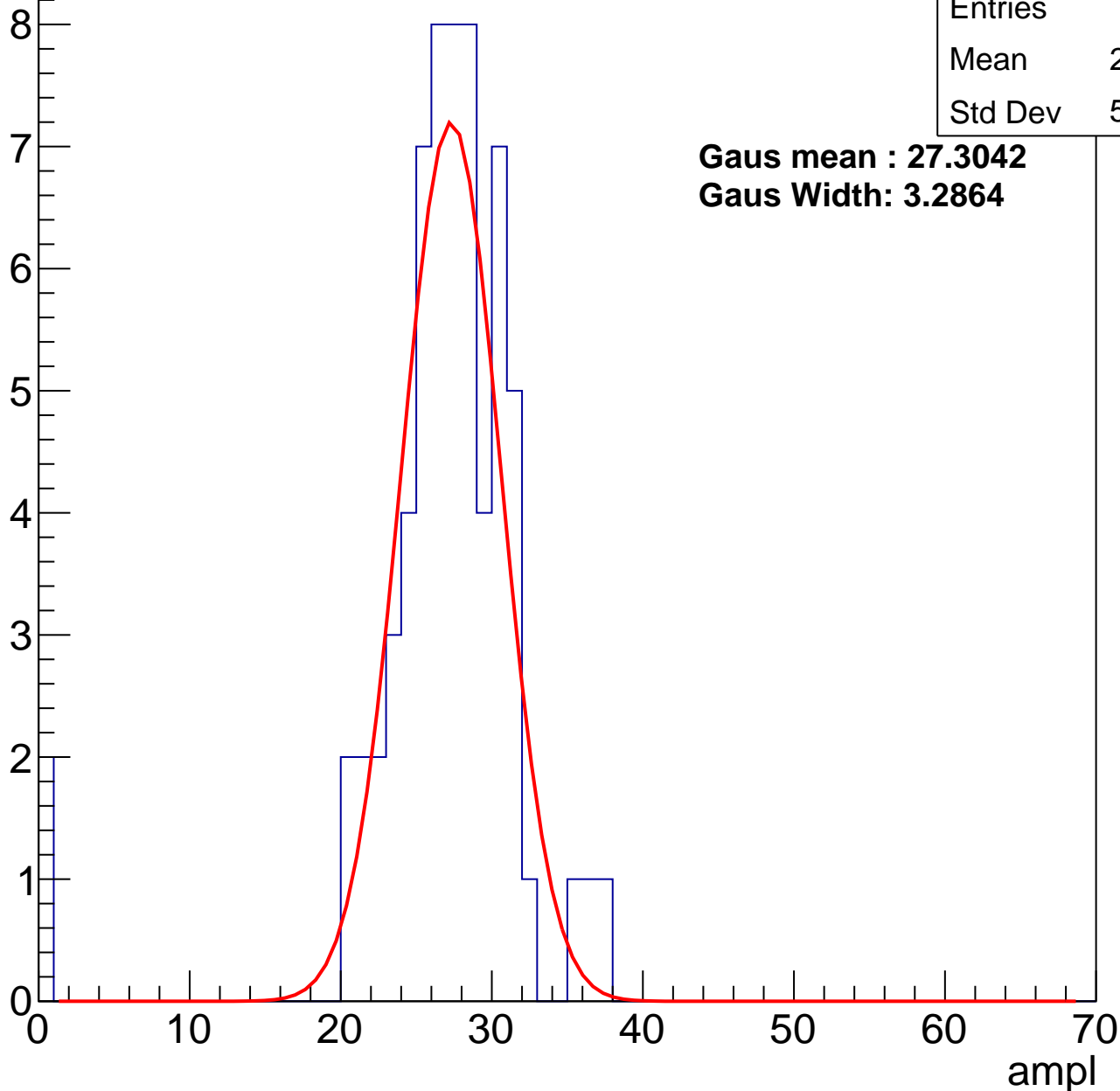
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	26.29
Std Dev	5.776

Gaus mean : 27.3042

Gaus Width: 3.2864



B1L100S, U14-ch82, adc1

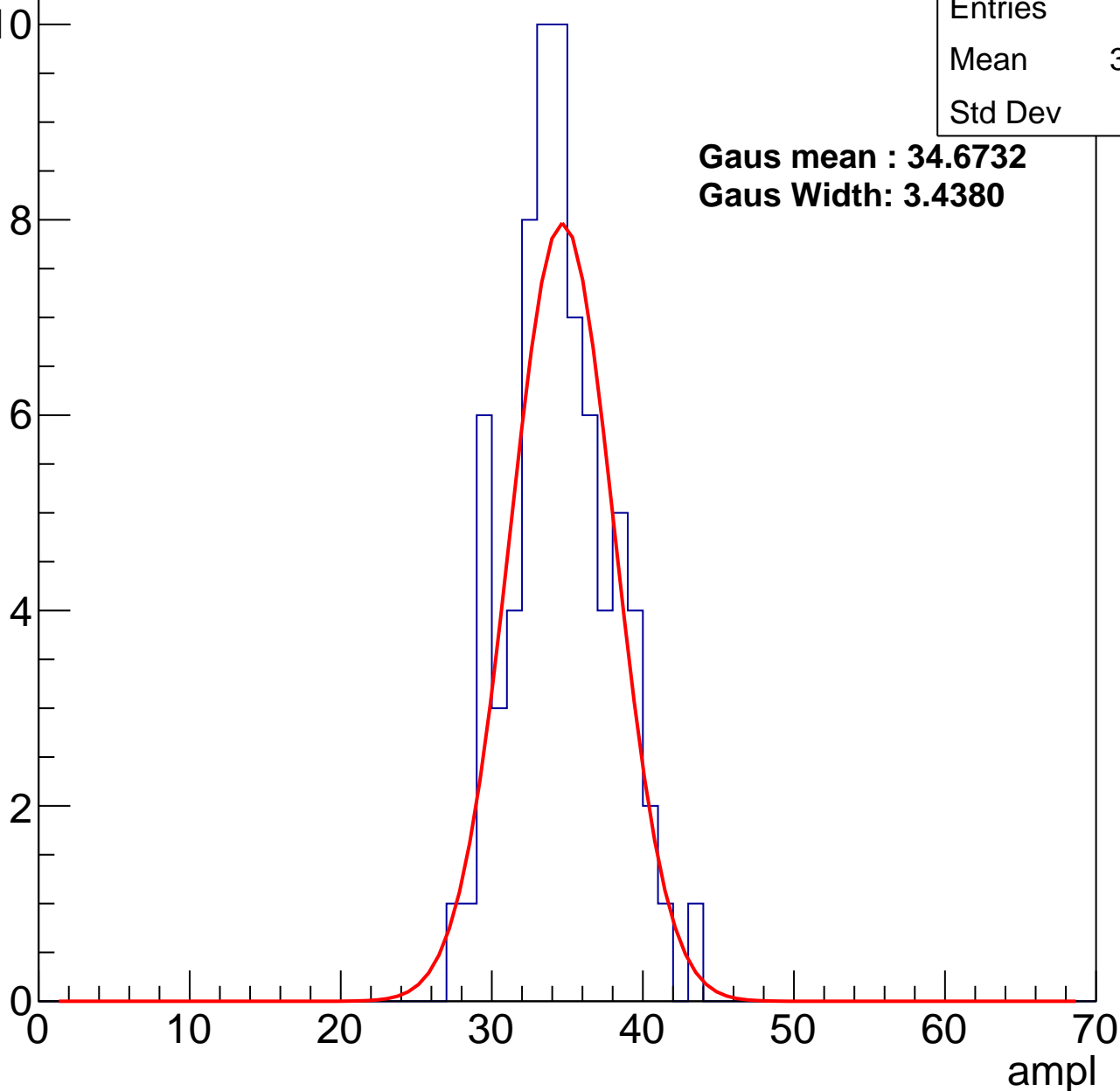
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	34.08
Std Dev	3.33

Gaus mean : 34.6732

Gaus Width: 3.4380

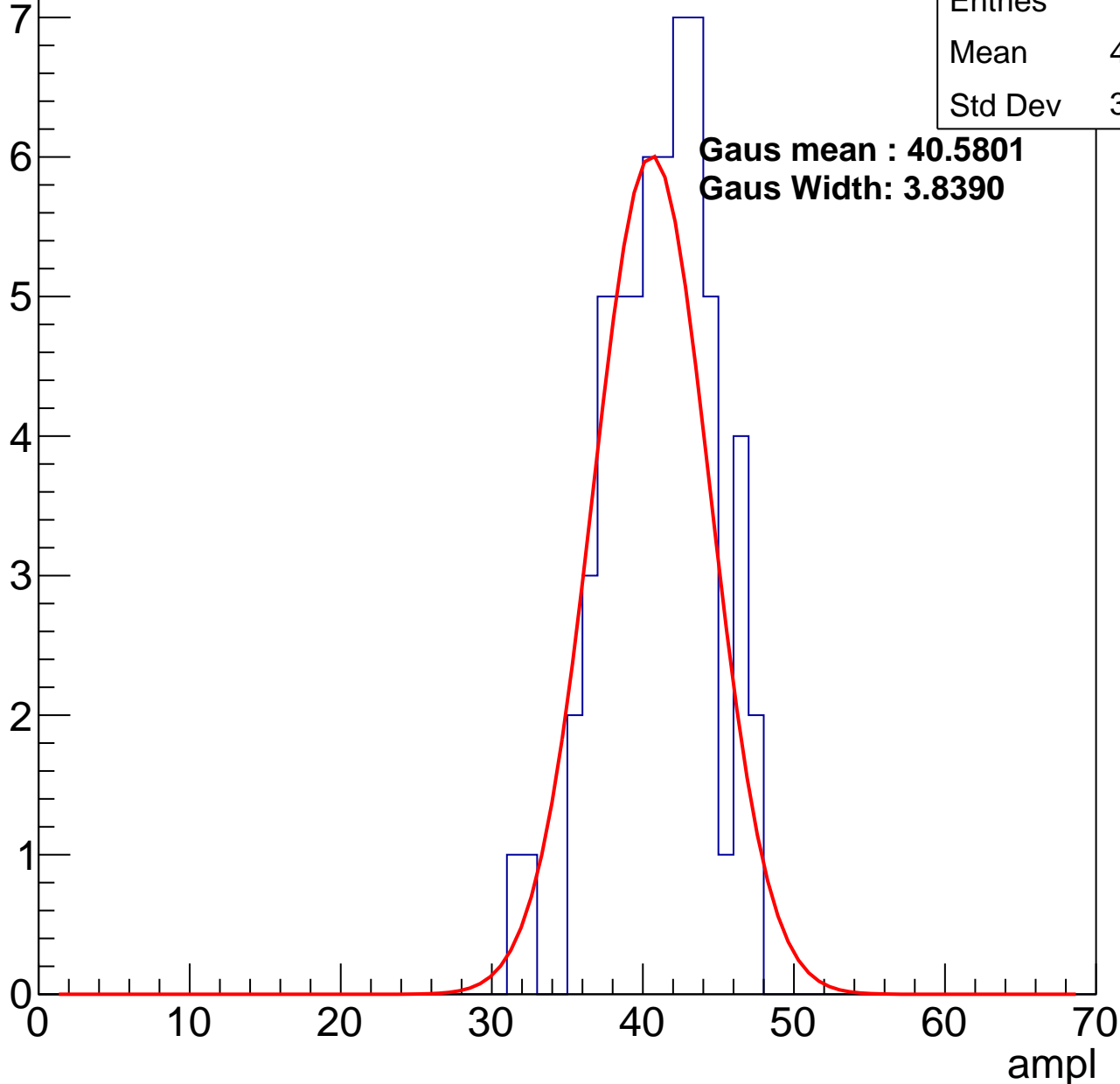


B1L100S, U14-ch82, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	40.58
Std Dev	3.504

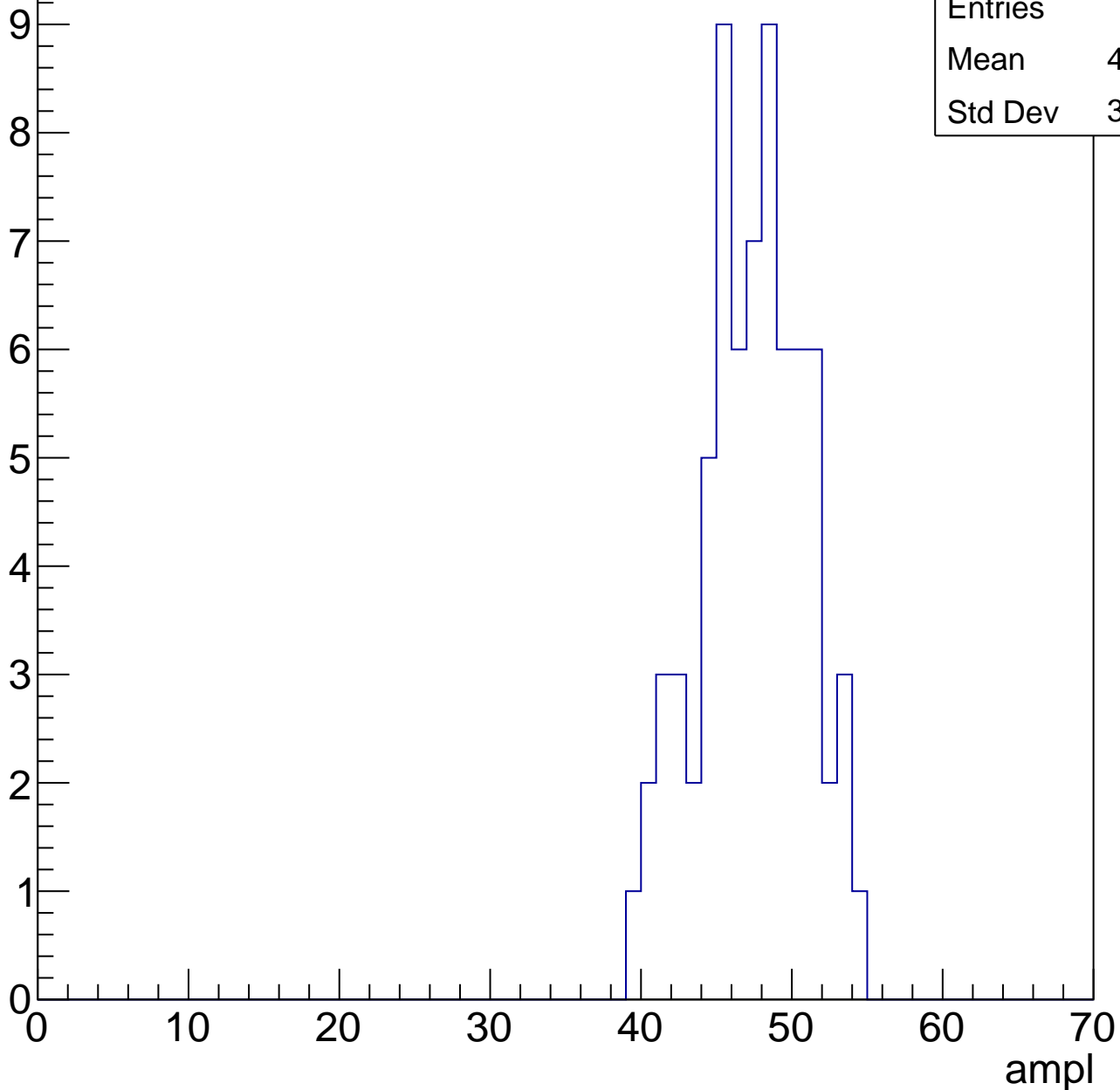


B1L100S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	46.94
Std Dev	3.468

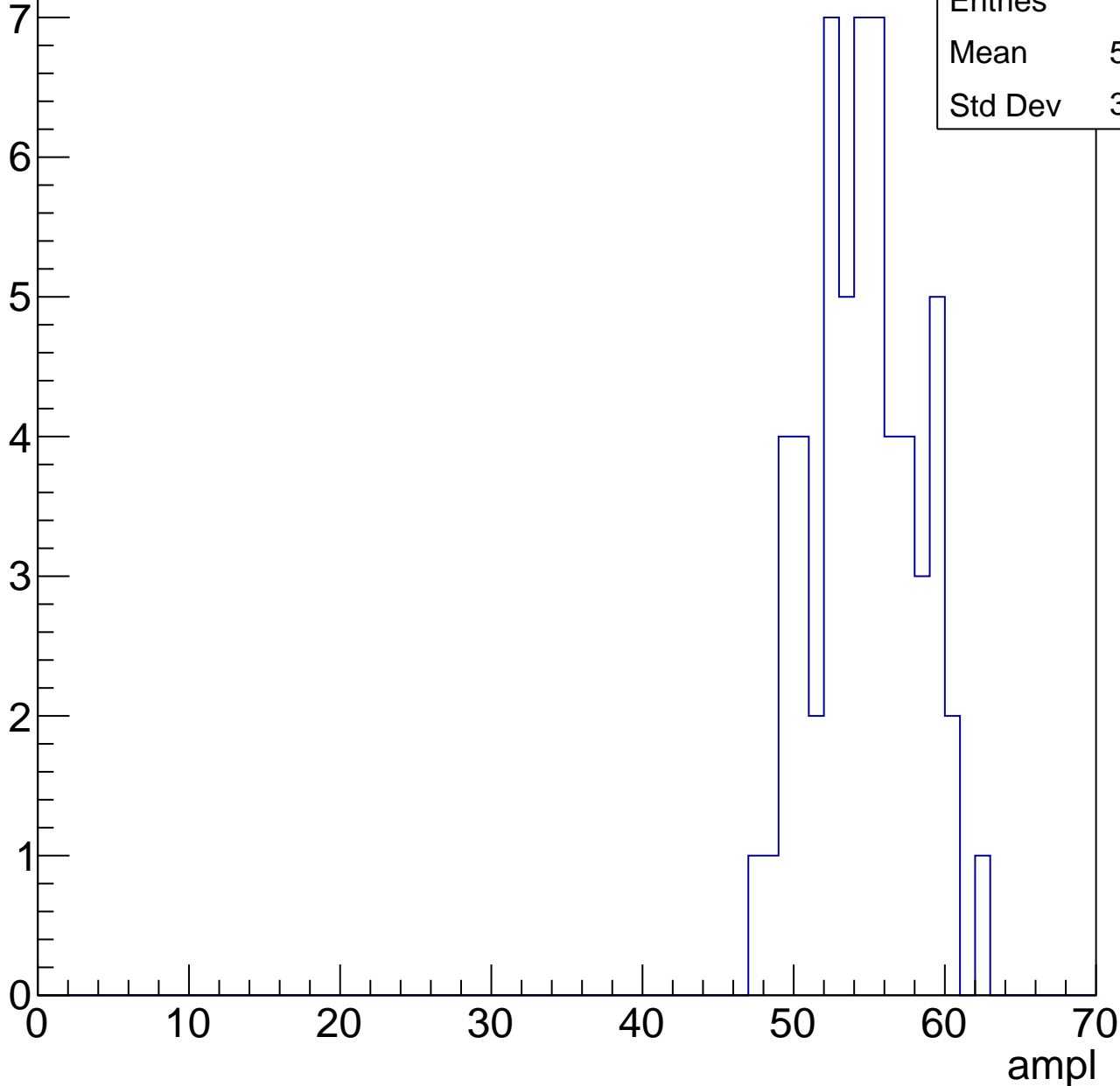


B1L100S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

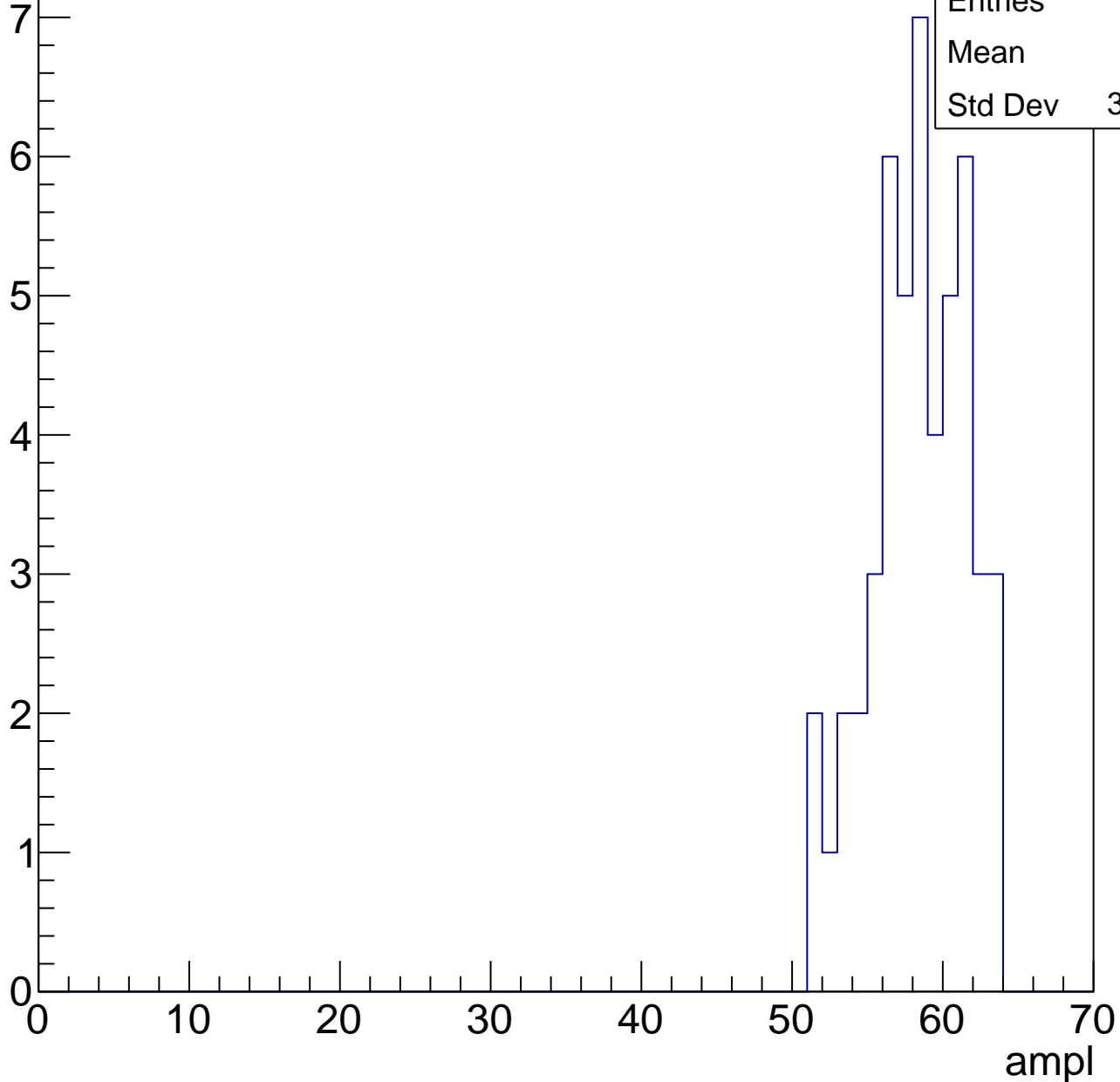
Entries	57
Mean	54.18
Std Dev	3.419



B1L100S, U14-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

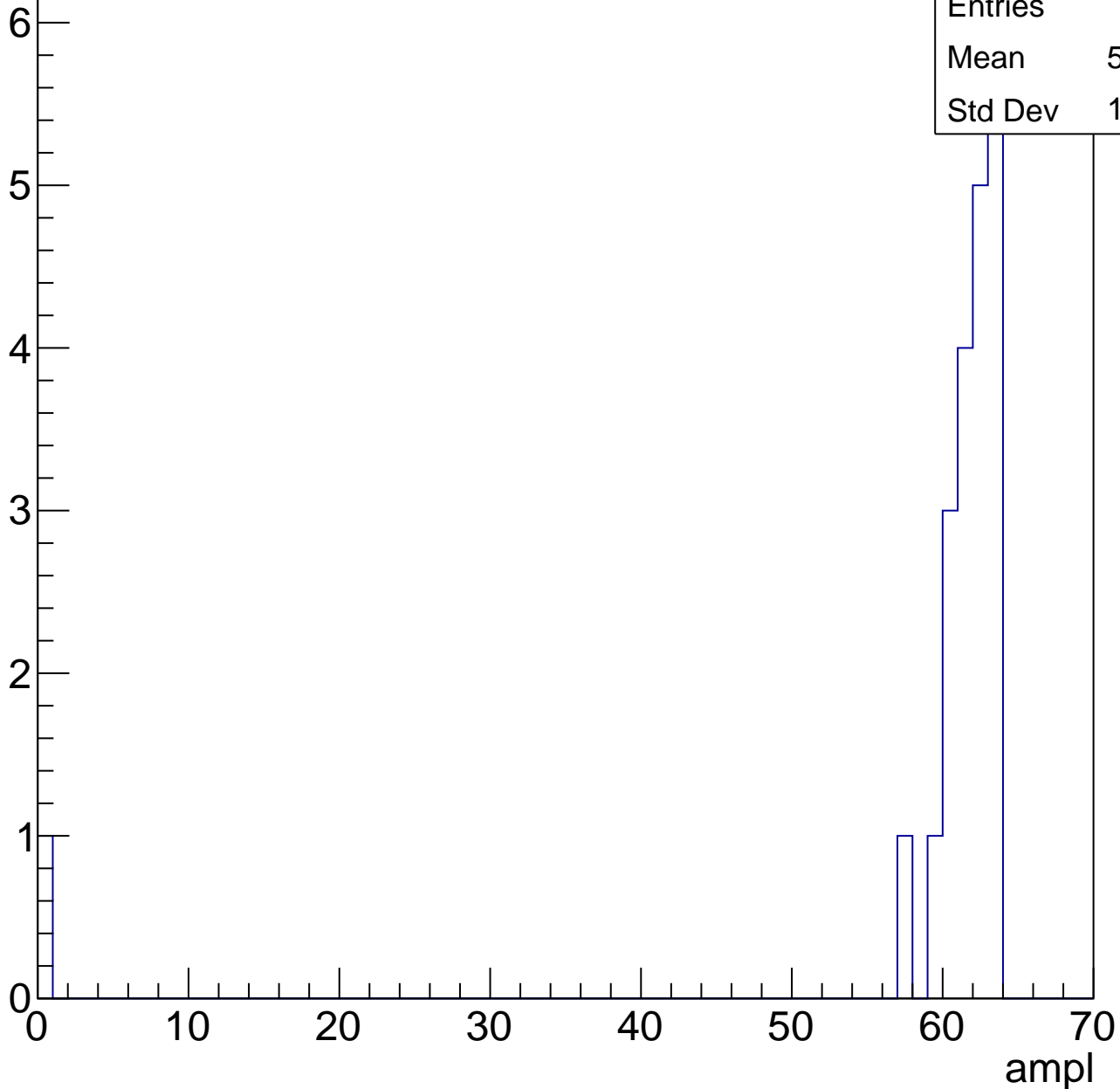


B1L100S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.48
Std Dev	13.16



B1L100S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch83, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	28.76
Std Dev	4.569

Gaus mean : 29.5658

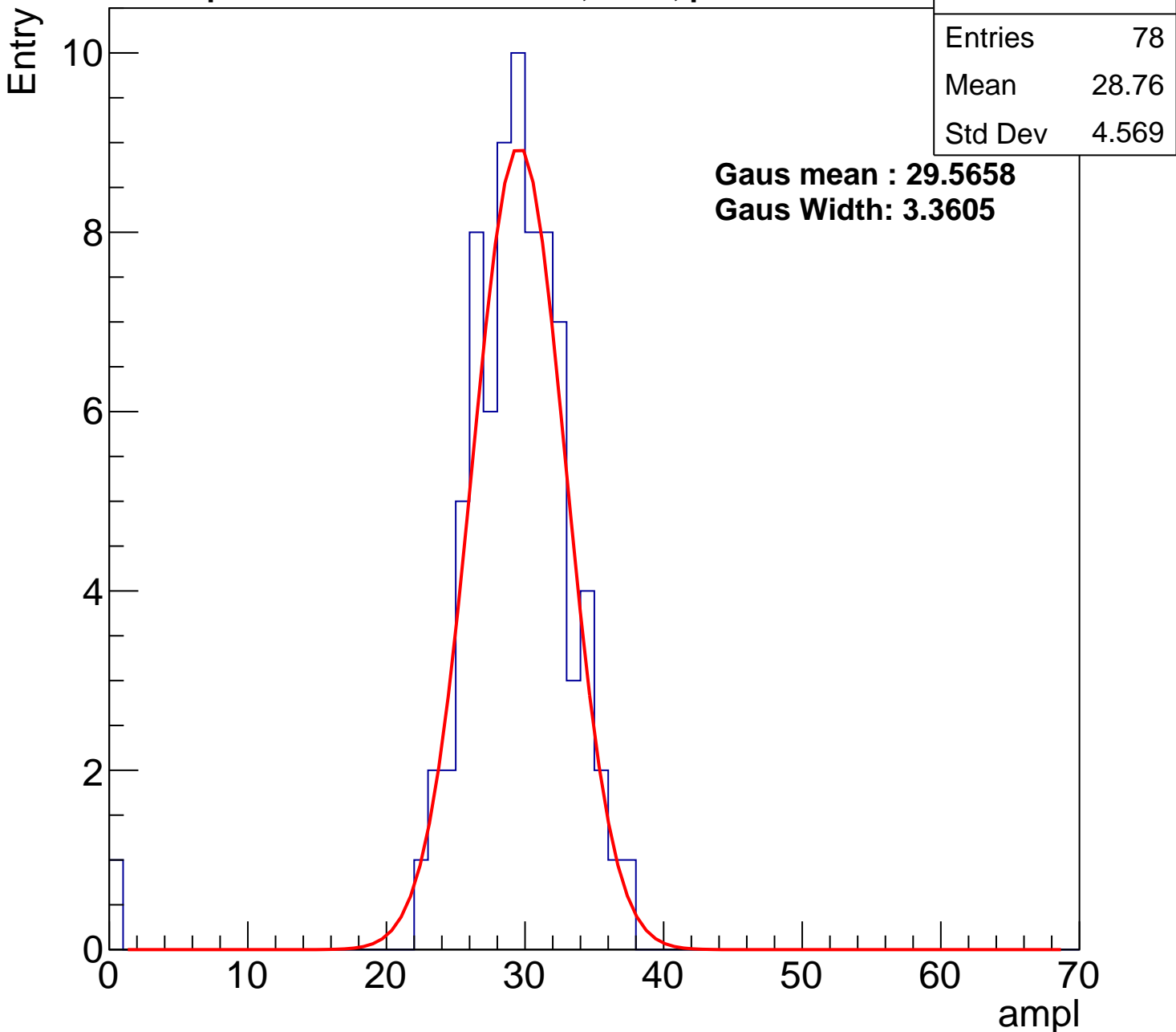
Gaus Width: 3.3605

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch83, adc1

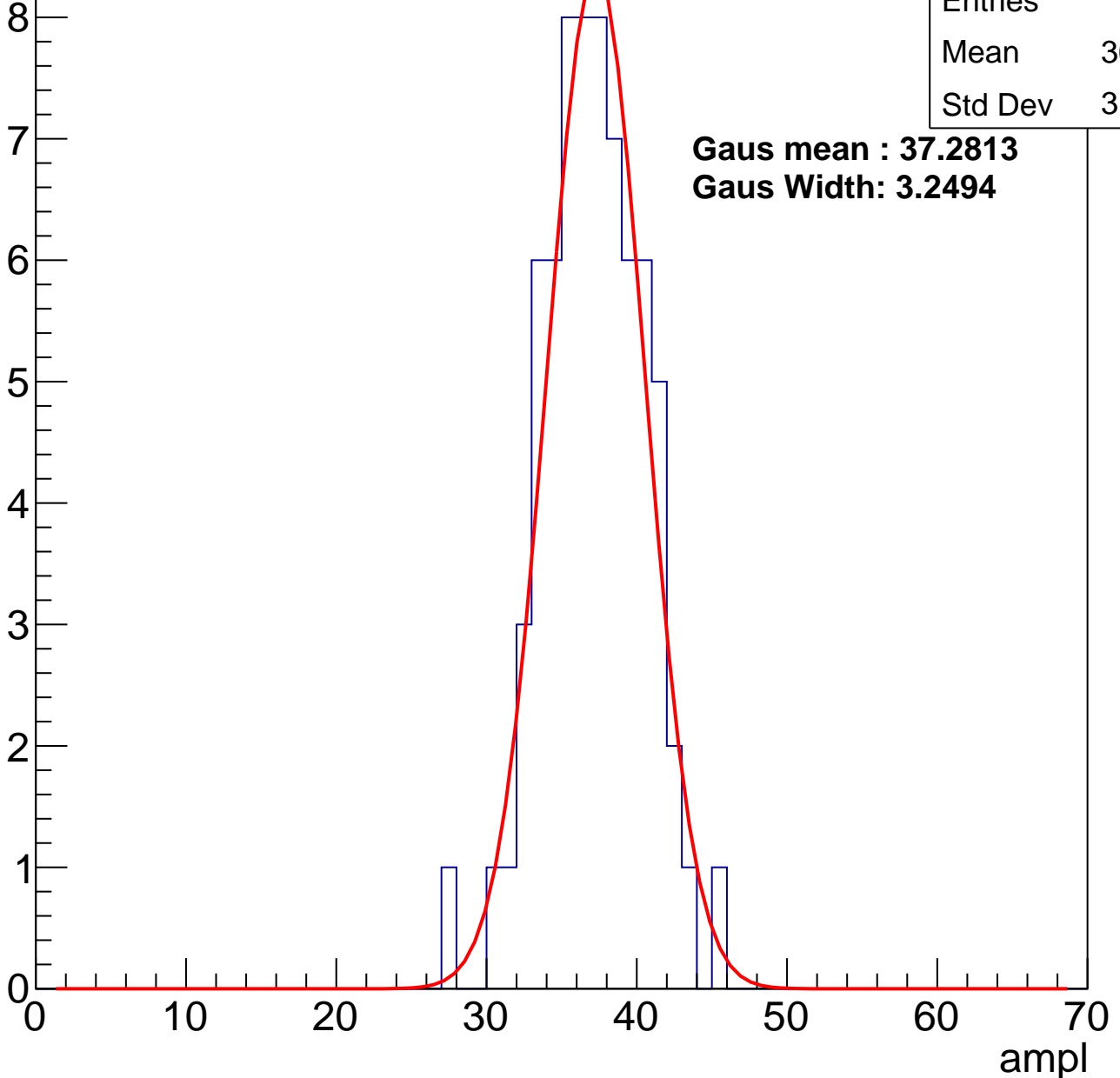
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.67
Std Dev	3.289

Gaus mean : 37.2813

Gaus Width: 3.2494



B1L100S, U14-ch83, adc2

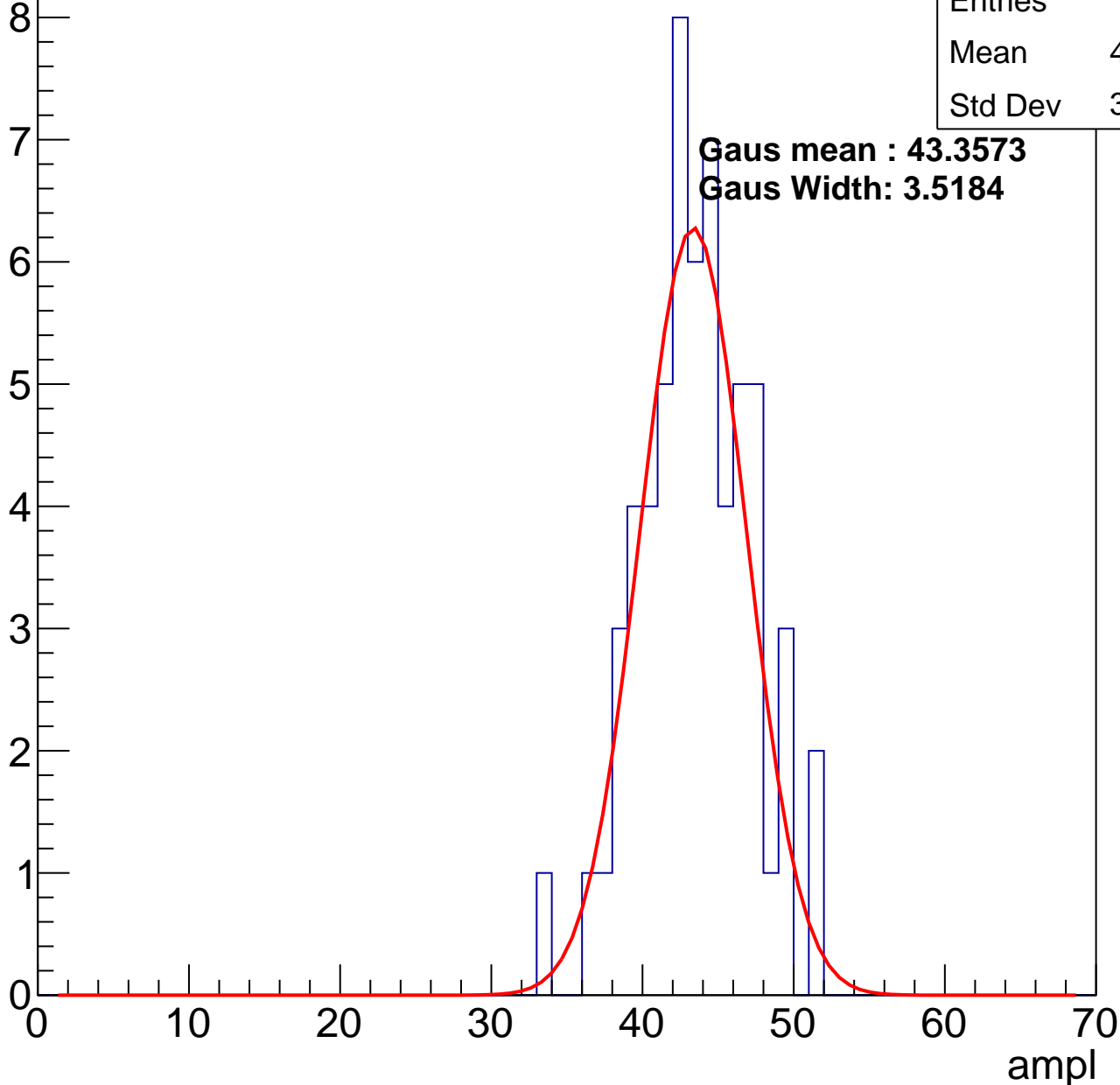
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.08
Std Dev	3.644

Gaus mean : 43.3573

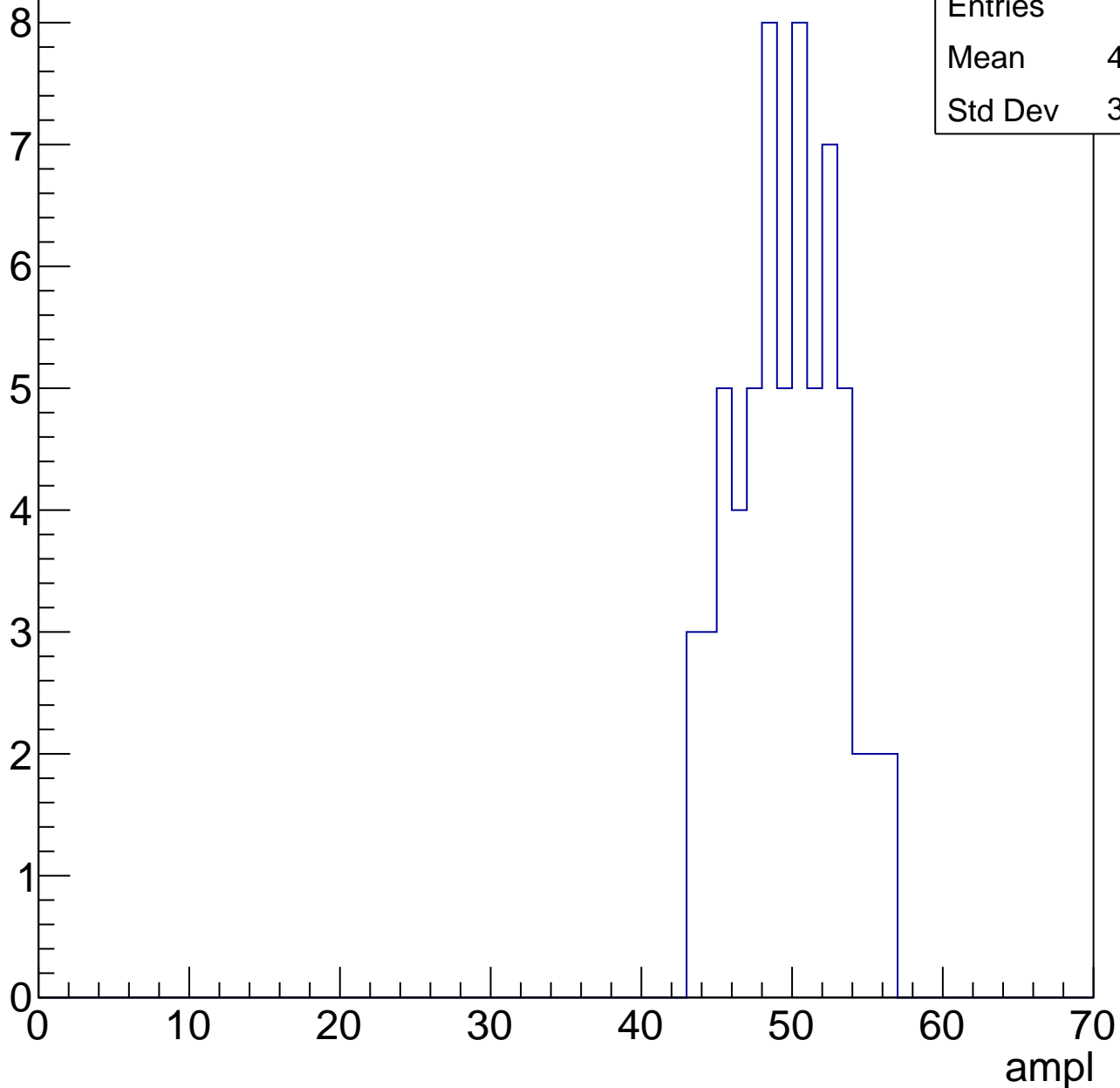
Gaus Width: 3.5184



B1L100S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

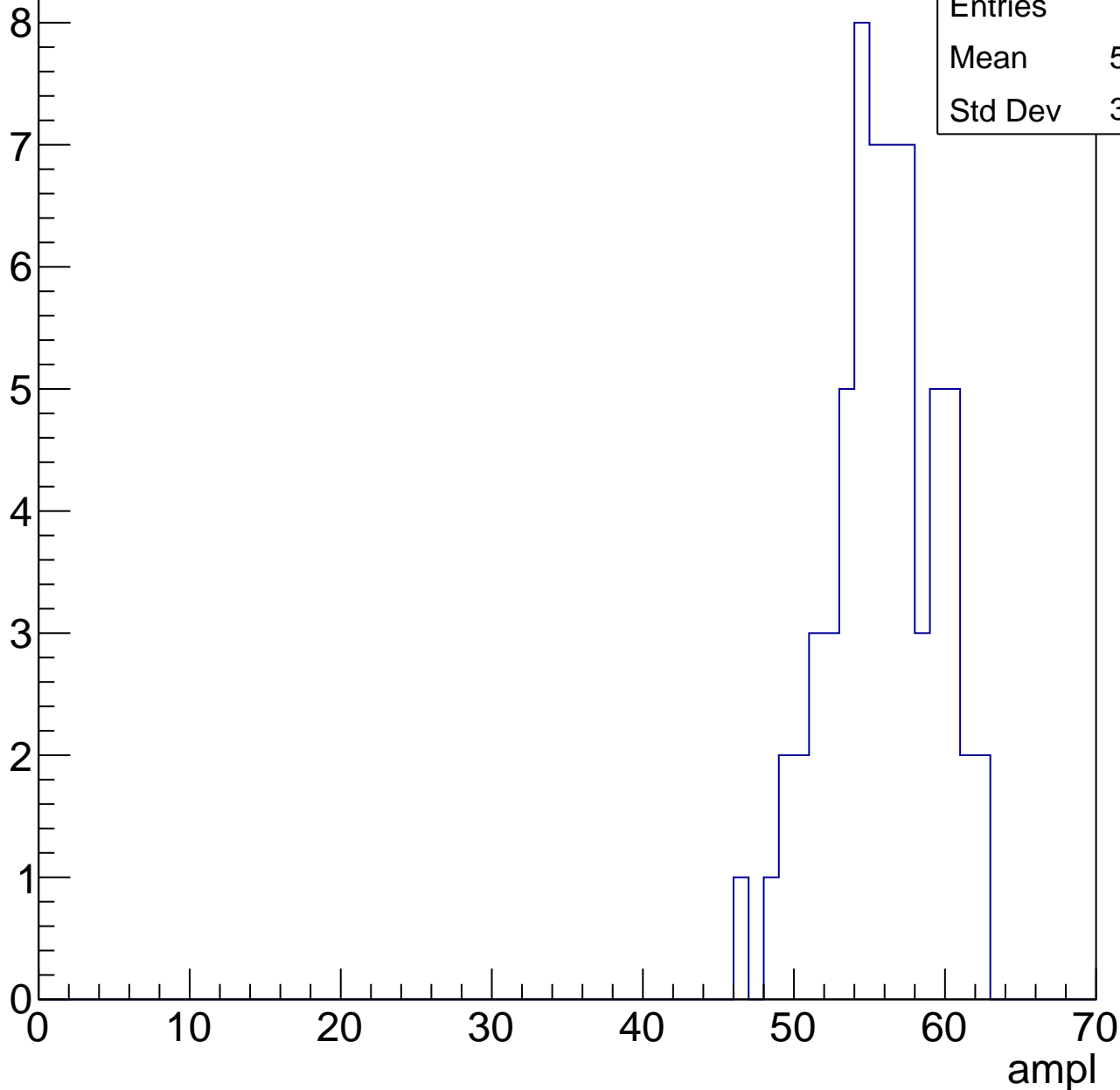


B1L100S, U14-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.38
Std Dev	3.525



B1L100S, U14-ch83, adc5

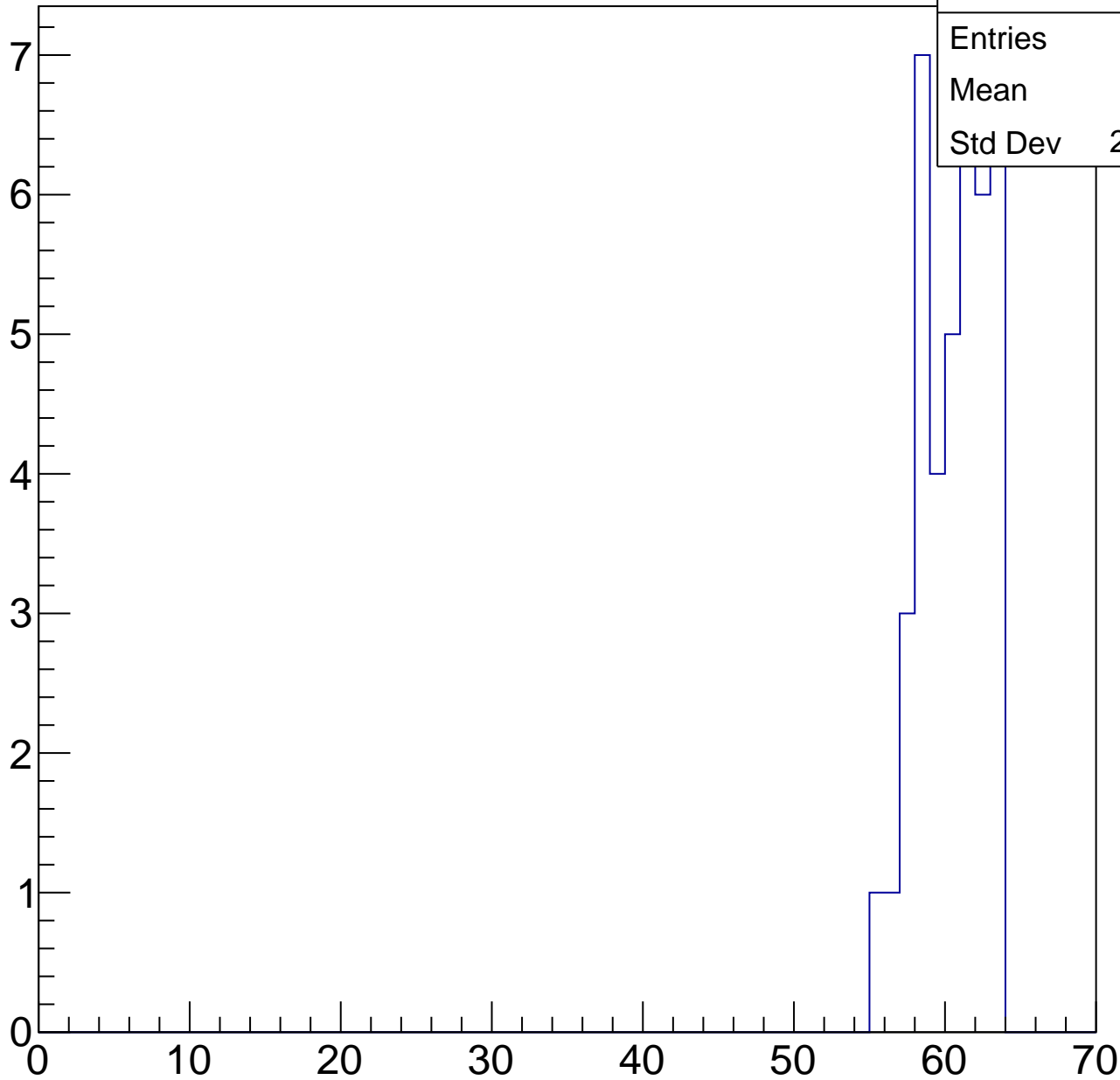
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	60.1
Std Dev	2.173

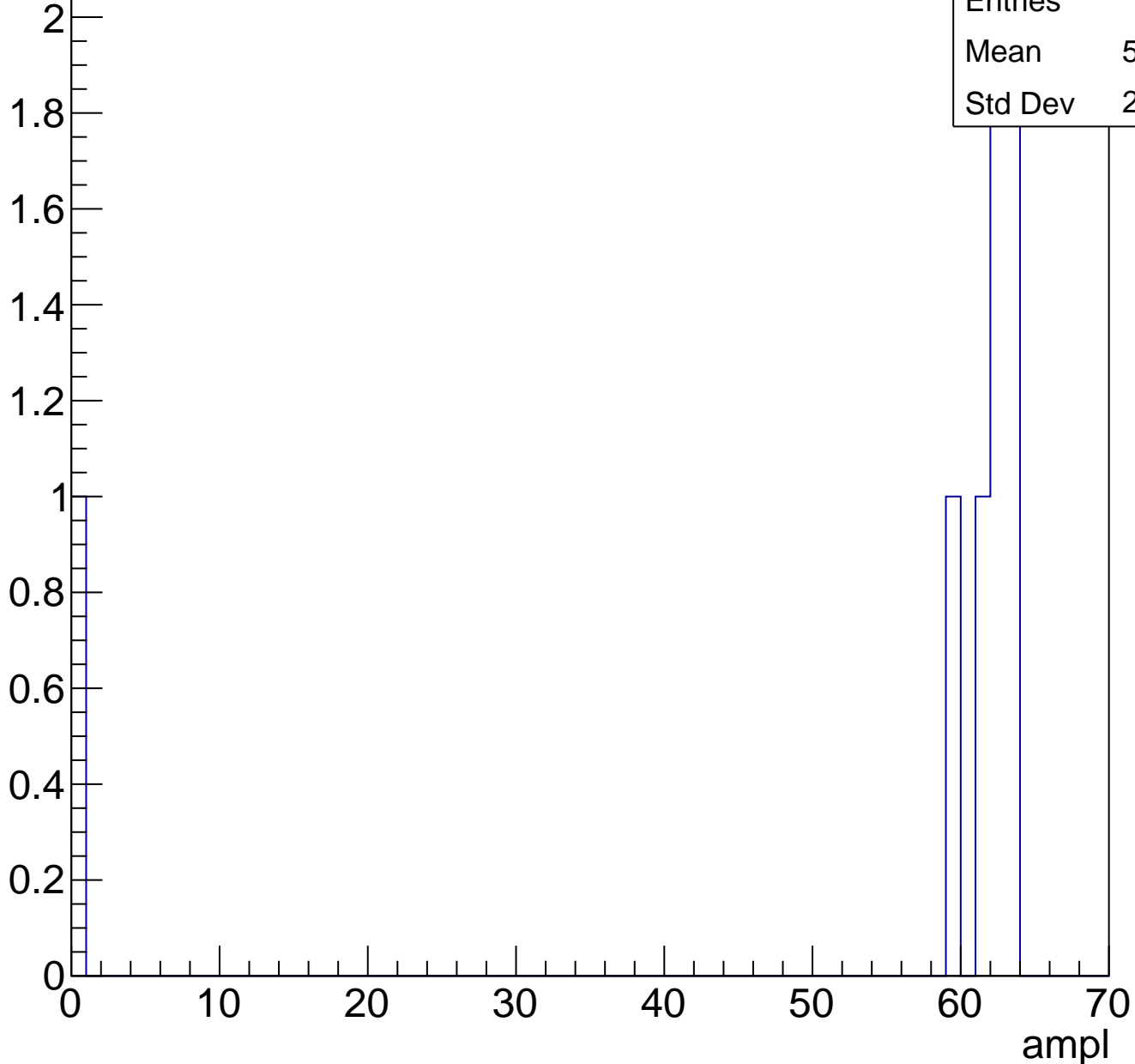
ampl



B1L100S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U14-ch84, adc0

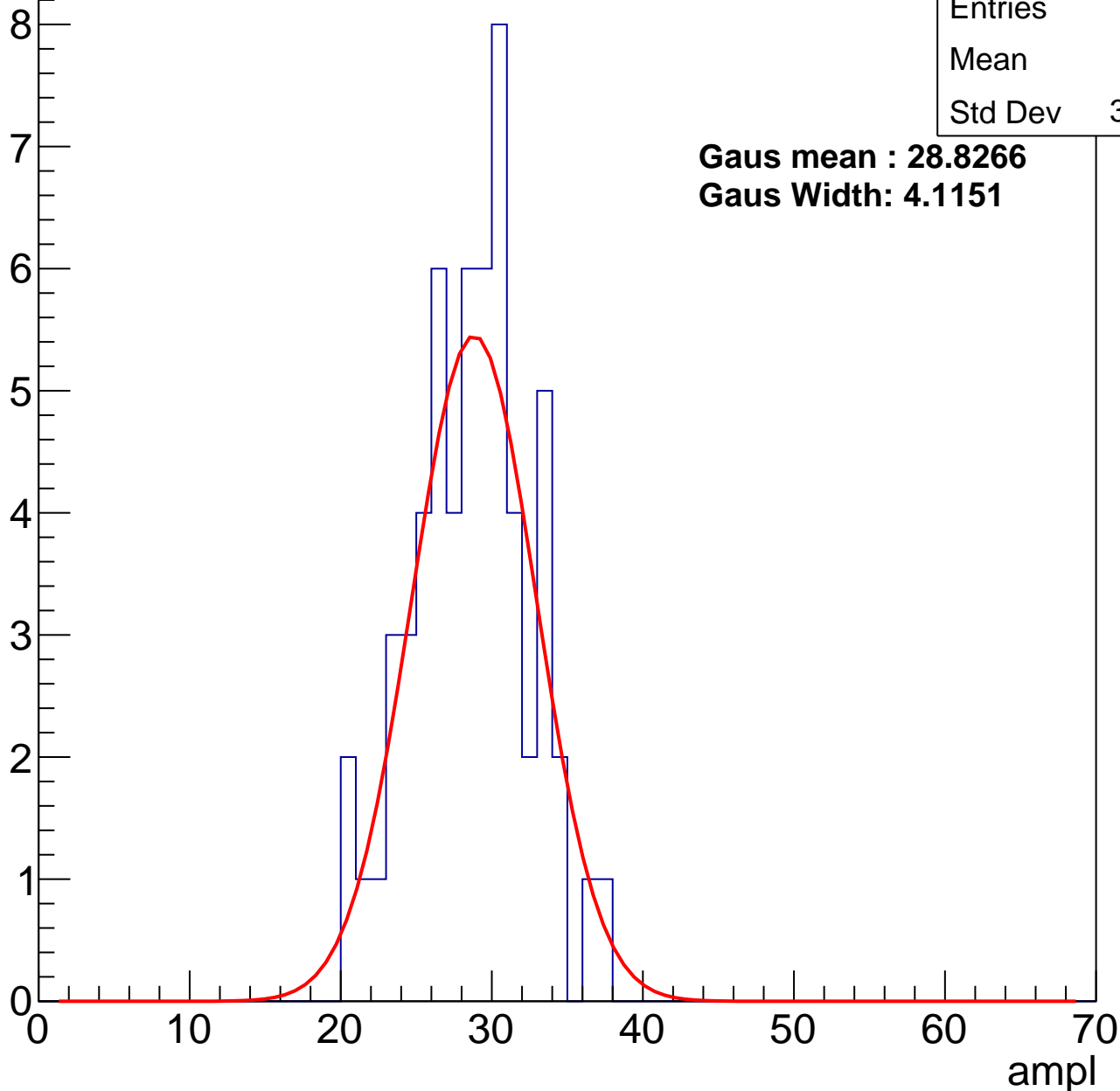
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.2
Std Dev	3.786

Gaus mean : 28.8266

Gaus Width: 4.1151



B1L100S, U14-ch84, adc1

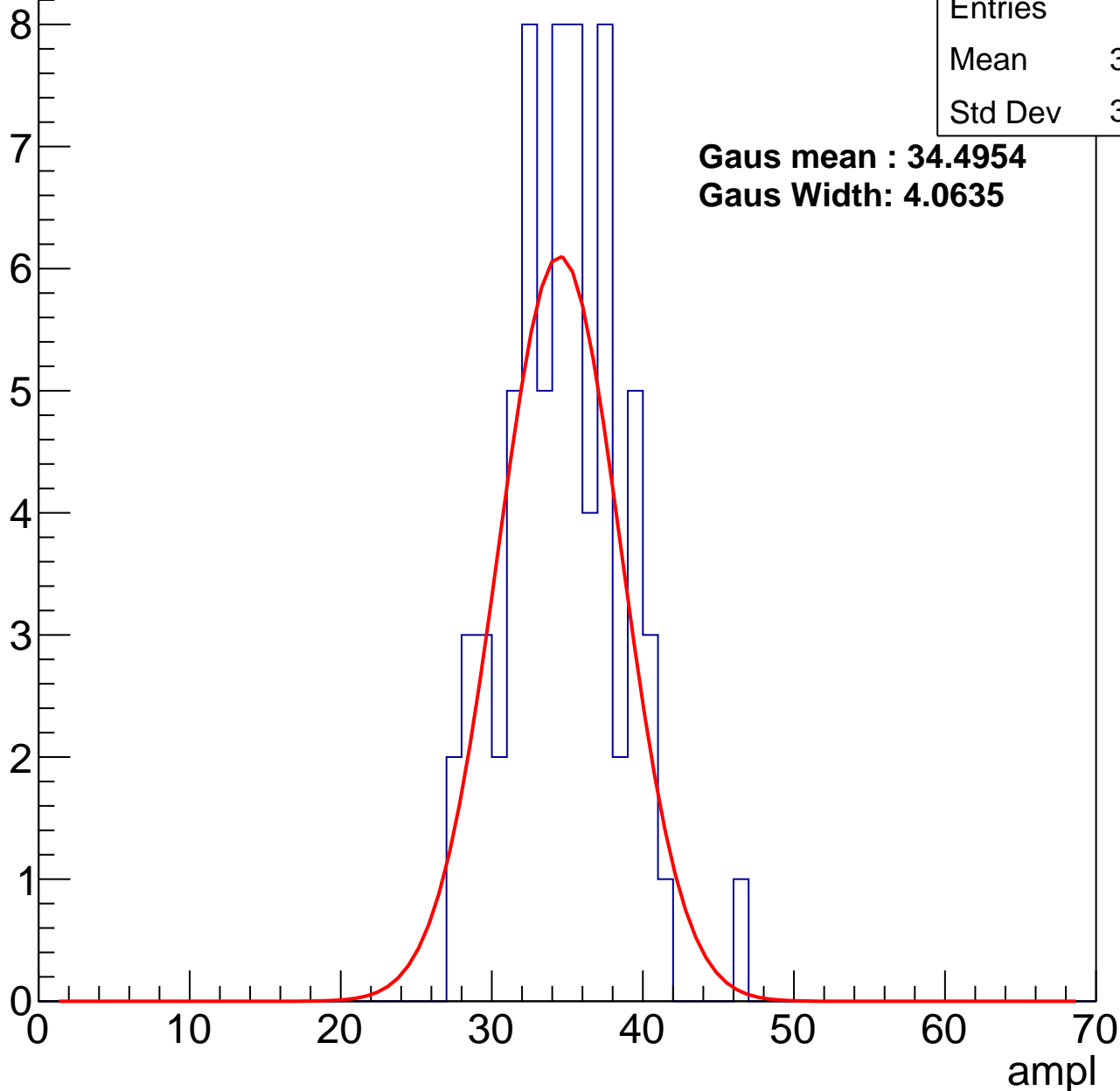
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	34.28
Std Dev	3.725

Gaus mean : 34.4954

Gaus Width: 4.0635



B1L100S, U14-ch84, adc2

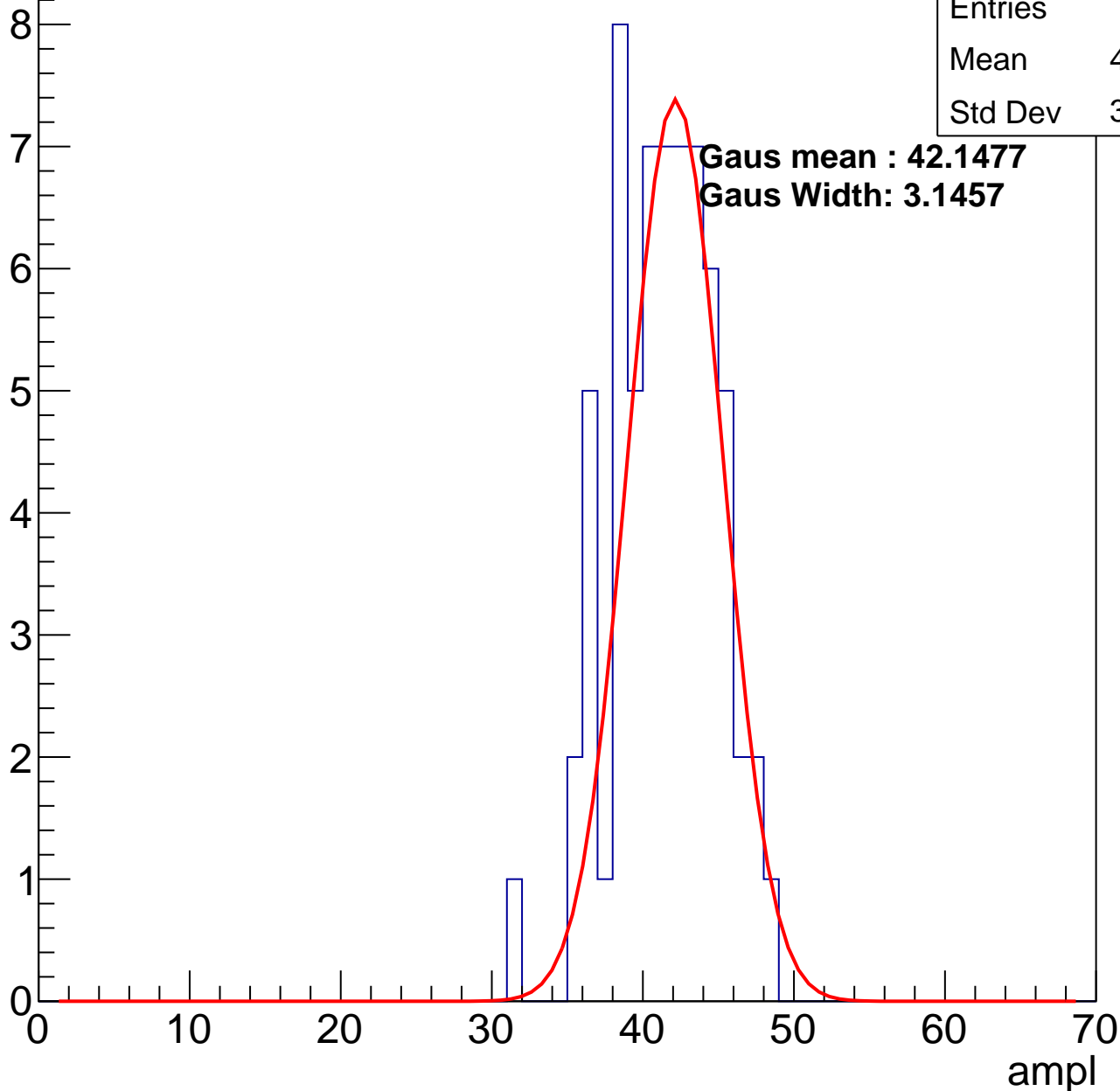
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	40.94
Std Dev	3.375

Gaus mean : 42.1477

Gaus Width: 3.1457

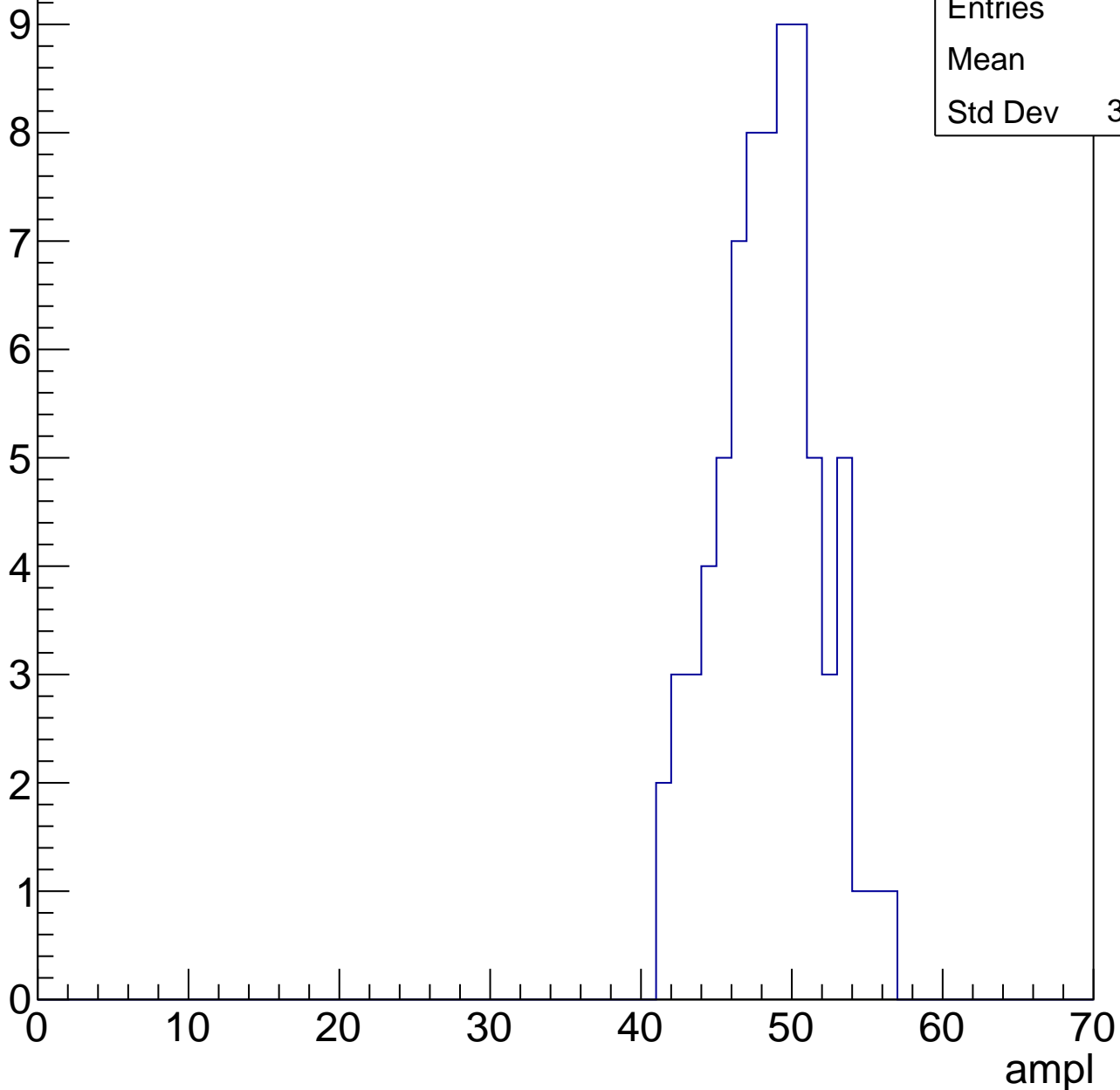


B1L100S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

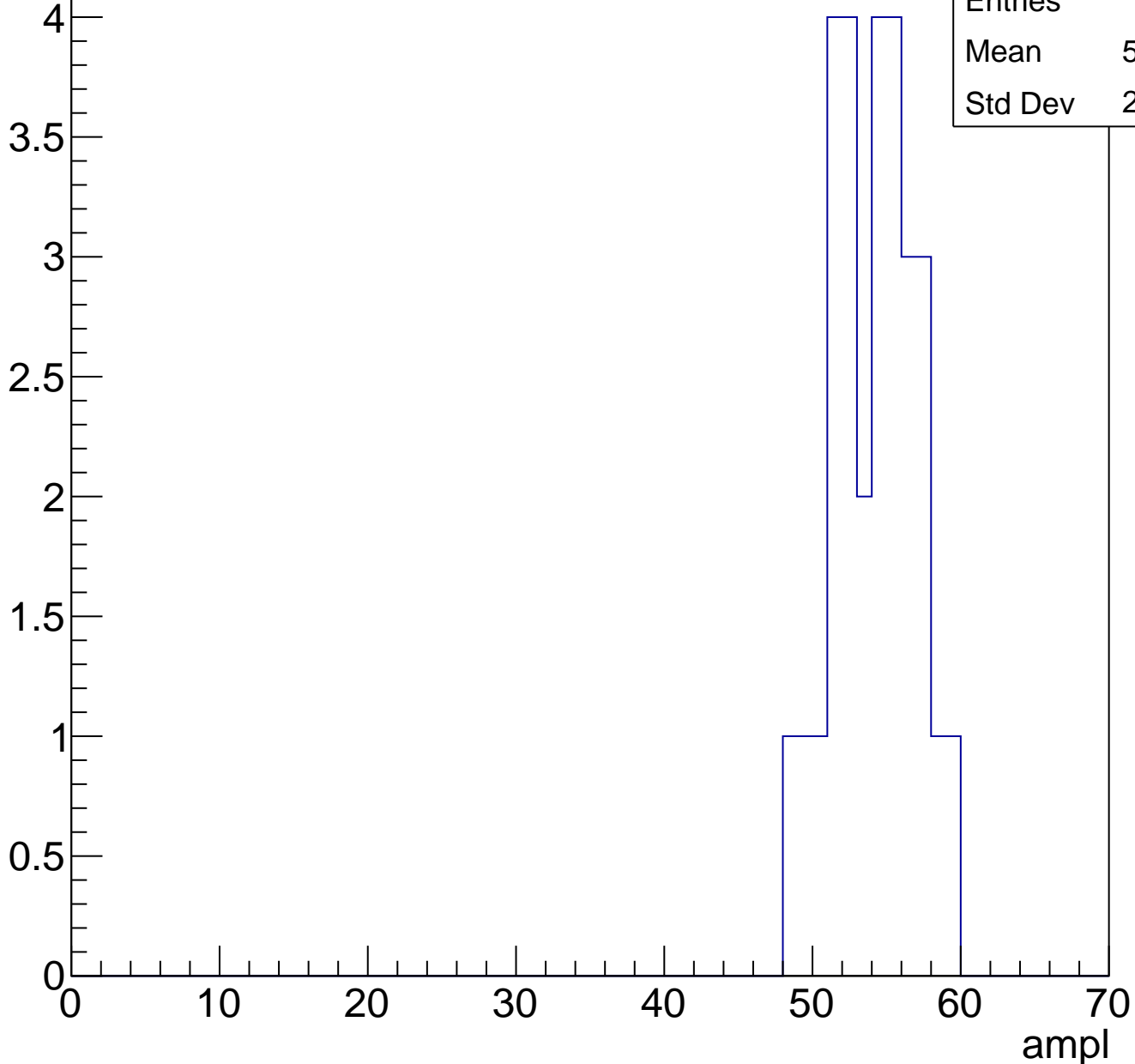
Entries	74
Mean	48
Std Dev	3.365



B1L100S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

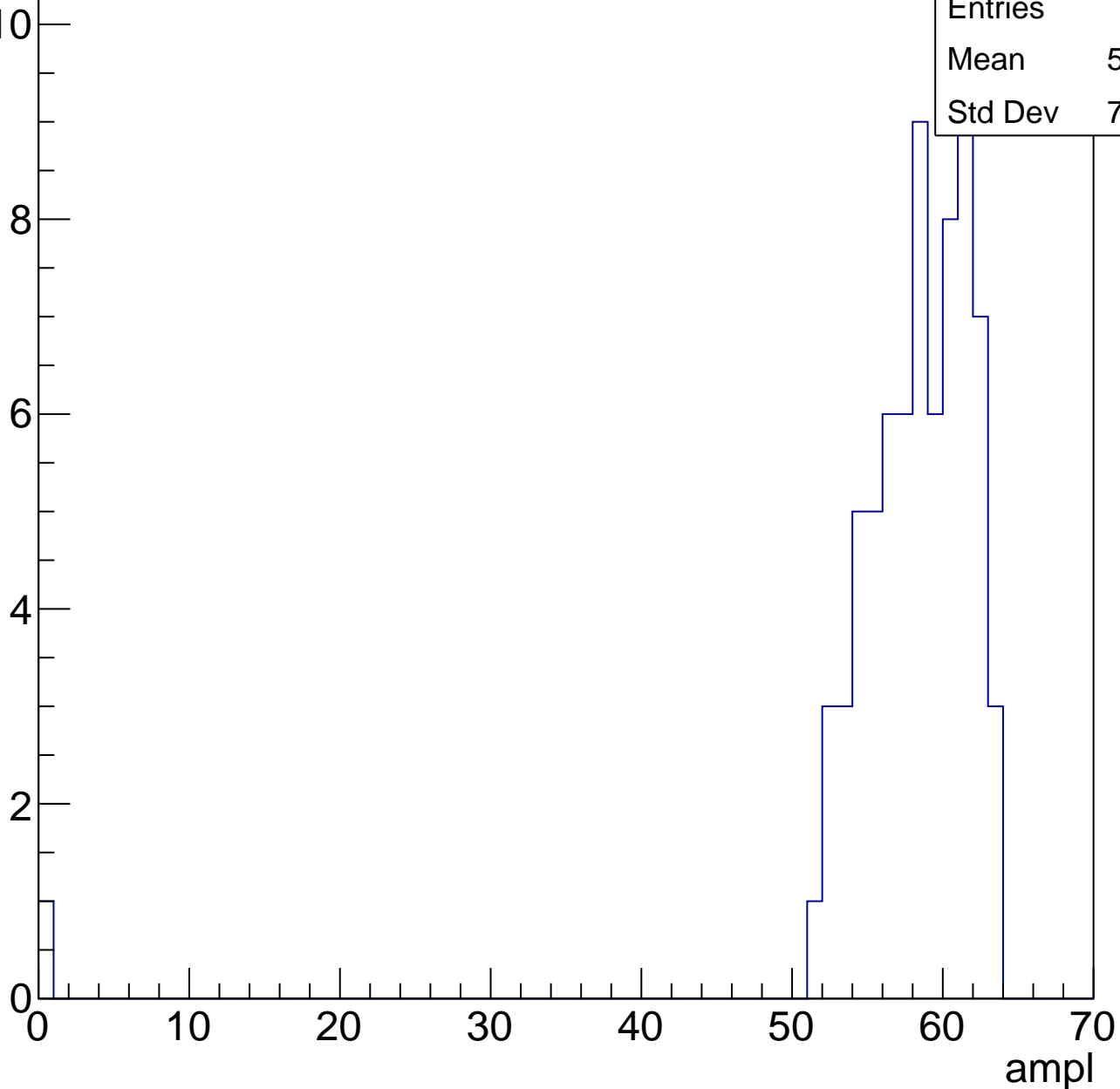


B1L100S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	57.23
Std Dev	7.424

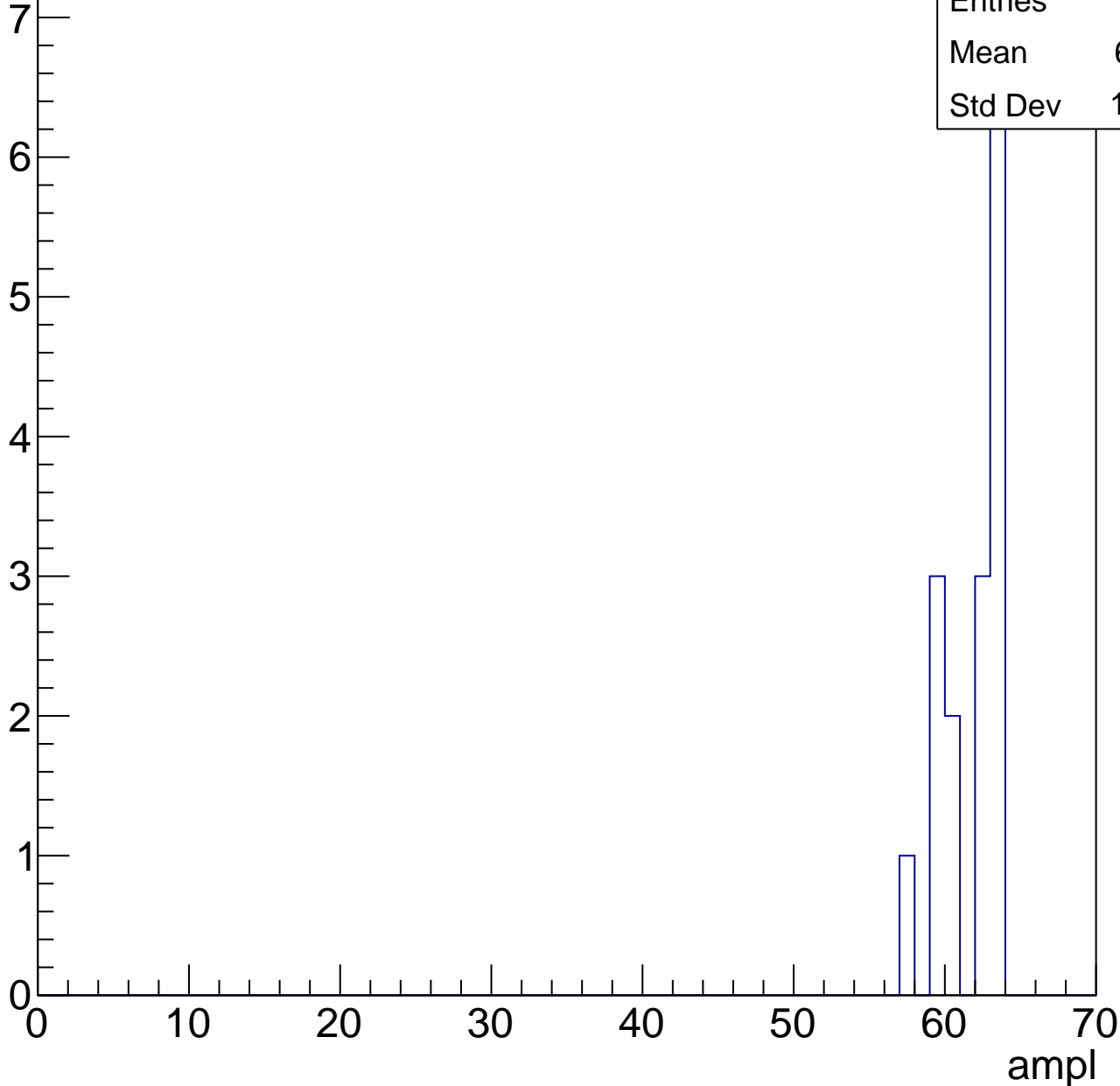


B1L100S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	61.31
Std Dev	1.927



B1L100S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch85, adc0

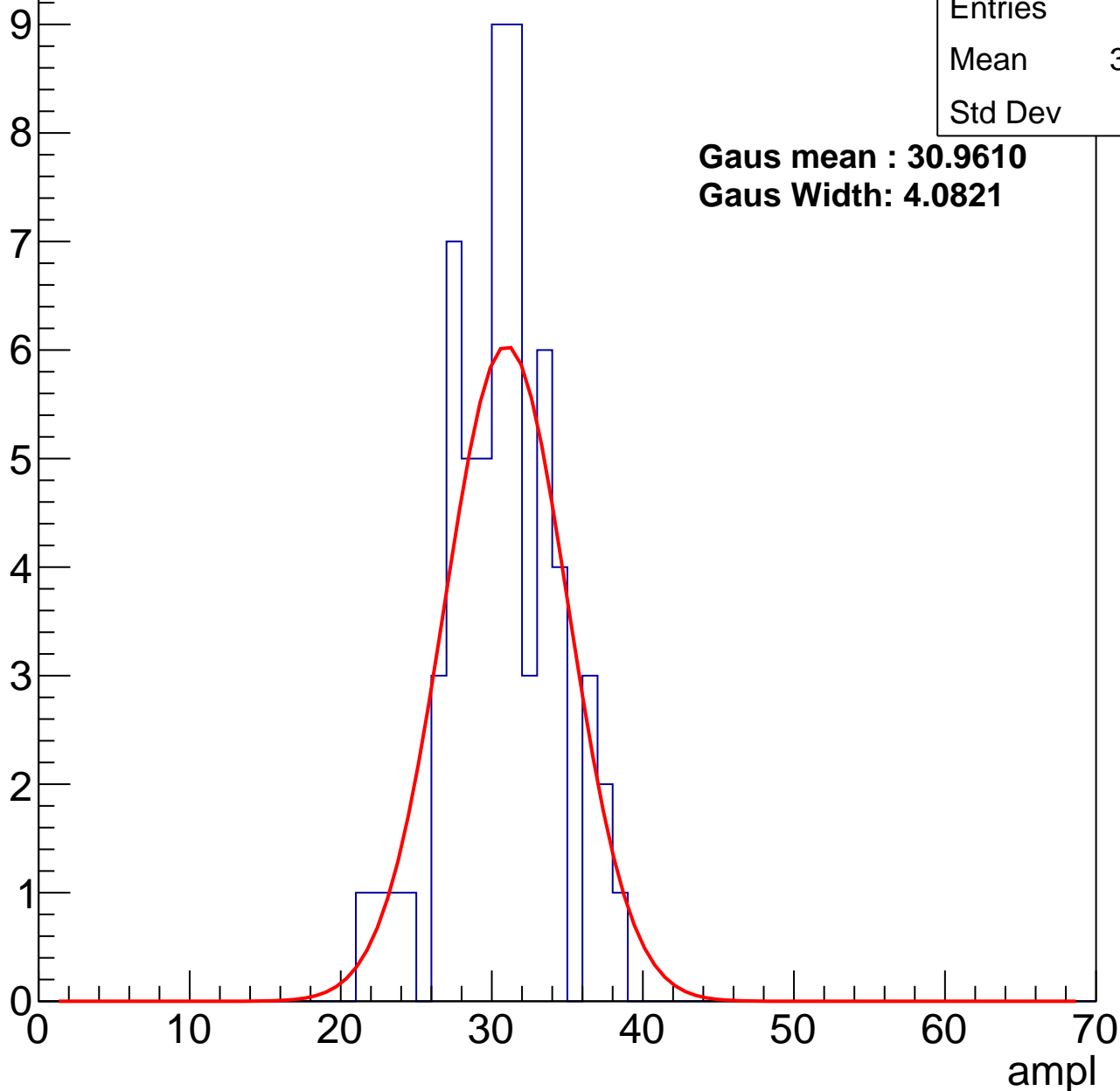
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.18
Std Dev	3.56

Gaus mean : 30.9610

Gaus Width: 4.0821



B1L100S, U14-ch85, adc1

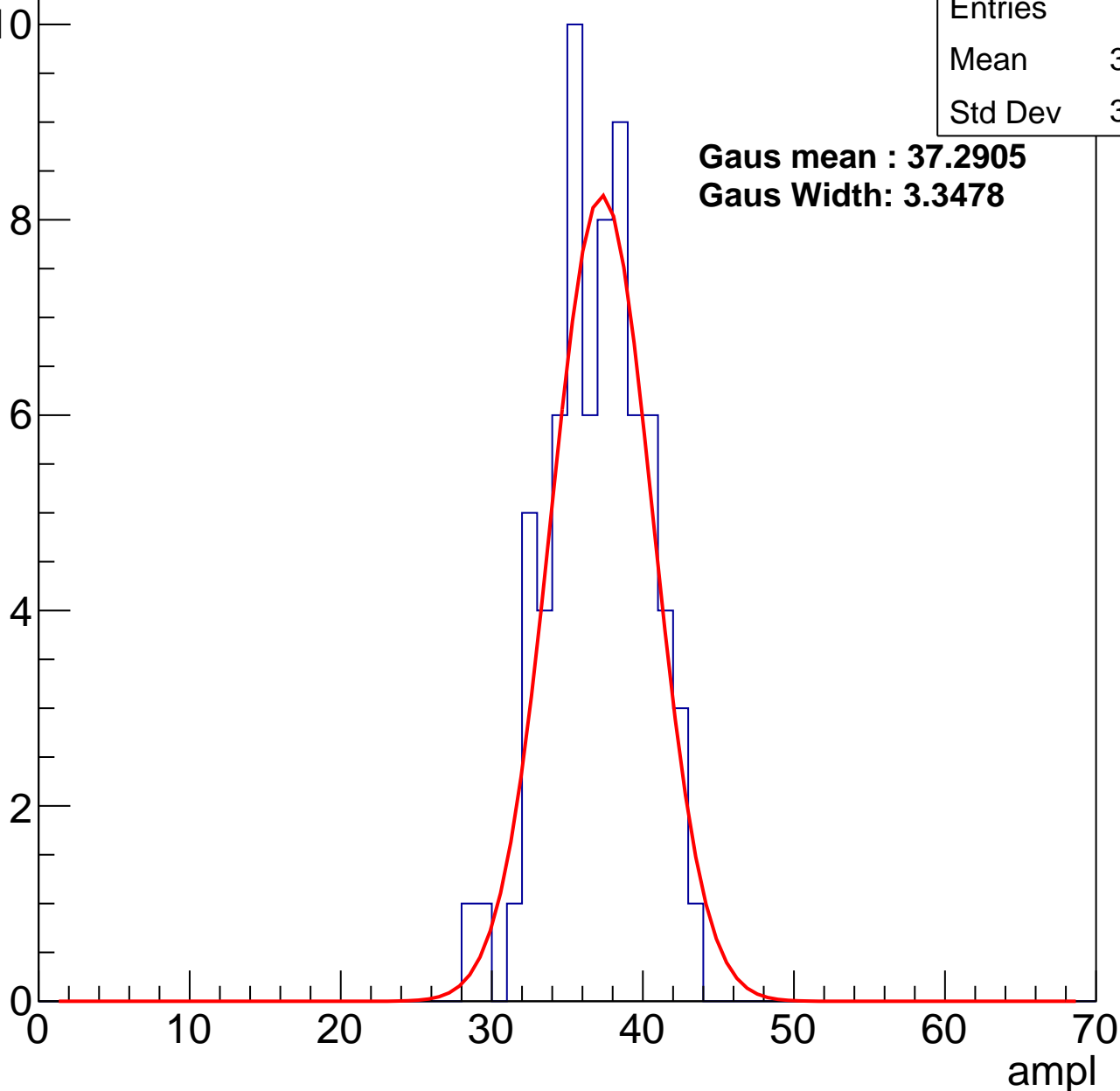
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.55
Std Dev	3.179

Gaus mean : 37.2905

Gaus Width: 3.3478



B1L100S, U14-ch85, adc2

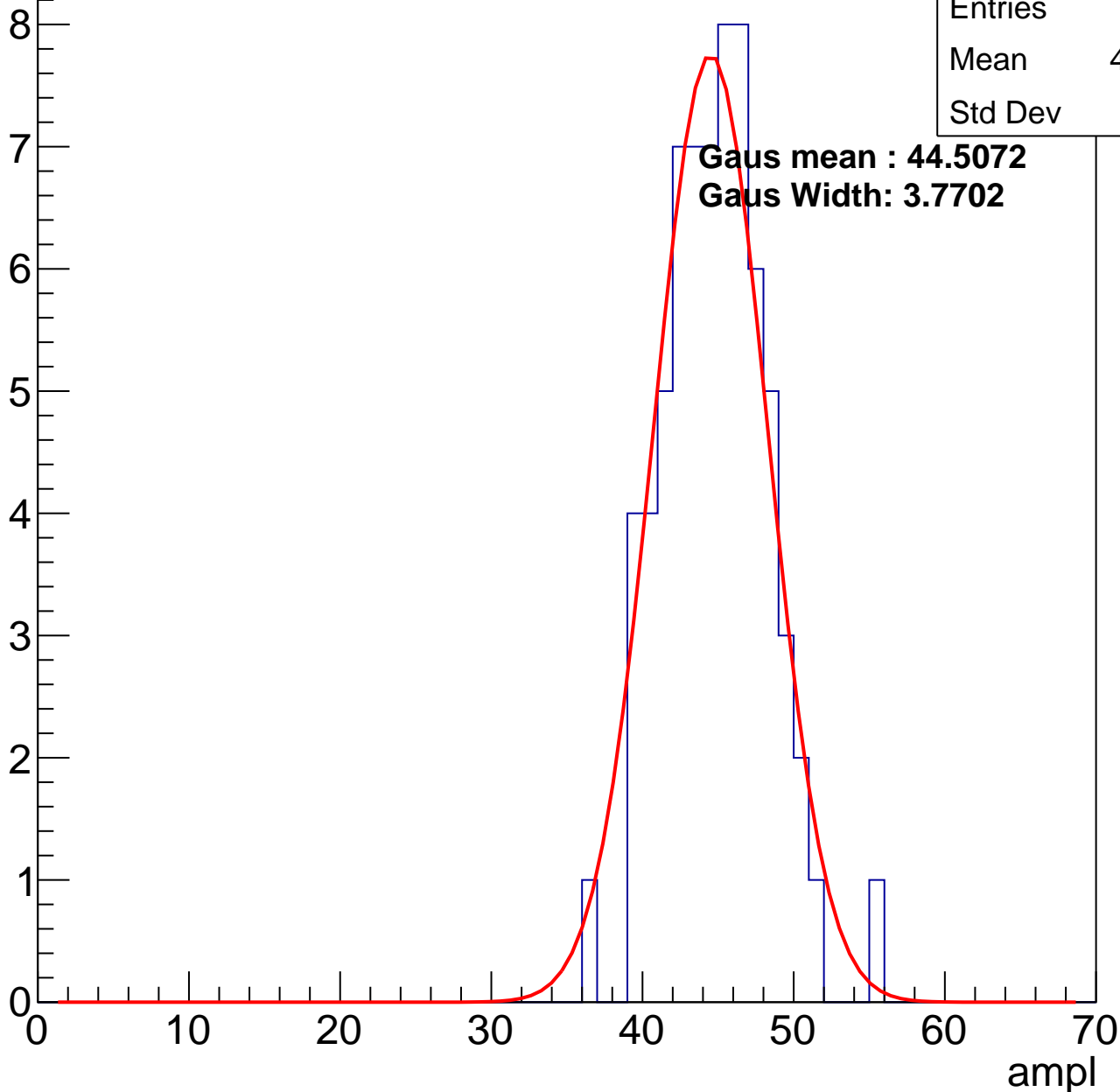
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.39
Std Dev	3.38

Gaus mean : 44.5072

Gaus Width: 3.7702

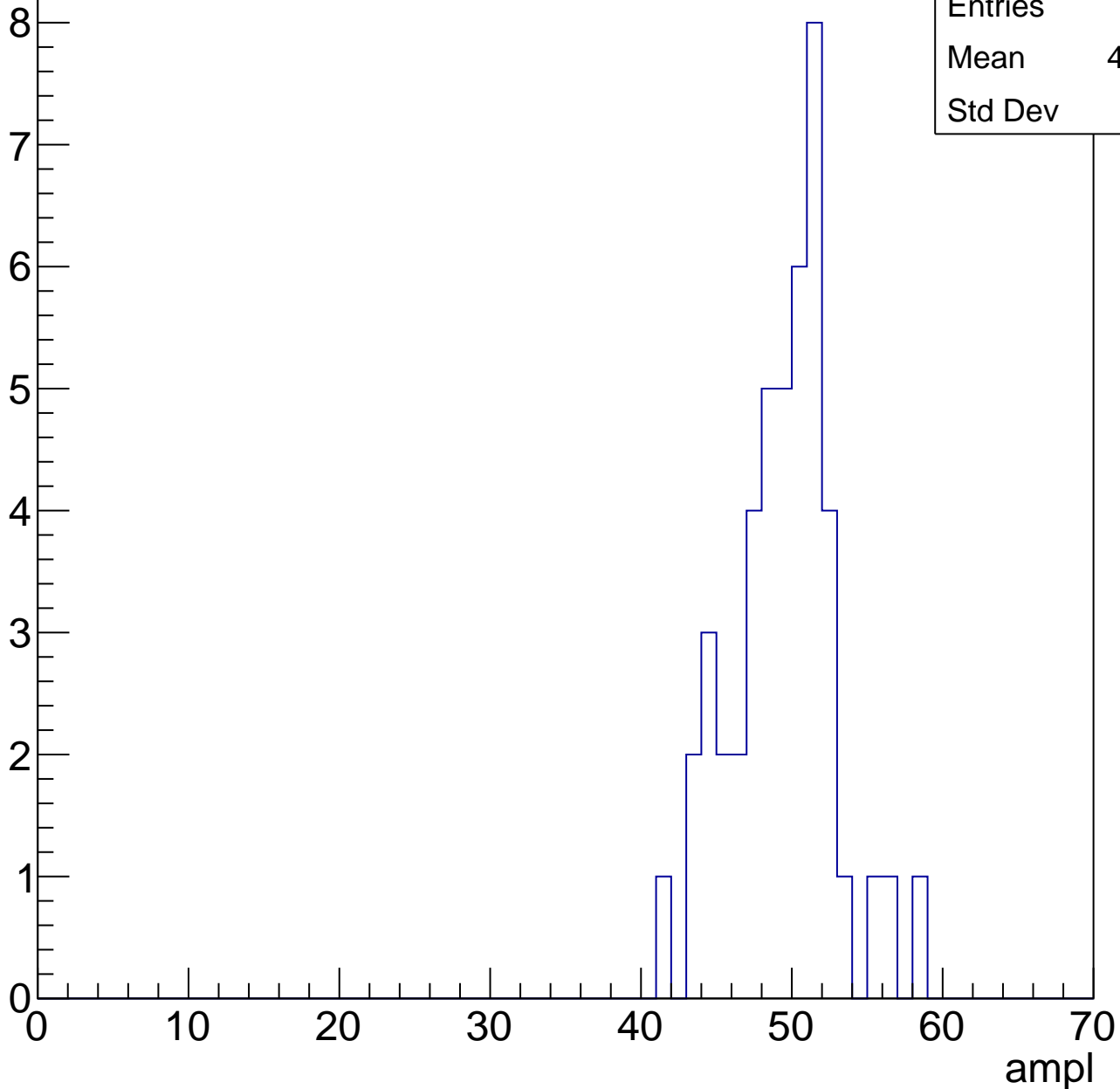


B1L100S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

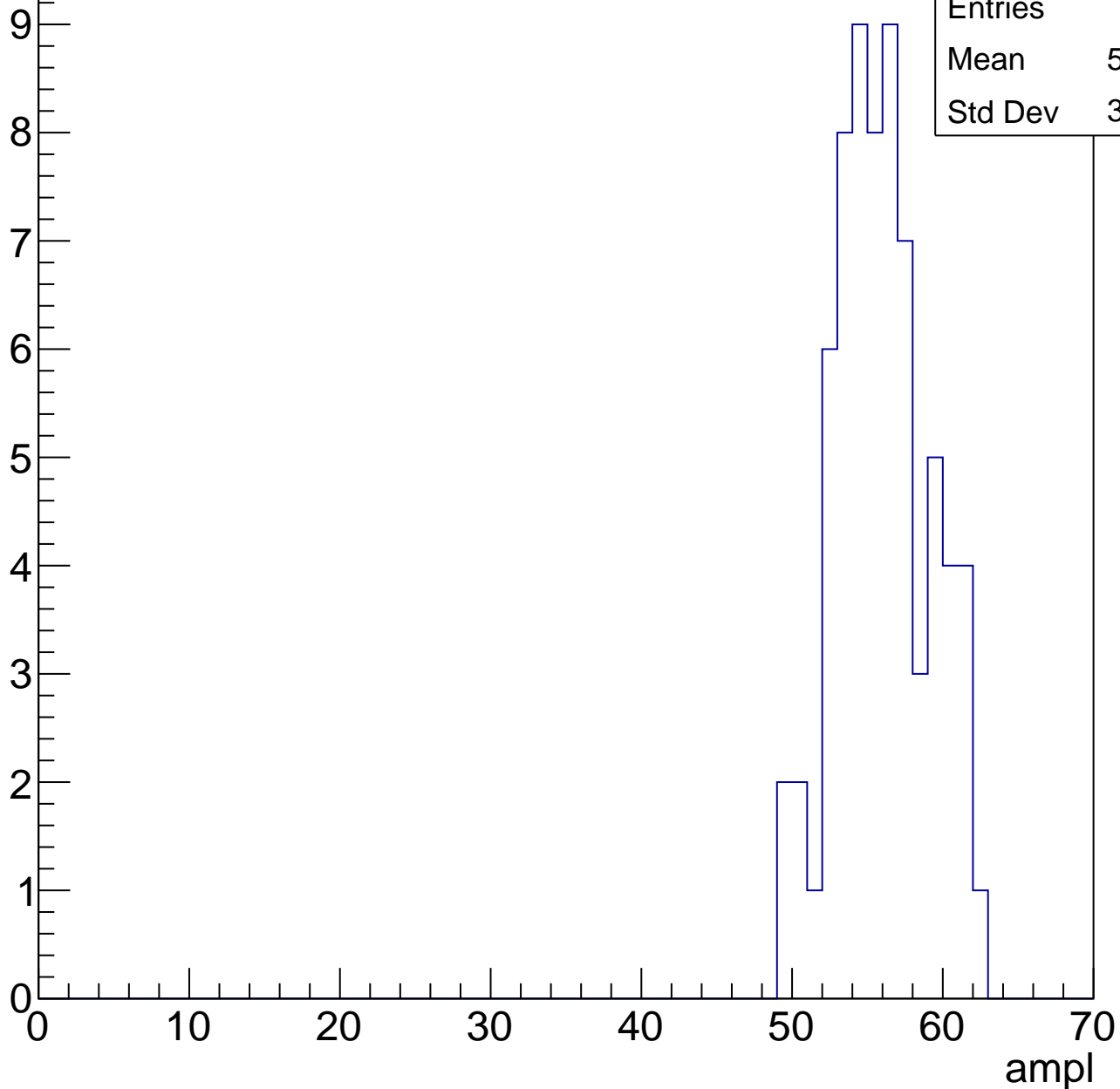
Entries	46
Mean	48.96
Std Dev	3.42



B1L100S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



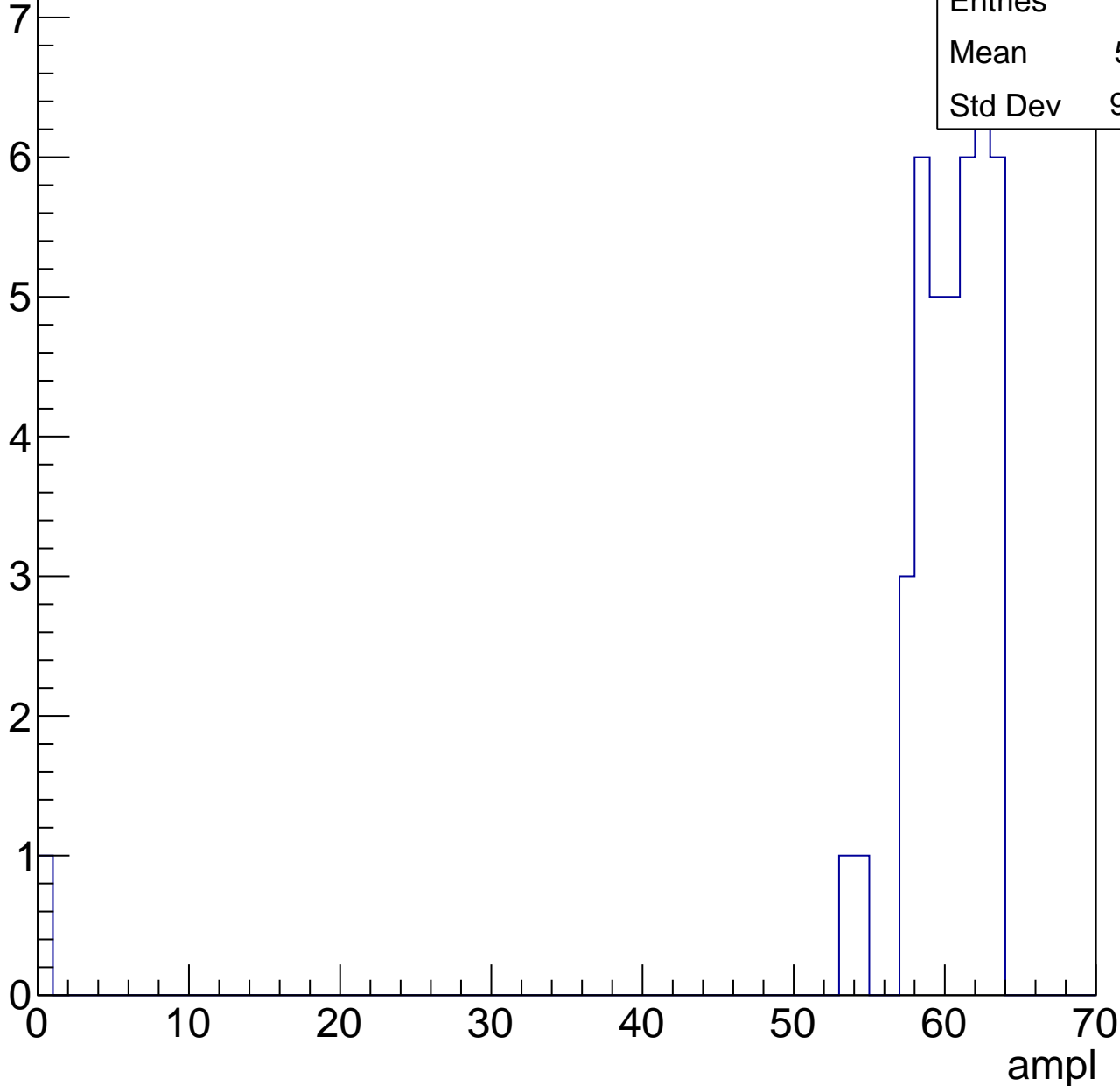
Entries	69
Mean	55.49
Std Dev	3.077

B1L100S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	58.51
Std Dev	9.549



B1L100S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch86, adc0

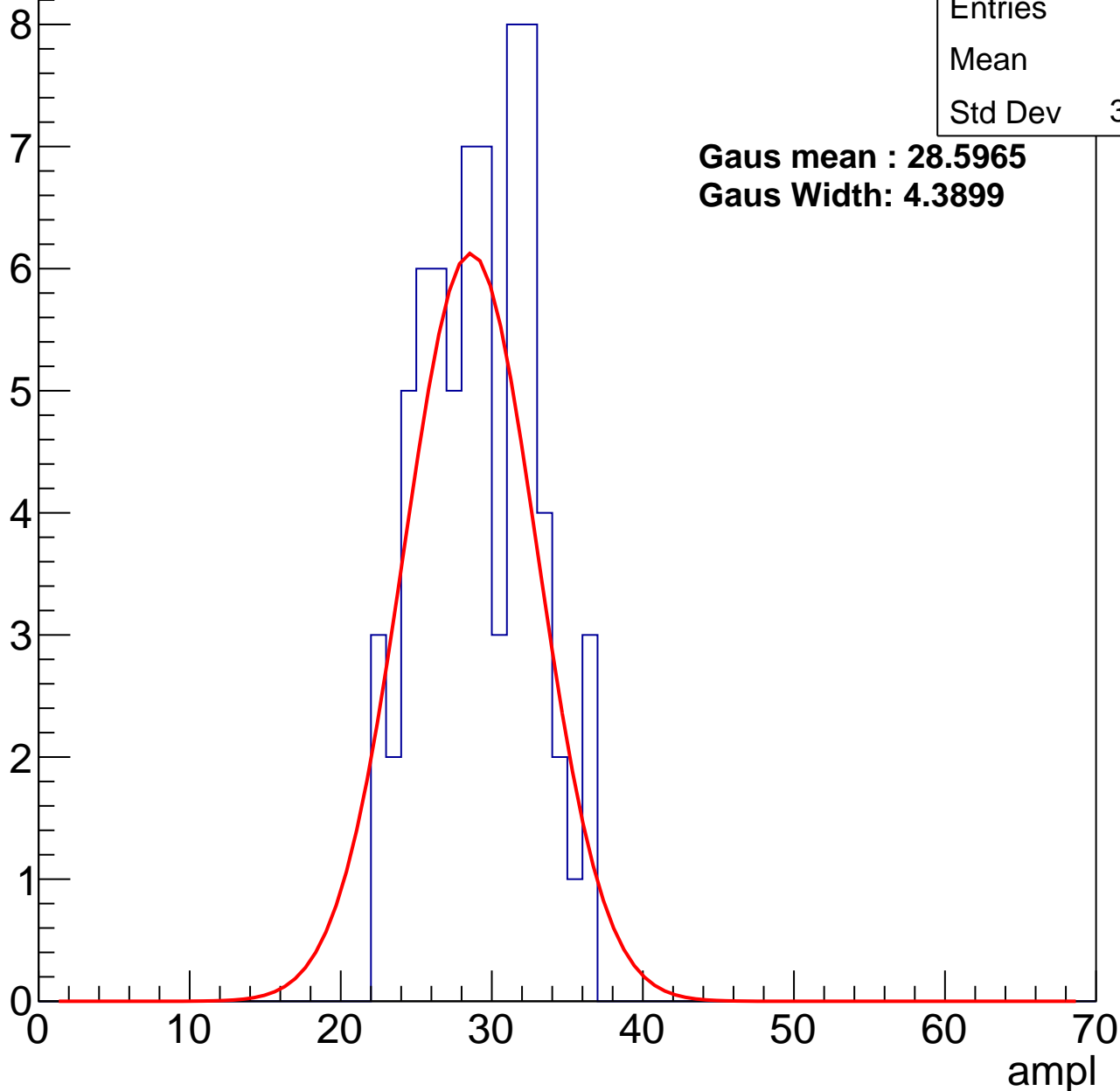
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.7
Std Dev	3.623

Gaus mean : 28.5965

Gaus Width: 4.3899



B1L100S, U14-ch86, adc1

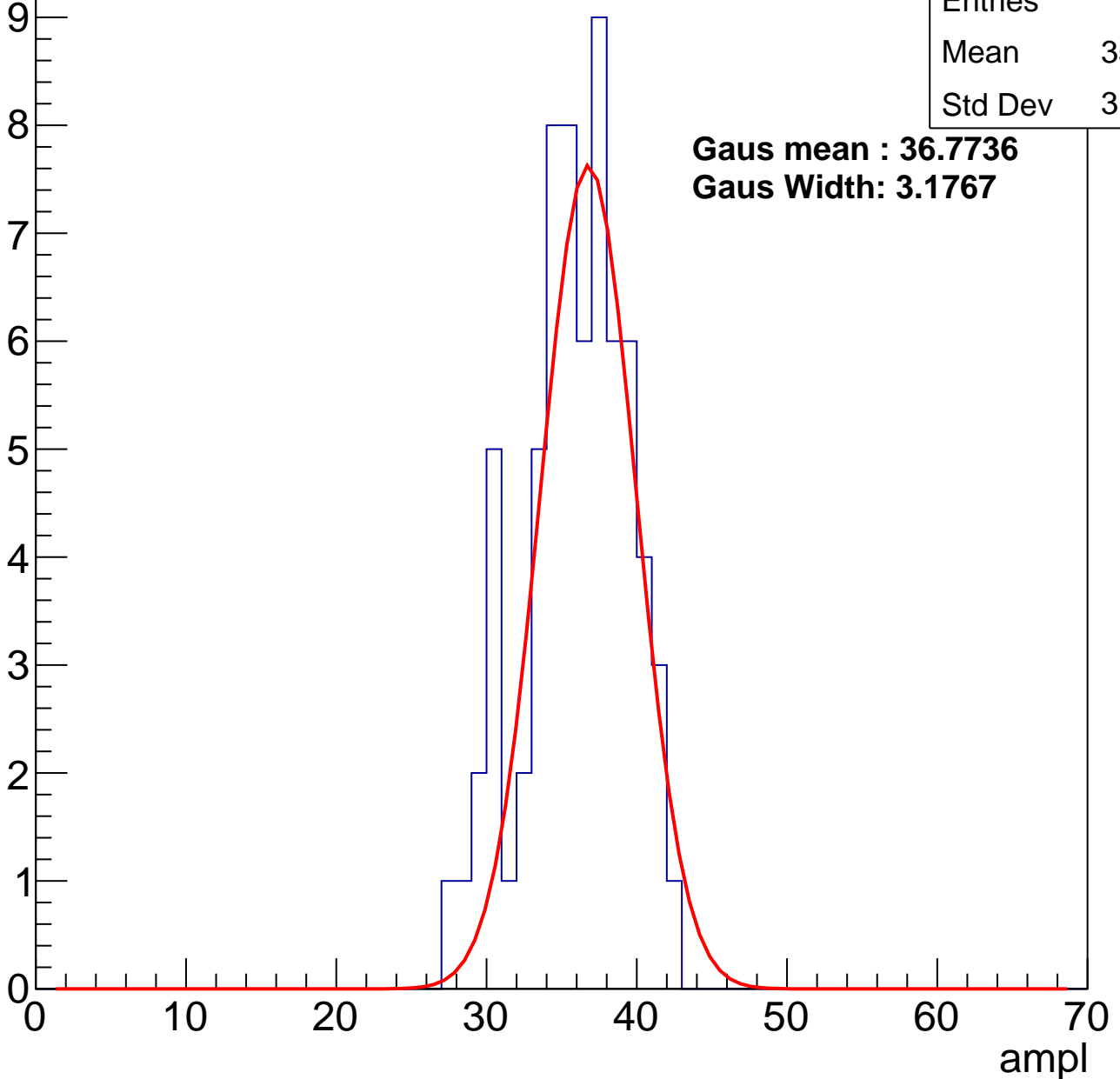
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.46
Std Dev	3.445

Gaus mean : 36.7736

Gaus Width: 3.1767



B1L100S, U14-ch86, adc2

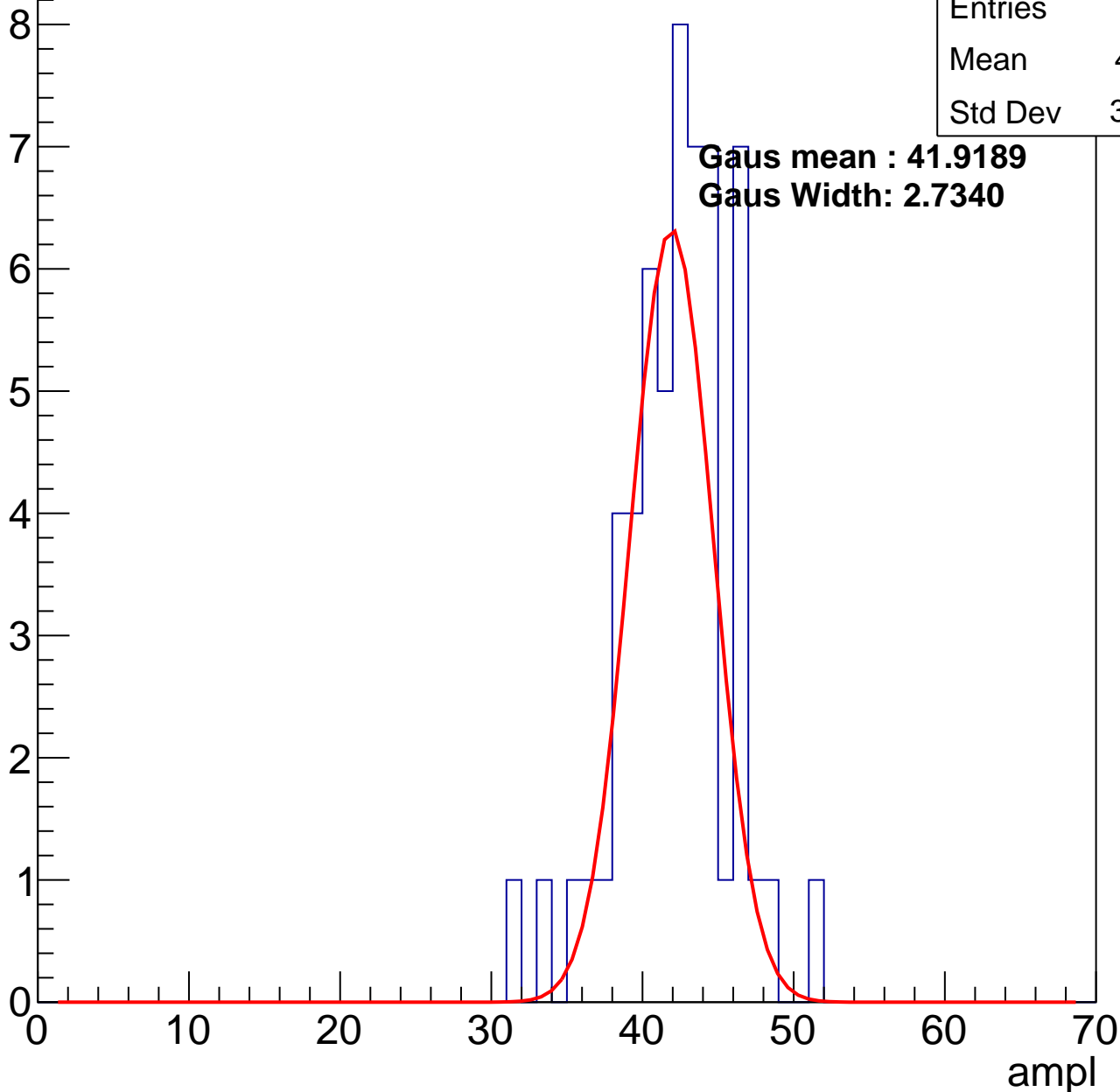
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	41.81
Std Dev	3.596

Gaus mean : 41.9189

Gaus Width: 2.7340

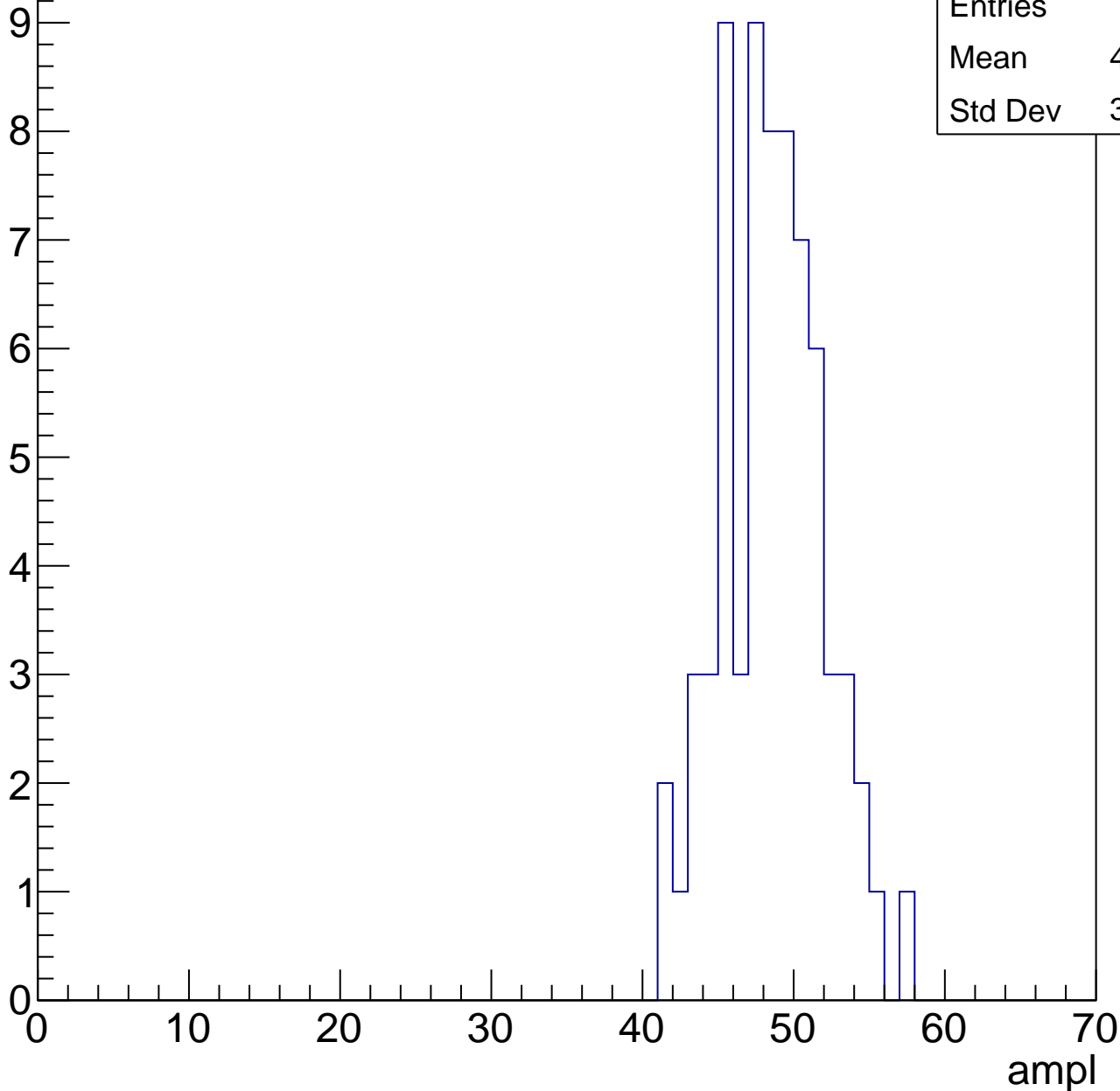


B1L100S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	48.09
Std Dev	3.348

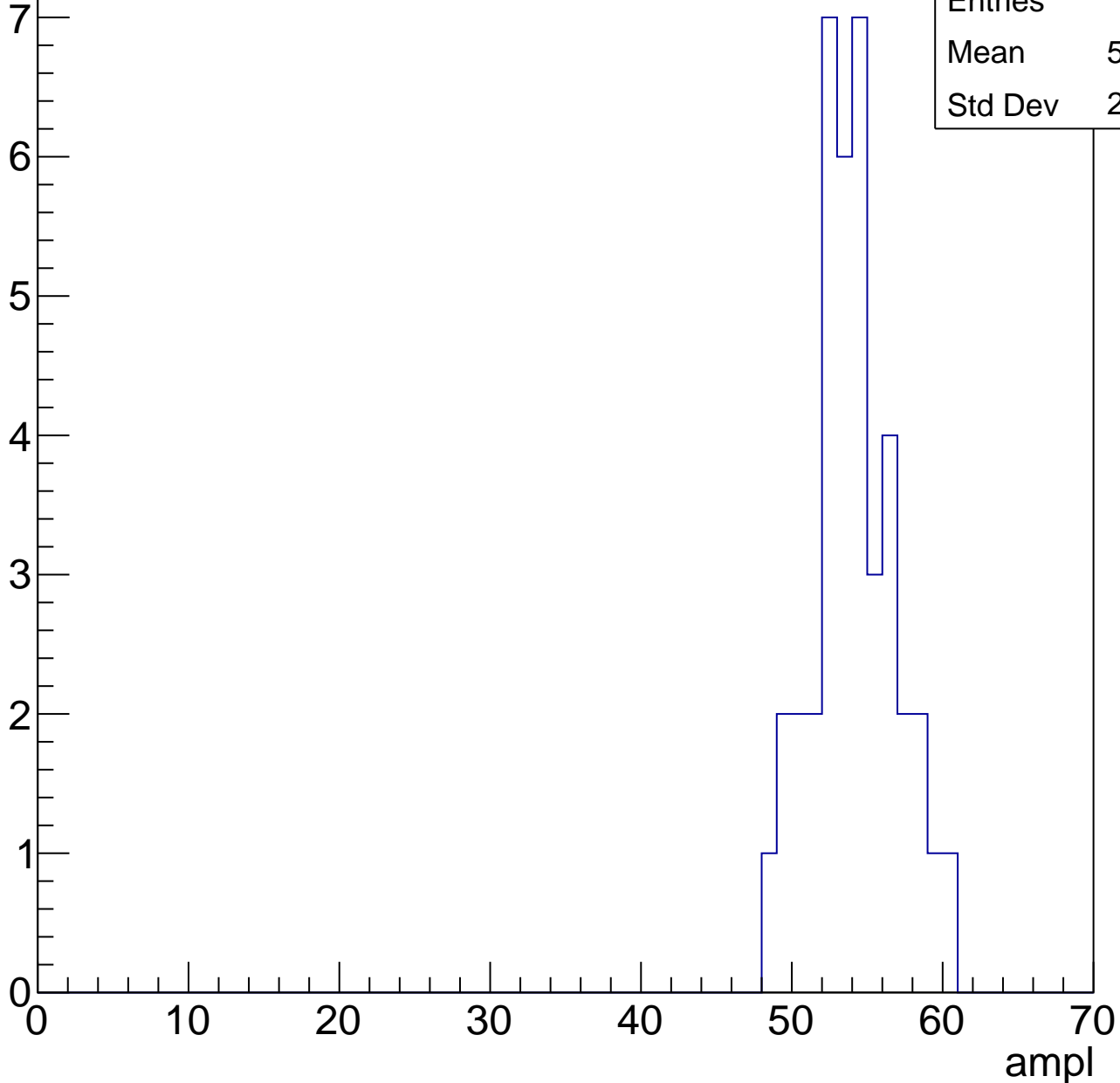


B1L100S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	53.65
Std Dev	2.716

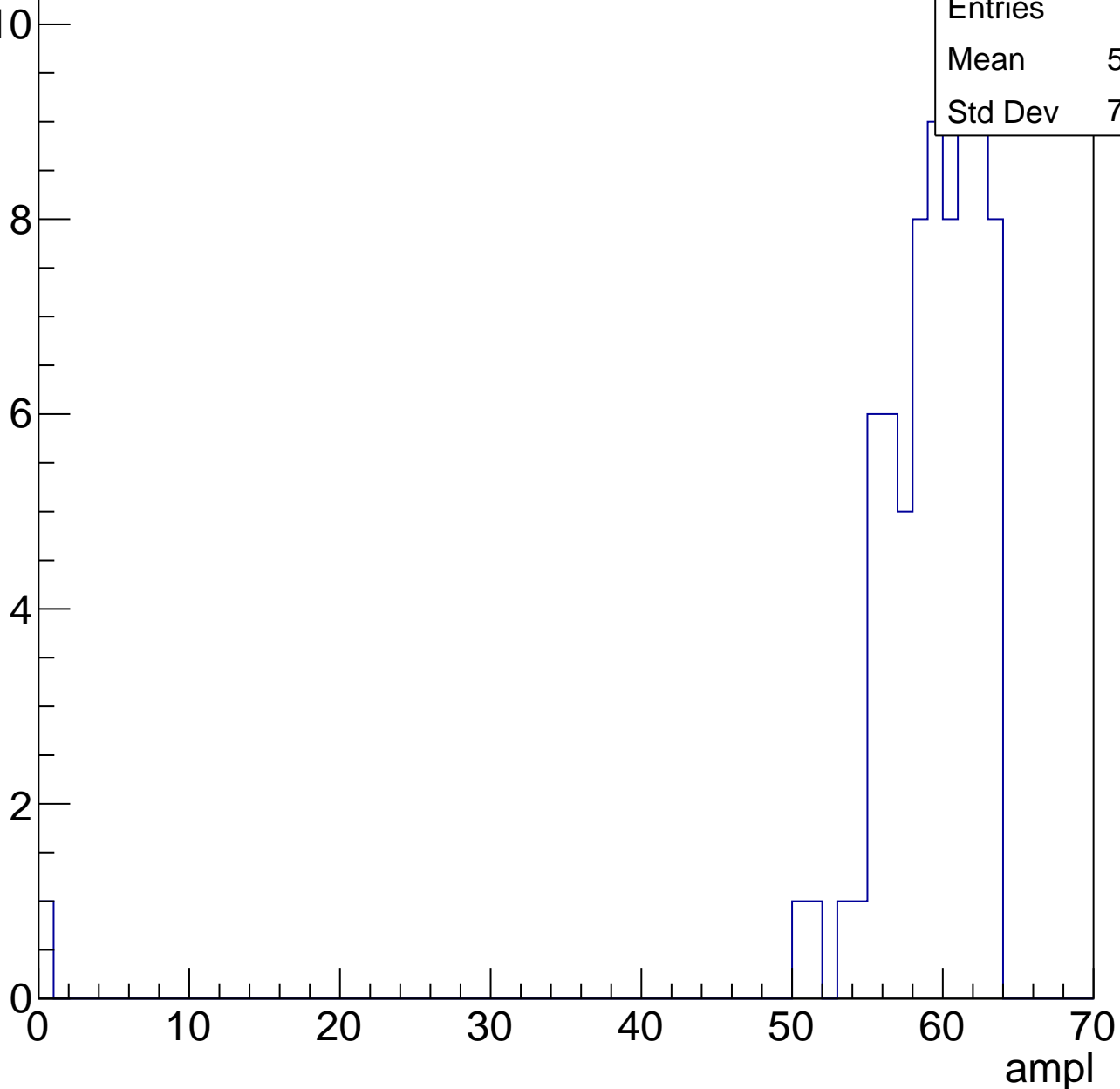


B1L100S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	58.19
Std Dev	7.419



B1L100S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

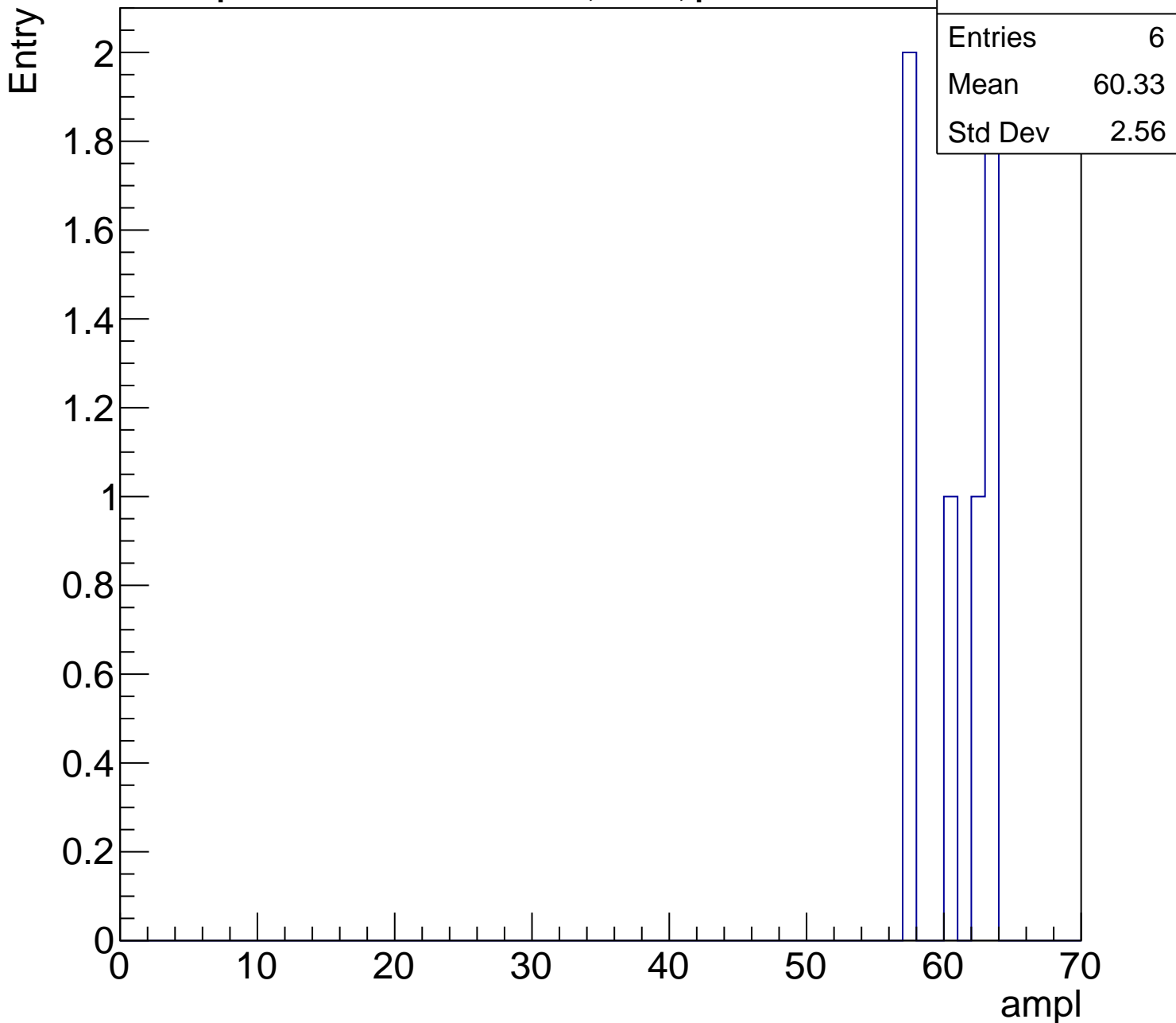
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.33
Std Dev	2.56

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch87, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.35
Std Dev	3.253

Gaus mean : 29.7829

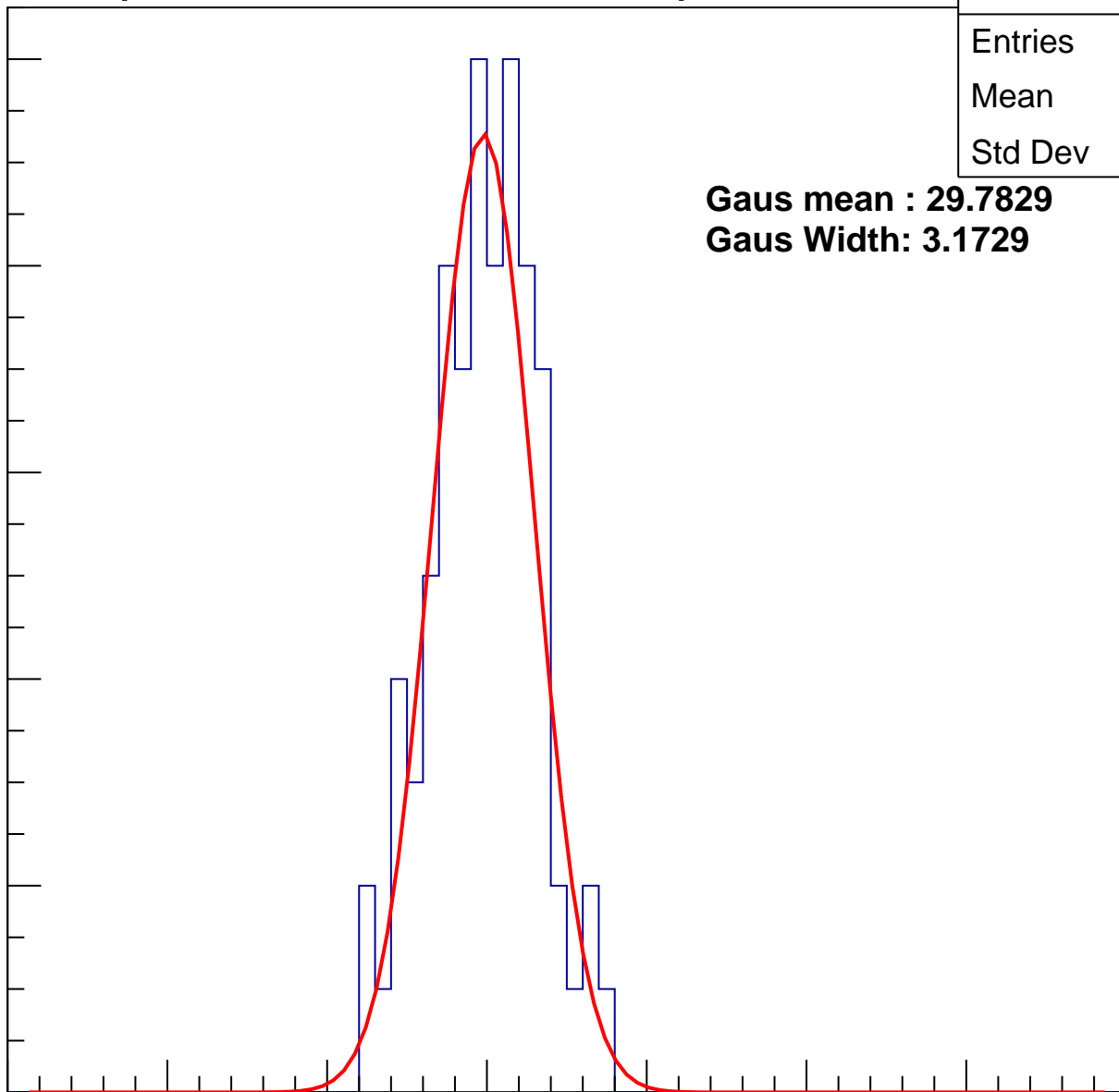
Gaus Width: 3.1729

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch87, adc1

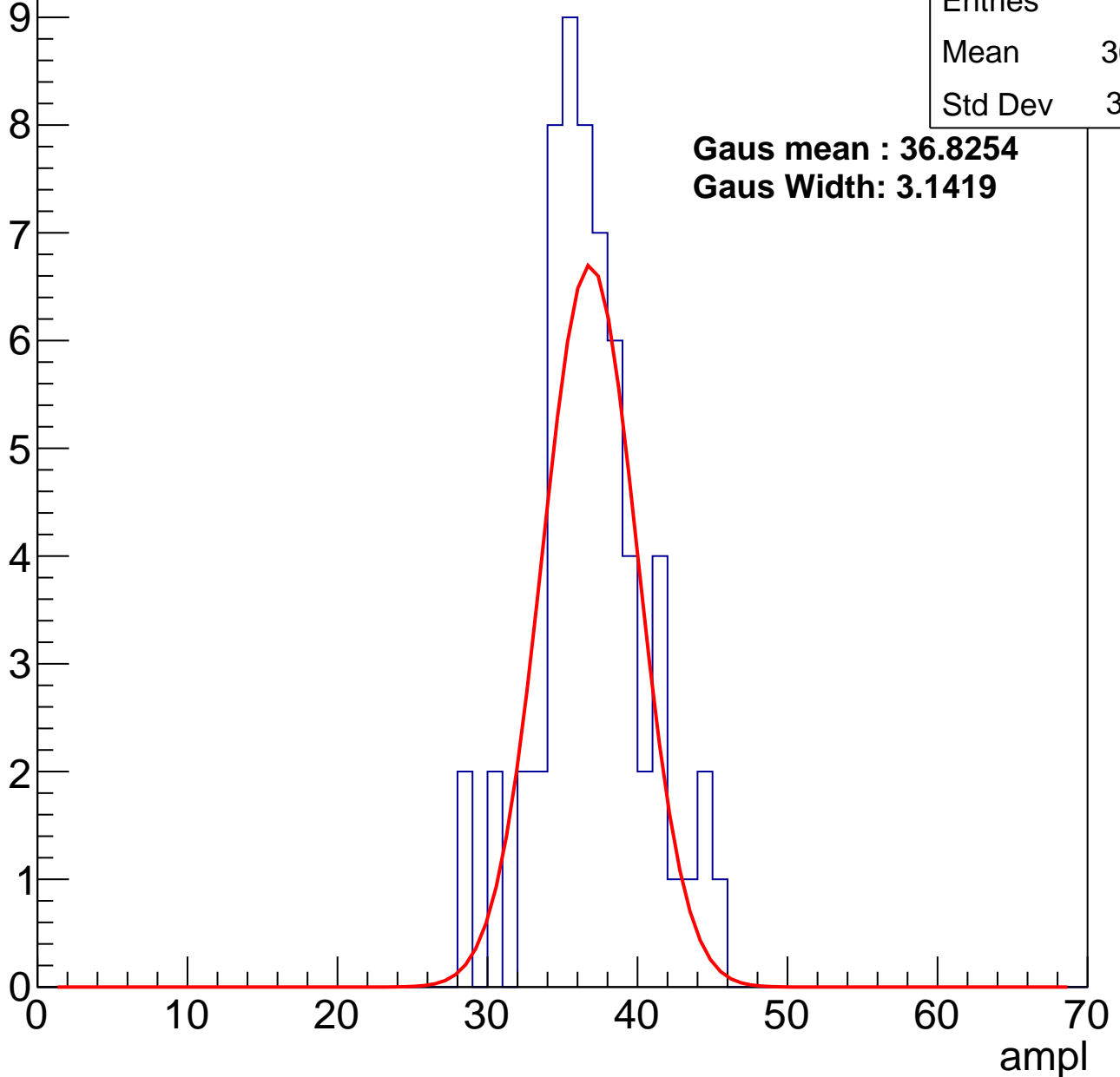
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.49
Std Dev	3.551

Gaus mean : 36.8254

Gaus Width: 3.1419



B1L100S, U14-ch87, adc2

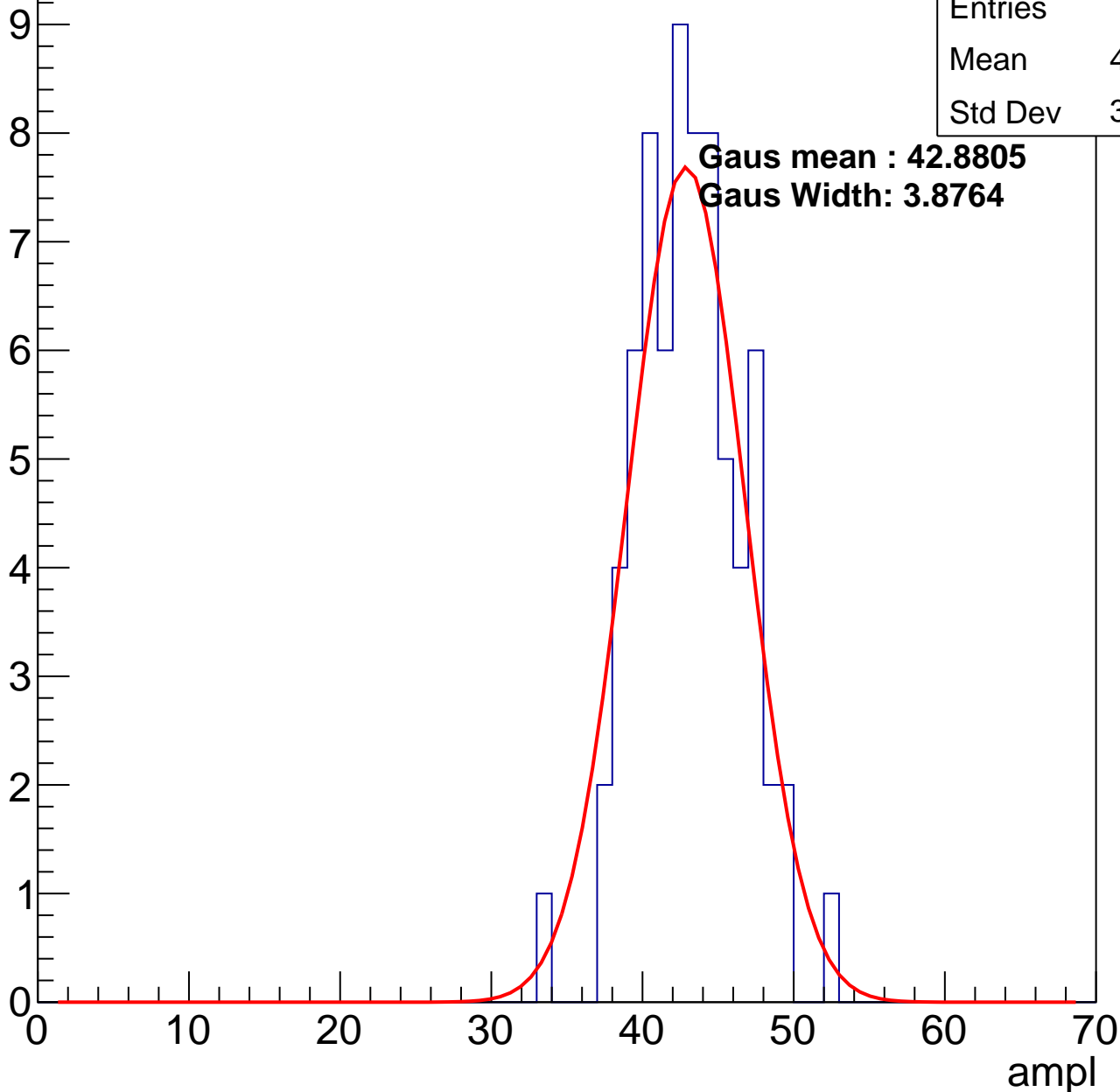
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.64
Std Dev	3.388

Gaus mean : 42.8805

Gaus Width: 3.8764

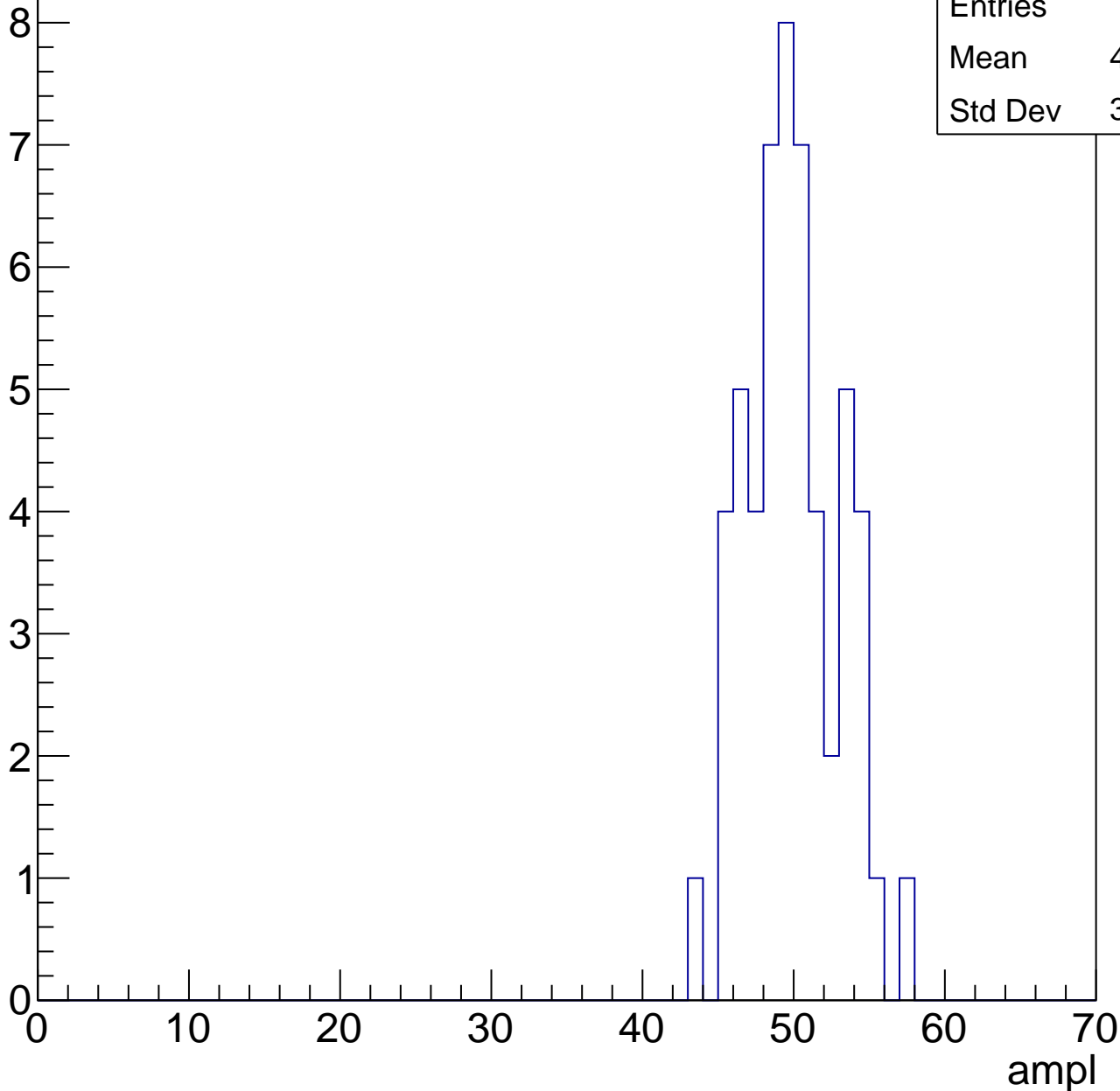


B1L100S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

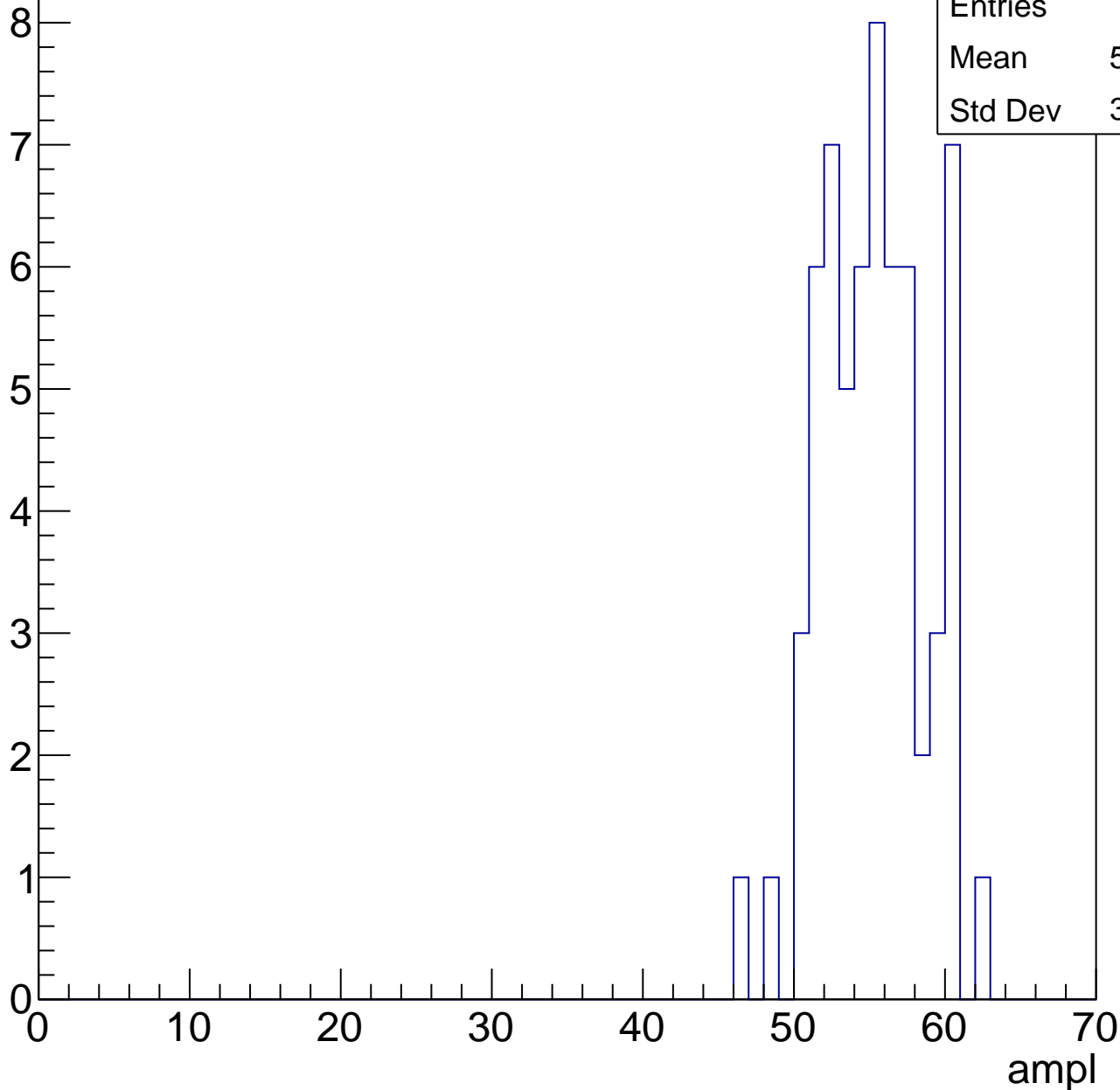
Entries	53
Mean	49.43
Std Dev	3.006



B1L100S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

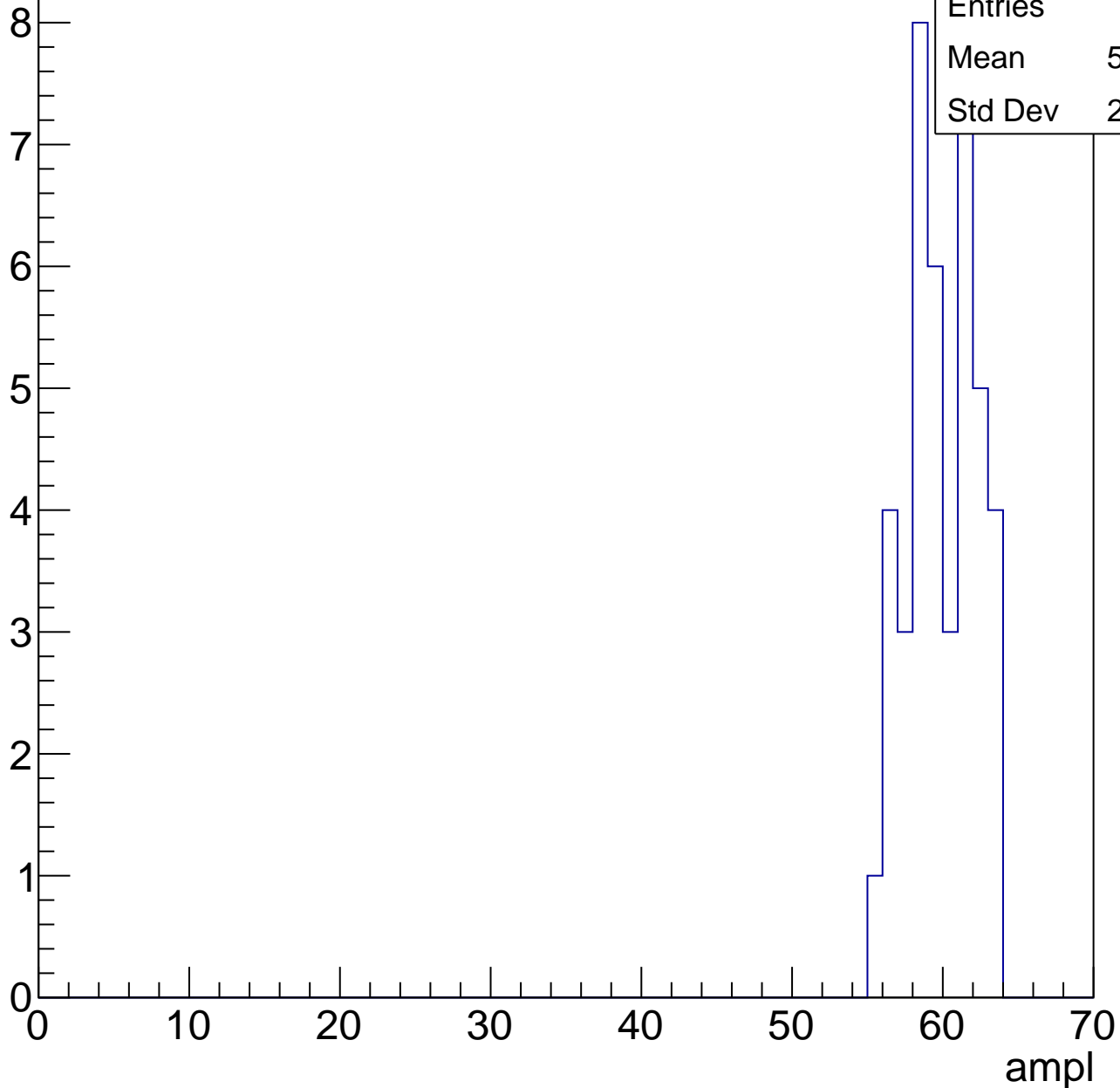
Entry



B1L100S, U14-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

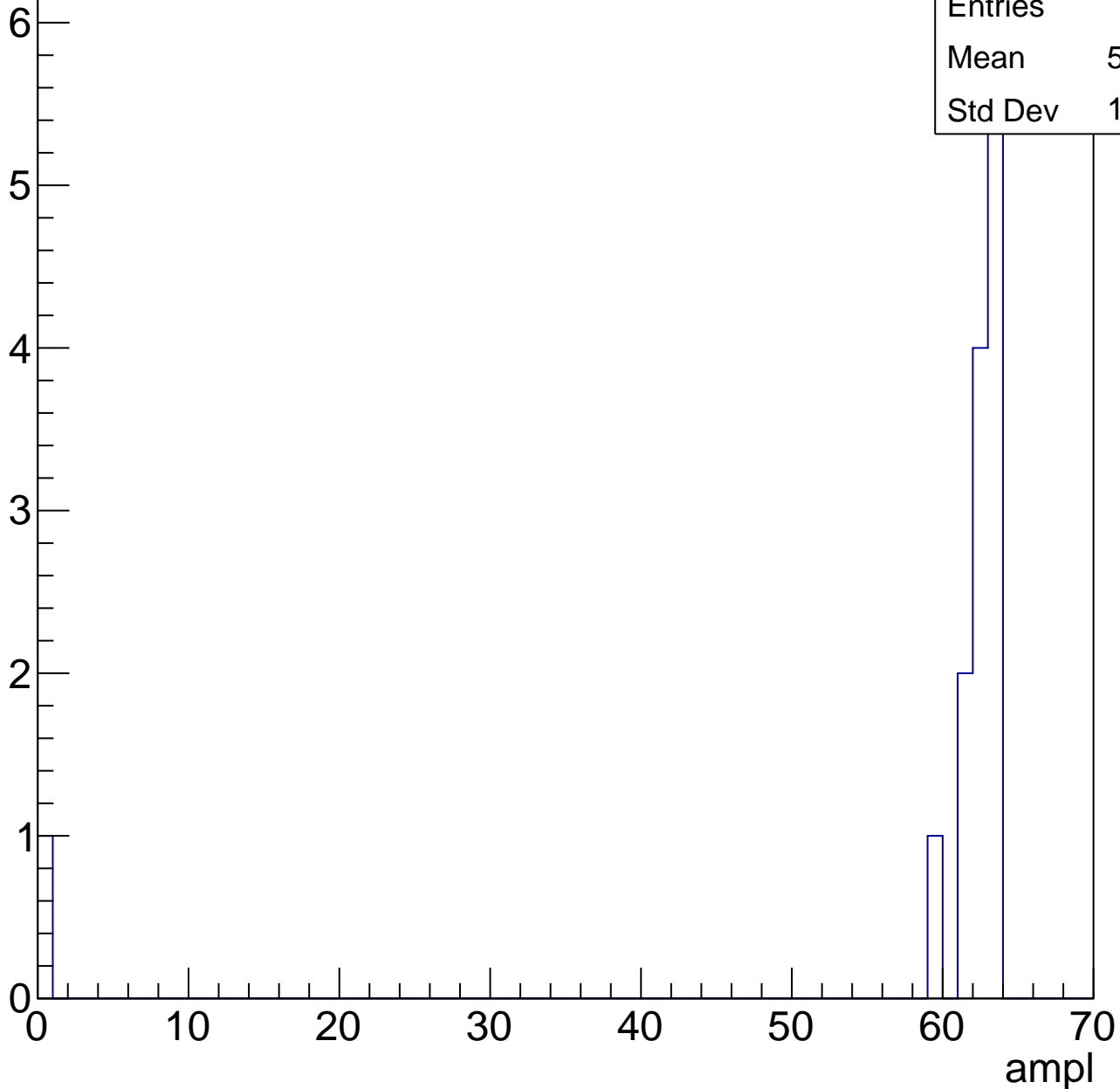


B1L100S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	57.64
Std Dev	16.03



B1L100S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L100S, U14-ch88, adc0

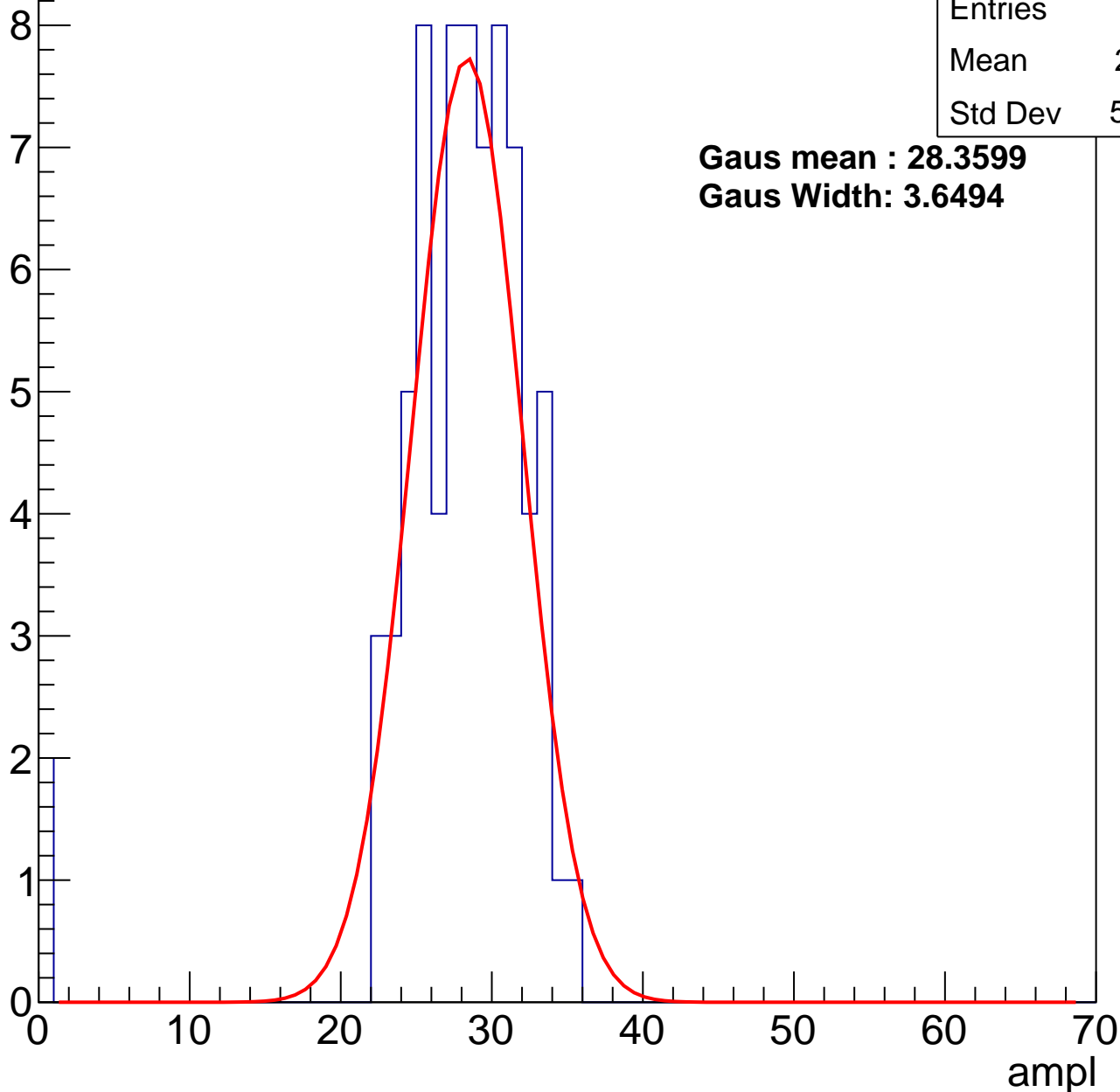
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	27.31
Std Dev	5.536

Gaus mean : 28.3599

Gaus Width: 3.6494



B1L100S, U14-ch88, adc1

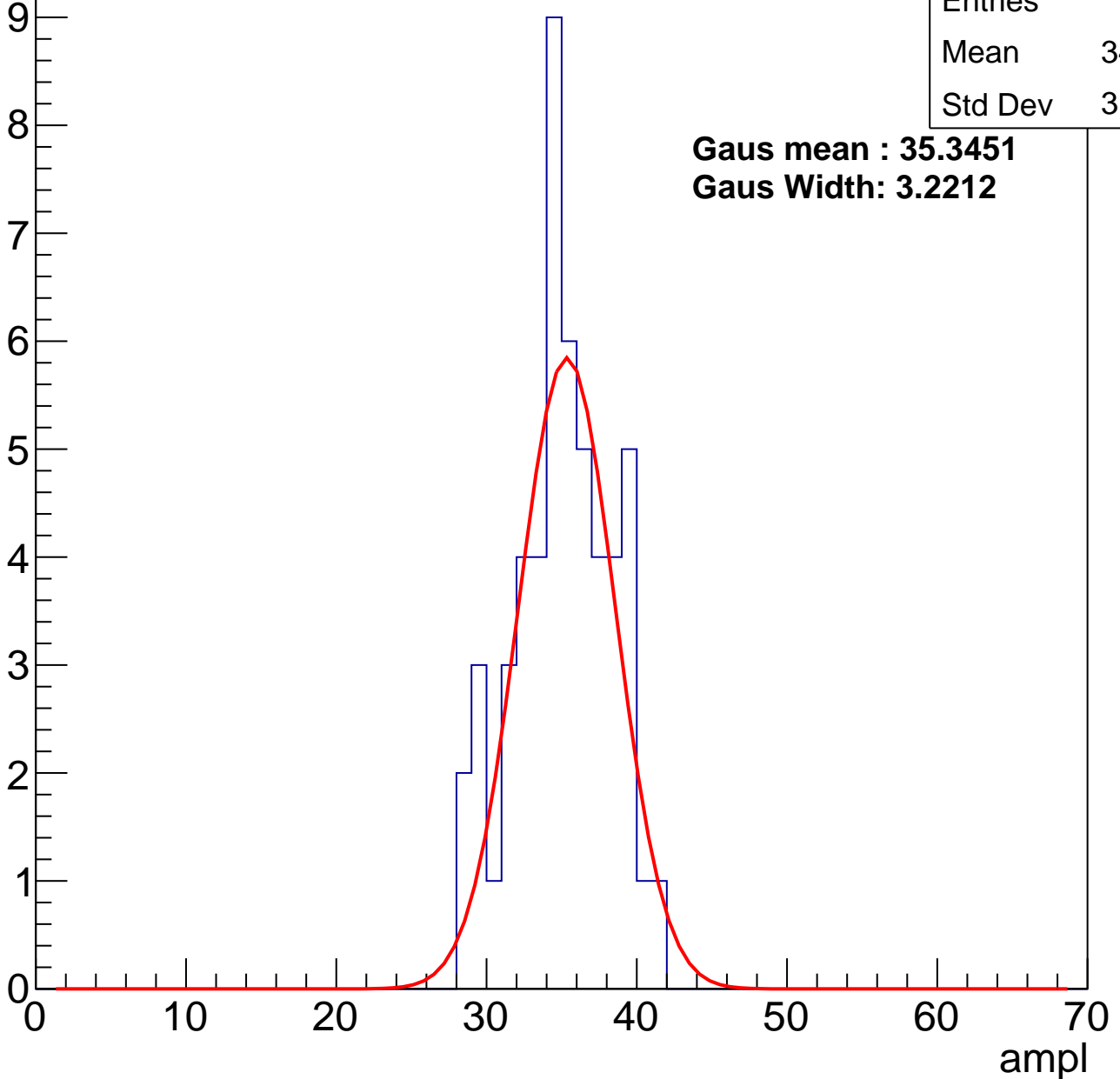
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	34.58
Std Dev	3.189

Gaus mean : 35.3451

Gaus Width: 3.2212



B1L100S, U14-ch88, adc2

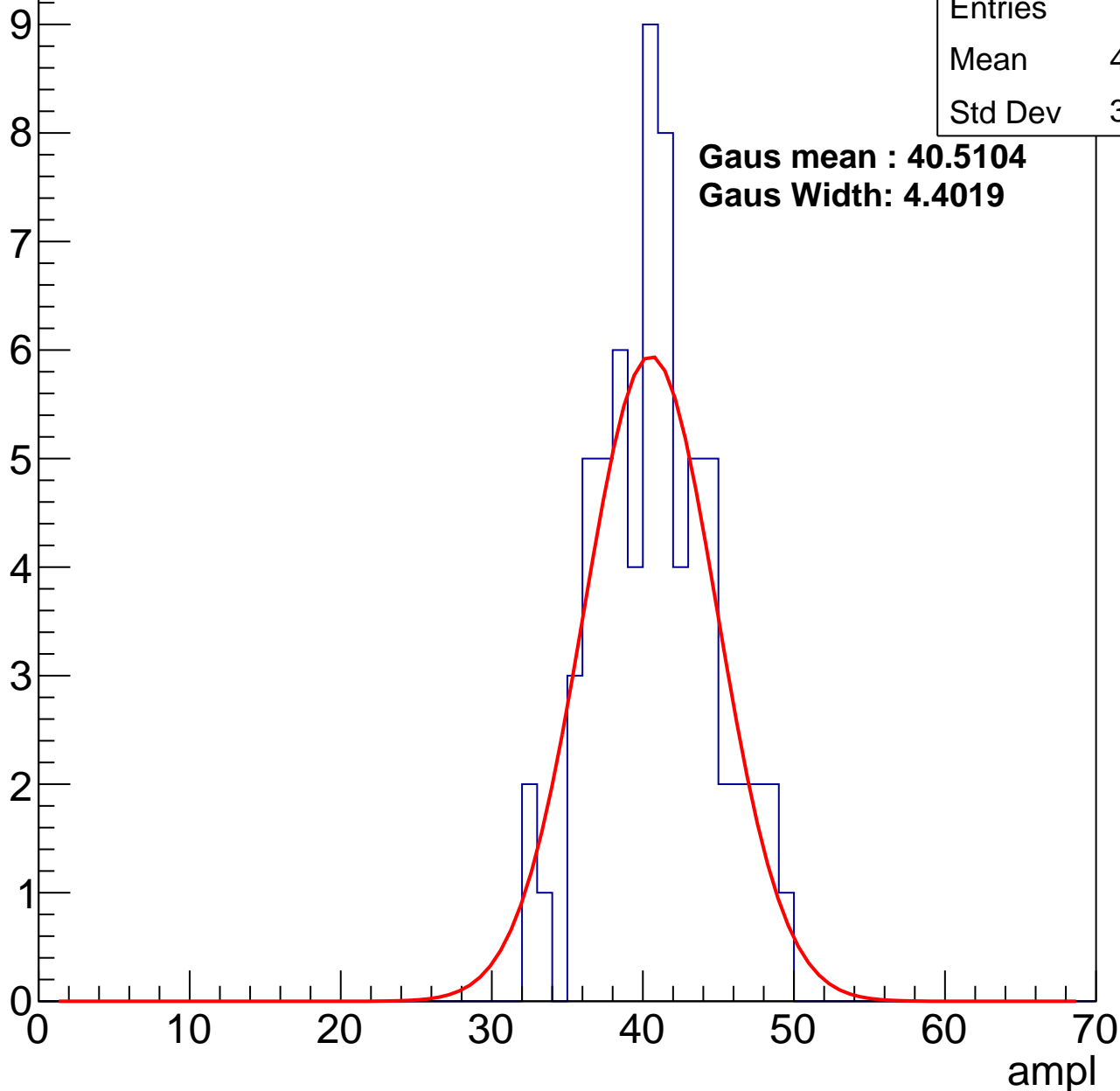
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	40.35
Std Dev	3.844

Gaus mean : 40.5104

Gaus Width: 4.4019

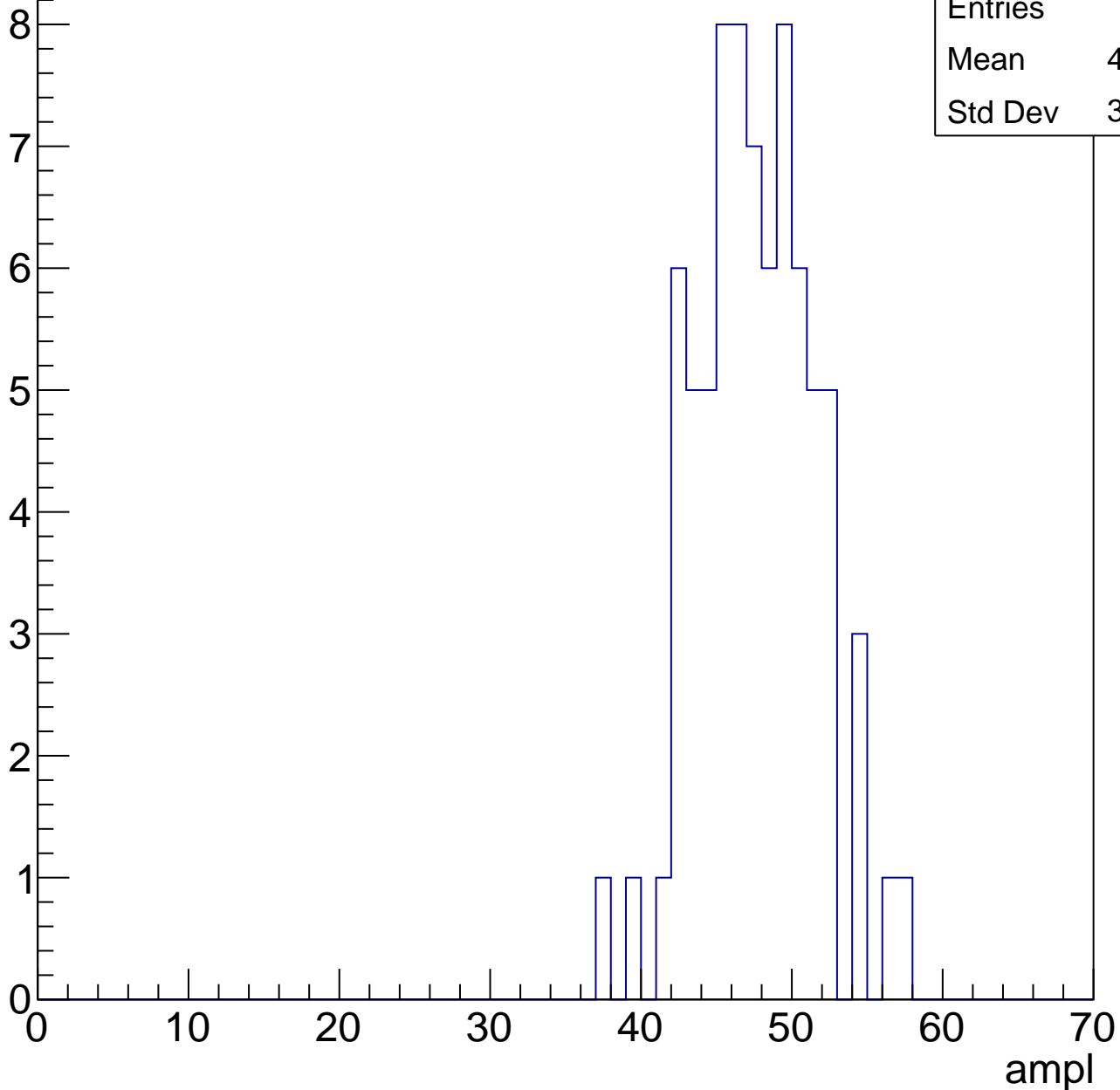


B1L100S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

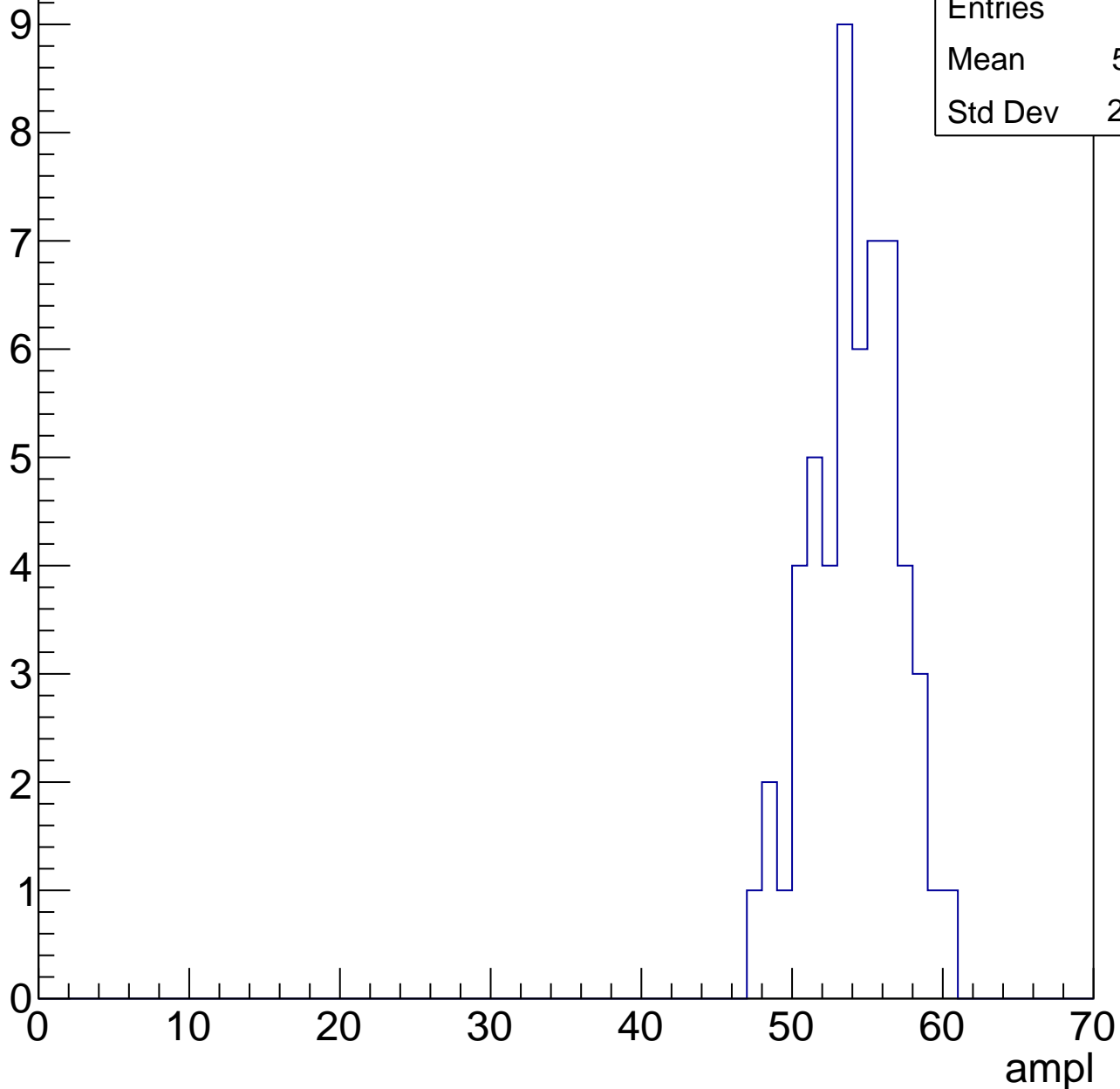
Entries	77
Mean	47.16
Std Dev	3.845



B1L100S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

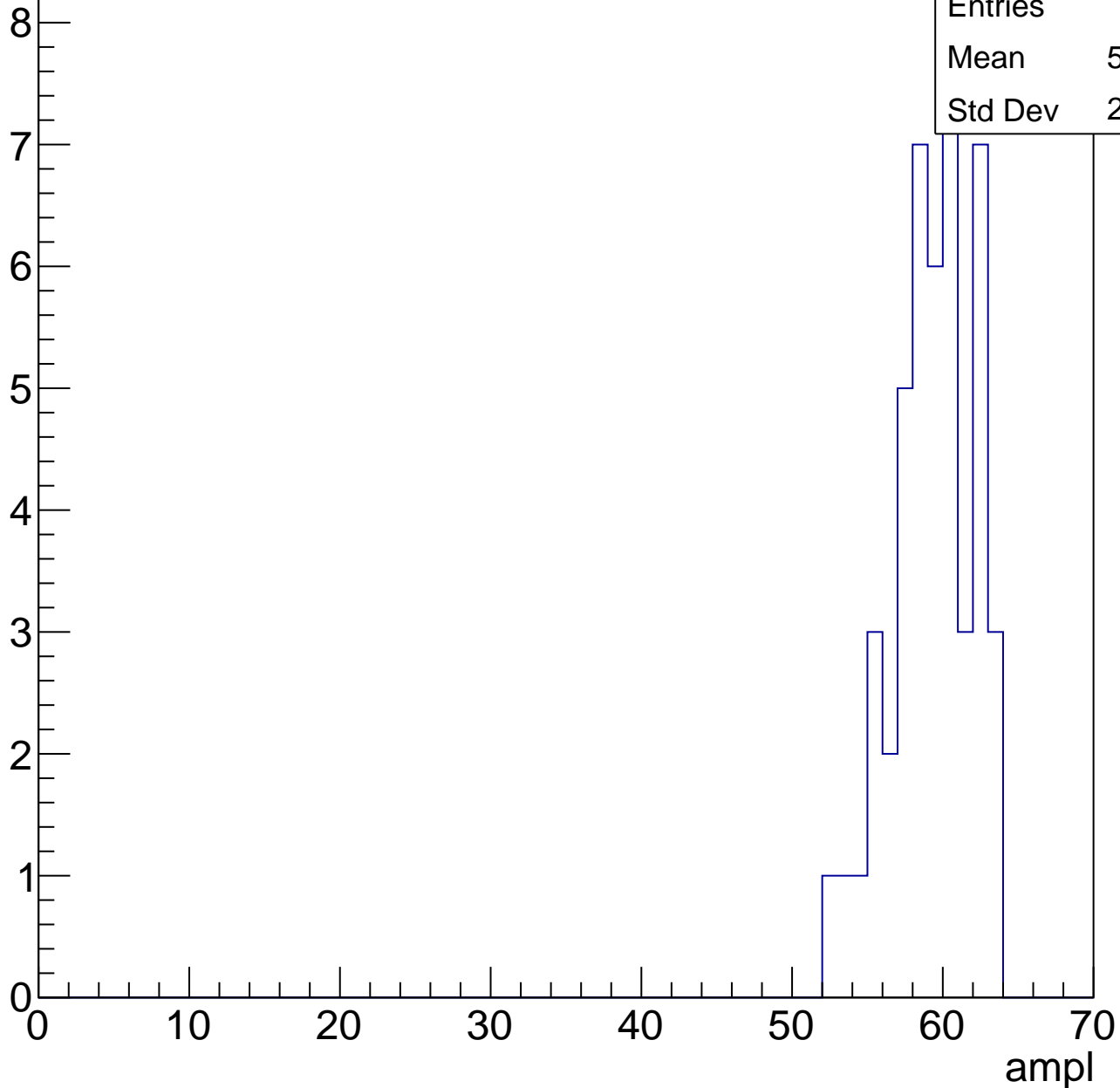
Entry



B1L100S, U14-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

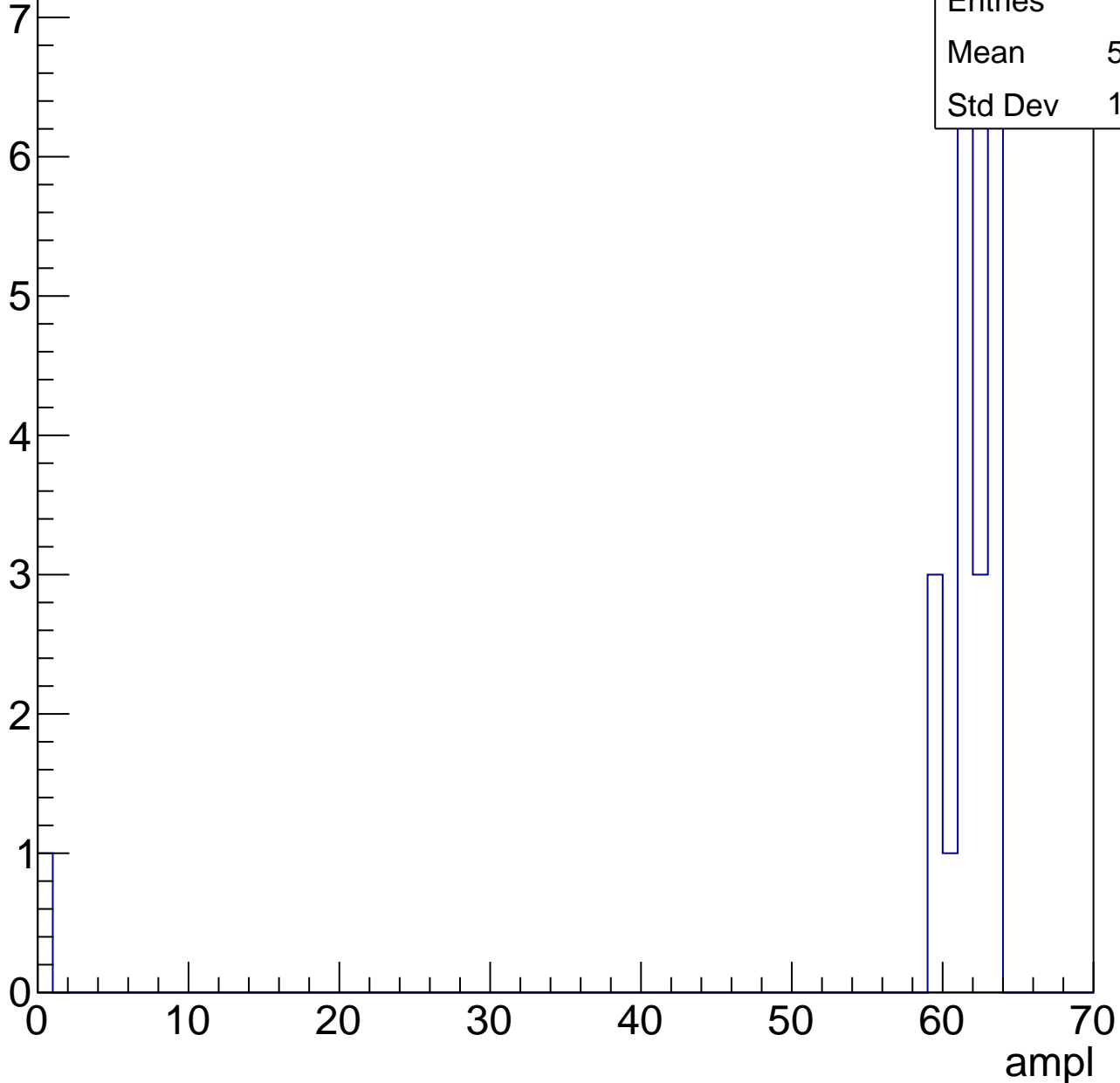
Entry



B1L100S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch89, adc0

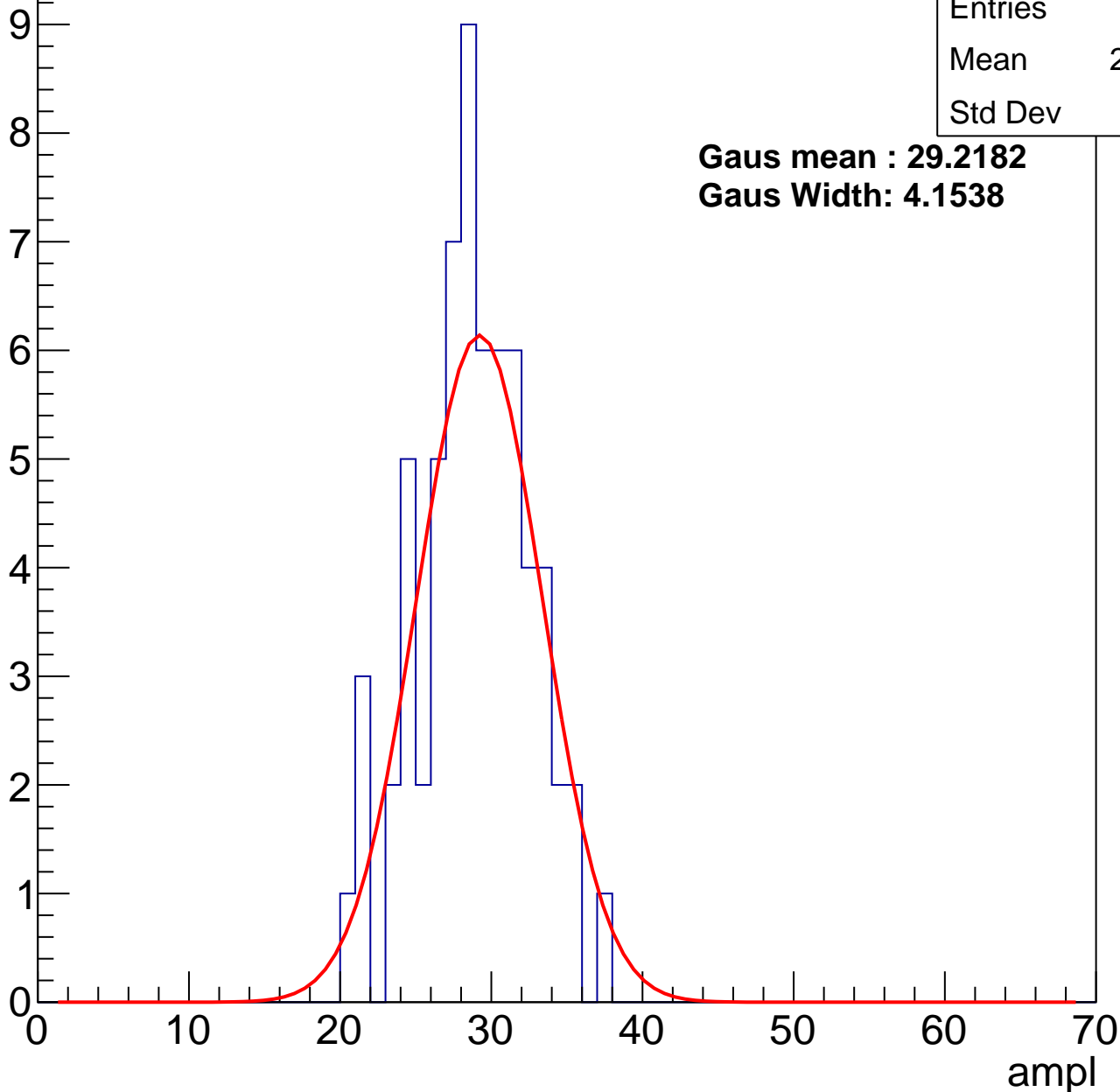
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.38
Std Dev	3.67

Gaus mean : 29.2182

Gaus Width: 4.1538



B1L100S, U14-ch89, adc1

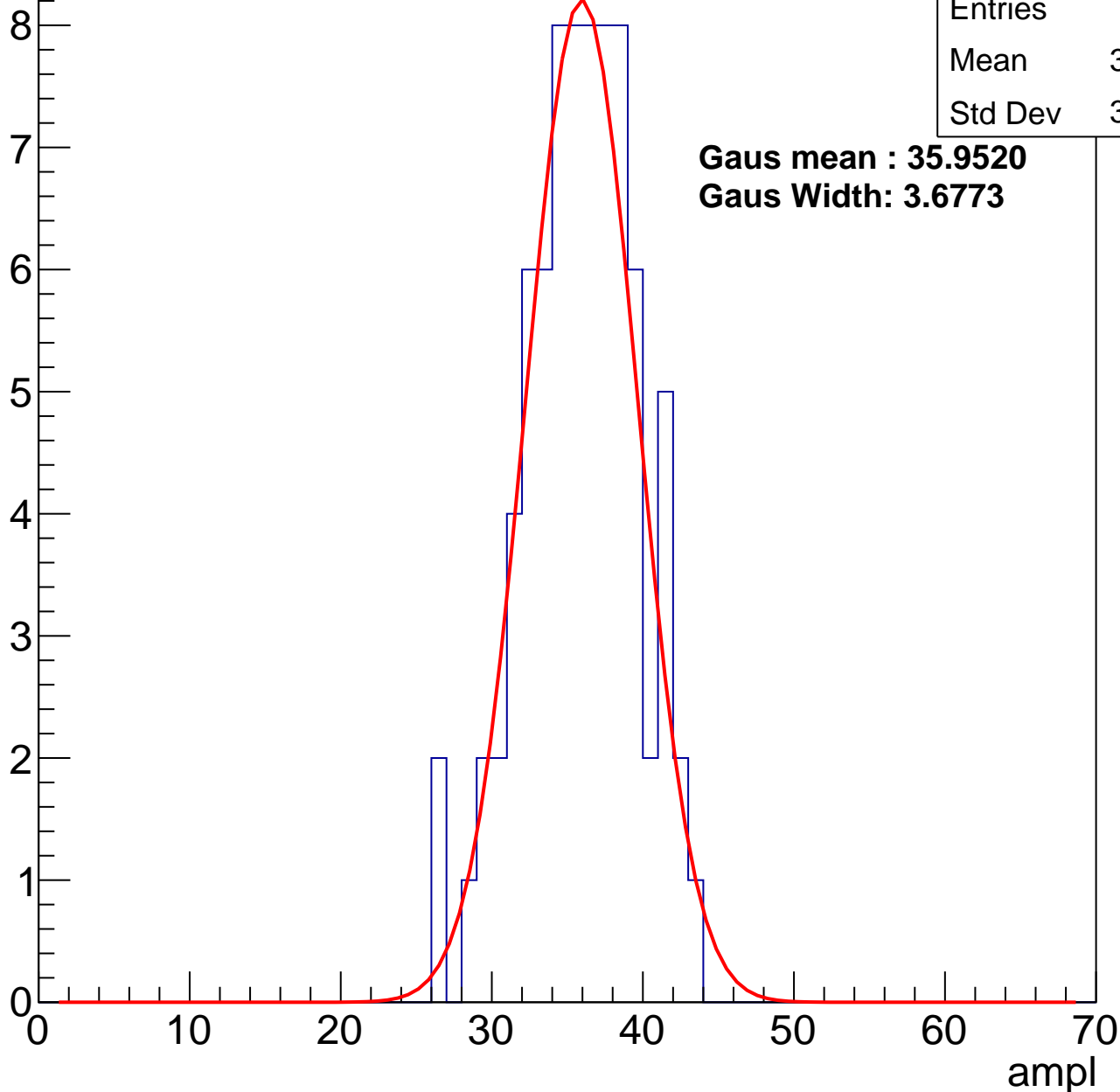
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.42
Std Dev	3.679

Gaus mean : 35.9520

Gaus Width: 3.6773



B1L100S, U14-ch89, adc2

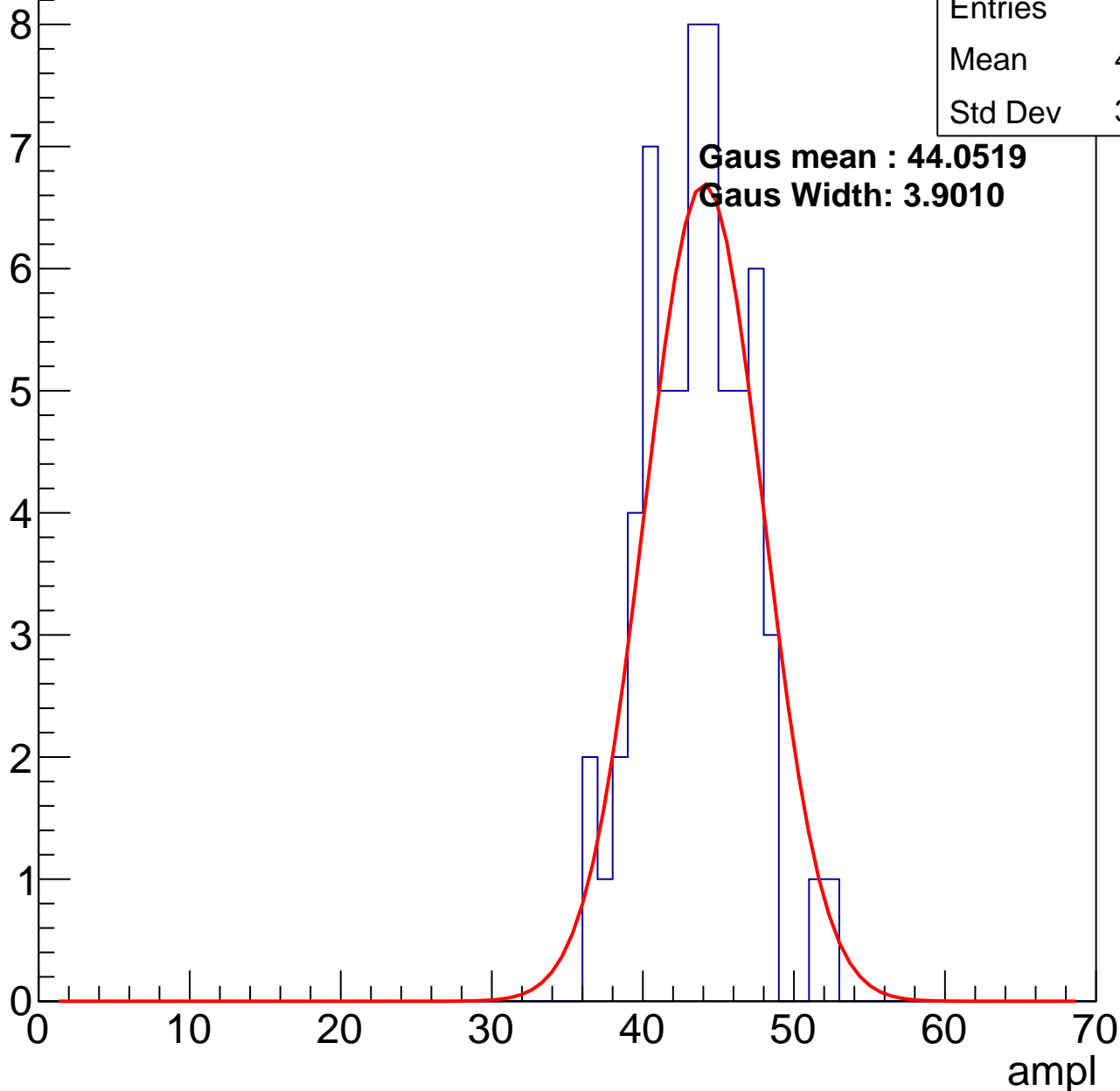
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.11
Std Dev	3.391

Gaus mean : 44.0519

Gaus Width: 3.9010

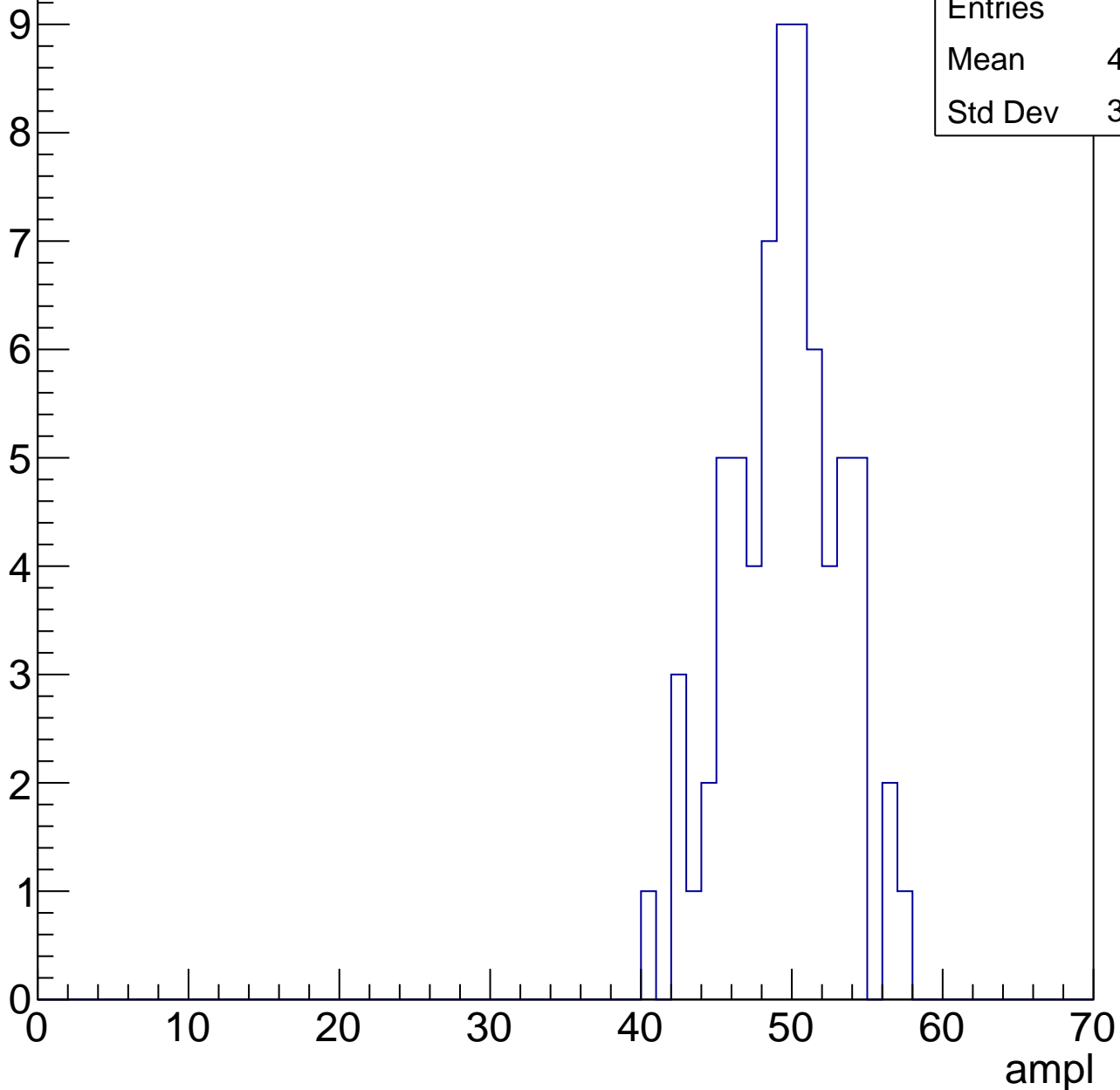


B1L100S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	49.06
Std Dev	3.607

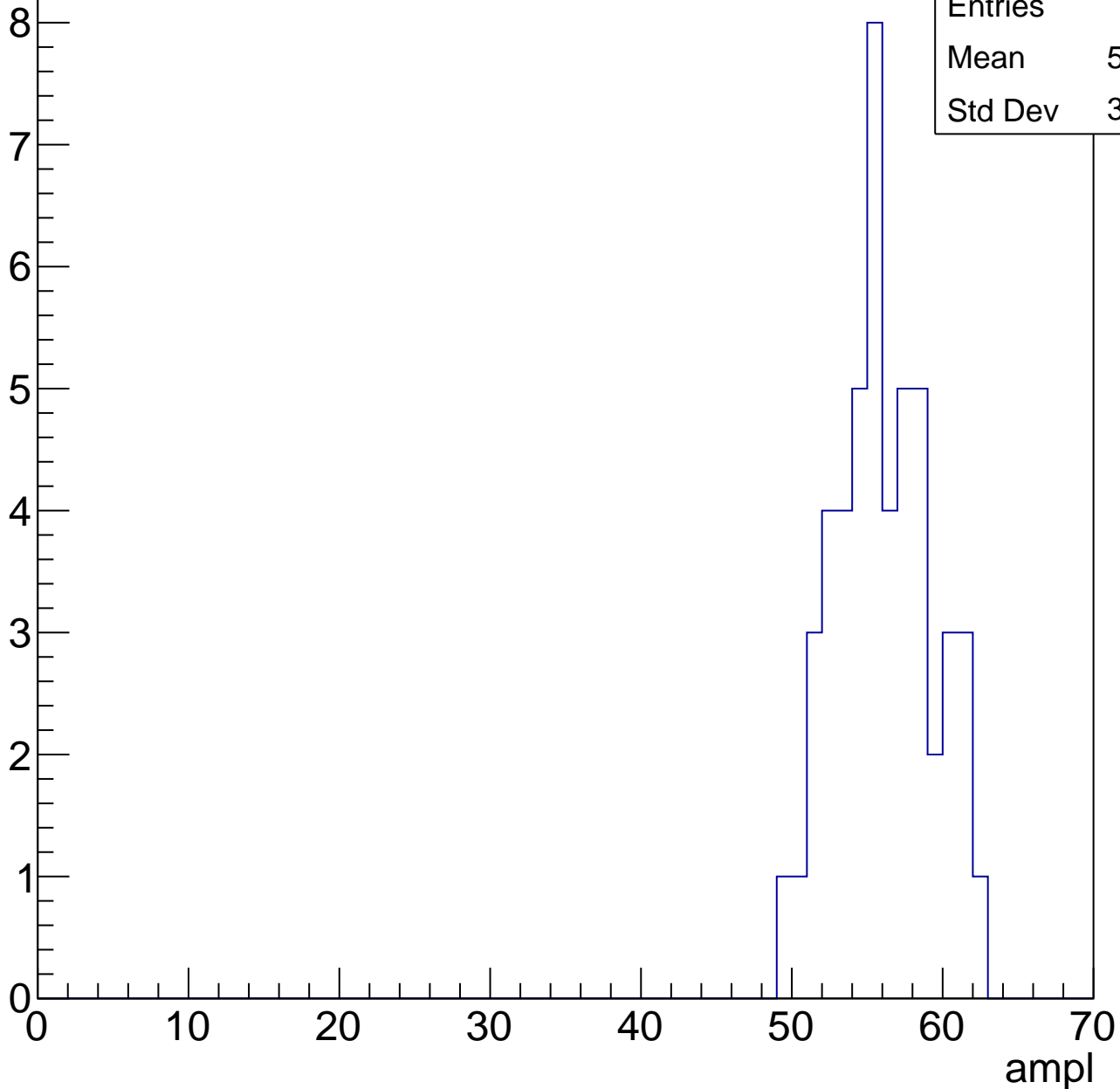


B1L100S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	55.59
Std Dev	3.136

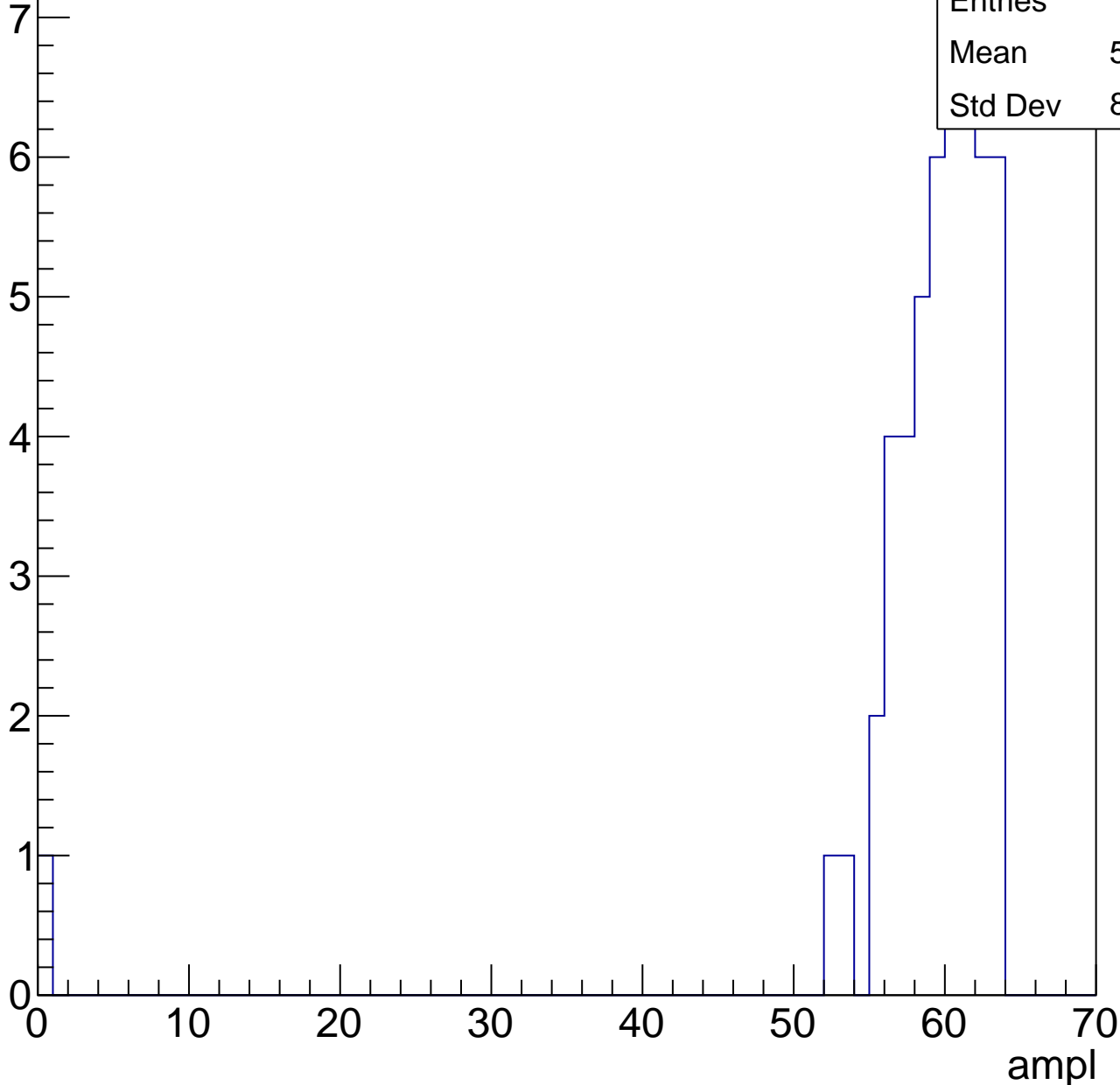


B1L100S, U14-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

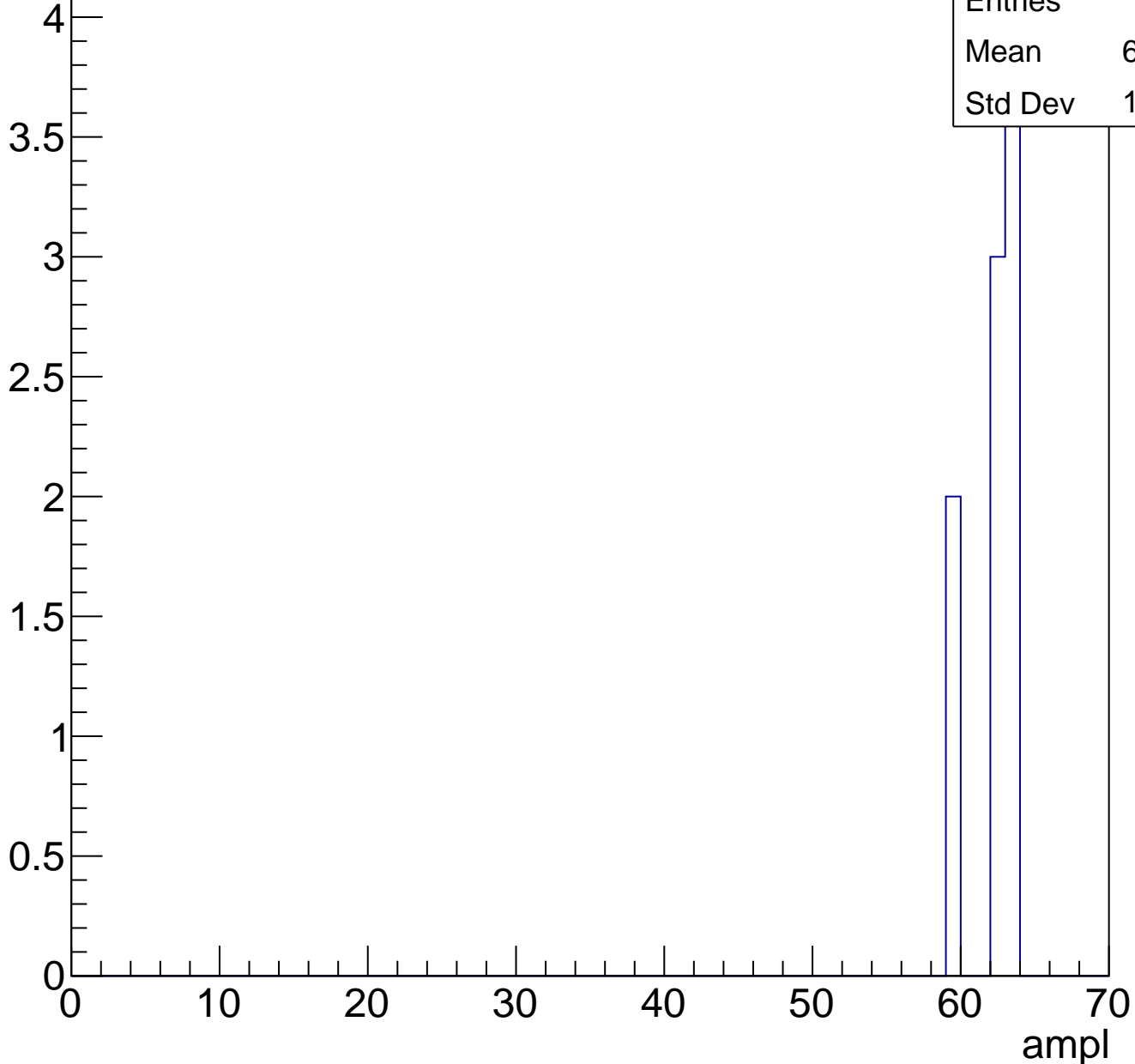
Entries	50
Mean	58.16
Std Dev	8.723



B1L100S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	9
Mean	61.78
Std Dev	1.548

B1L100S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

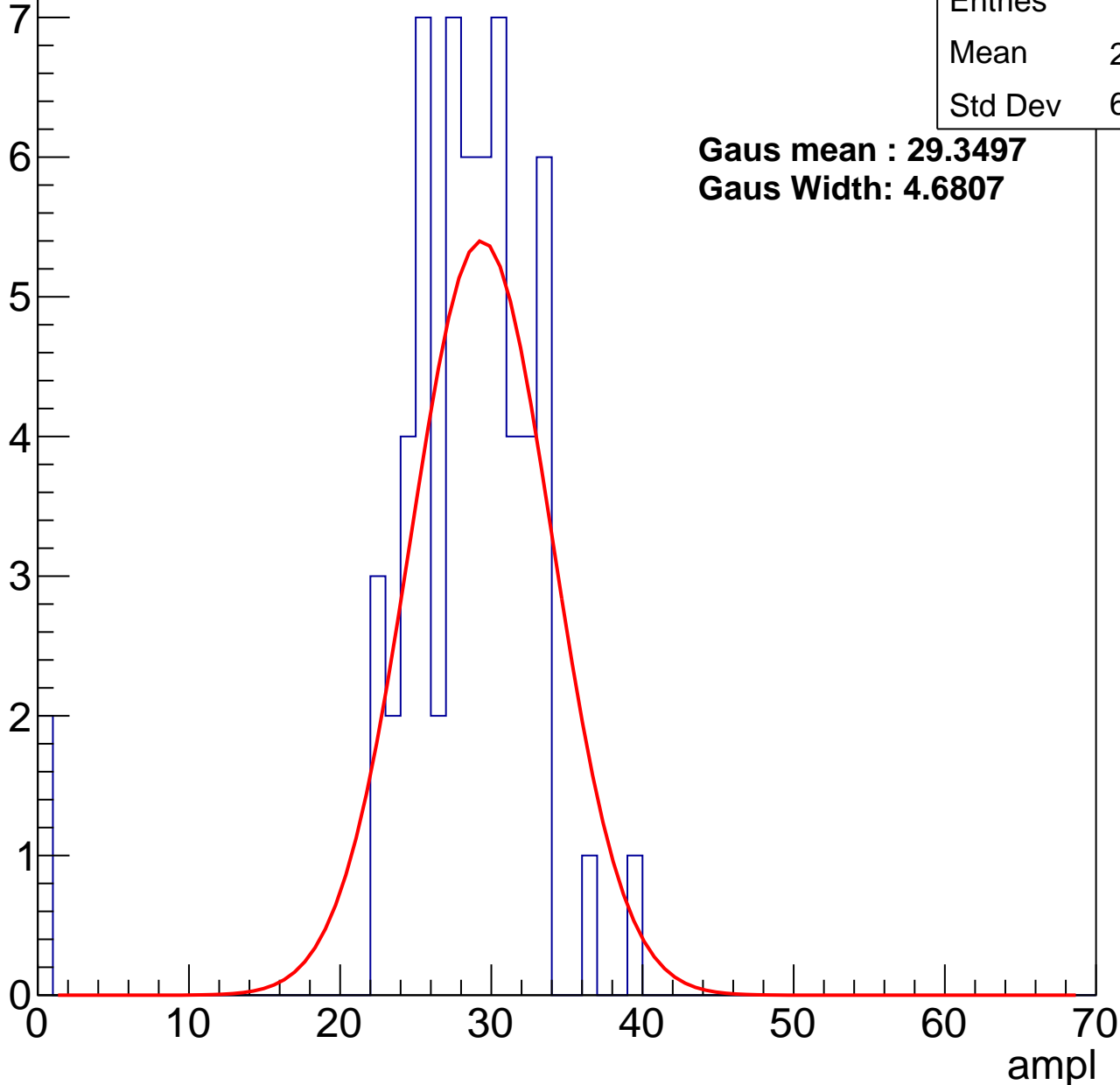
B1L100S, U14-ch90, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.44
Std Dev	6.113

Gaus mean : 29.3497
Gaus Width: 4.6807



B1L100S, U14-ch90, adc1

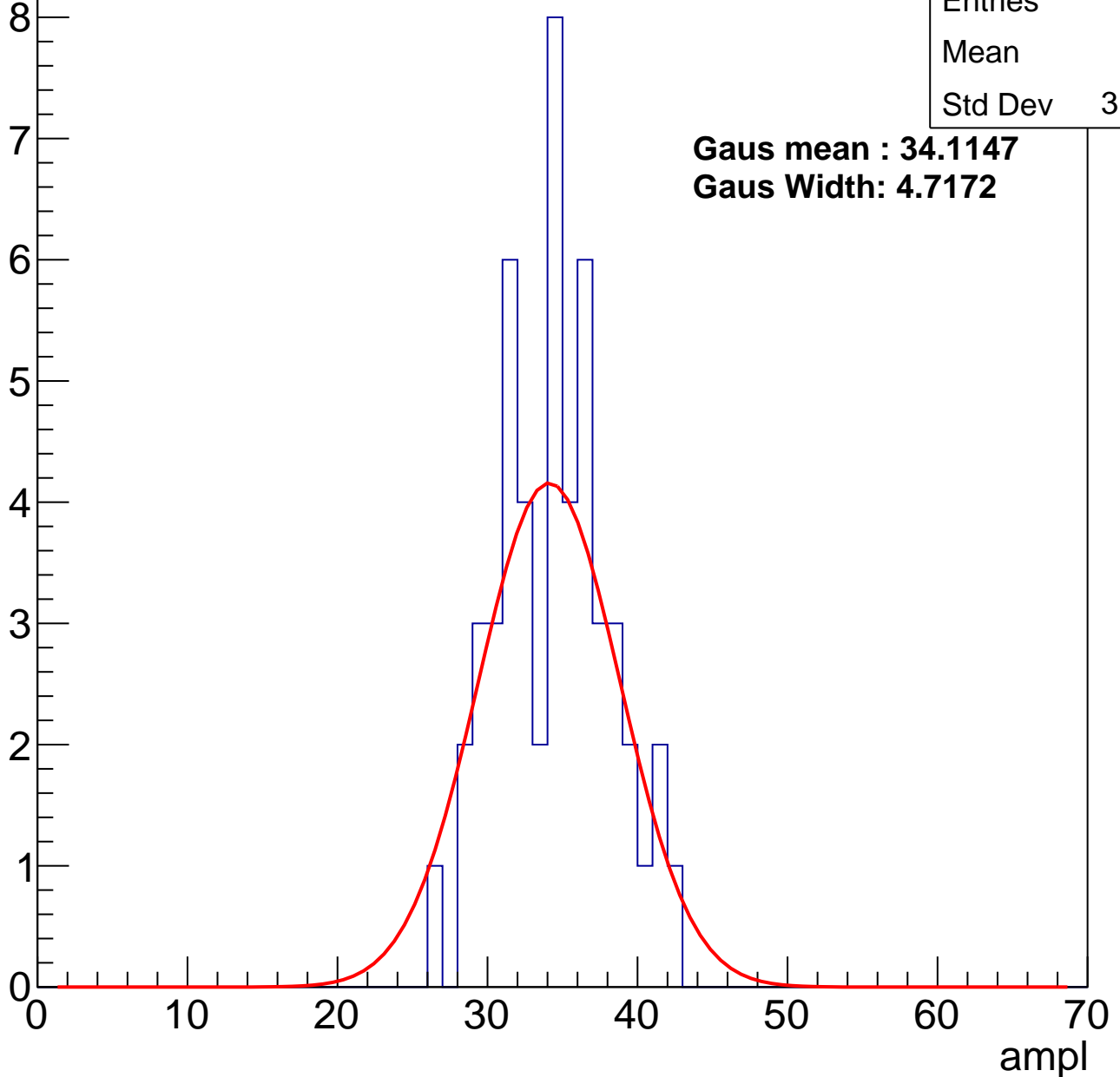
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	34
Std Dev	3.657

Gaus mean : 34.1147

Gaus Width: 4.7172



B1L100S, U14-ch90, adc2

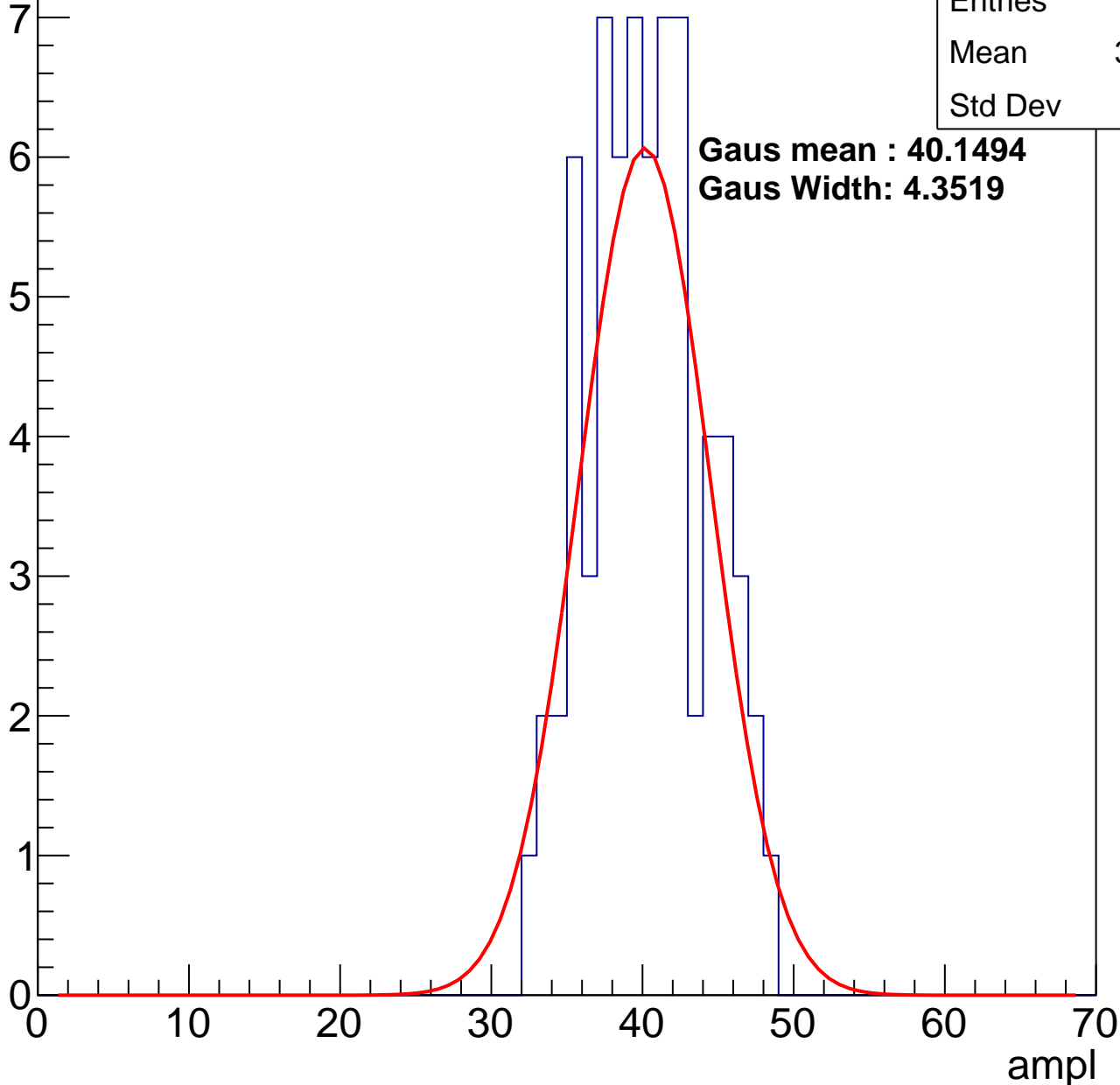
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	39.81
Std Dev	3.8

Gaus mean : 40.1494

Gaus Width: 4.3519

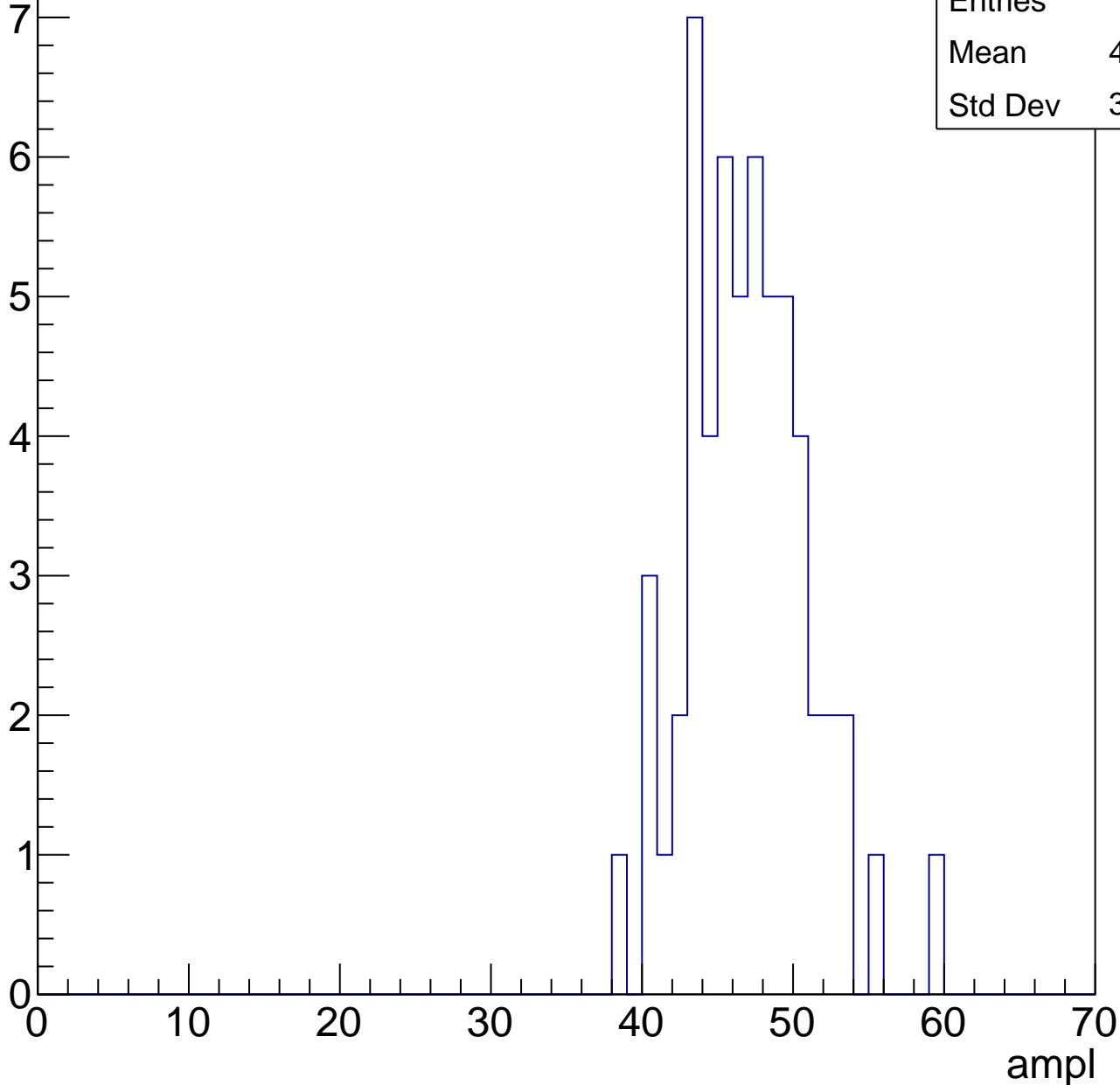


B1L100S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	46.54
Std Dev	3.978

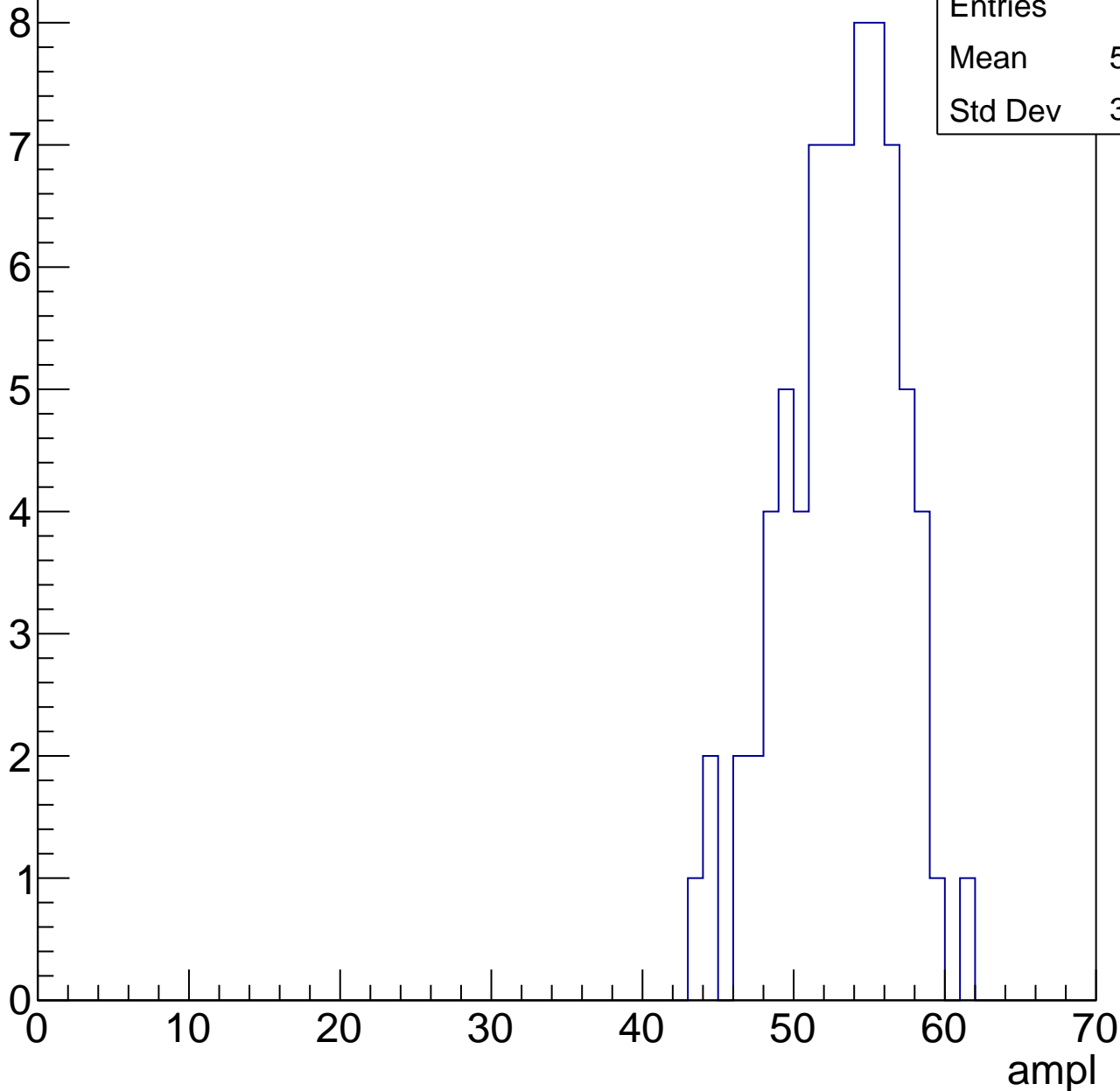


B1L100S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

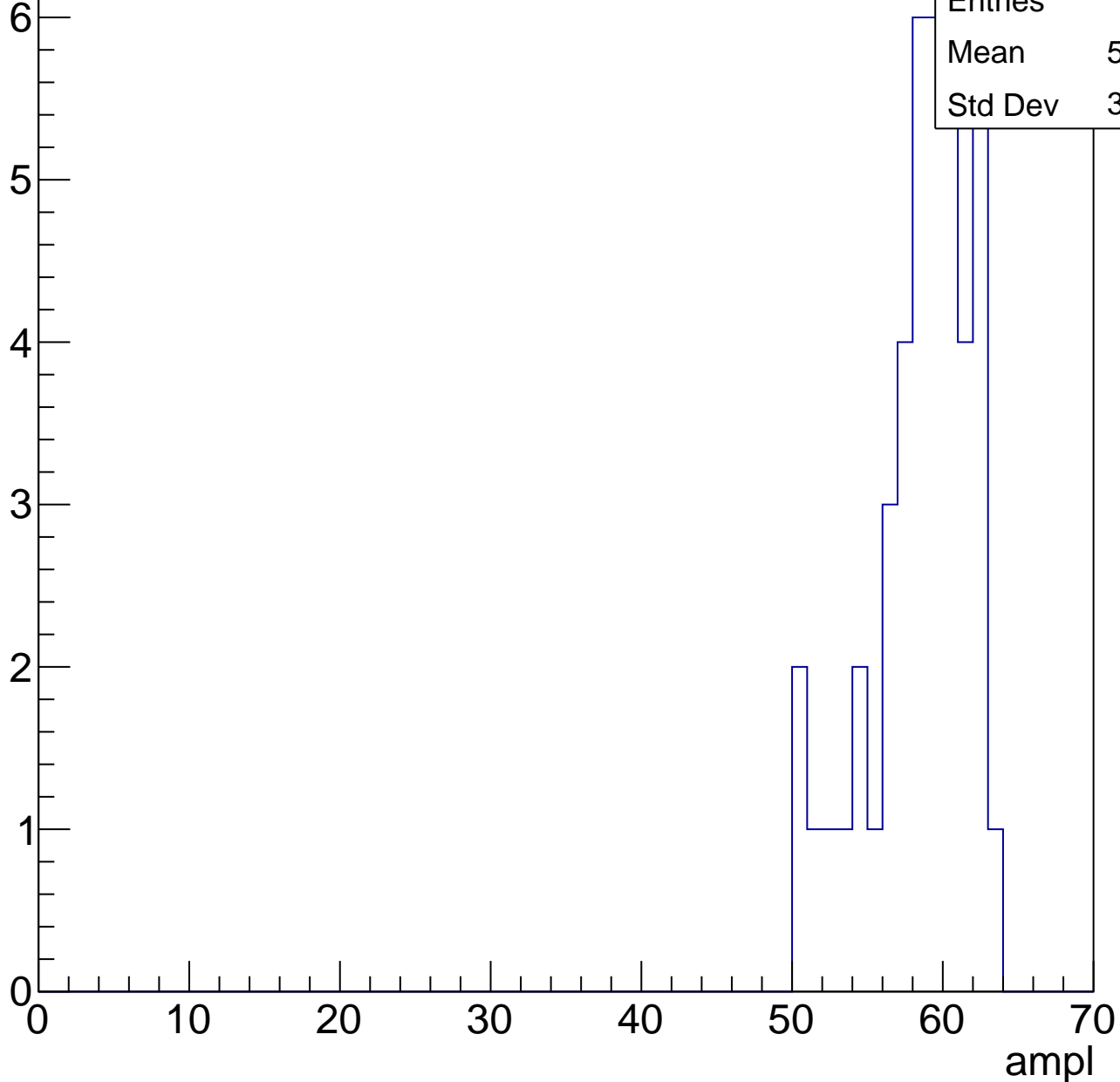
Entries	75
Mean	52.63
Std Dev	3.752



B1L100S, U14-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

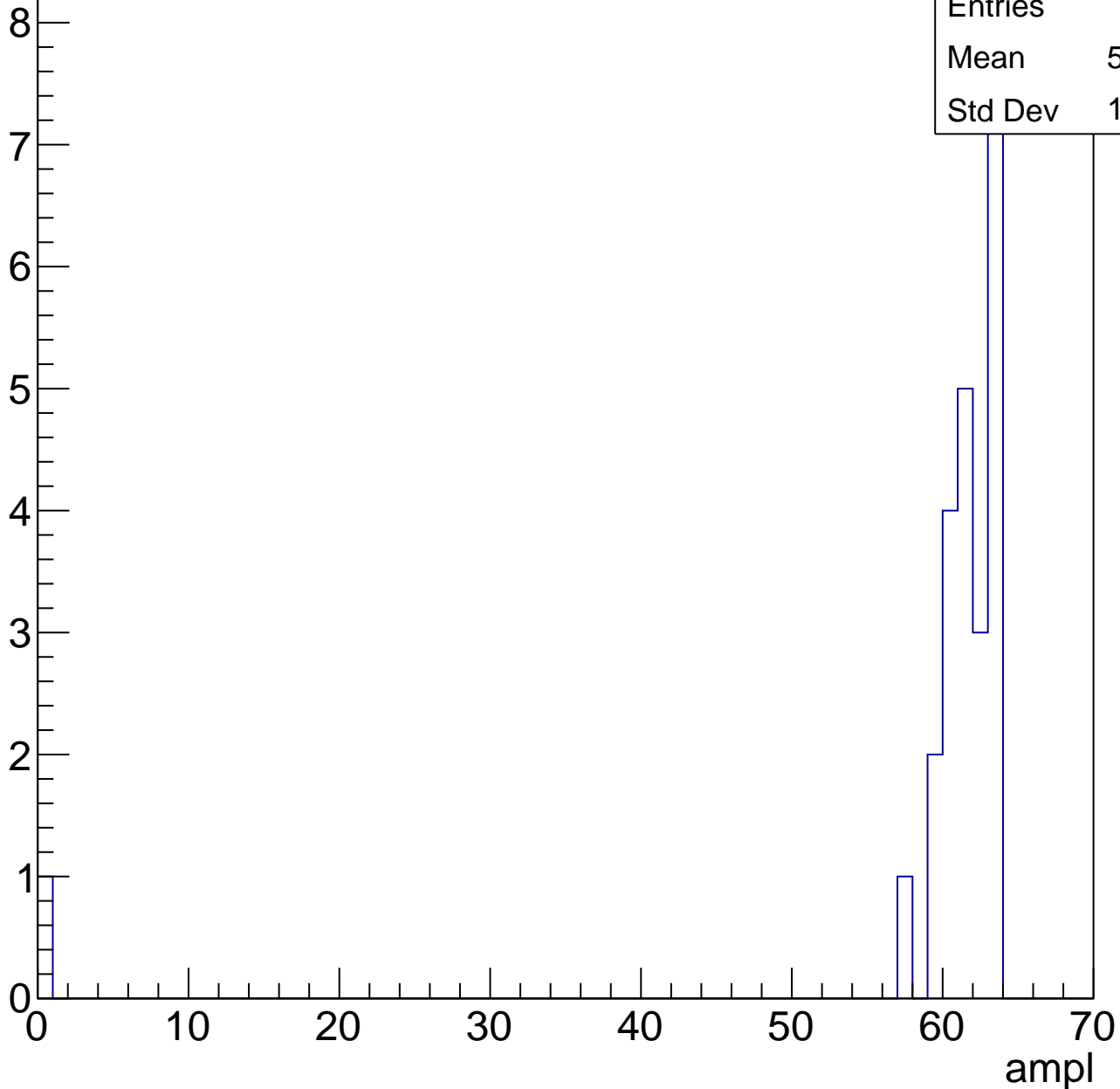


B1L100S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.75
Std Dev	12.35



B1L100S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch91, adc0

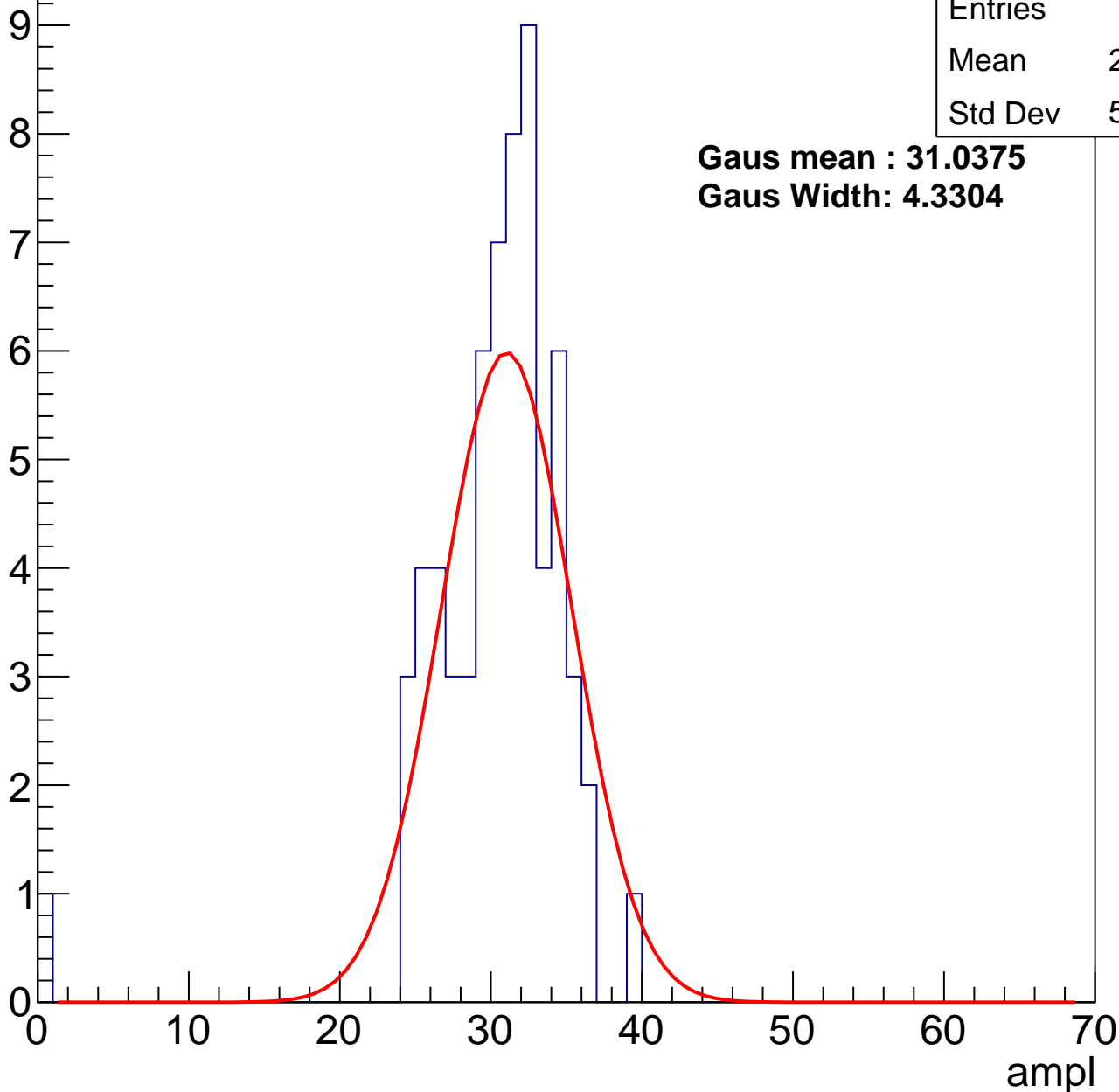
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.89
Std Dev	5.032

Gaus mean : 31.0375

Gaus Width: 4.3304



B1L100S, U14-ch91, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	37.06
Std Dev	3.294

Gaus mean : 37.2518

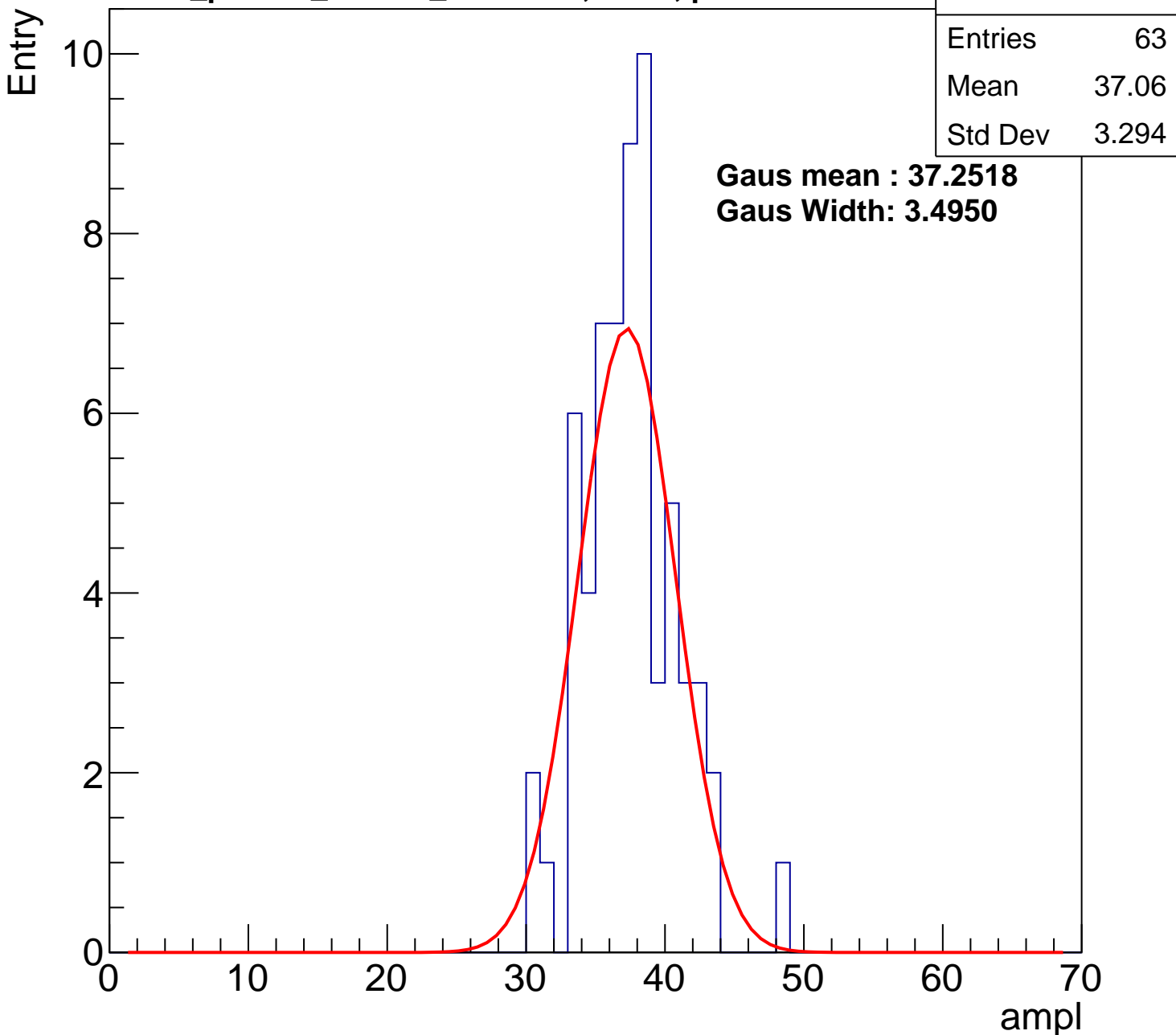
Gaus Width: 3.4950

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch91, adc2

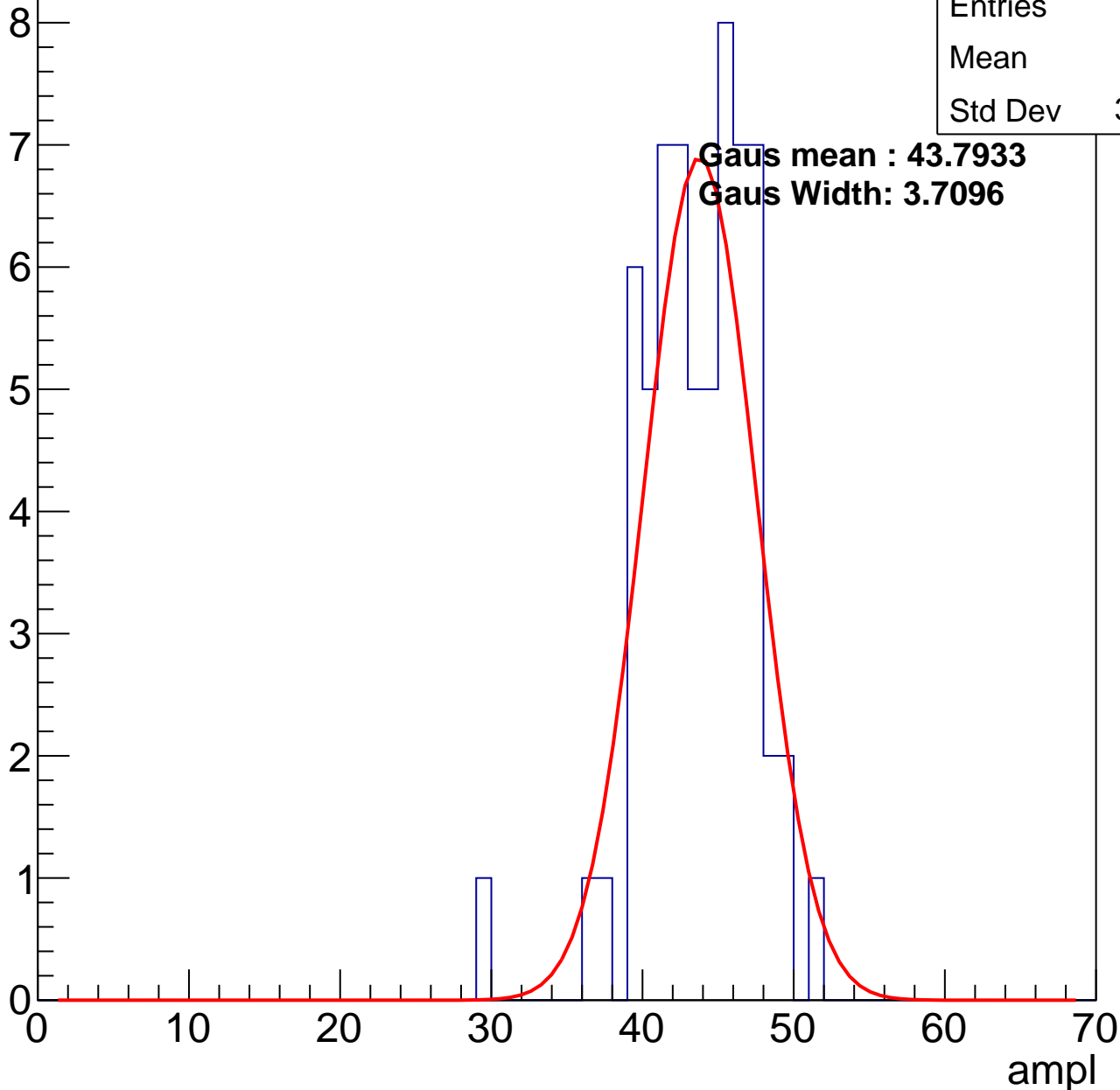
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.2
Std Dev	3.621

Gaus mean : 43.7933

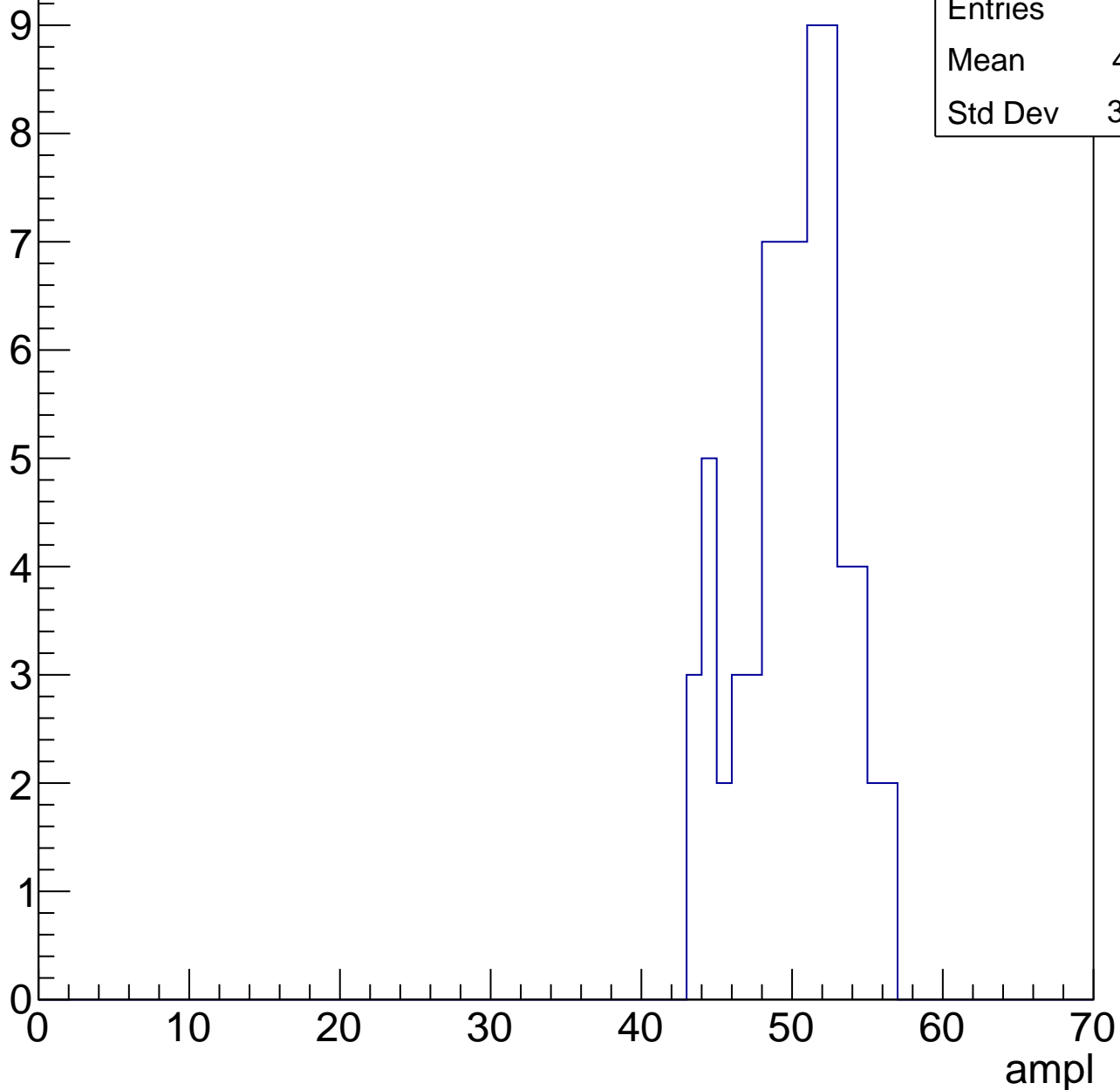
Gaus Width: 3.7096



B1L100S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

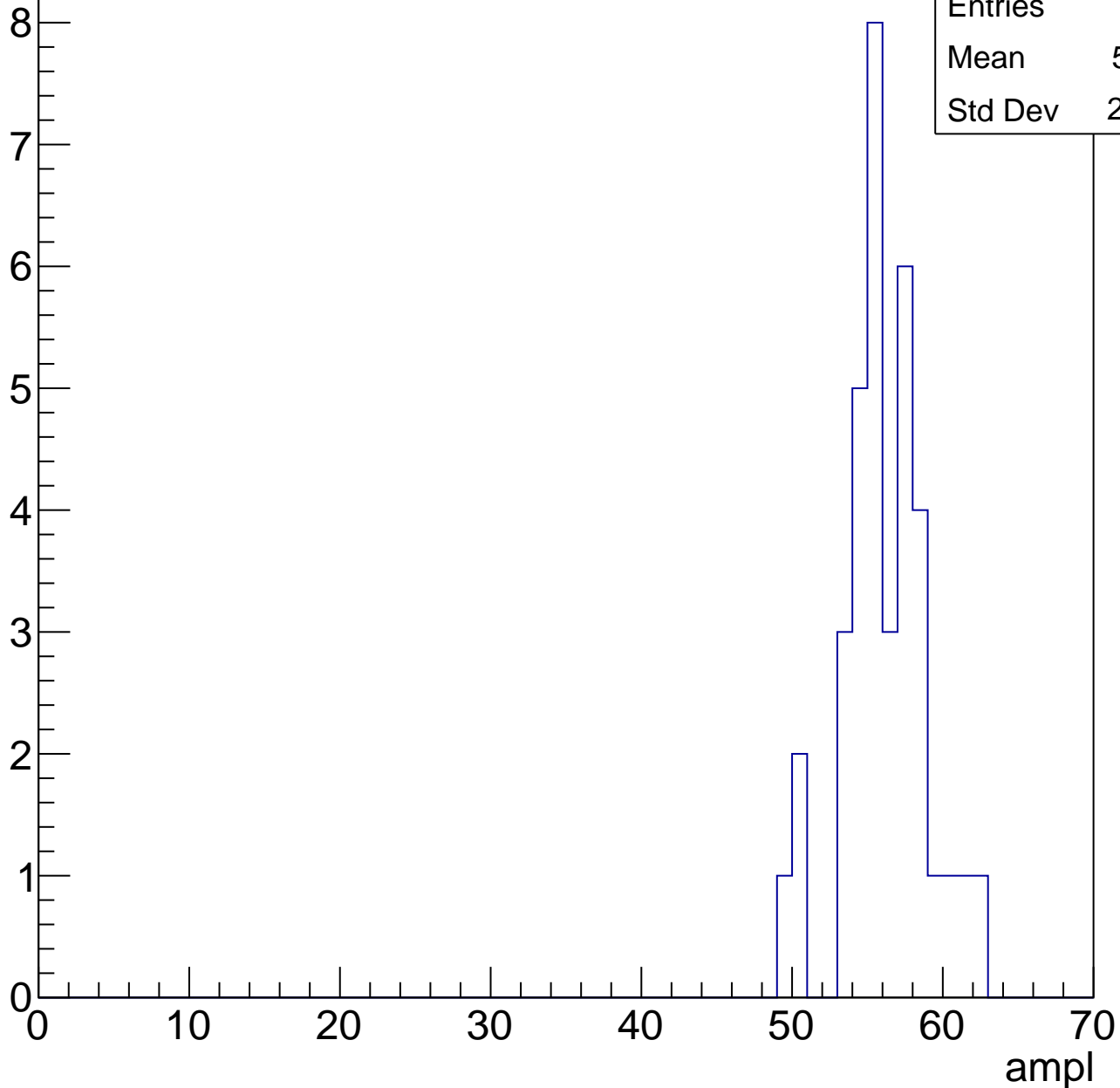
Entry



B1L100S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

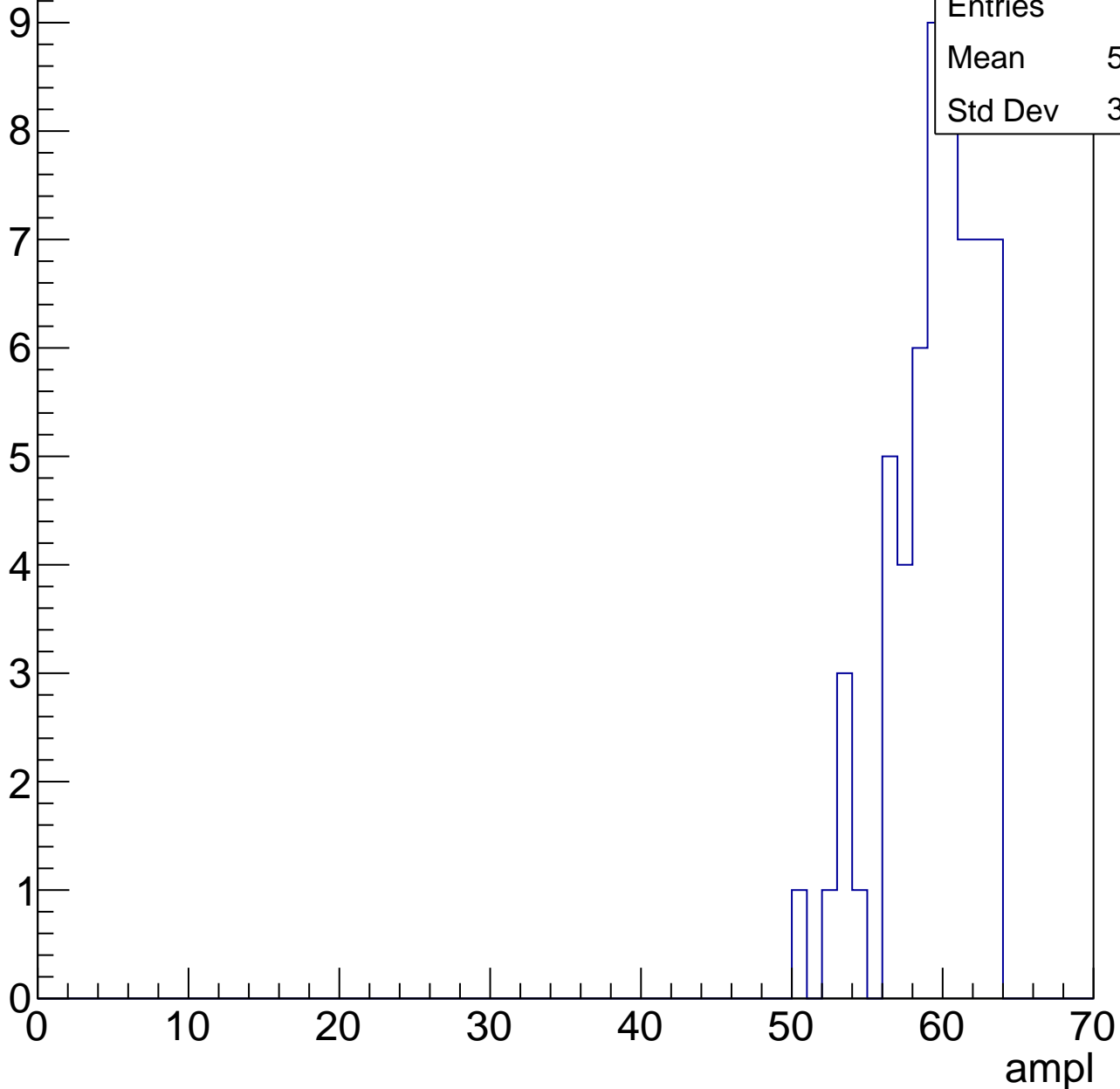


B1L100S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

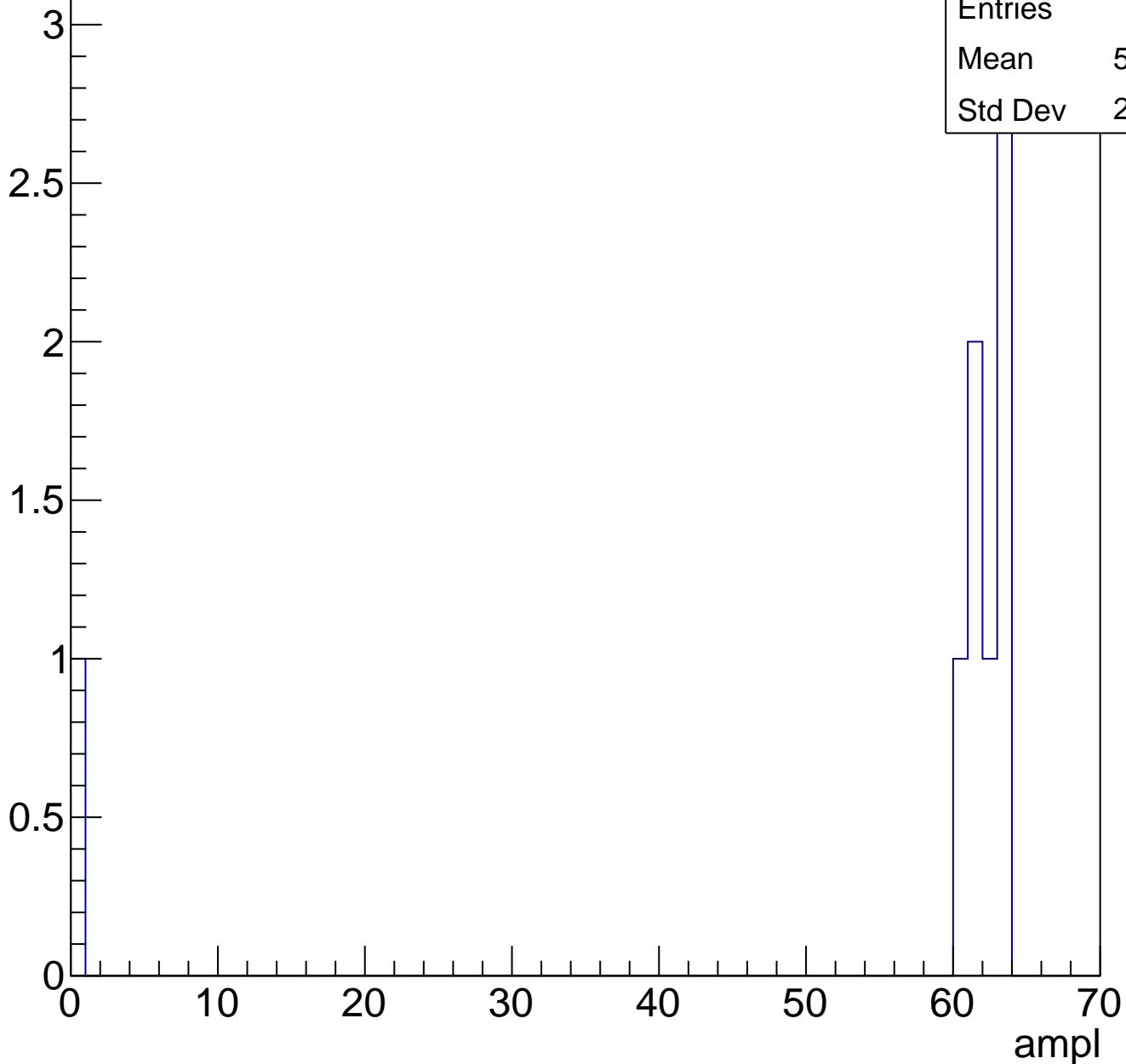
Entries	59
Mean	59.05
Std Dev	3.028



B1L100S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

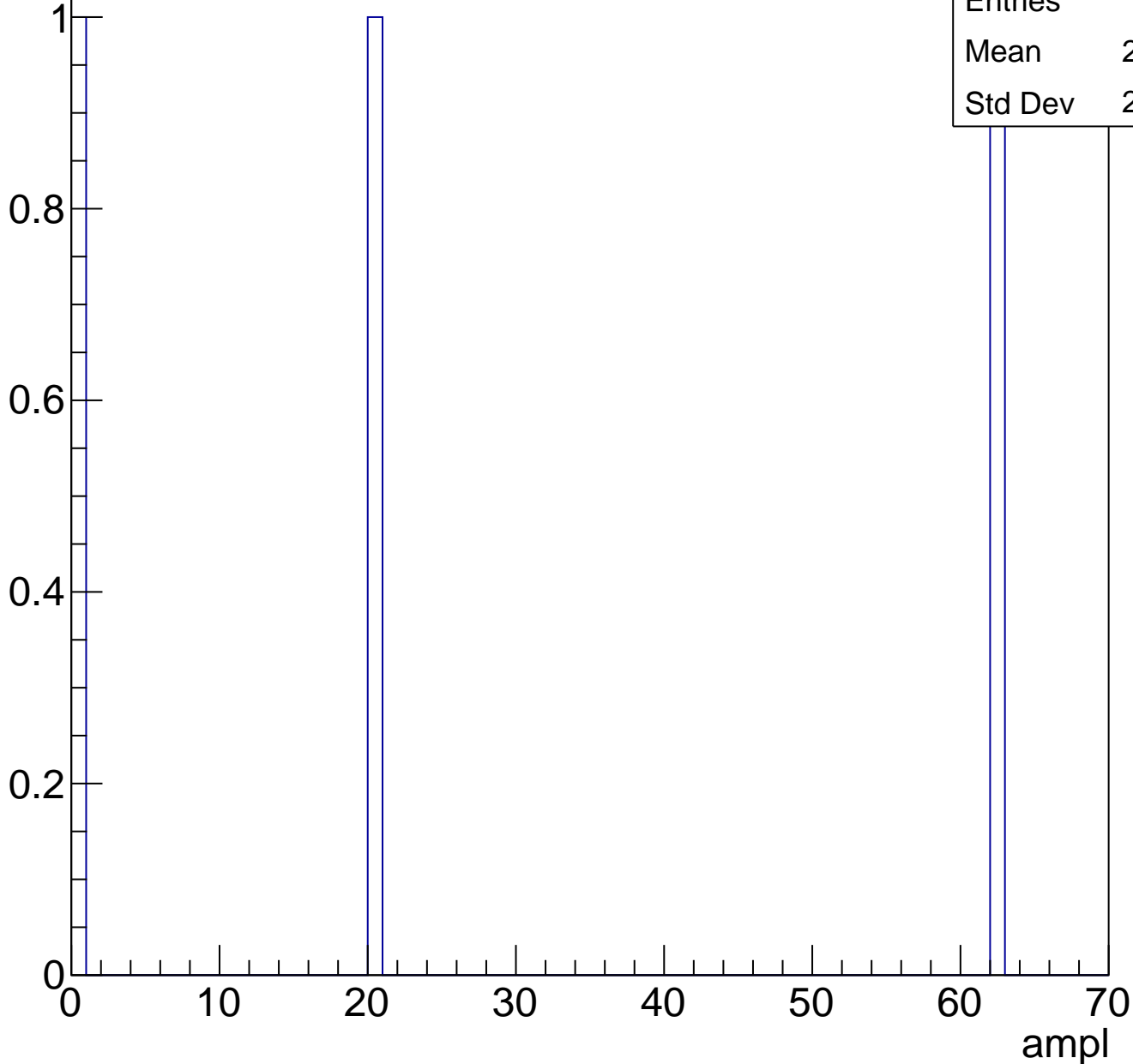


Entries	8
Mean	54.12
Std Dev	20.48

B1L100S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B1L100S, U14-ch92, adc0

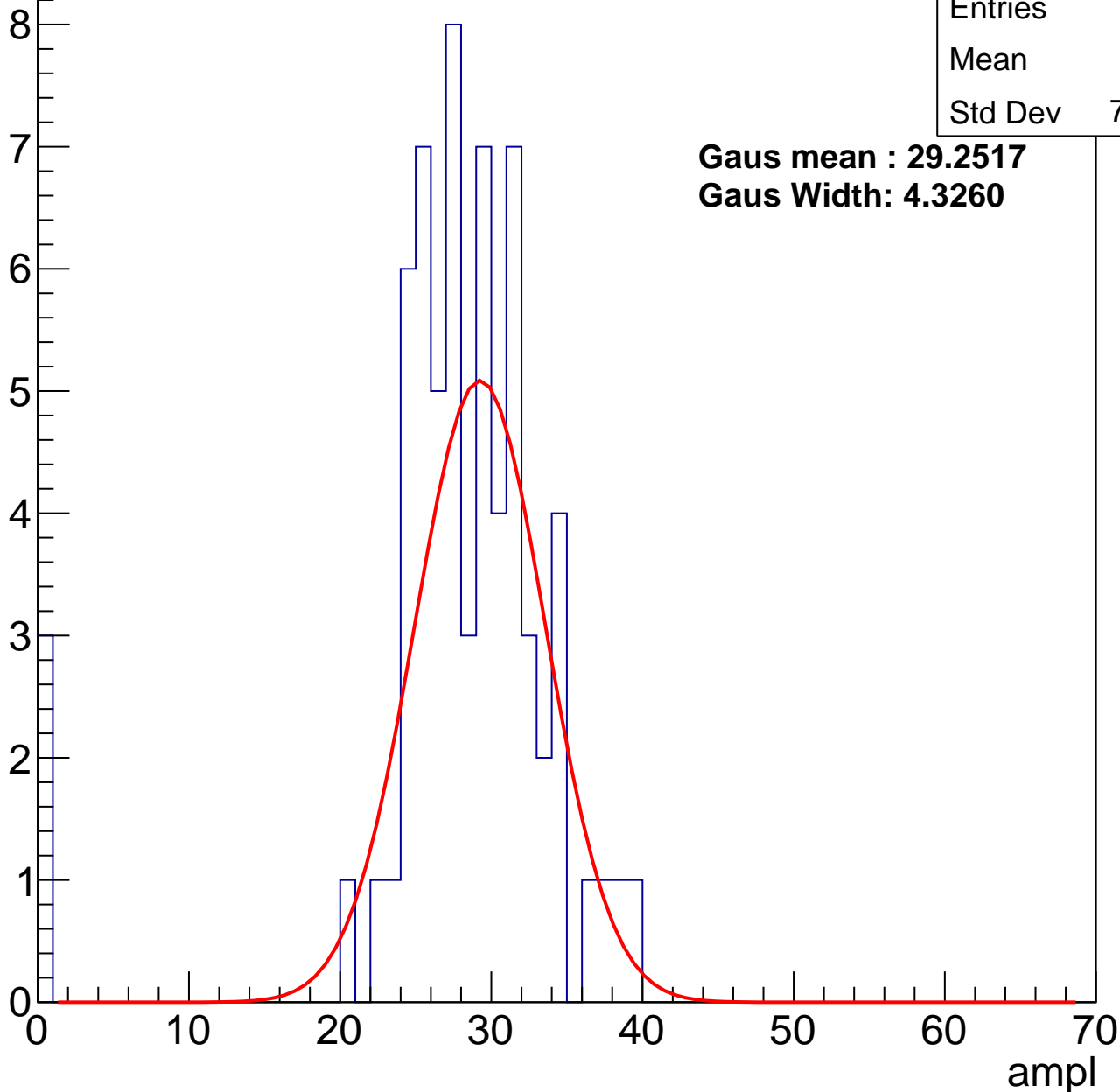
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	27.3
Std Dev	7.099

Gaus mean : 29.2517

Gaus Width: 4.3260



B1L100S, U14-ch92, adc1

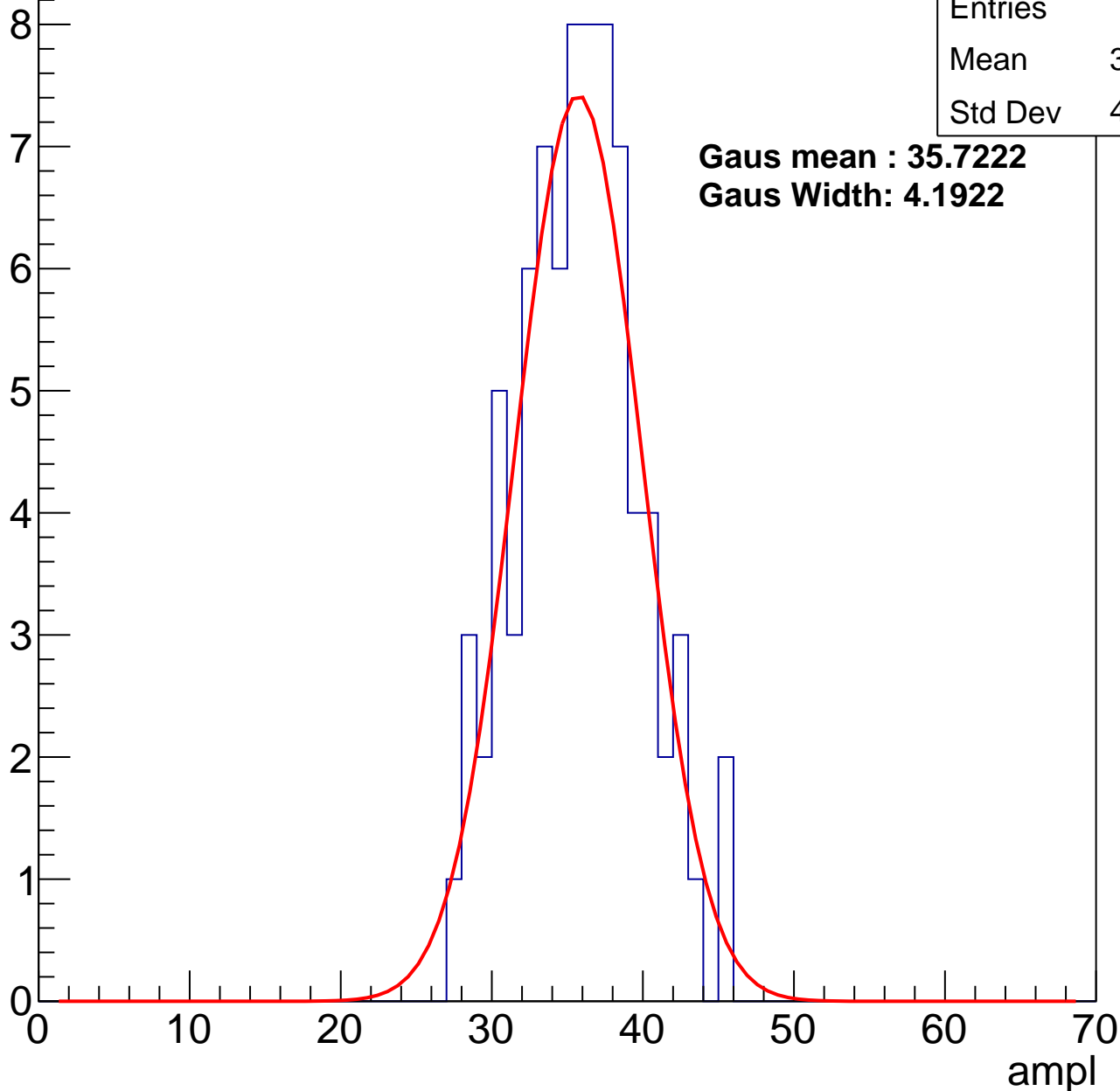
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	35.33
Std Dev	4.009

Gaus mean : 35.7222

Gaus Width: 4.1922



B1L100S, U14-ch92, adc2

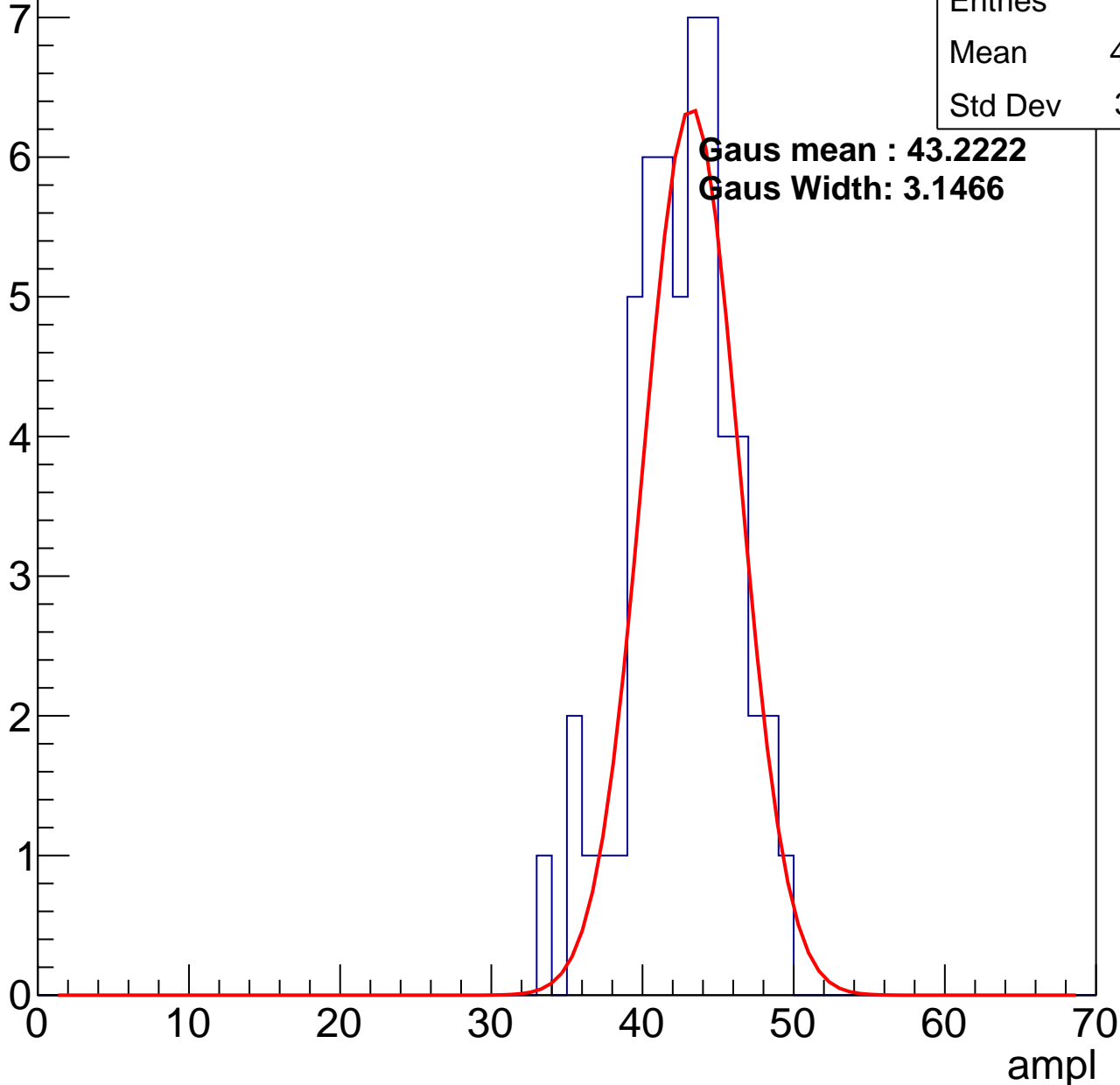
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.13
Std Dev	3.401

Gaus mean : 43.2222

Gaus Width: 3.1466

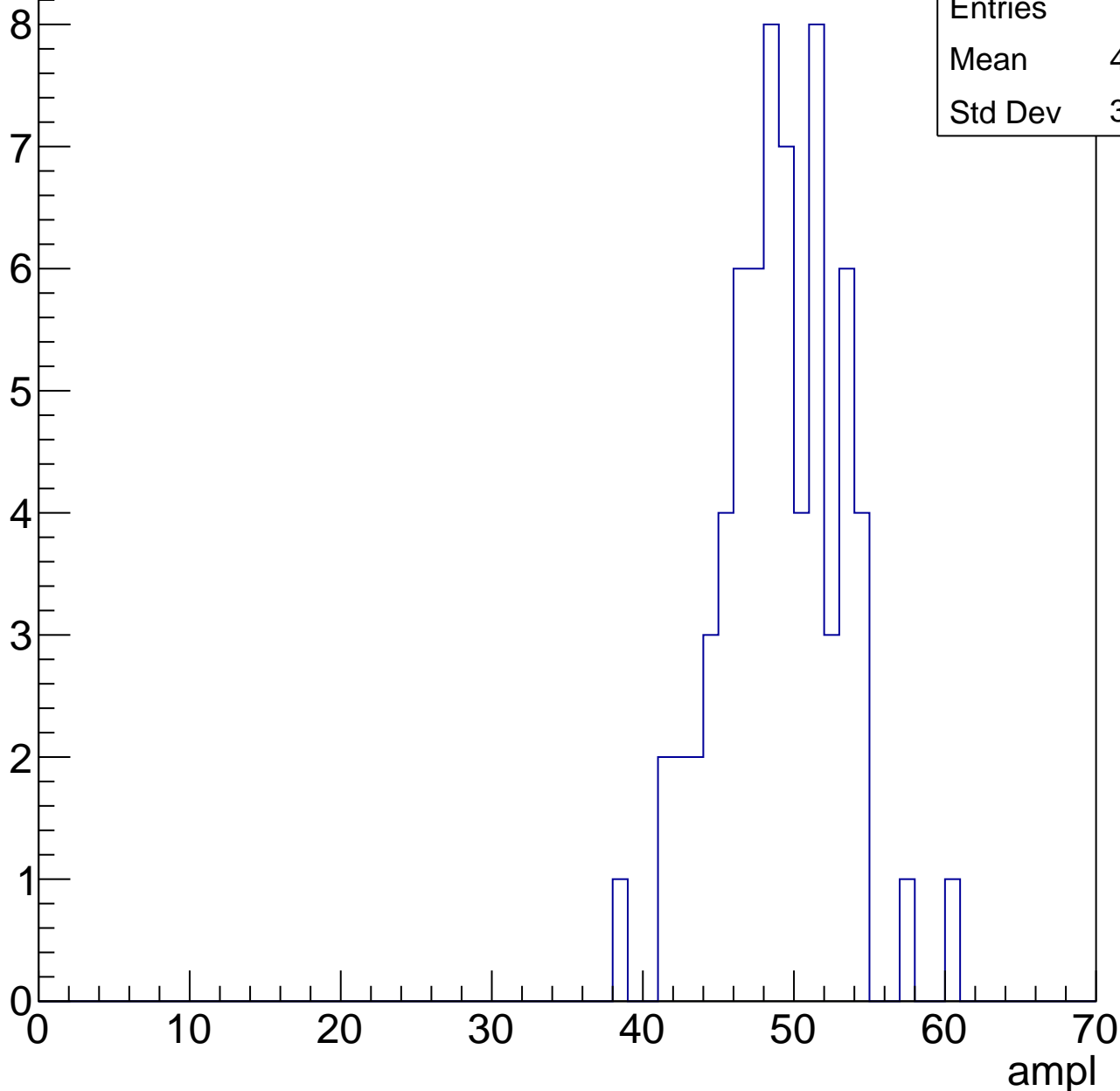


B1L100S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

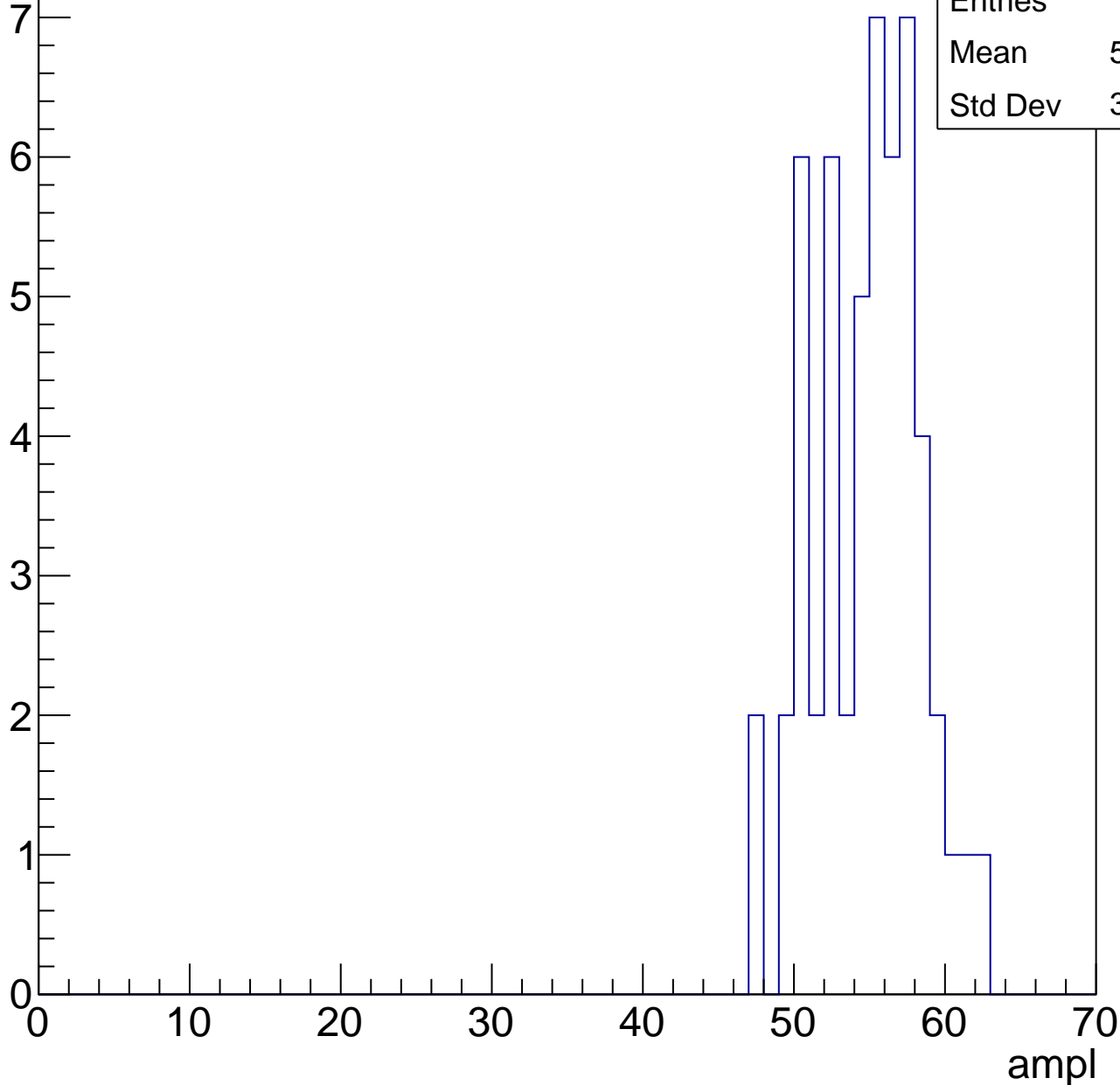
Entries	68
Mean	48.56
Std Dev	3.976



B1L100S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

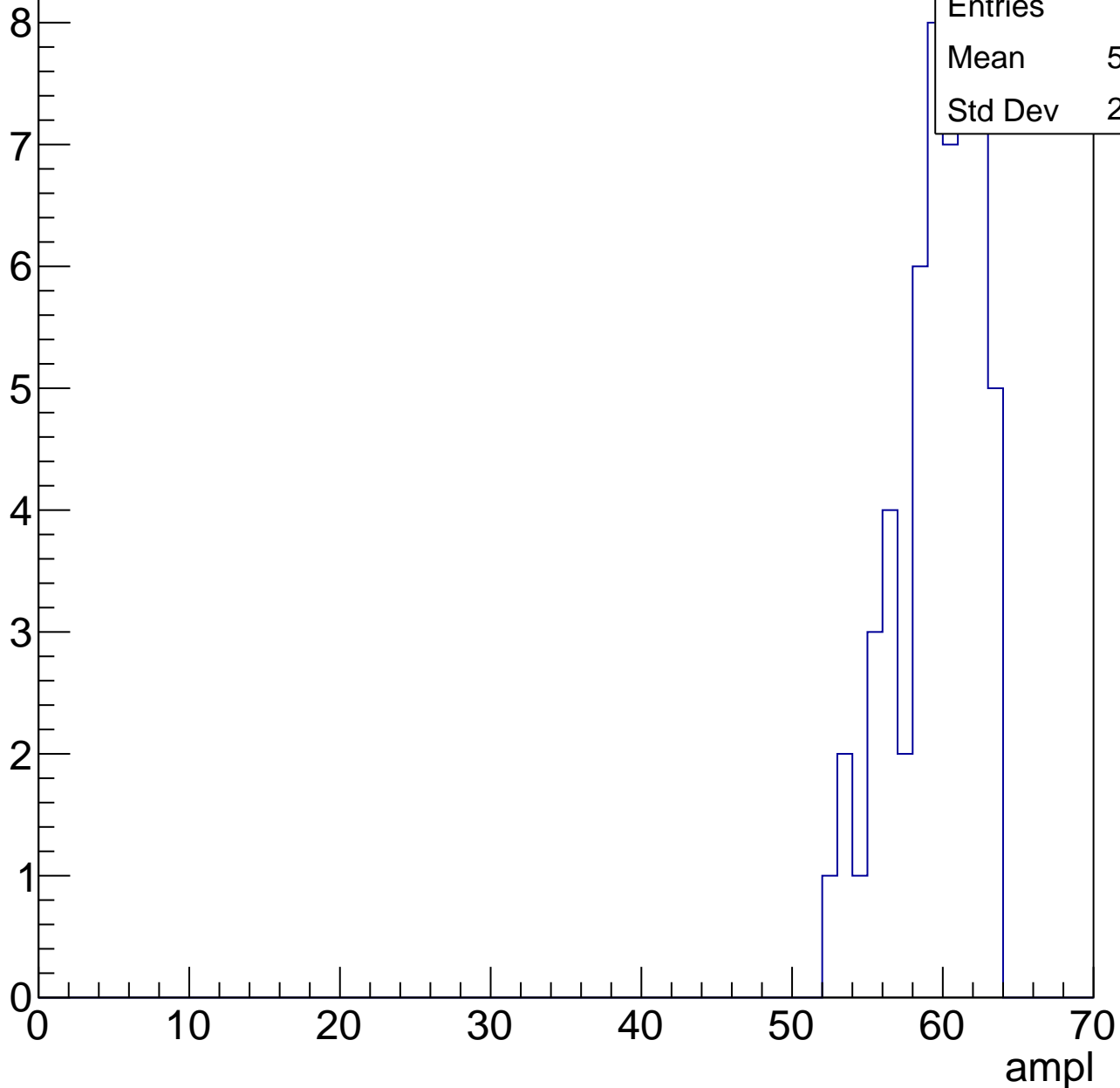
Entry



B1L100S, U14-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

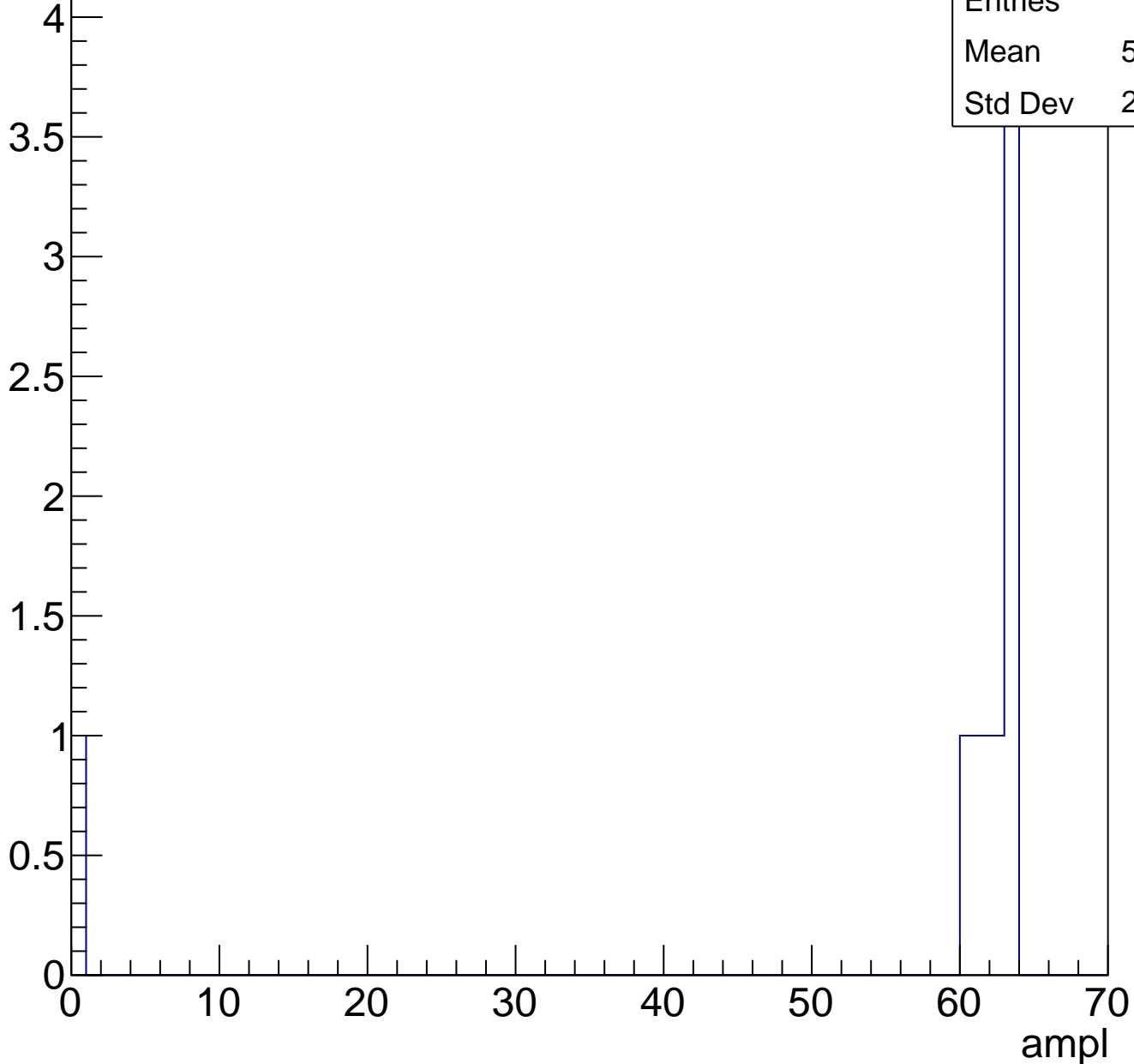
Entry



B1L100S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

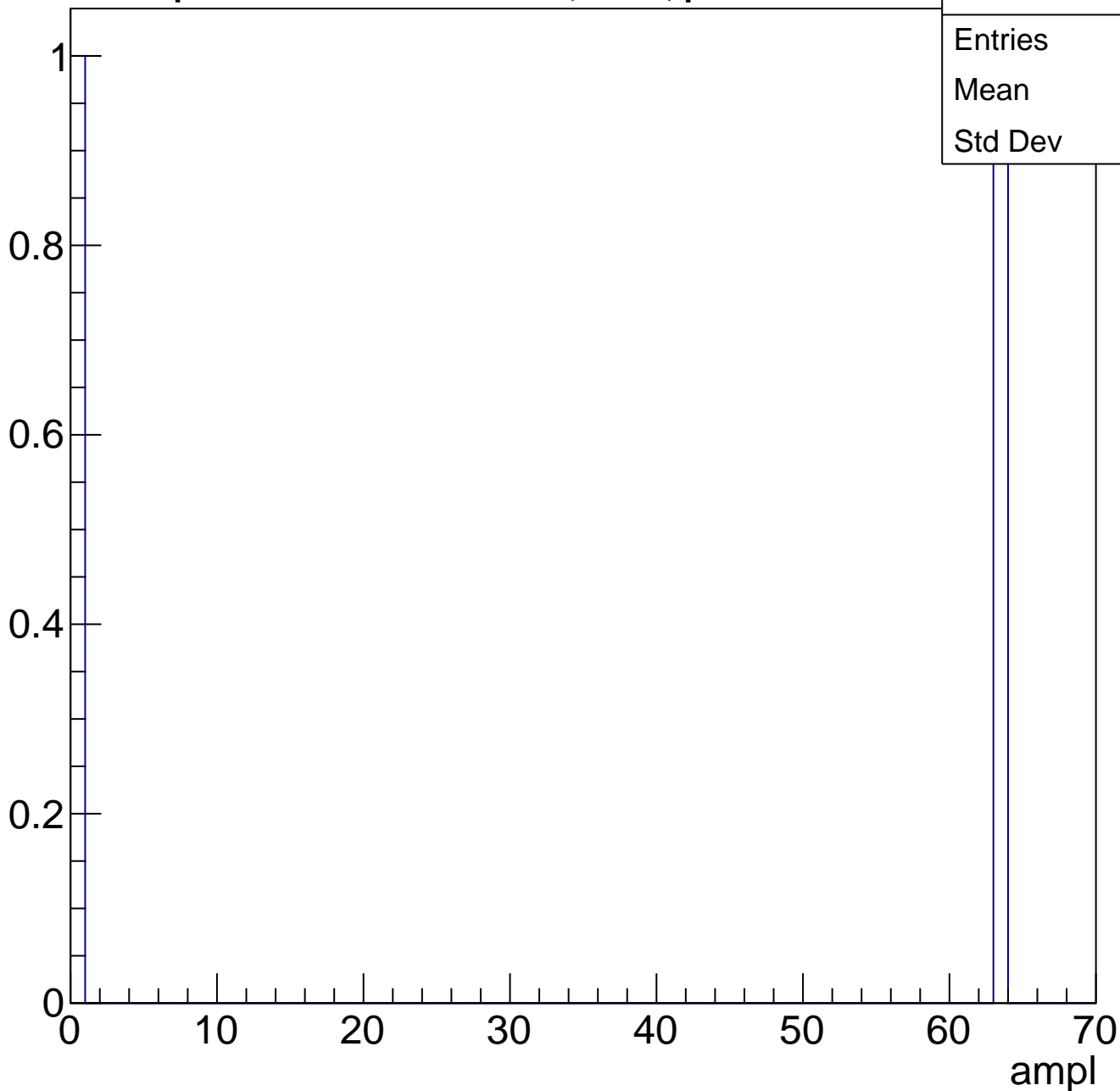
Entry



B1L100S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch93, adc0

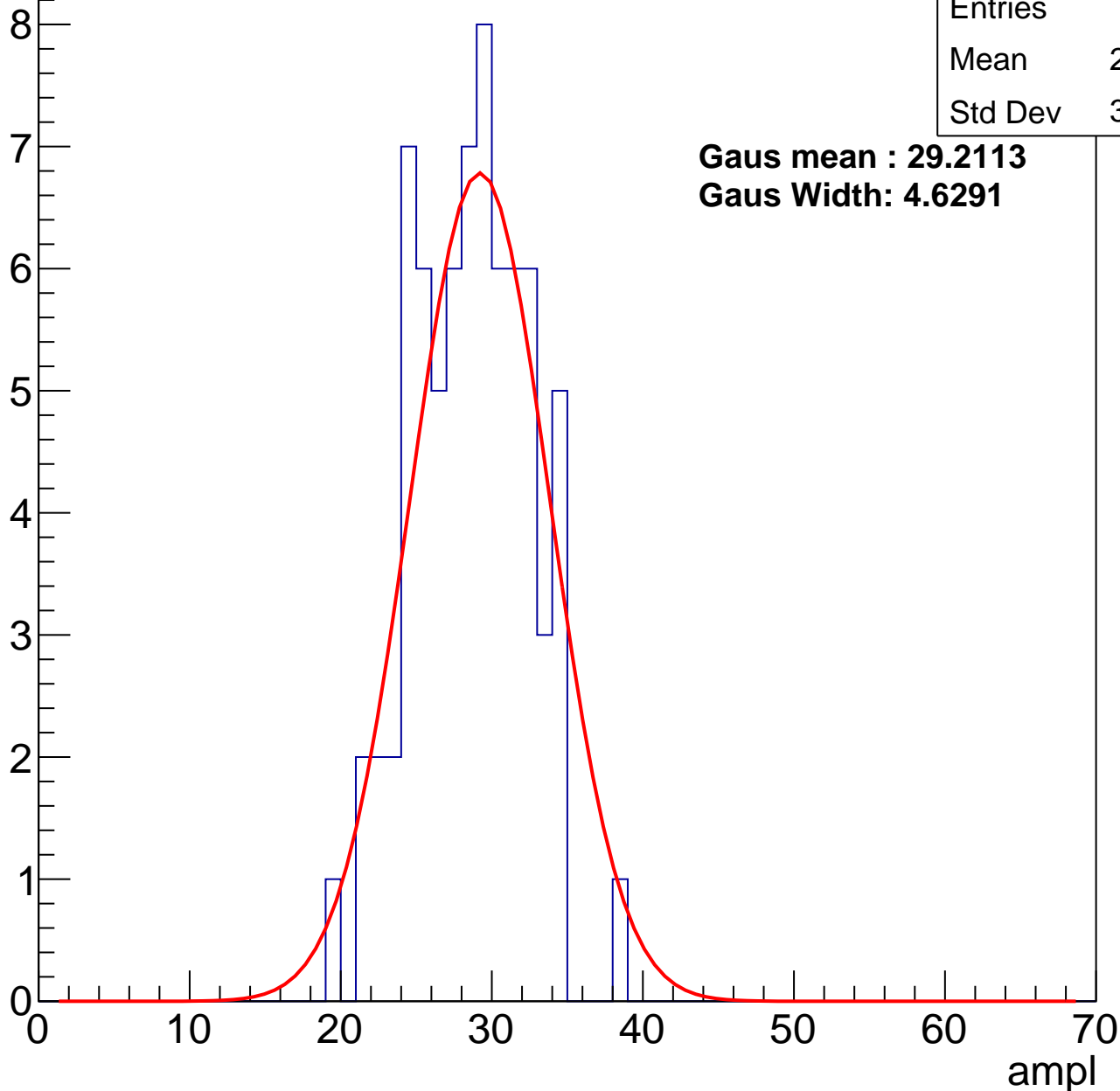
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.14
Std Dev	3.754

Gaus mean : 29.2113

Gaus Width: 4.6291



B1L100S, U14-ch93, adc1

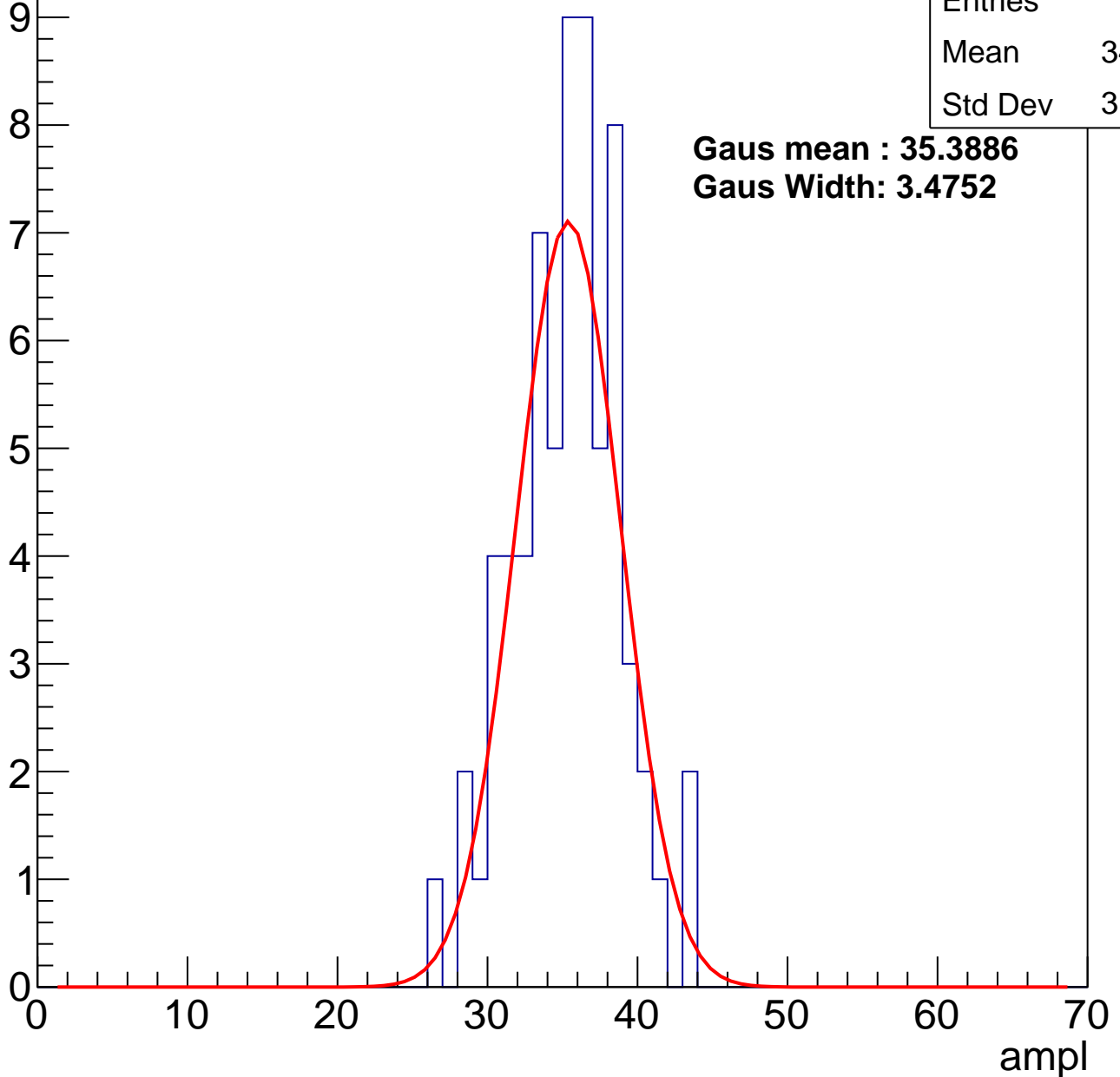
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	34.87
Std Dev	3.477

Gaus mean : 35.3886

Gaus Width: 3.4752



B1L100S, U14-ch93, adc2

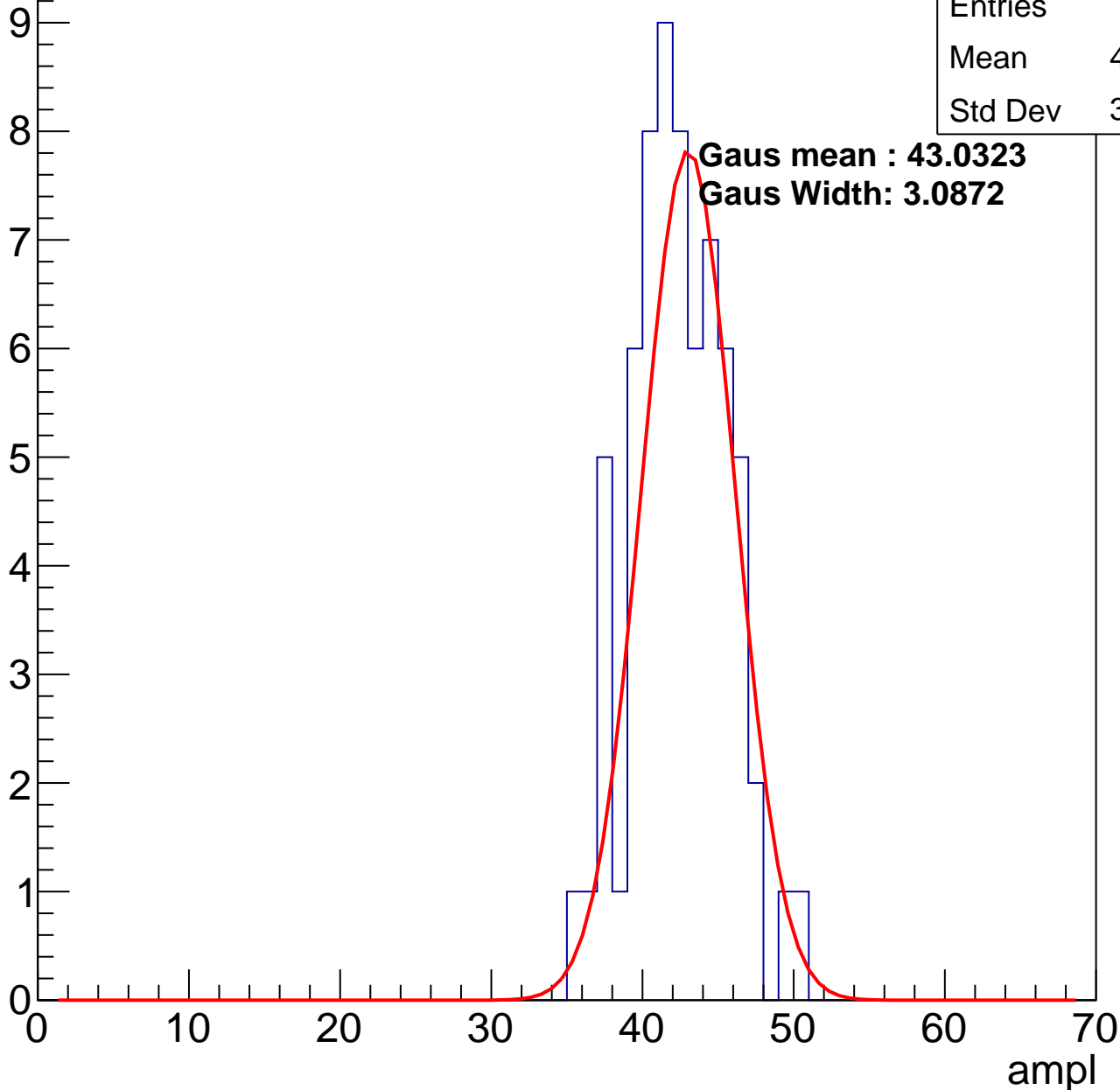
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	41.97
Std Dev	3.119

Gaus mean : 43.0323

Gaus Width: 3.0872

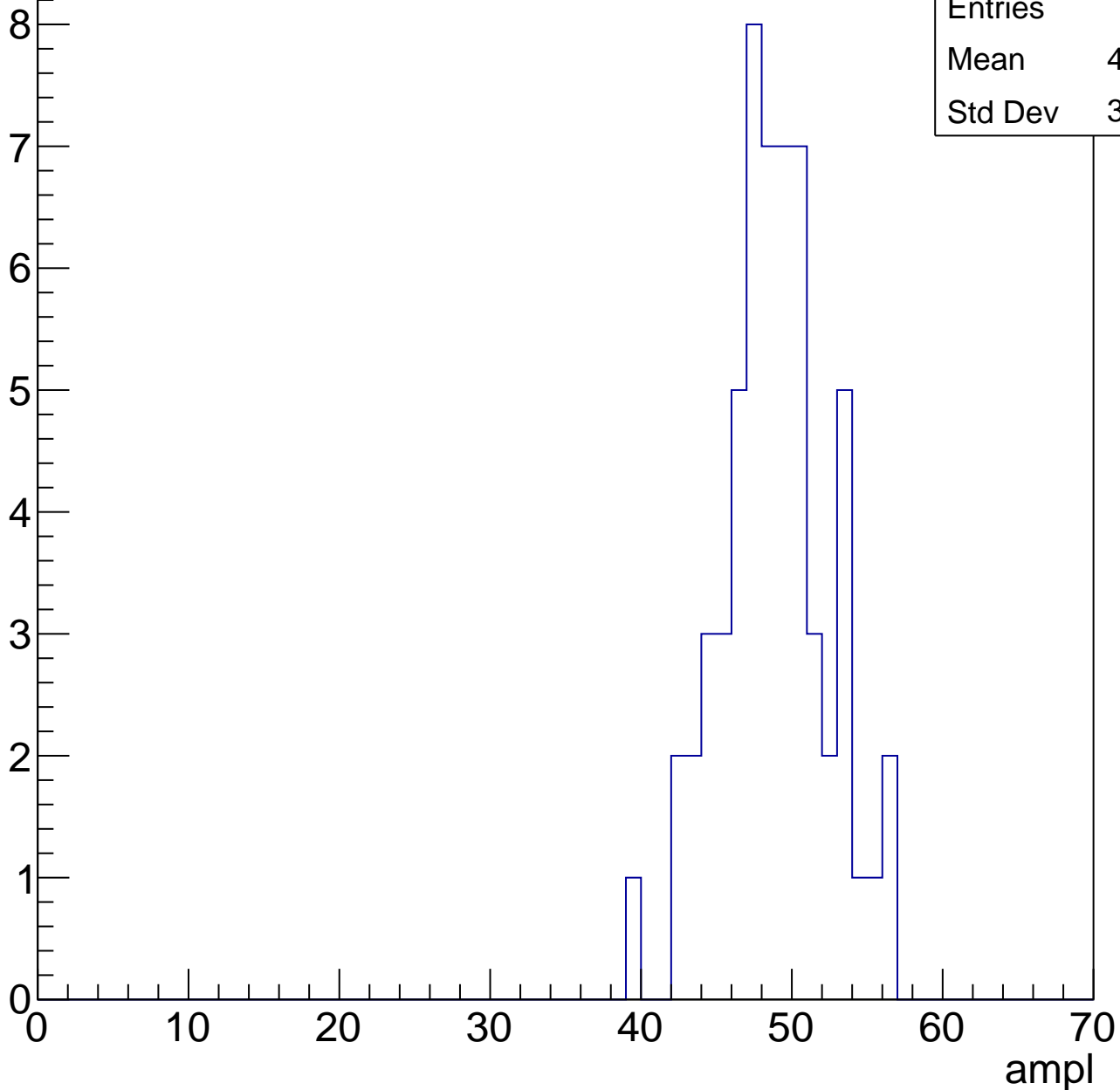


B1L100S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	48.37
Std Dev	3.517

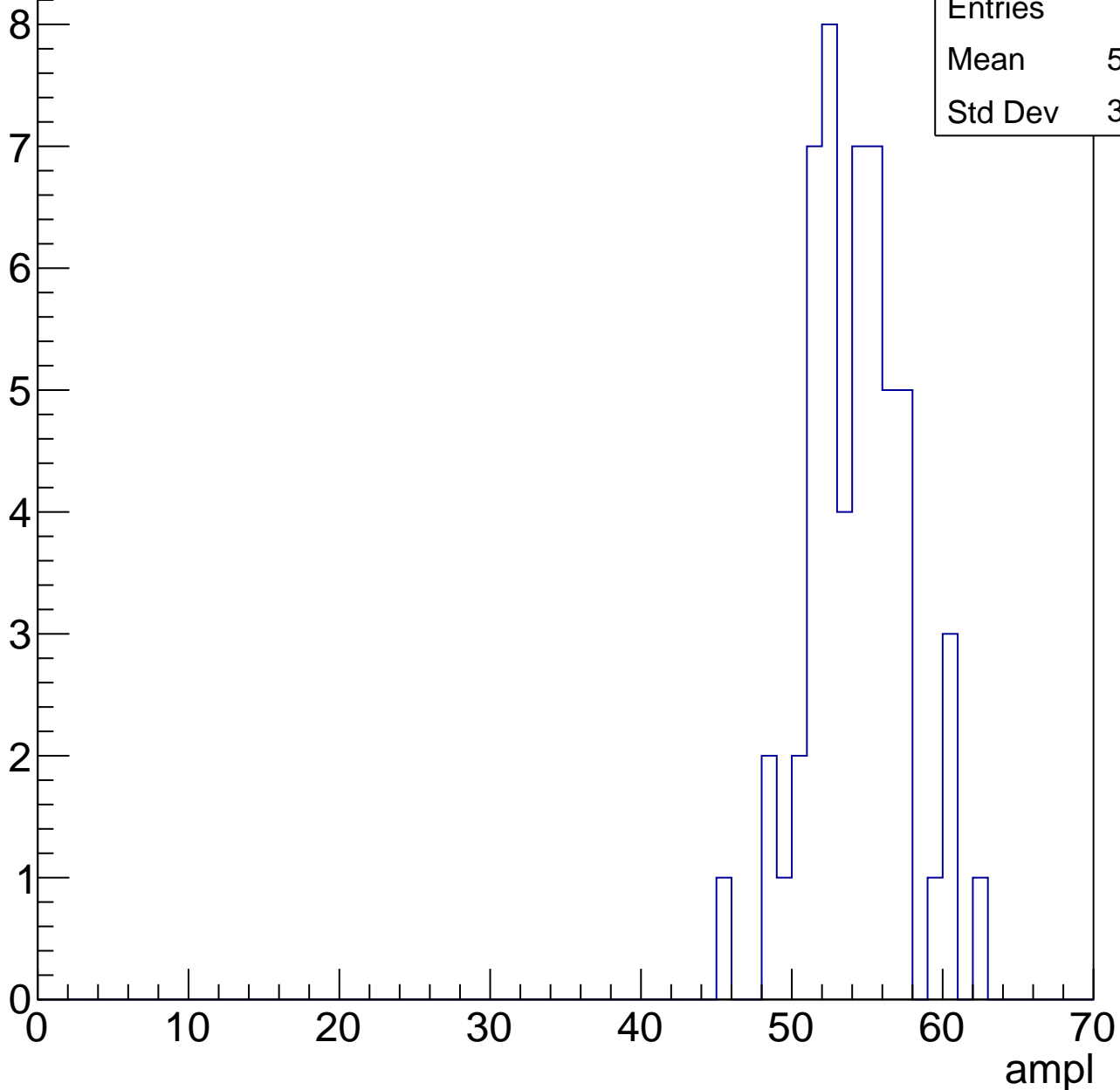


B1L100S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	53.78
Std Dev	3.264



B1L100S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

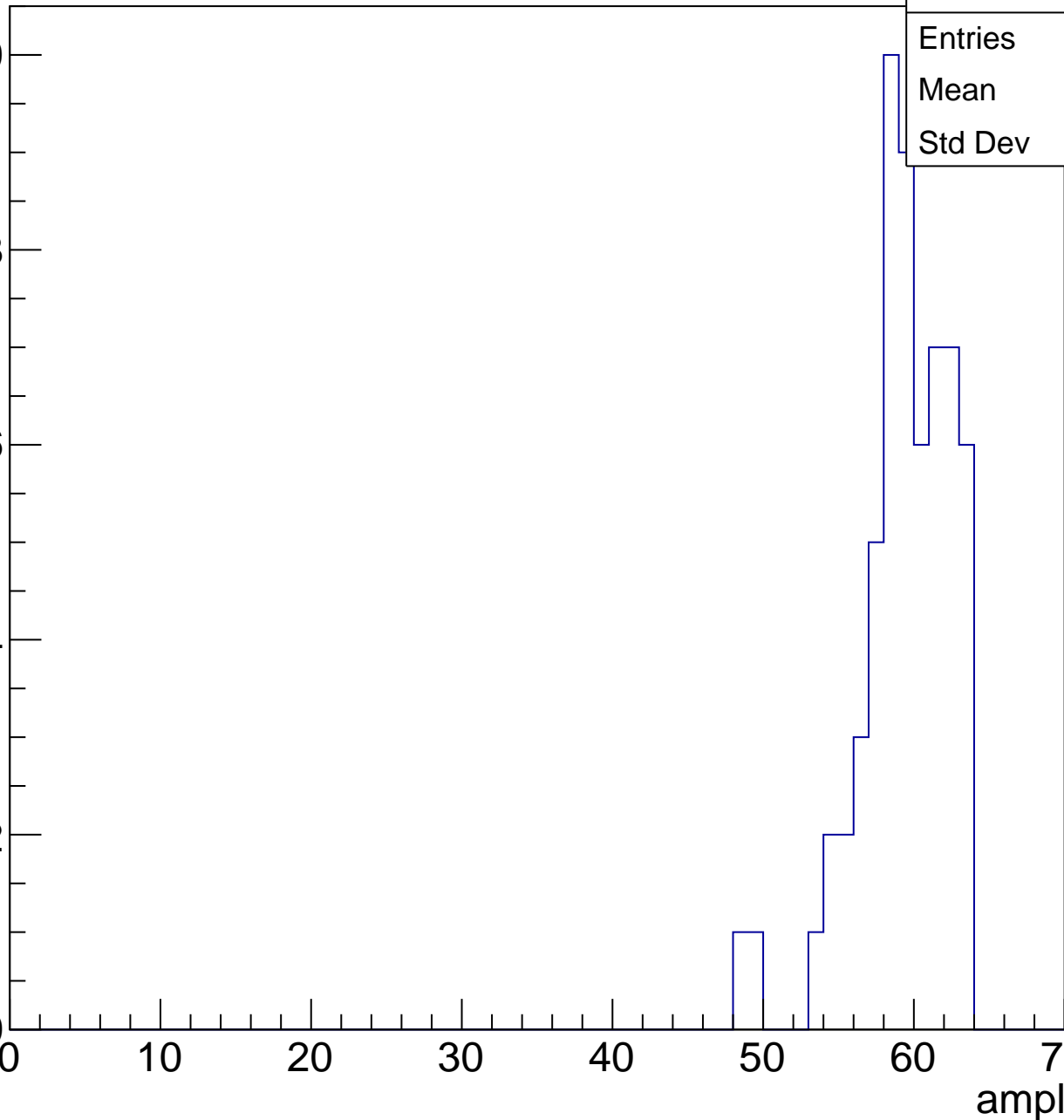
50

60

70

ampl

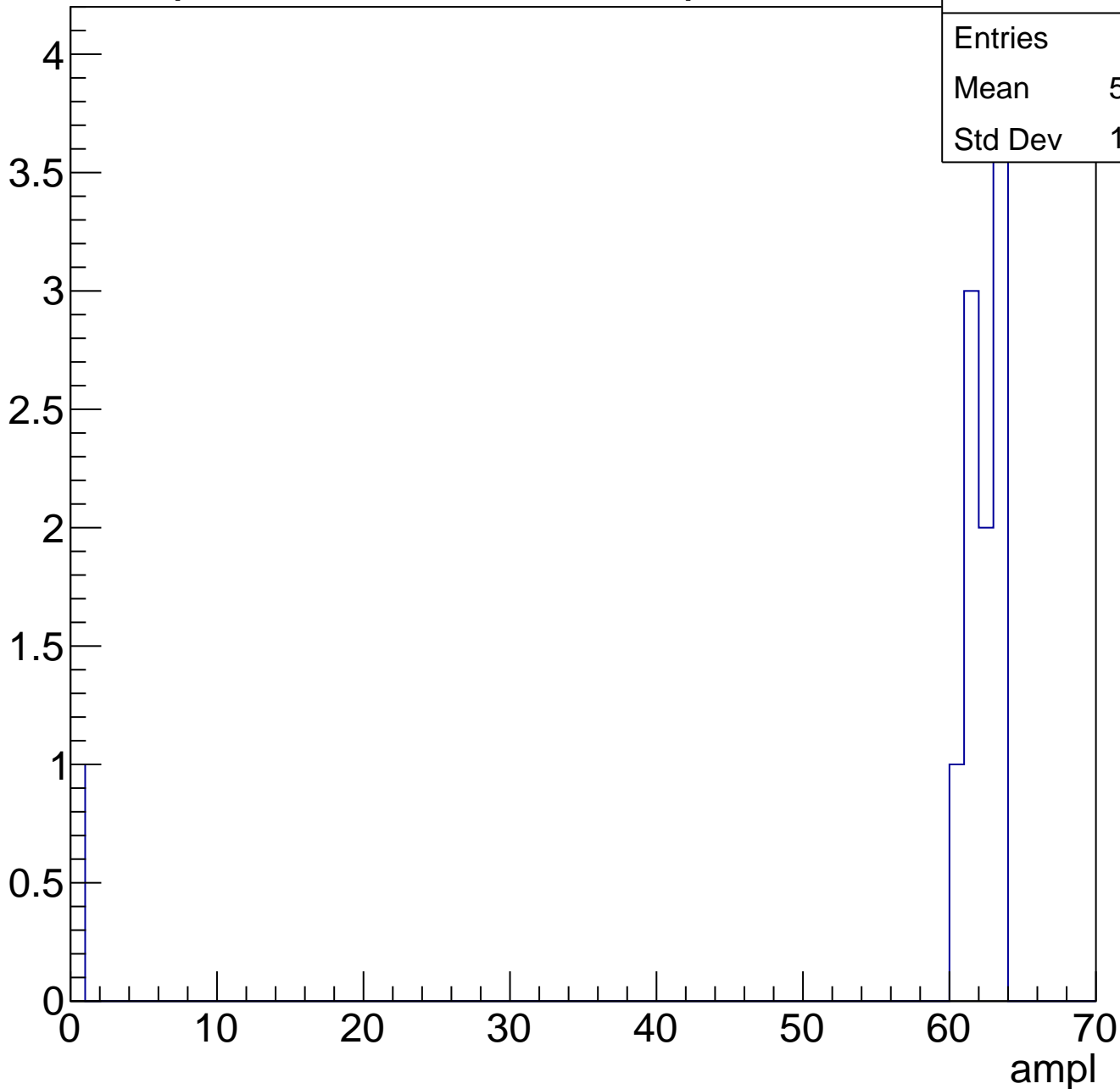
Entries	60
Mean	58.85
Std Dev	3.13



B1L100S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	11
Mean	56.27
Std Dev	17.82

B1L100S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch94, adc0

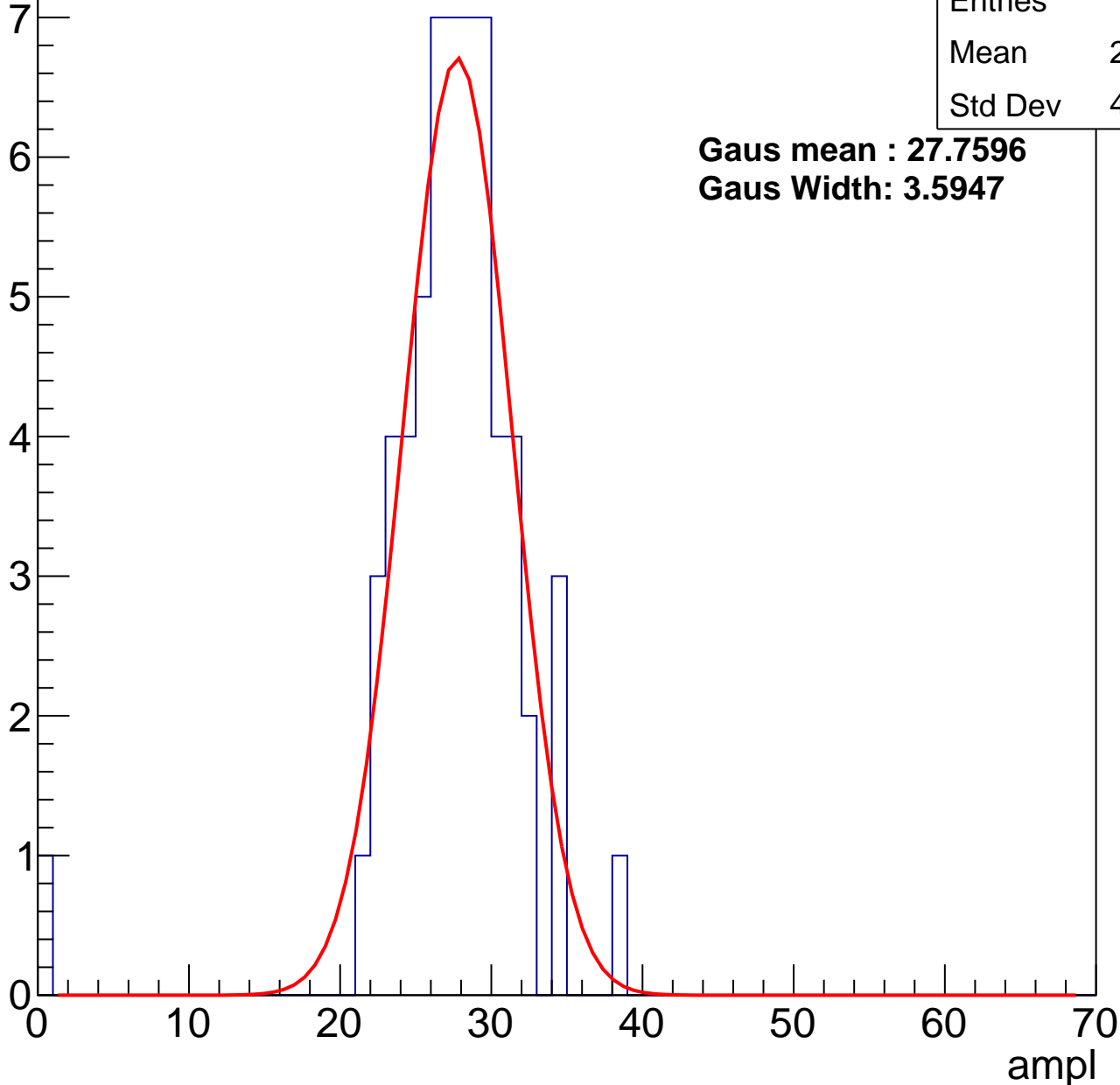
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	26.97
Std Dev	4.865

Gaus mean : 27.7596

Gaus Width: 3.5947



B1L100S, U14-ch94, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	33.75
Std Dev	3.529

Gaus mean : 34.6430

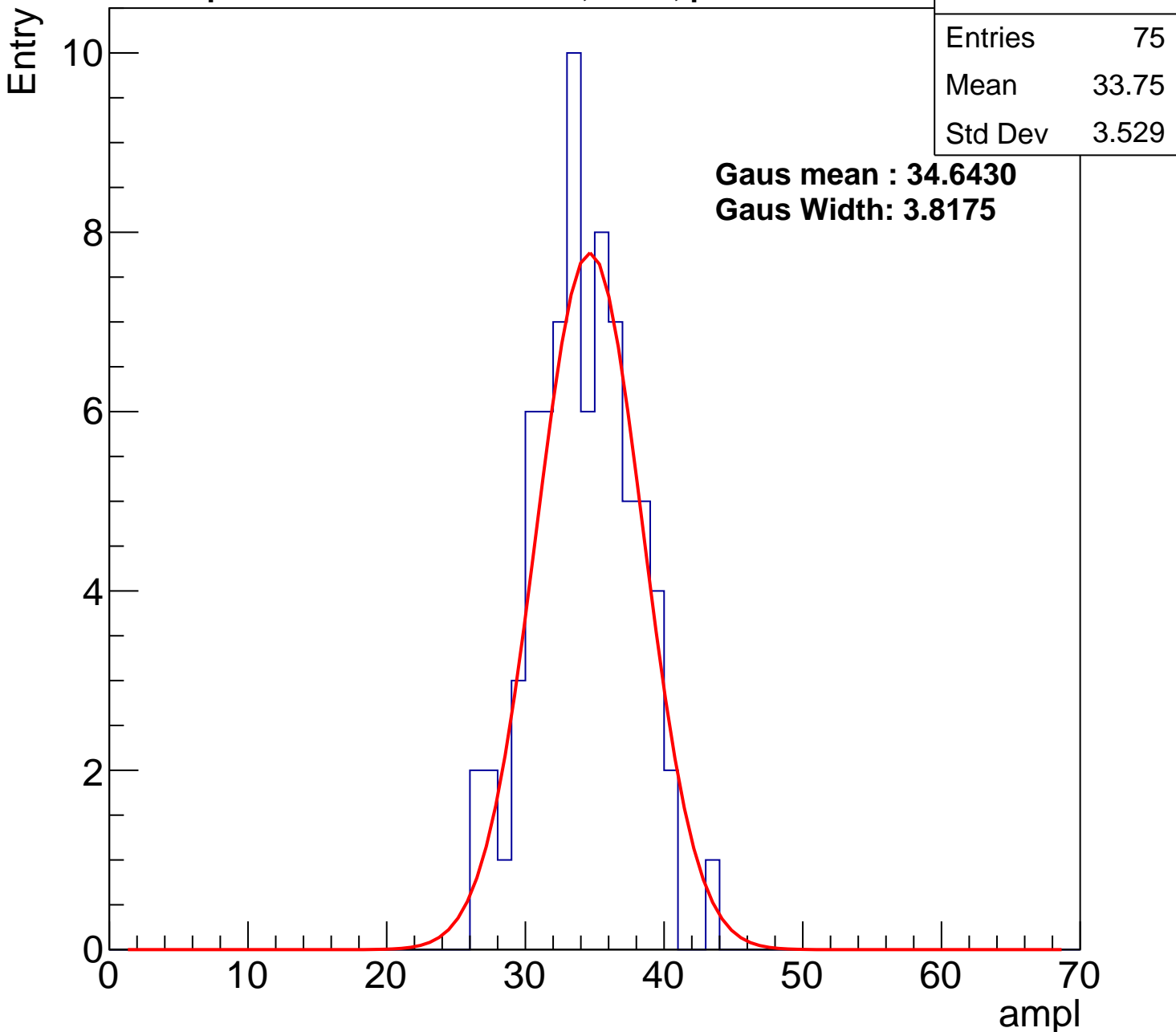
Gaus Width: 3.8175

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch94, adc2

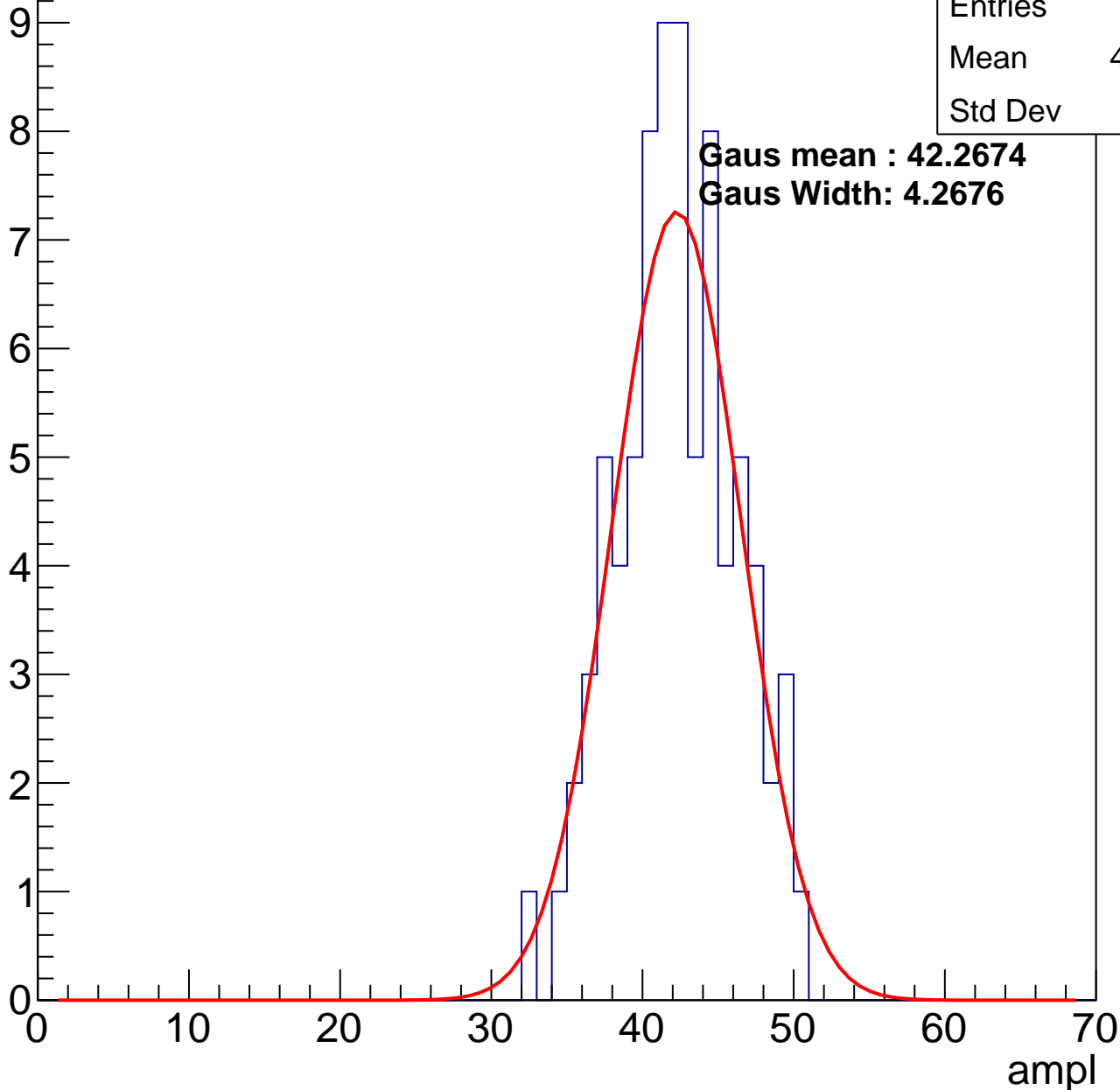
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	41.78
Std Dev	3.86

Gaus mean : 42.2674

Gaus Width: 4.2676

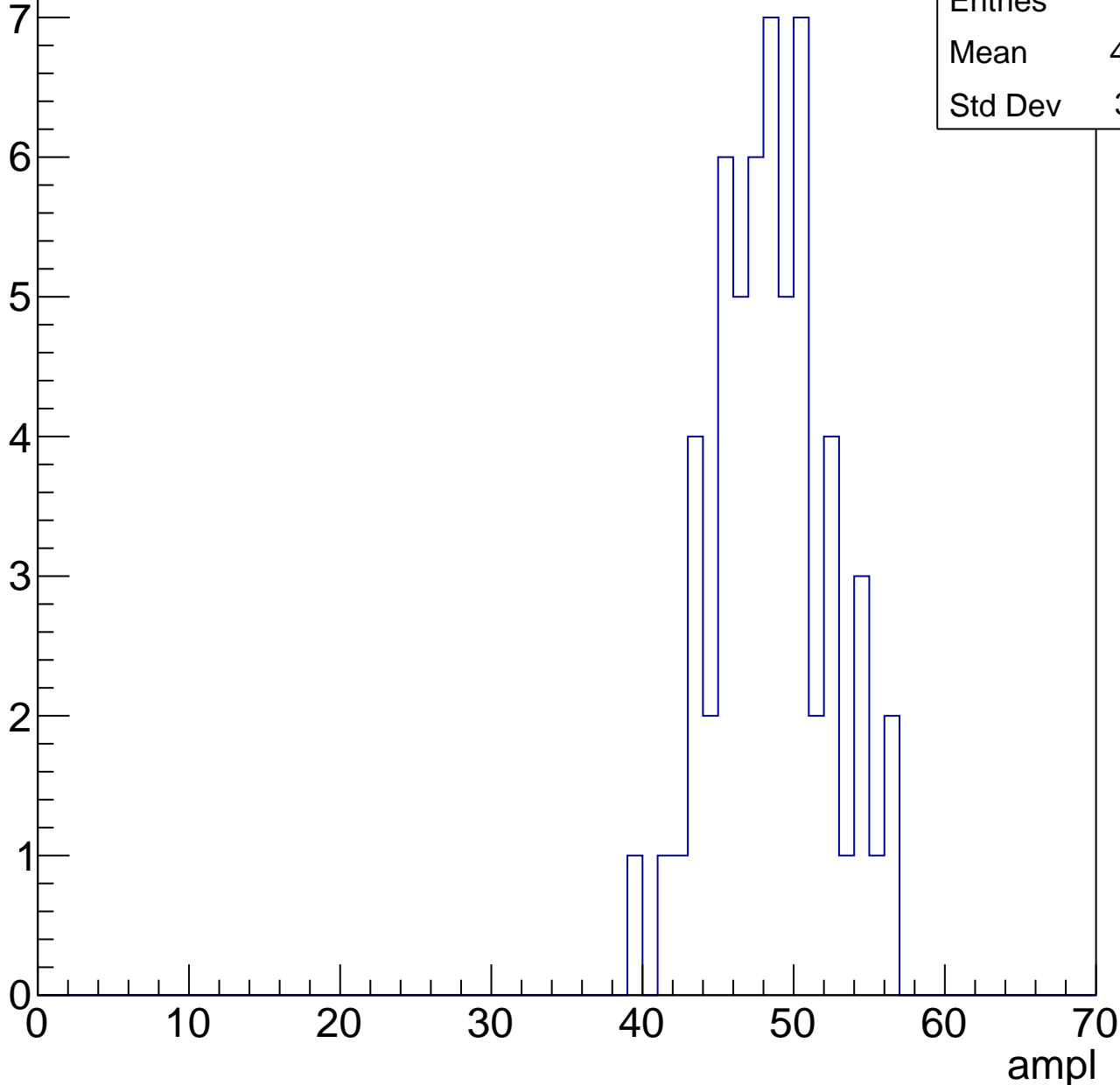


B1L100S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.05
Std Dev	3.721

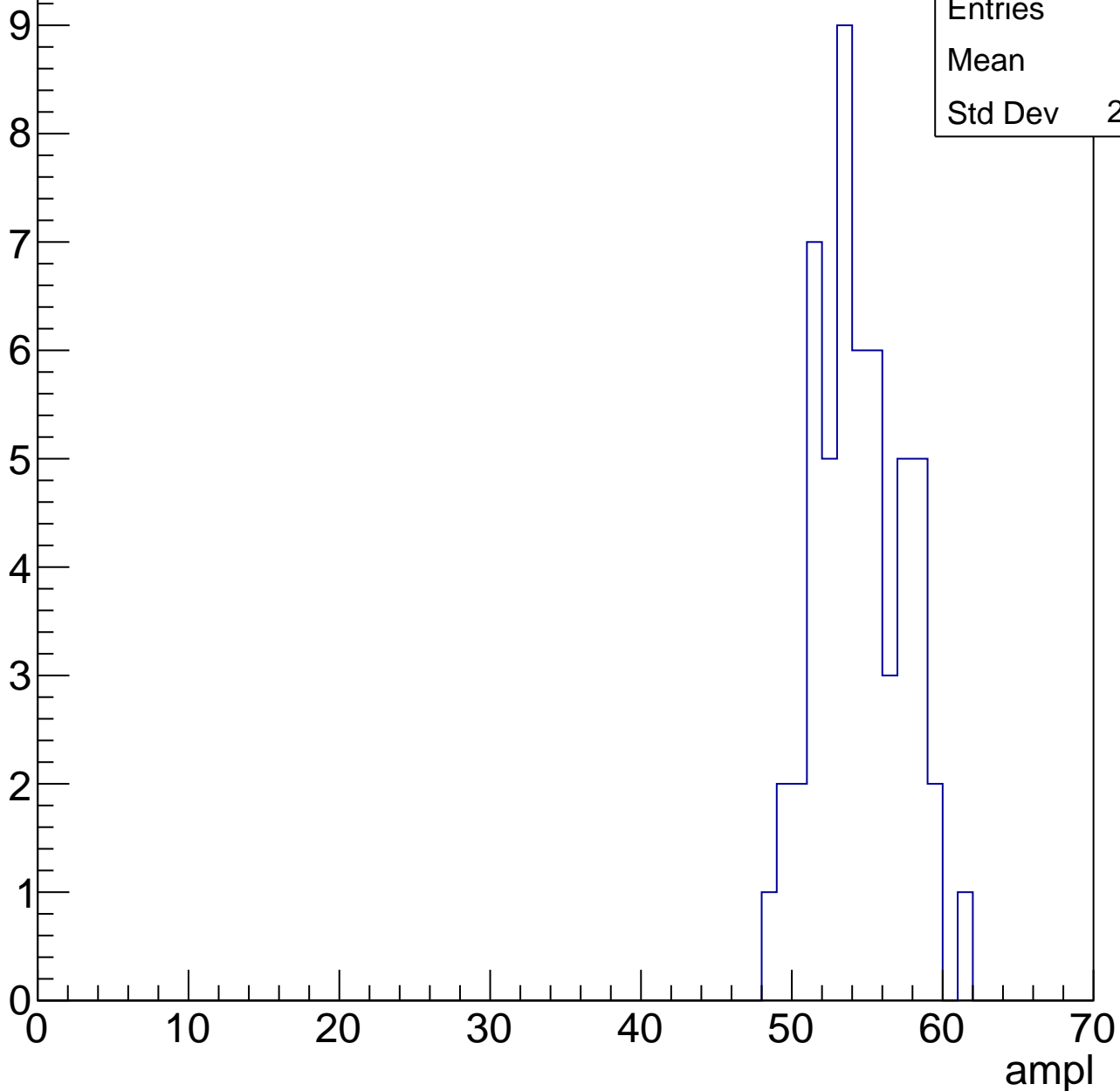


B1L100S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

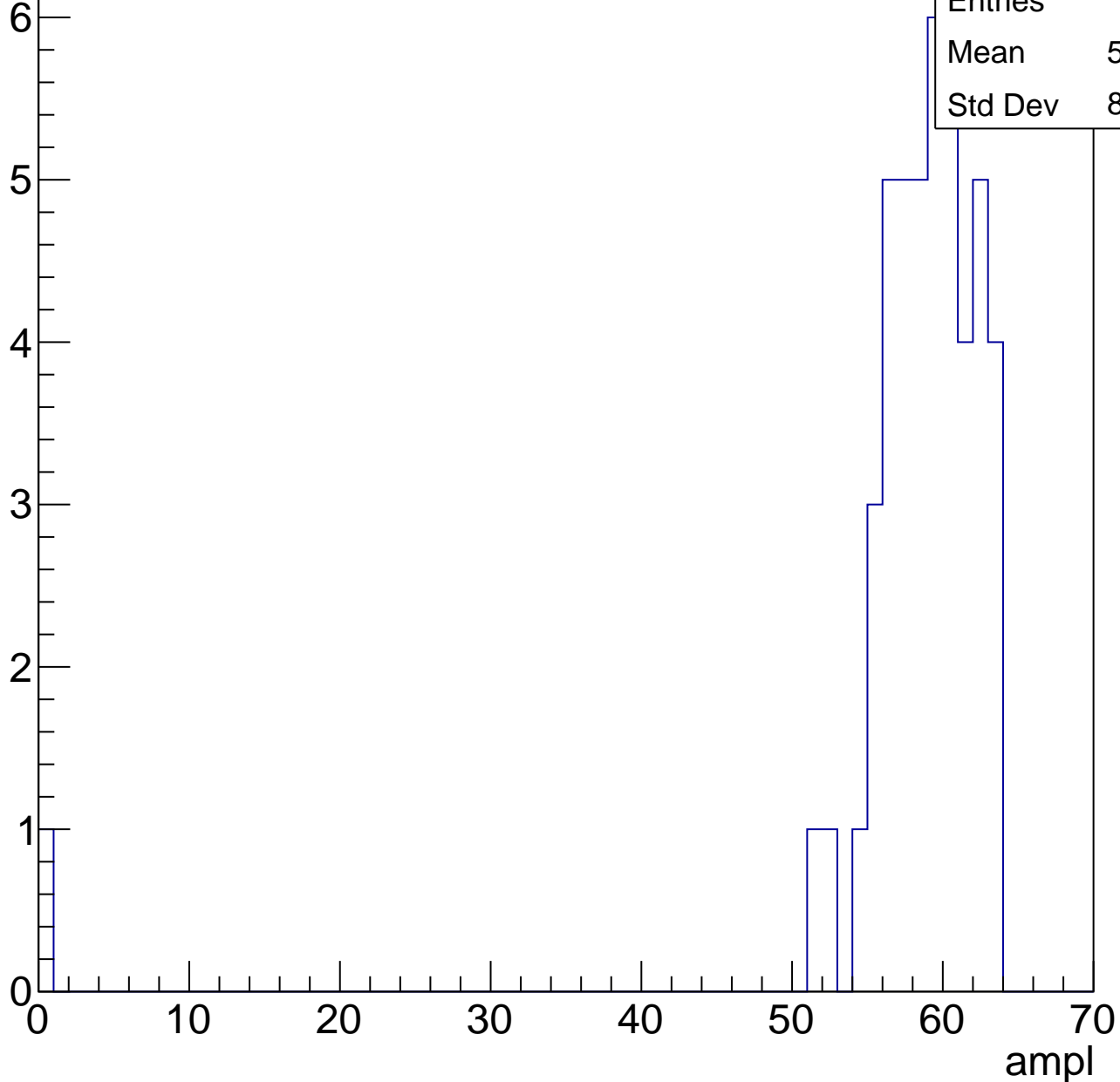
Entries	54
Mean	54
Std Dev	2.893



B1L100S, U14-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

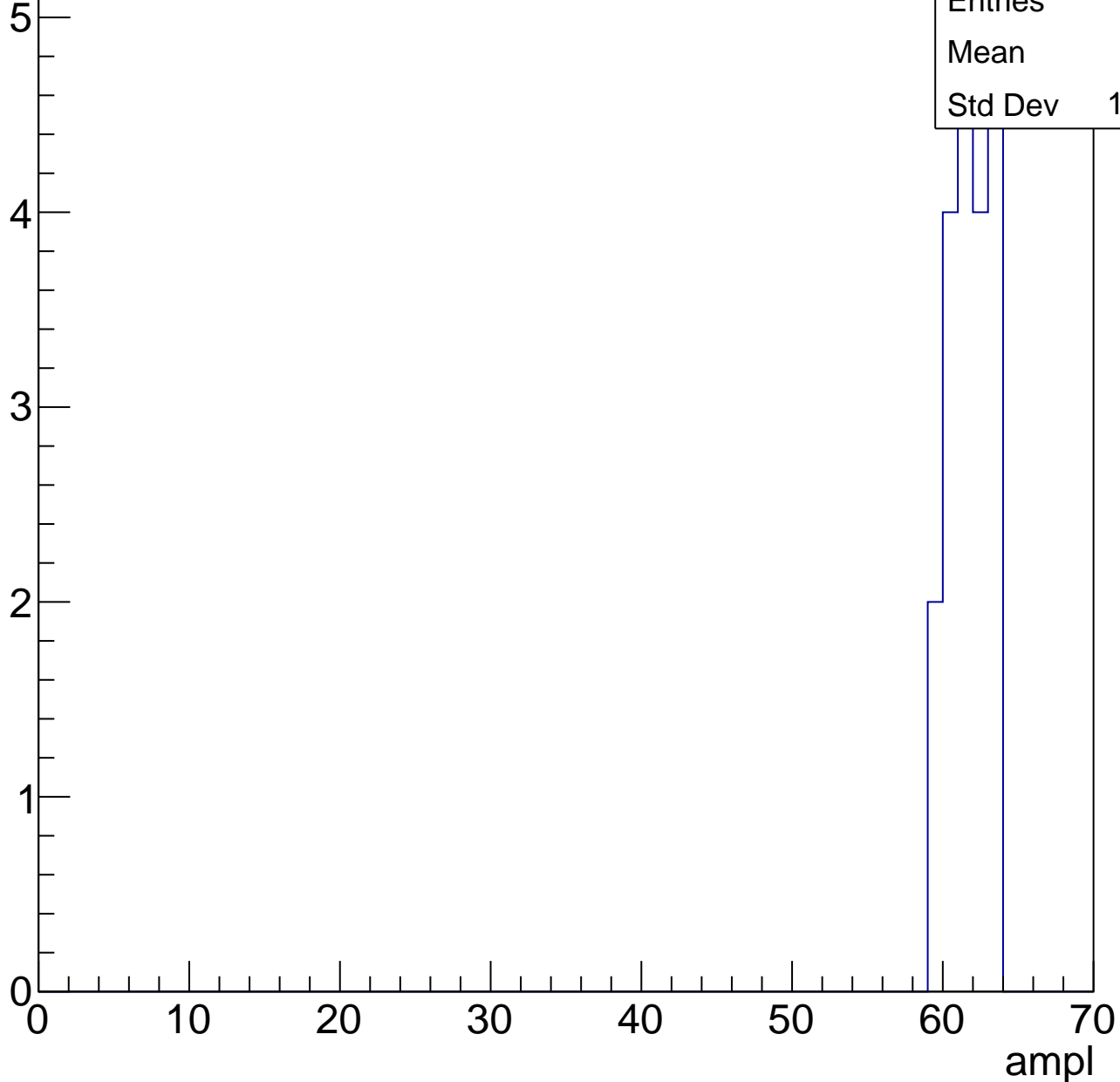
Entry



B1L100S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	20
Mean	61.3
Std Dev	1.308

B1L100S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch95, adc0

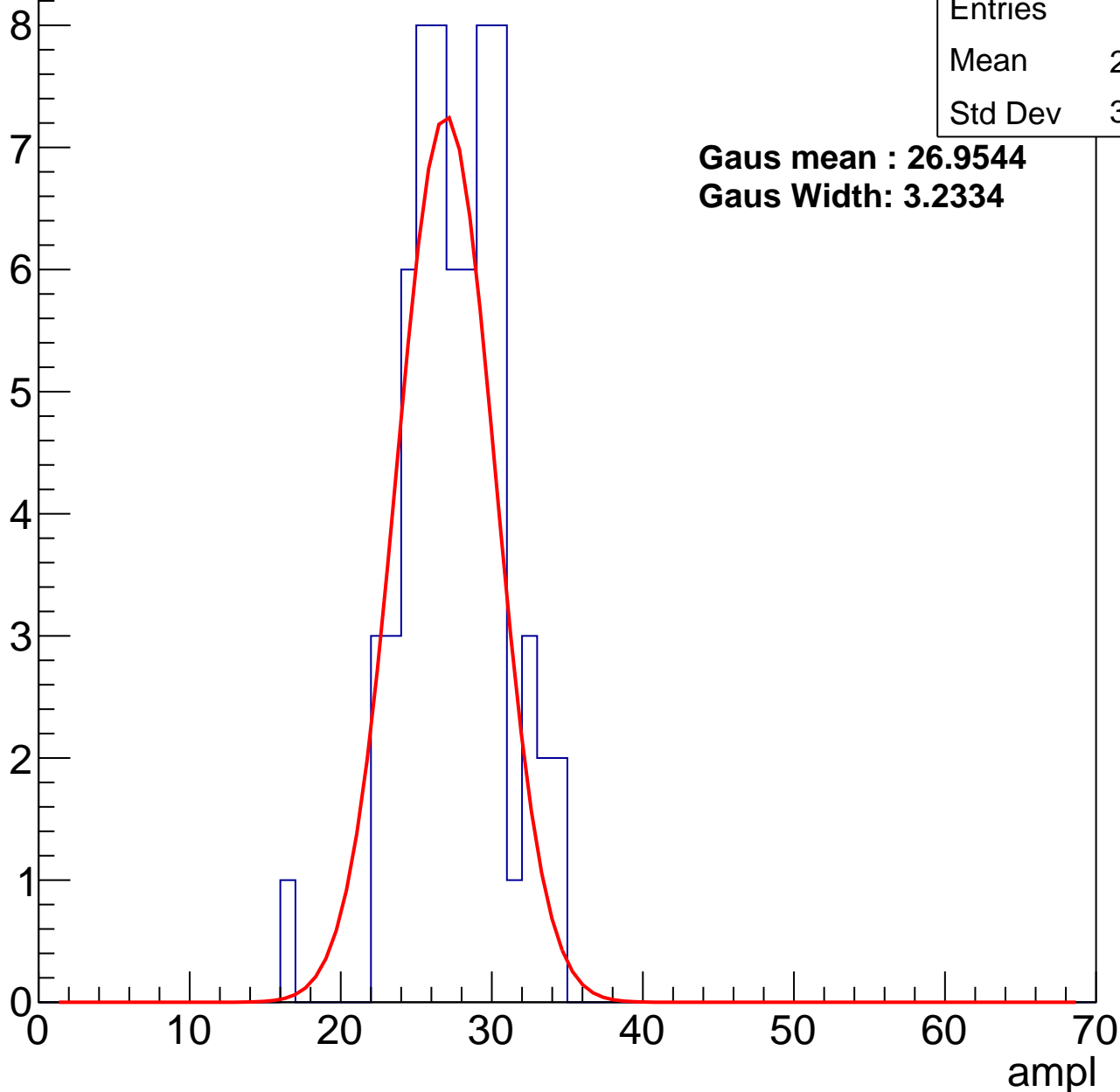
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.17
Std Dev	3.298

Gaus mean : 26.9544

Gaus Width: 3.2334



B1L100S, U14-ch95, adc1

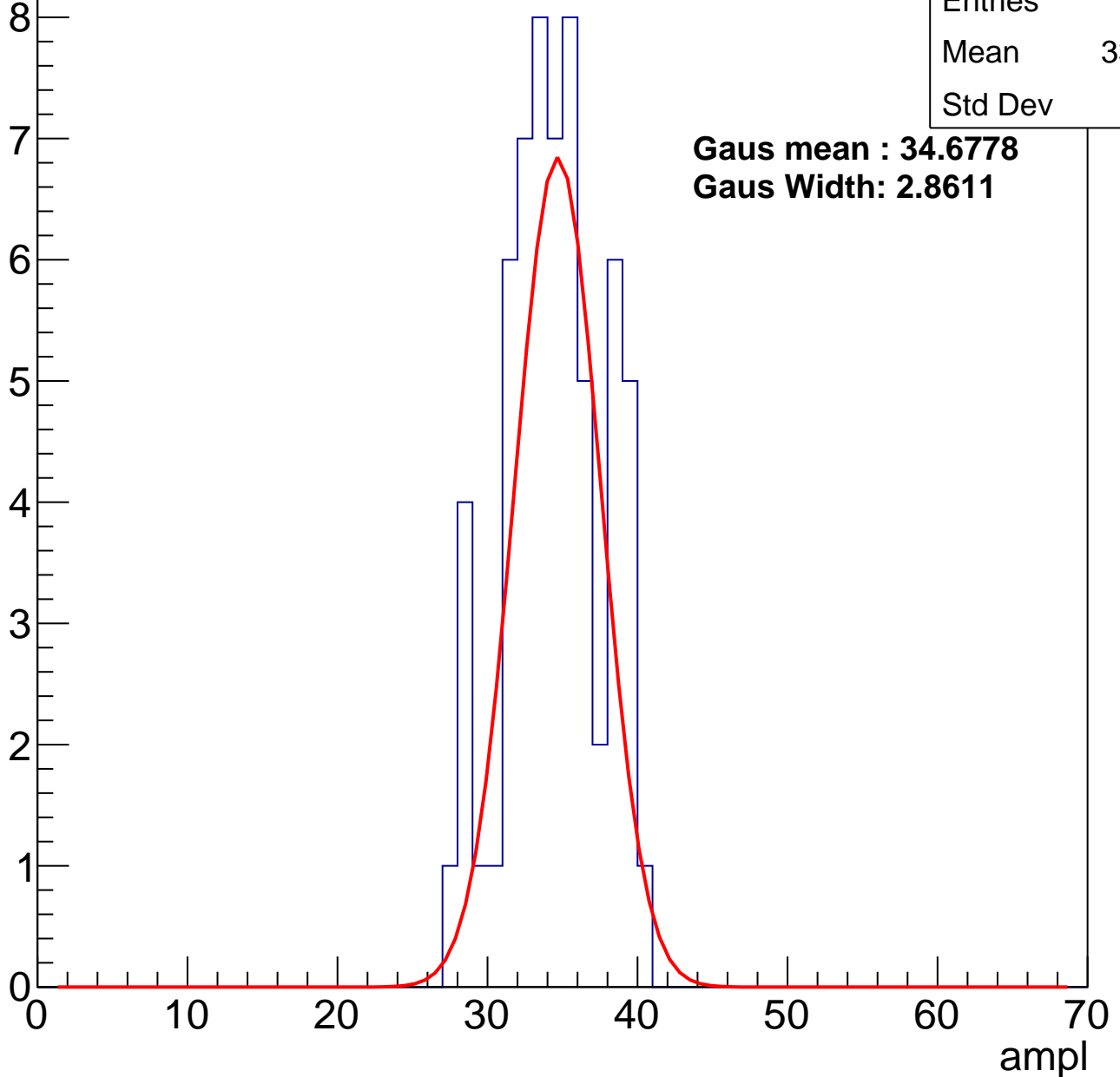
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	33.98
Std Dev	3.18

Gaus mean : 34.6778

Gaus Width: 2.8611



B1L100S, U14-ch95, adc2

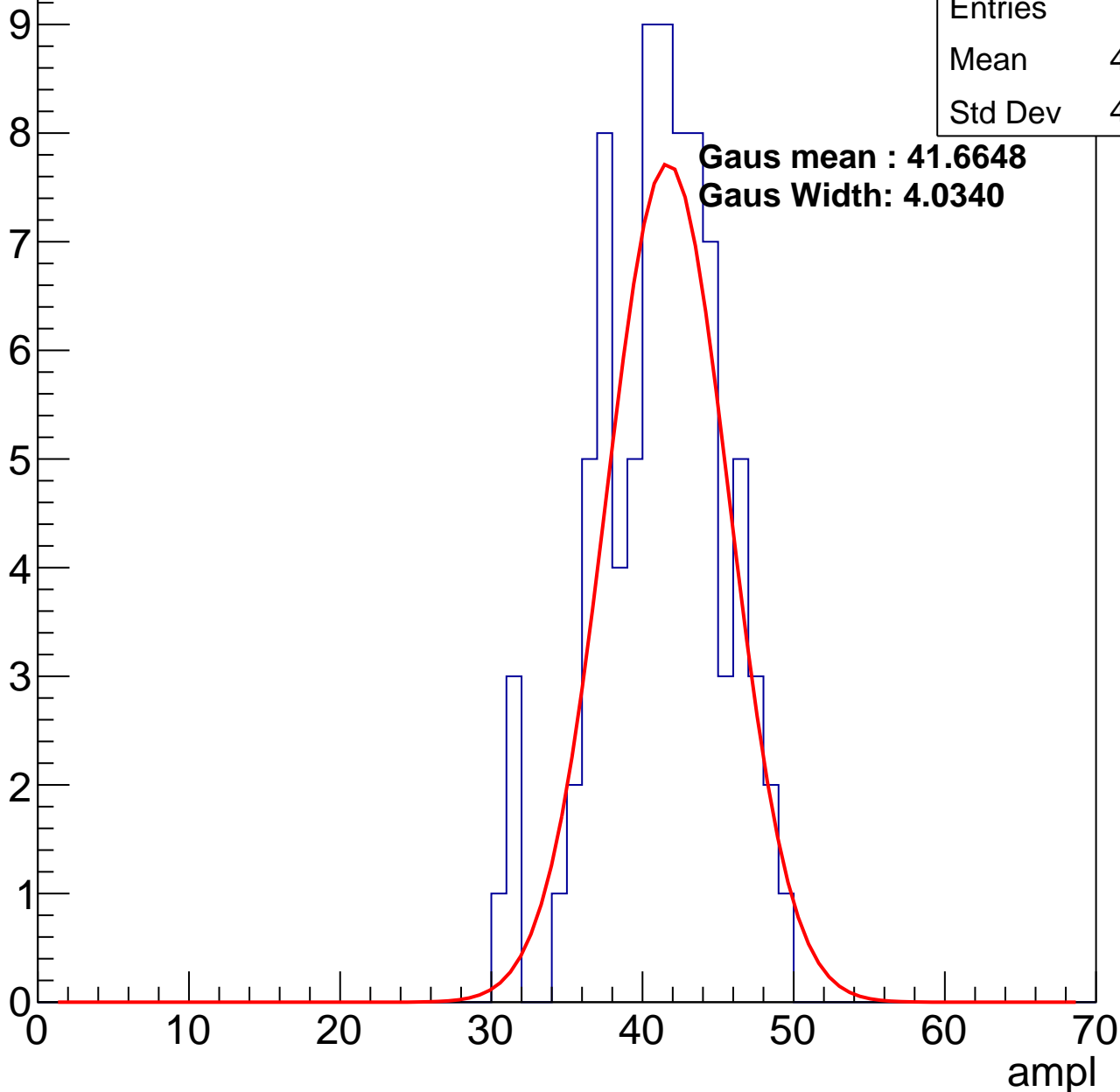
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	40.69
Std Dev	4.079

Gaus mean : 41.6648

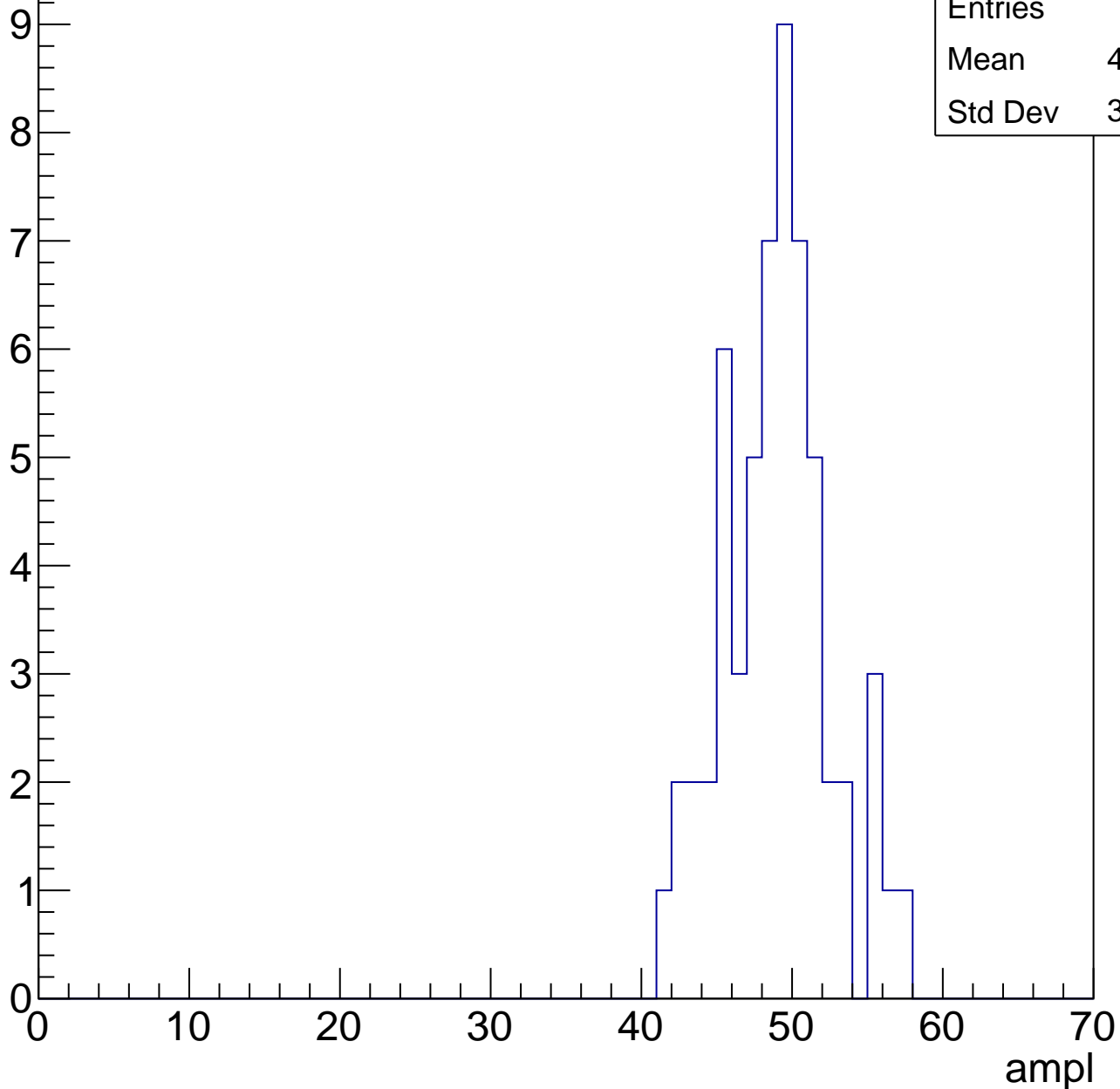
Gaus Width: 4.0340



B1L100S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



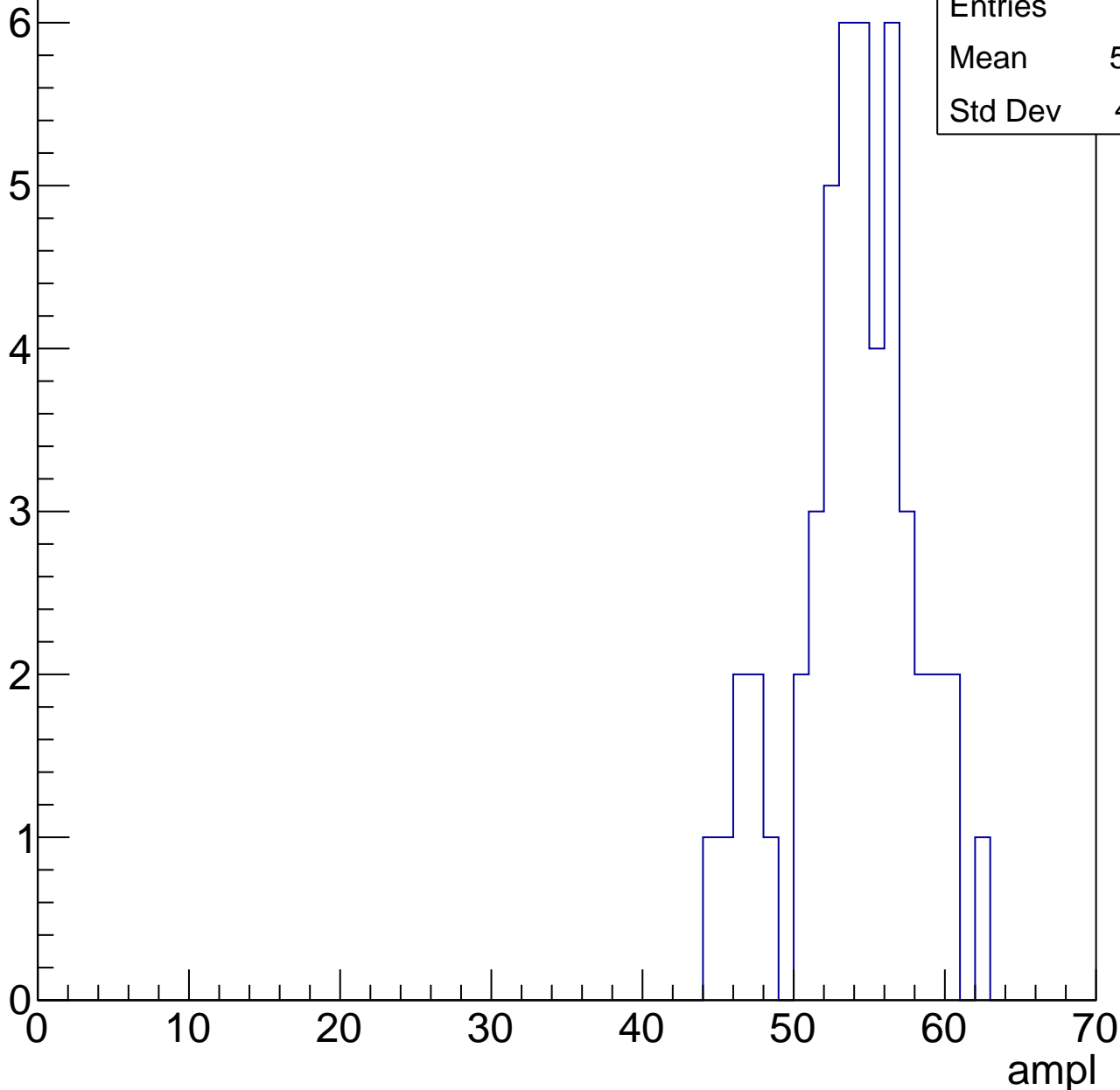
Entries	58
Mean	48.48
Std Dev	3.515

B1L100S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

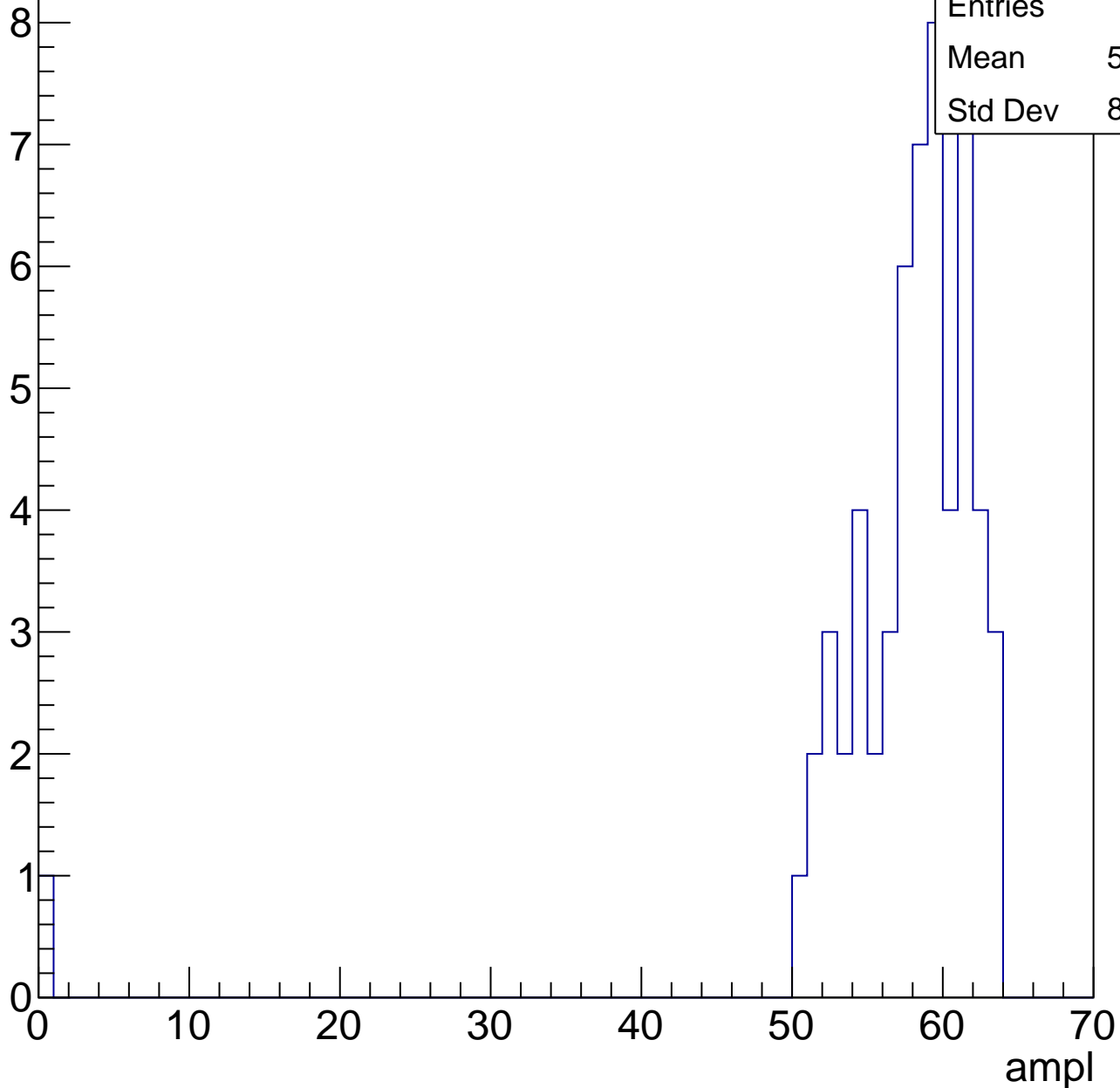
Entries	49
Mean	53.49
Std Dev	4.011



B1L100S, U14-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

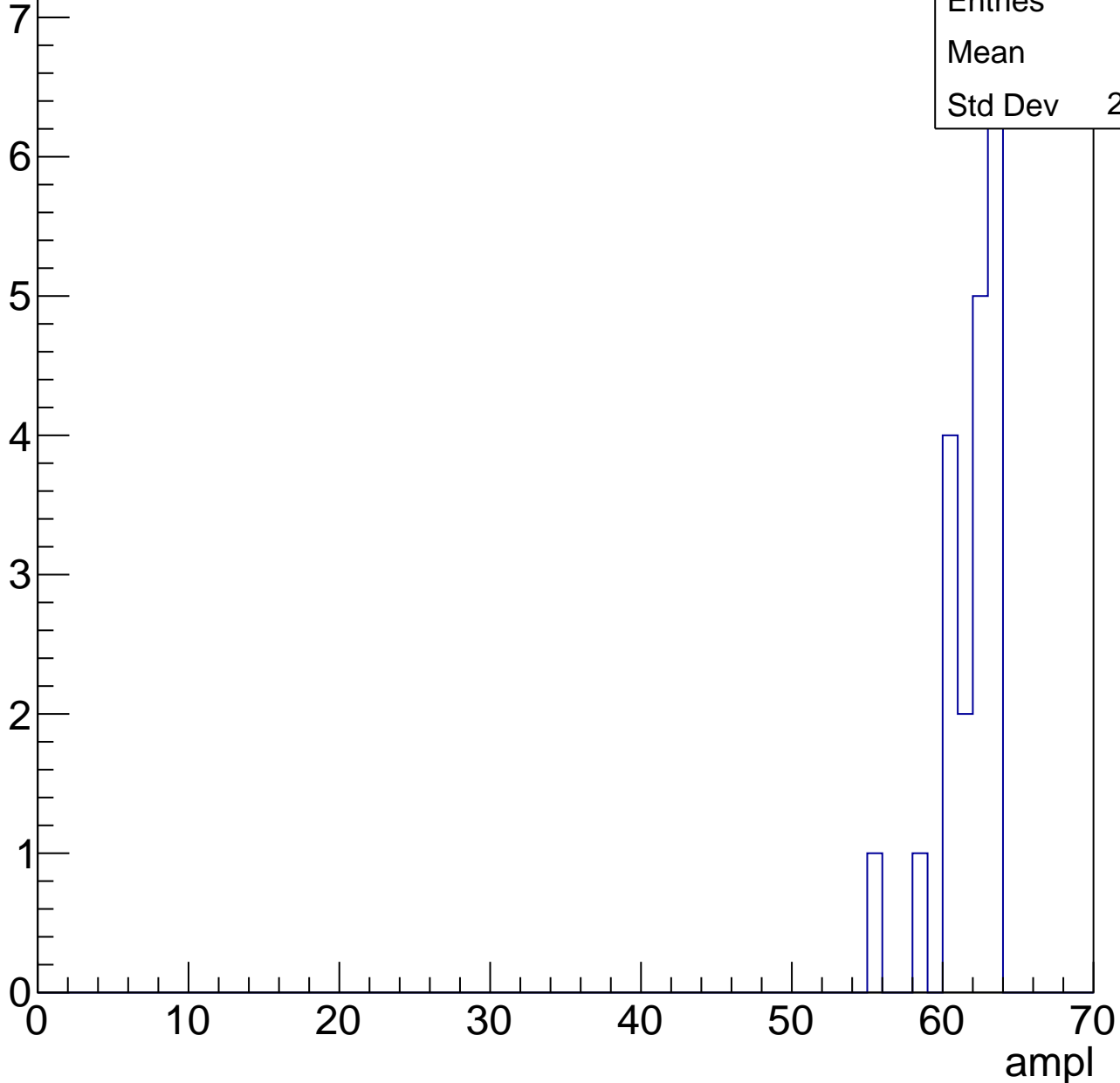


B1L100S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	61.3
Std Dev	2.002



B1L100S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch96, adc0

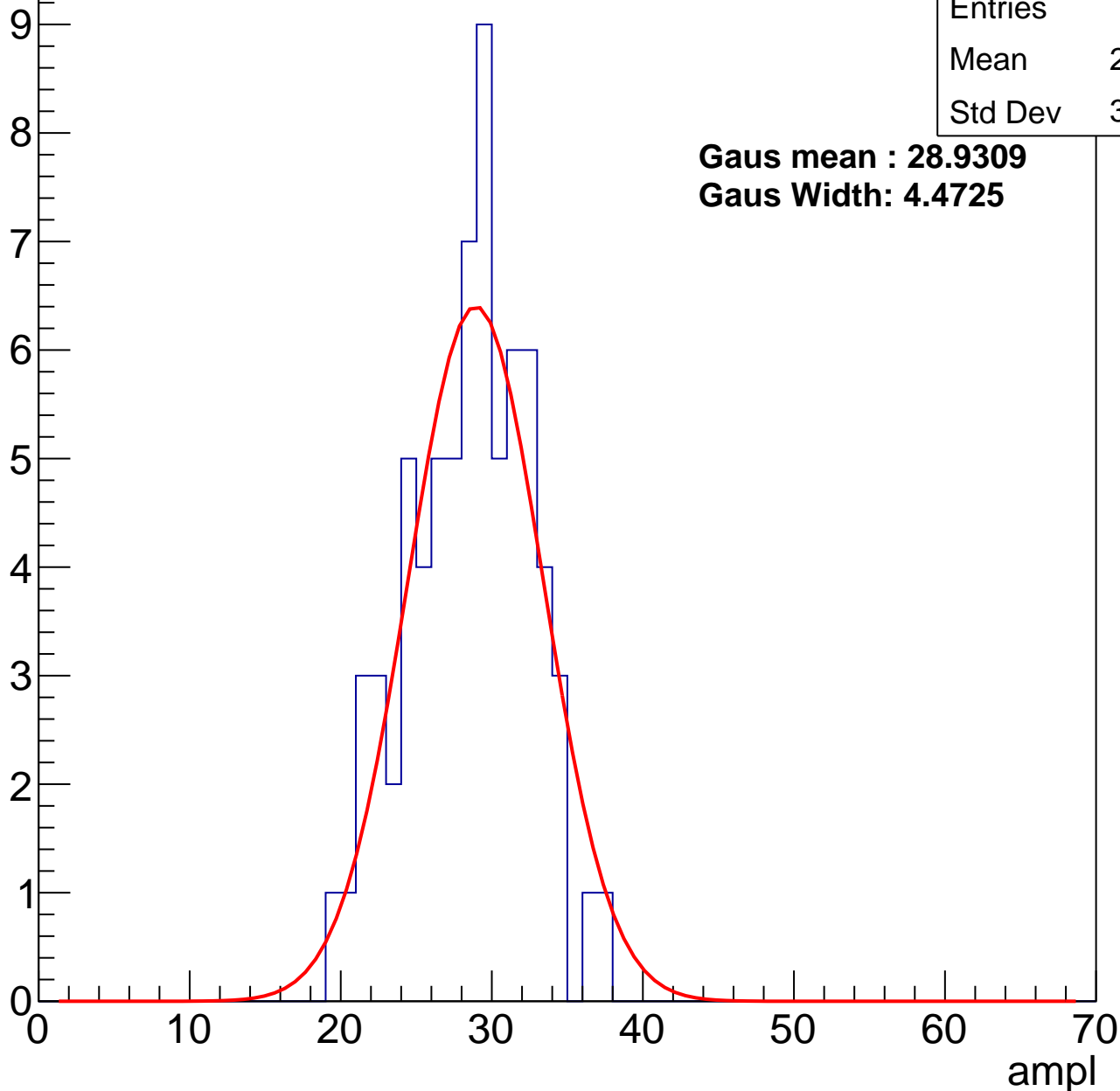
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	28.04
Std Dev	3.977

Gaus mean : 28.9309

Gaus Width: 4.4725



B1L100S, U14-ch96, adc1

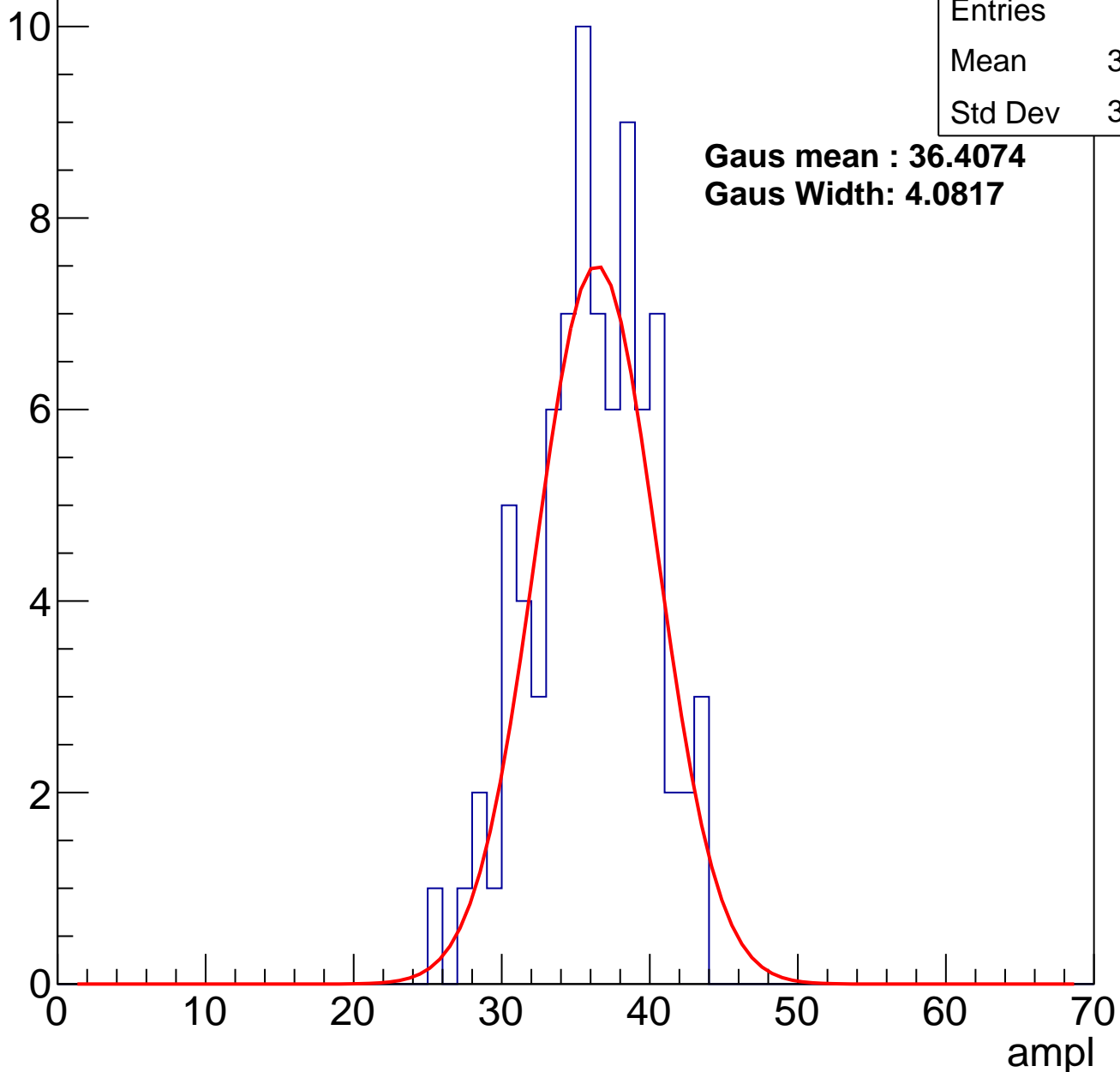
calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	35.59
Std Dev	3.923

Gaus mean : 36.4074

Gaus Width: 4.0817

Entry



B1L100S, U14-ch96, adc2

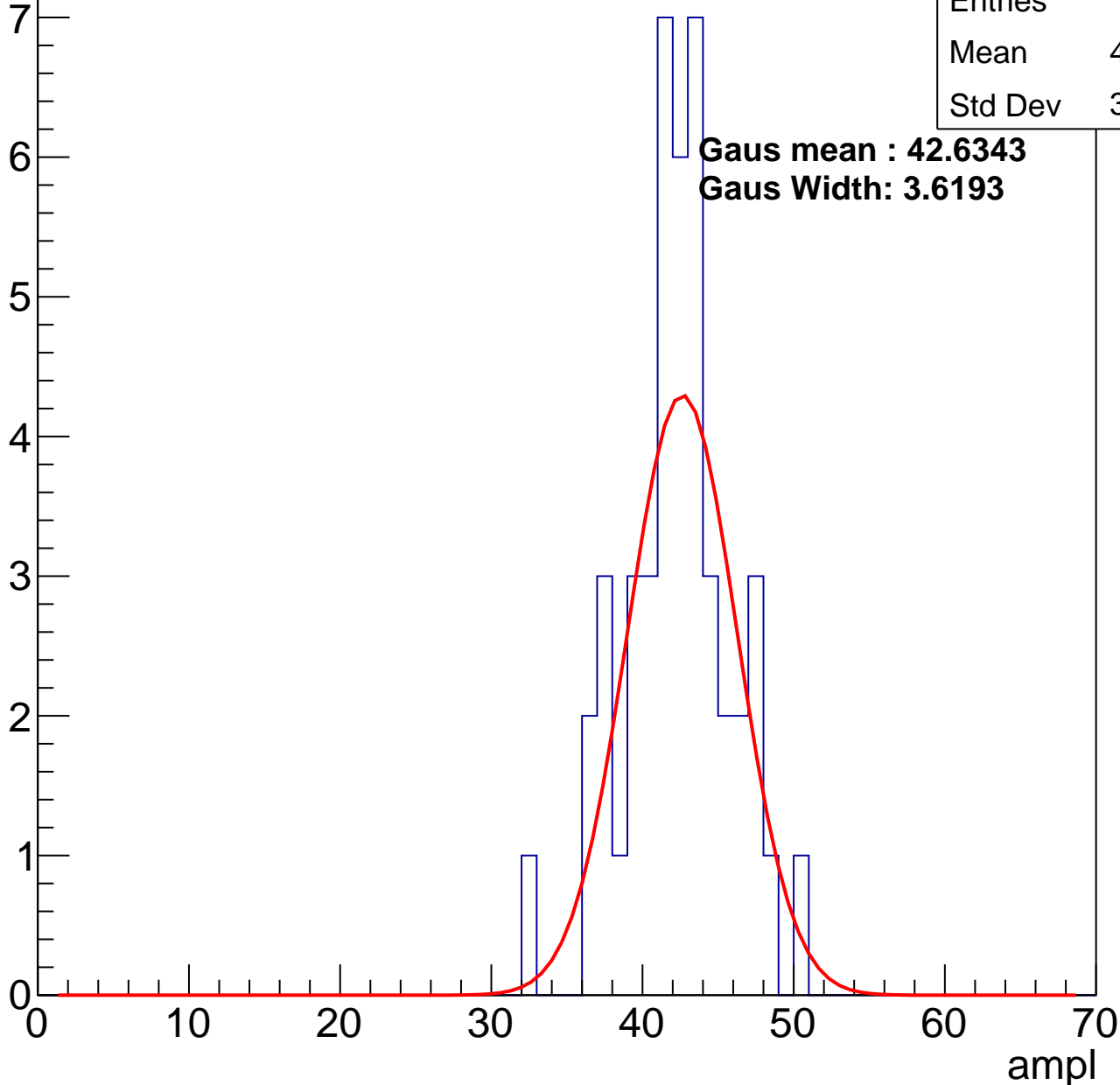
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	41.84
Std Dev	3.502

Gaus mean : 42.6343

Gaus Width: 3.6193

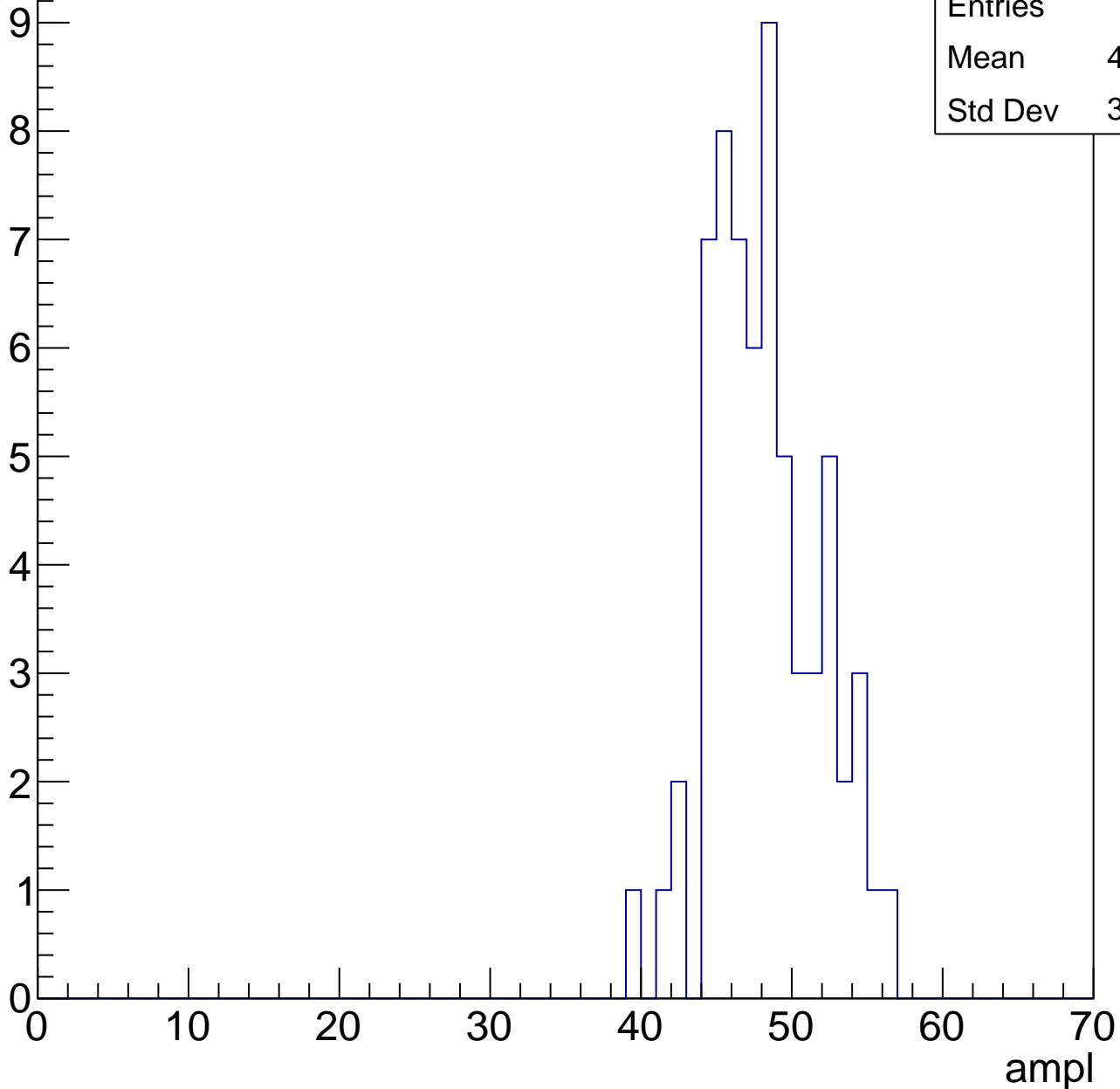


B1L100S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	47.73
Std Dev	3.559

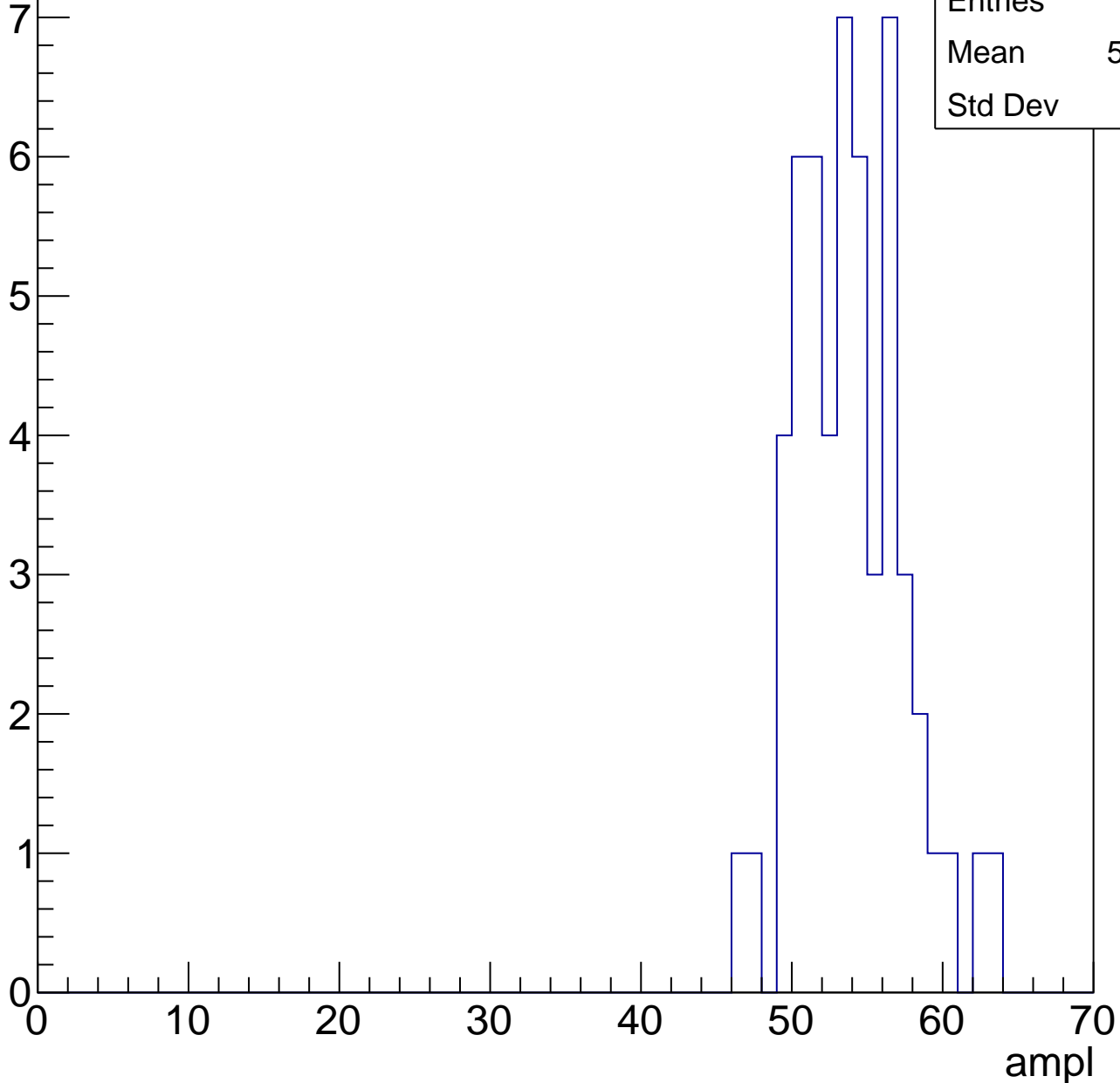


B1L100S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

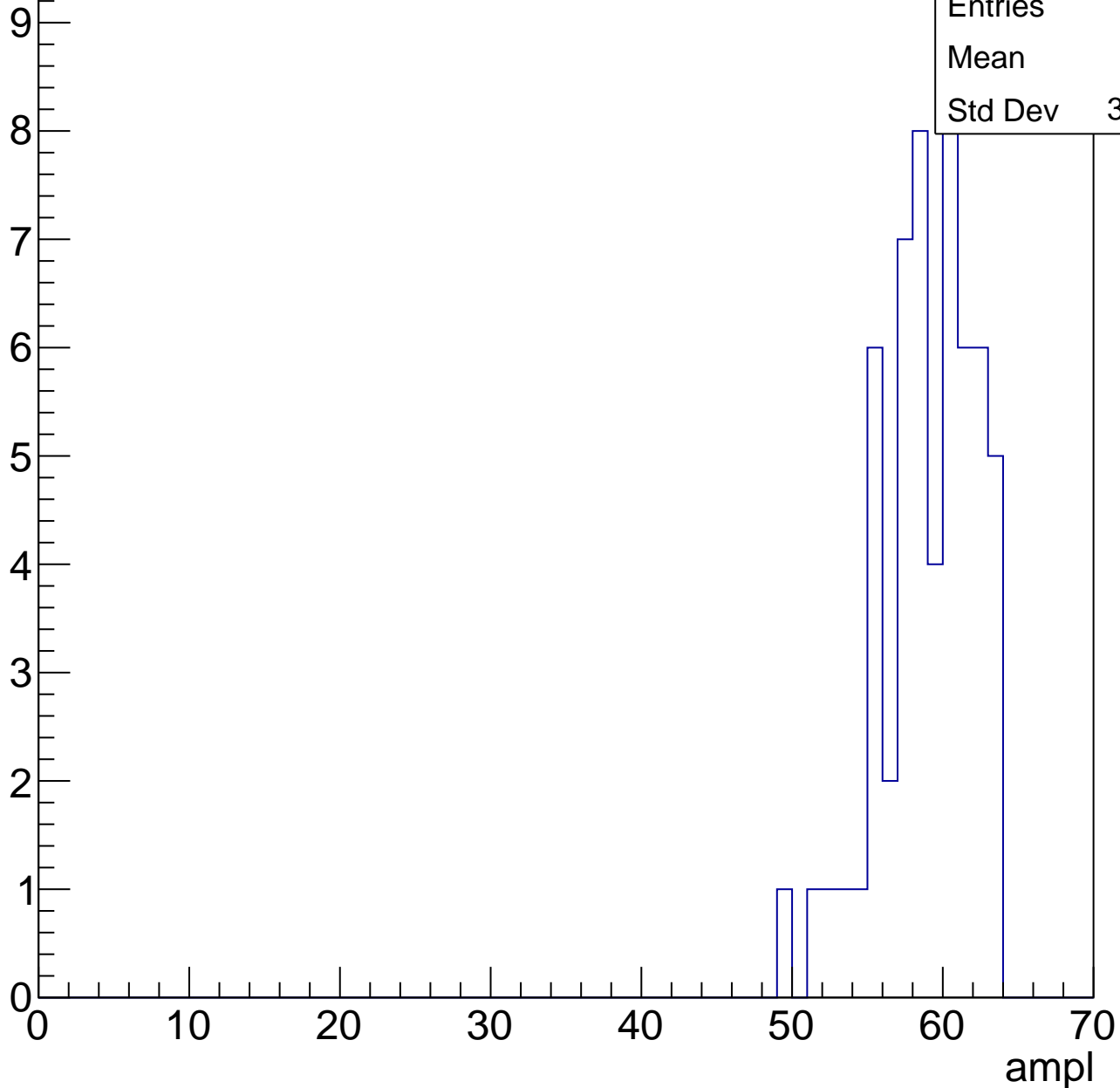
Entries	54
Mean	53.44
Std Dev	3.51



B1L100S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

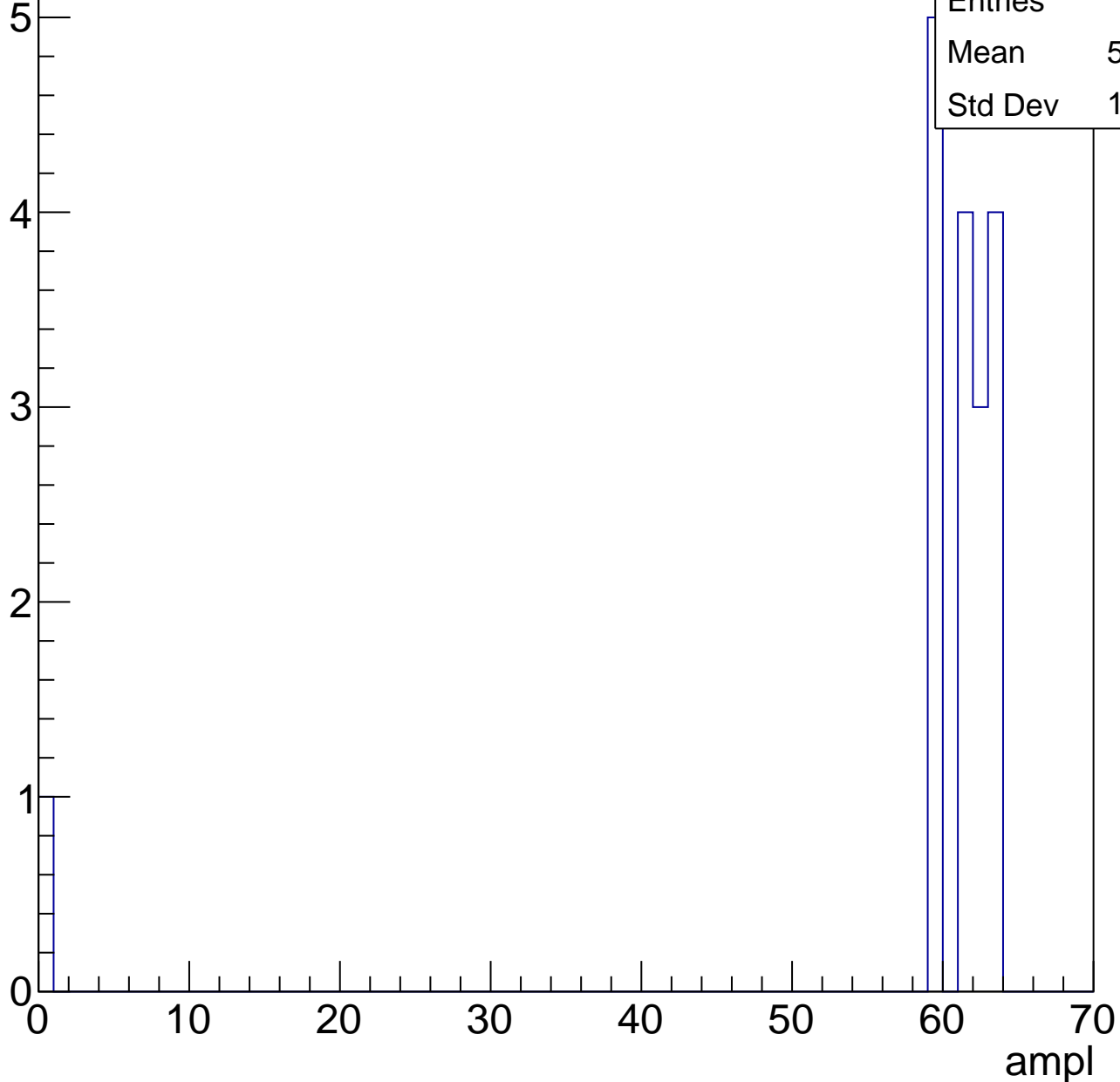
Entry



B1L100S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	17
Mean	57.47
Std Dev	14.45

B1L100S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch97, adc0

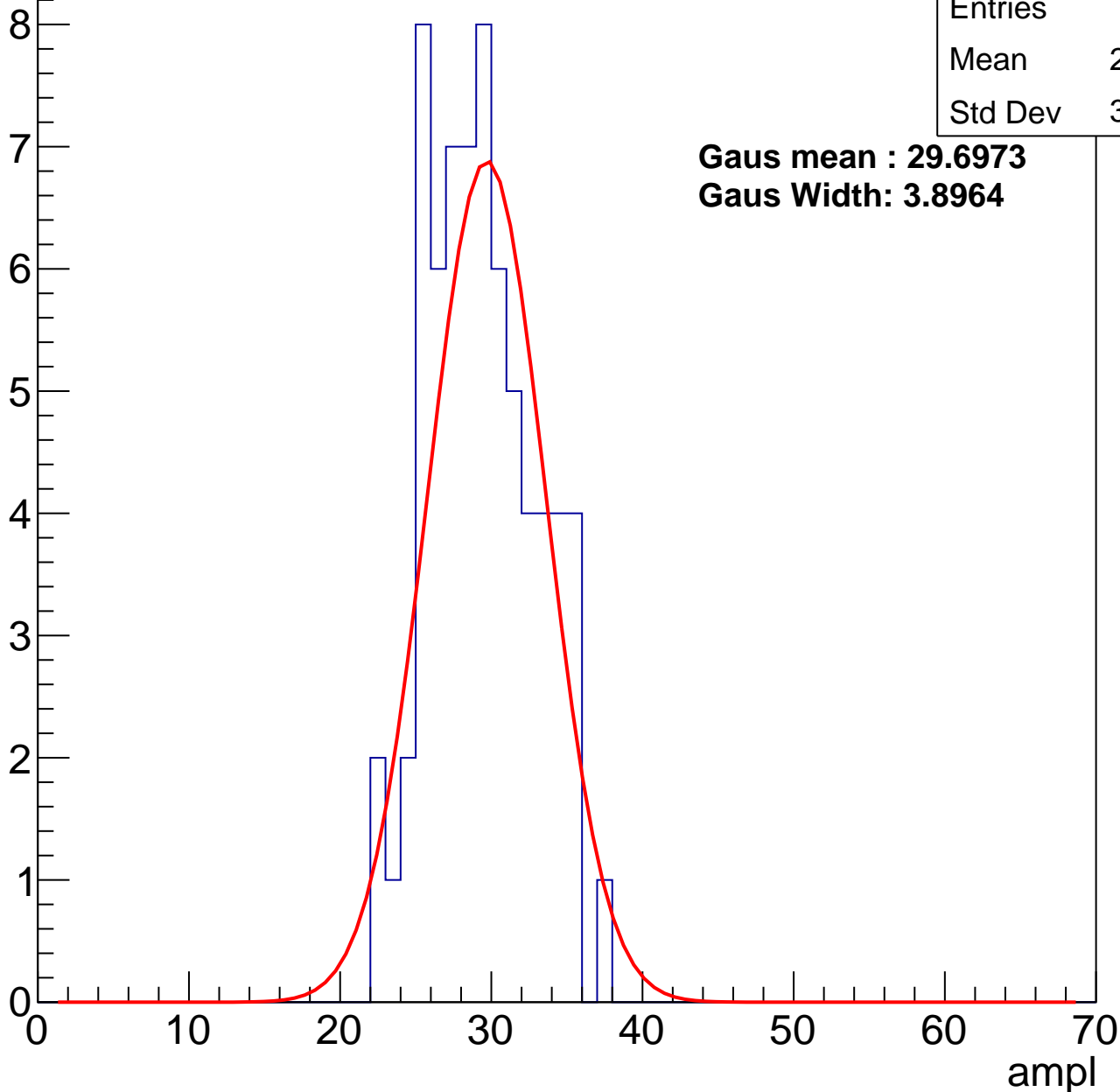
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.93
Std Dev	3.478

Gaus mean : 29.6973

Gaus Width: 3.8964



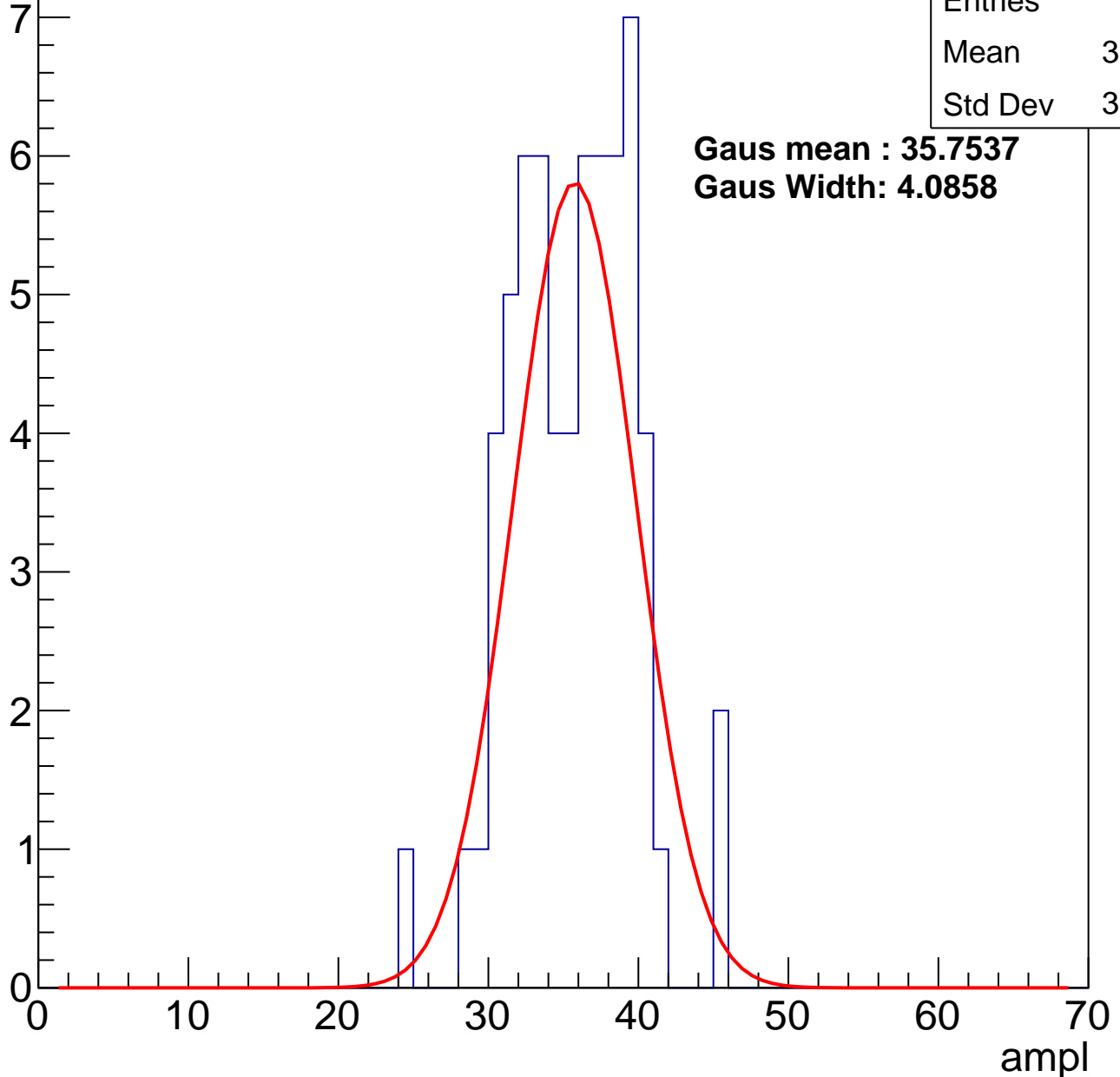
B1L100S, U14-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	35.19
Std Dev	3.948

Gaus mean : 35.7537
Gaus Width: 4.0858



B1L100S, U14-ch97, adc2

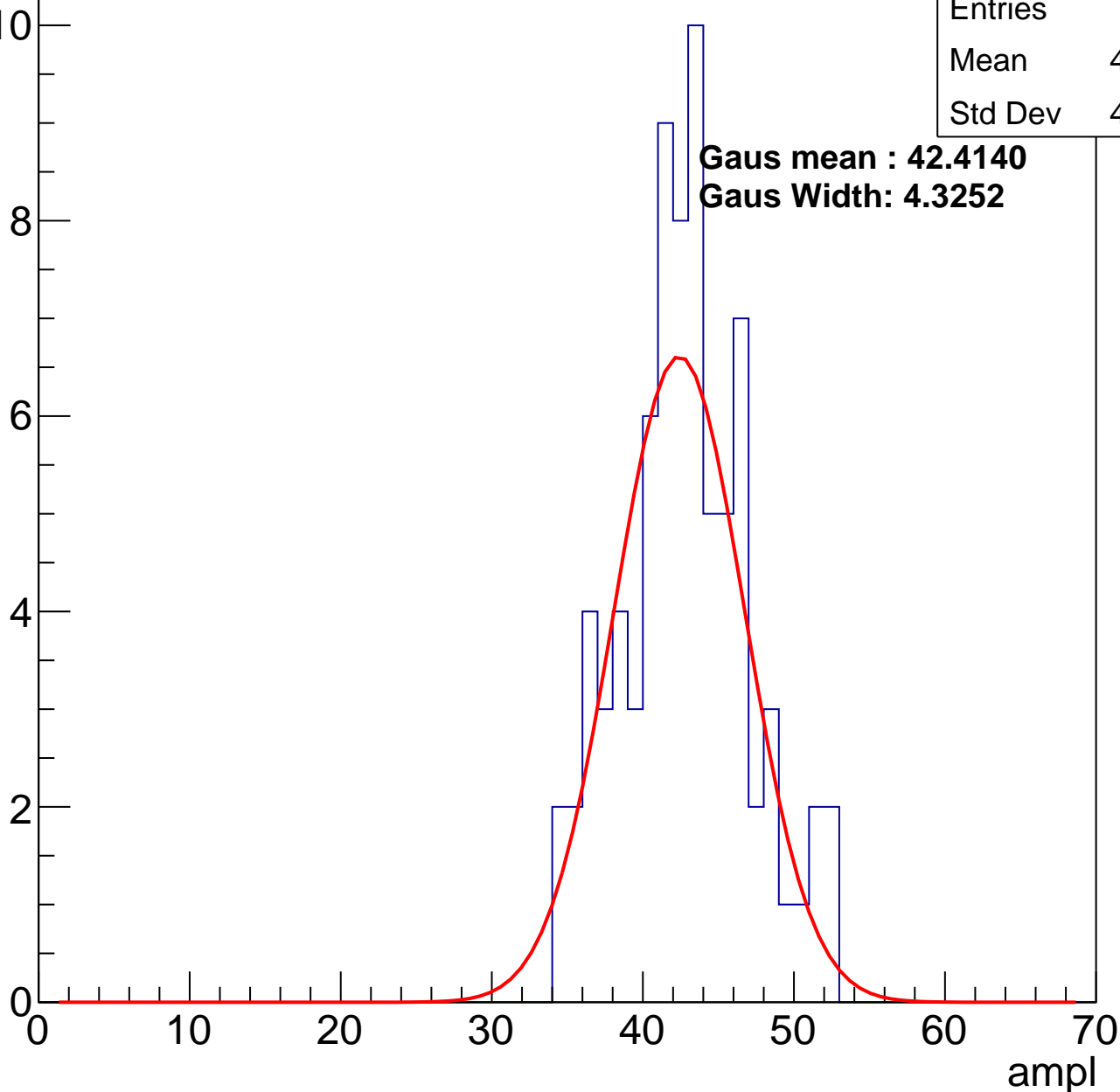
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	42.37
Std Dev	4.177

Gaus mean : 42.4140

Gaus Width: 4.3252

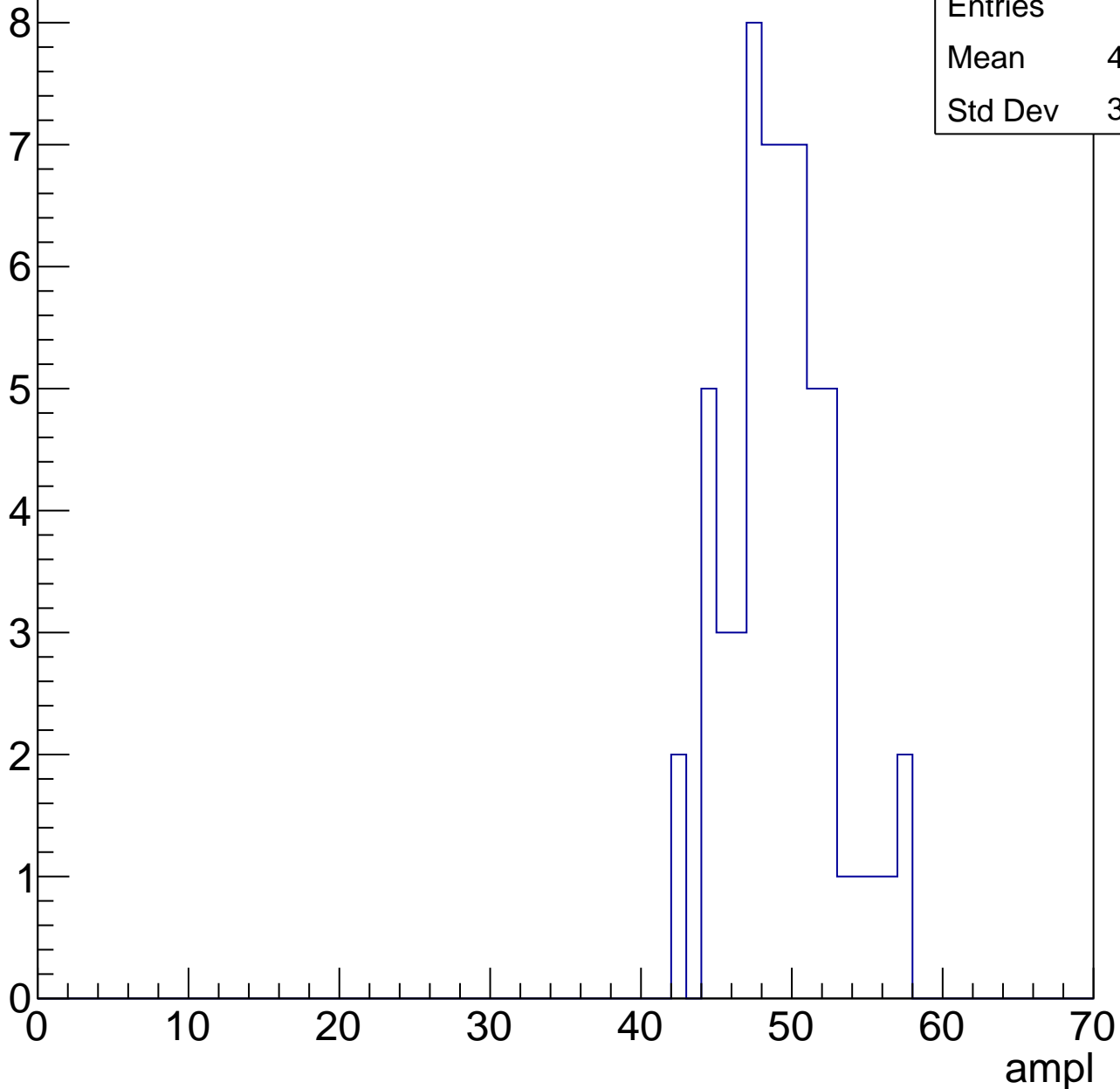


B1L100S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

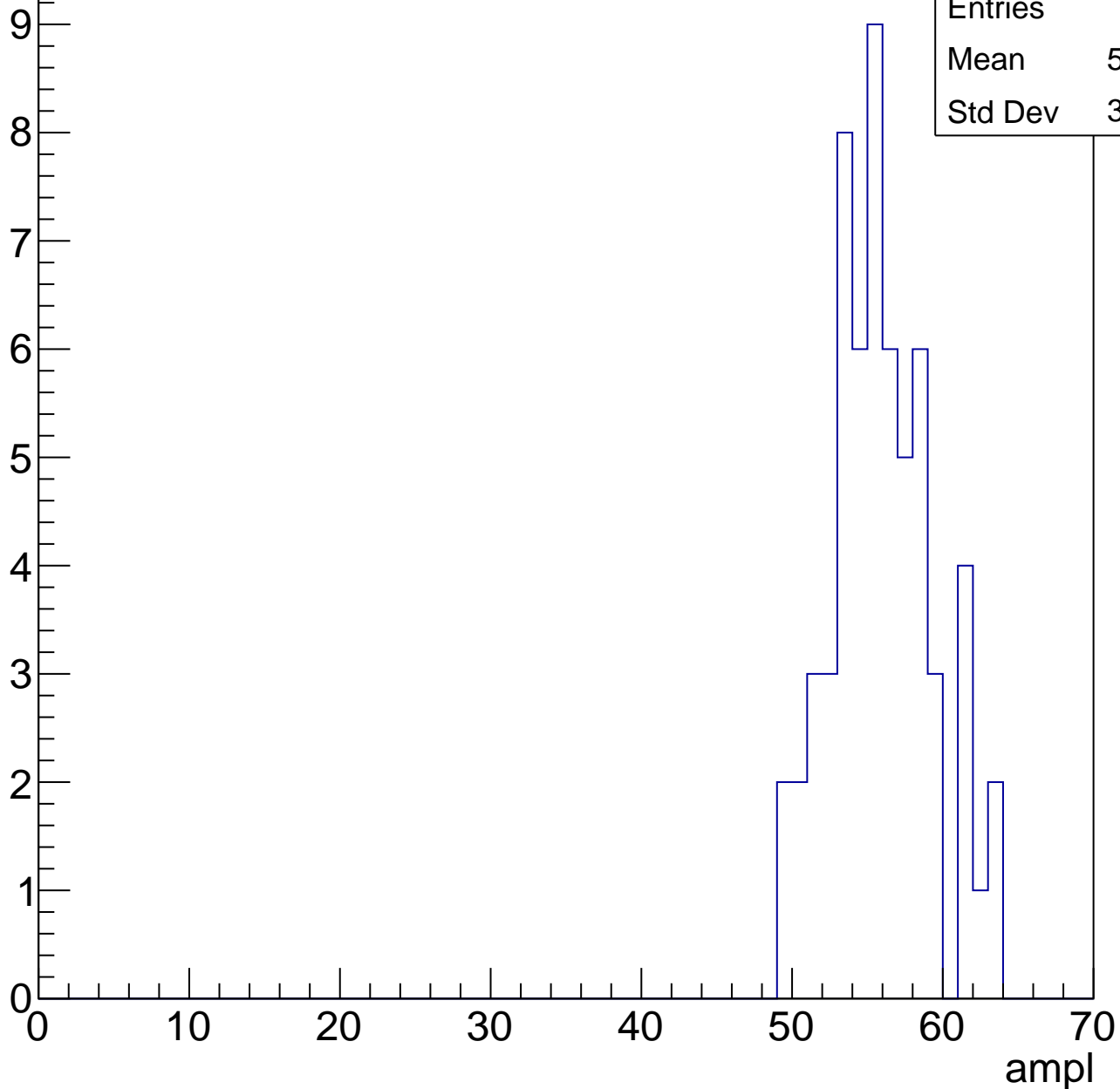
Entries	58
Mean	48.78
Std Dev	3.374



B1L100S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	60
Mean	55.47
Std Dev	3.344

B1L100S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

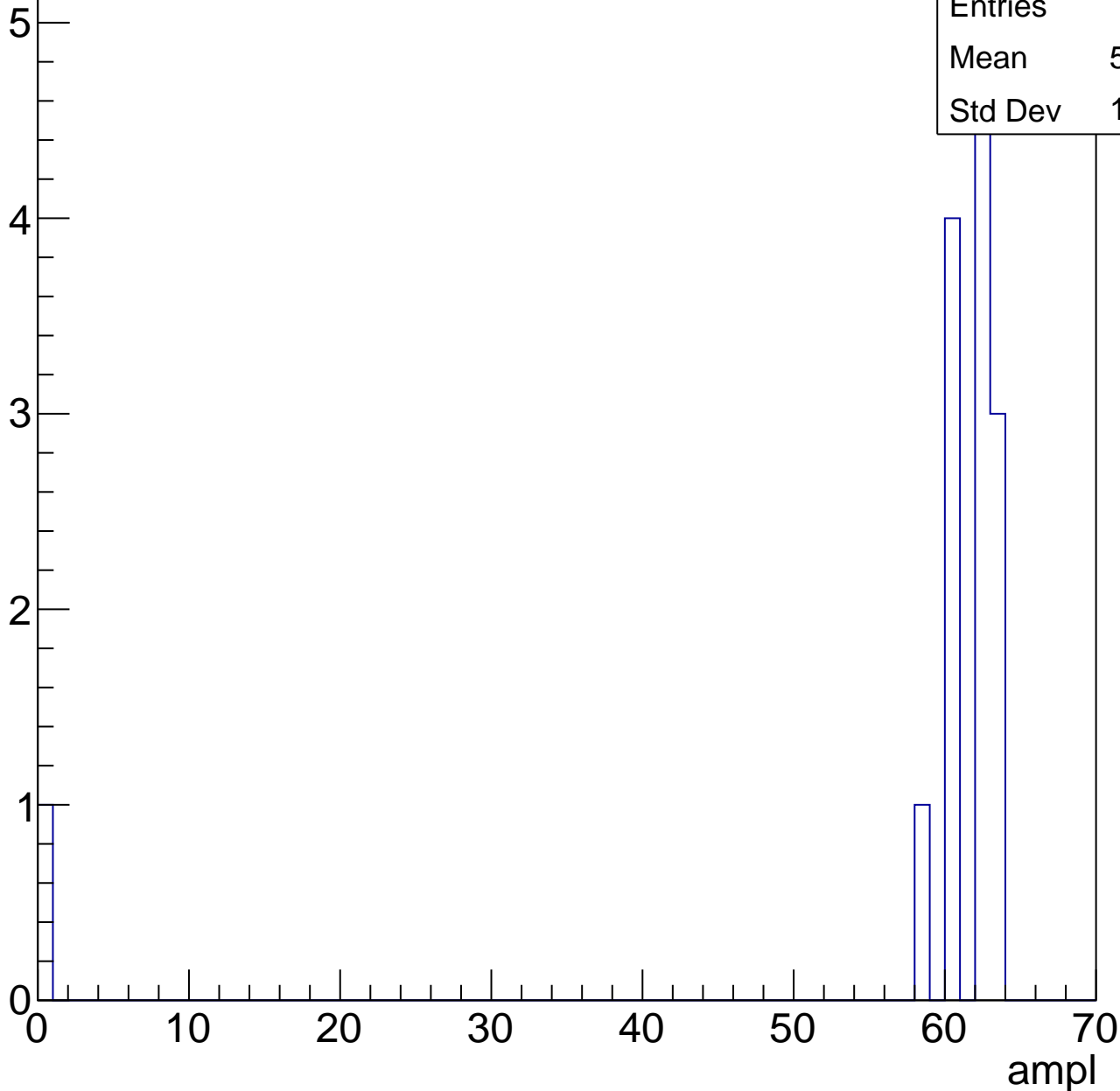
Entries	41
Mean	59.27
Std Dev	2.687

B1L100S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	56.93
Std Dev	15.85



B1L100S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U14-ch98, adc0

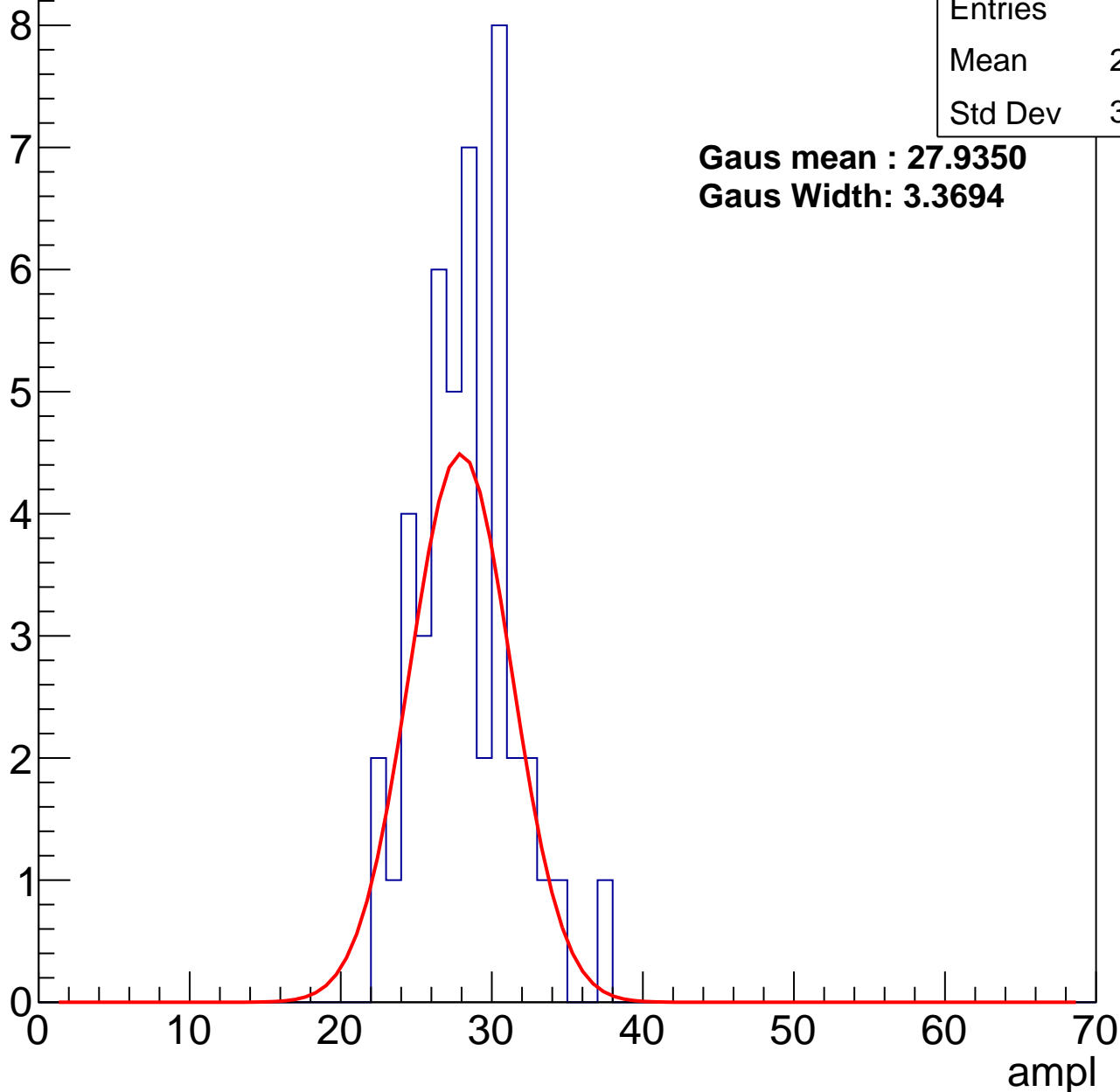
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	27.84
Std Dev	3.134

Gaus mean : 27.9350

Gaus Width: 3.3694



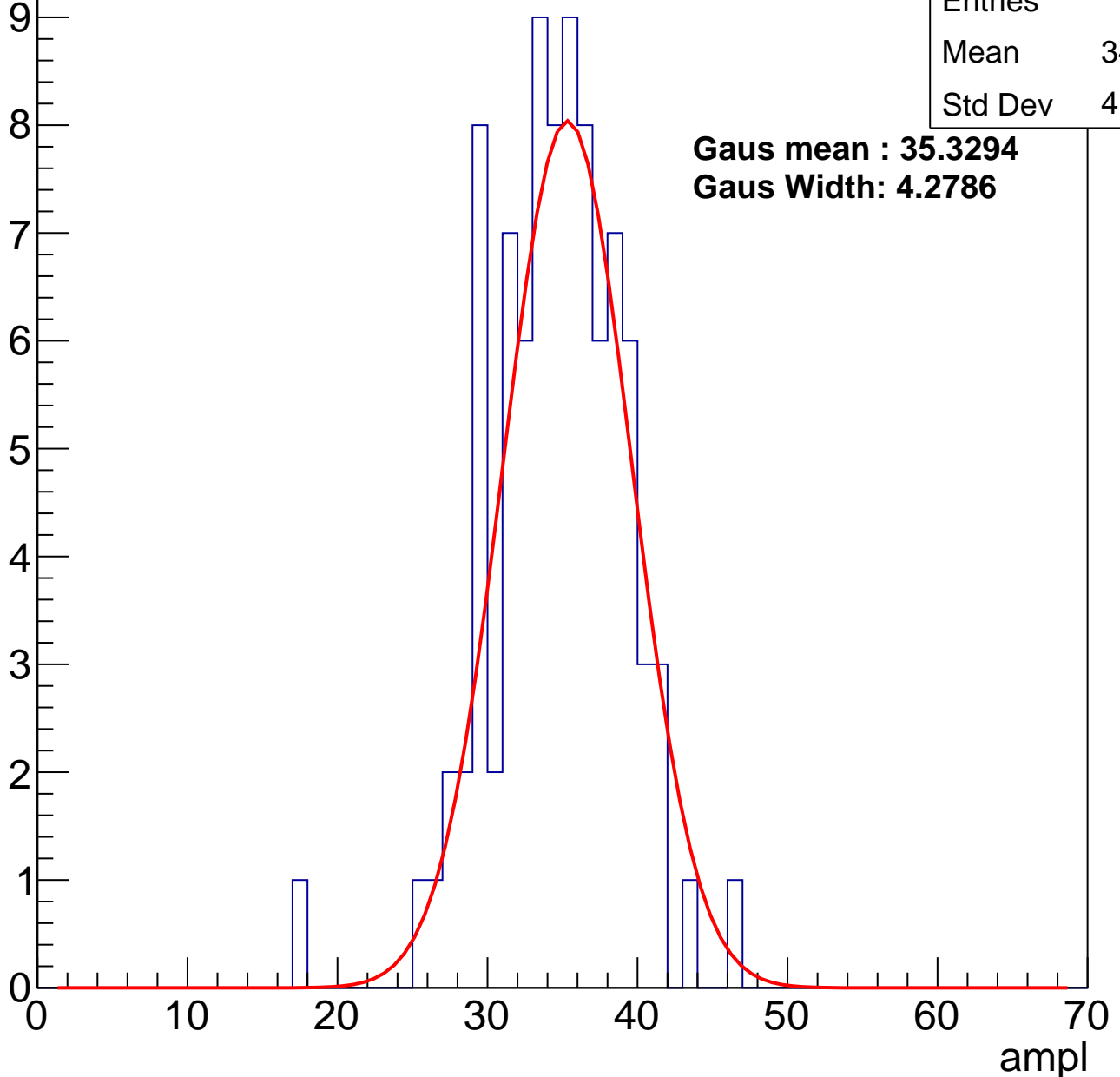
B1L100S, U14-ch98, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	91
Mean	34.12
Std Dev	4.407

Gaus mean : 35.3294
Gaus Width: 4.2786



B1L100S, U14-ch98, adc2

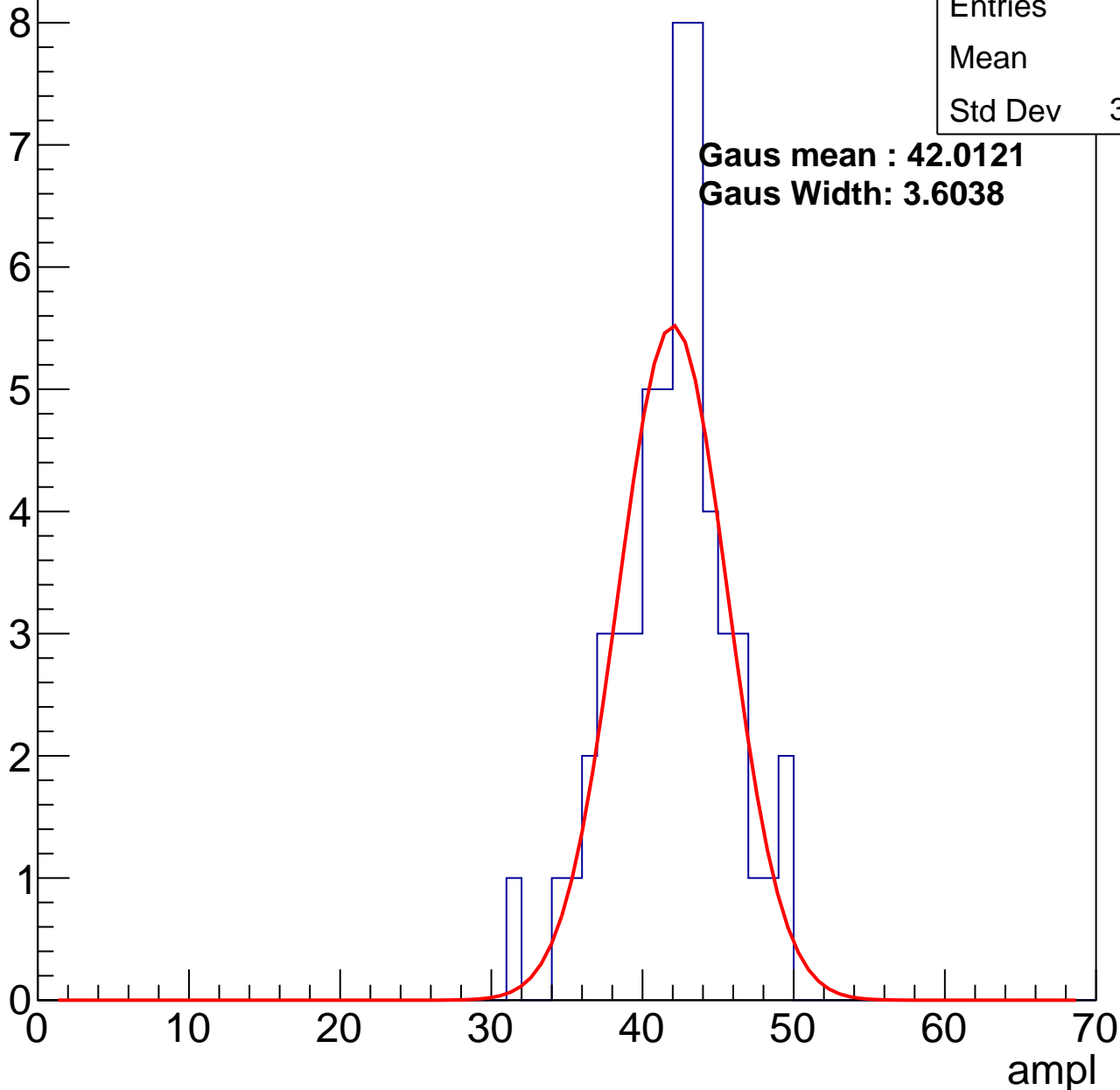
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	41.5
Std Dev	3.665

Gaus mean : 42.0121

Gaus Width: 3.6038

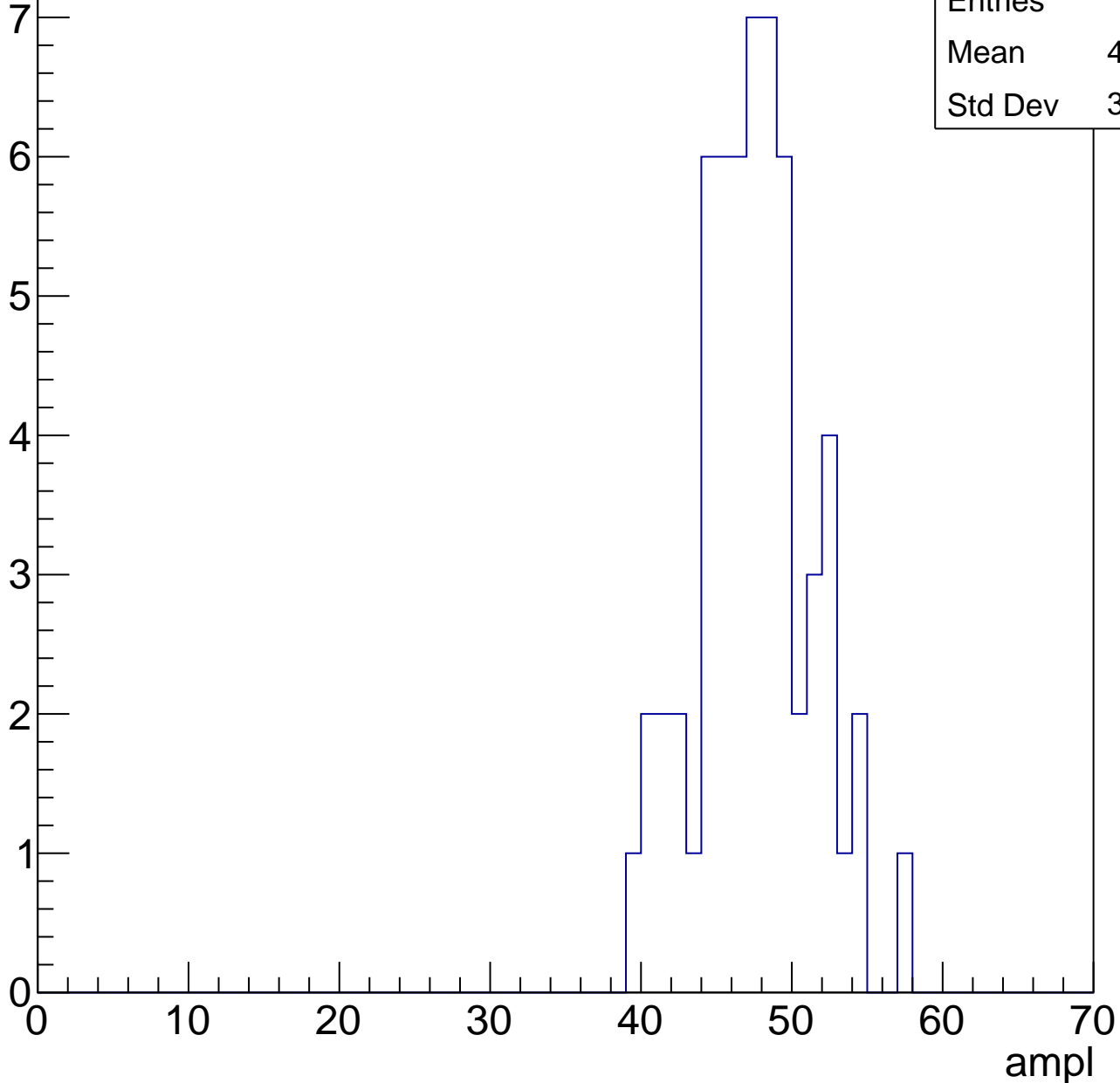


B1L100S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	47.05
Std Dev	3.716

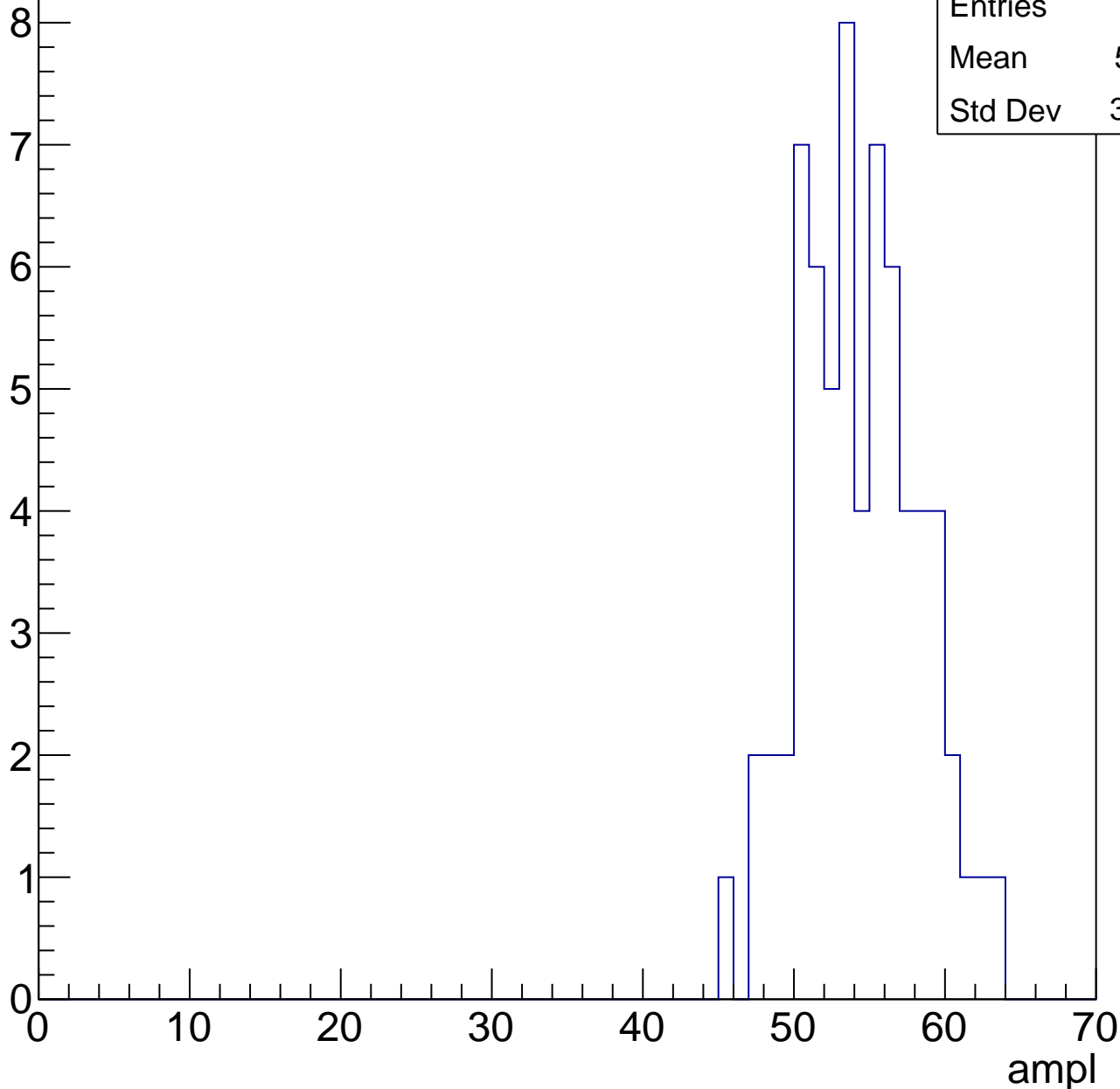


B1L100S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	53.91
Std Dev	3.847



B1L100S, U14-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

51

Mean

58.18

Std Dev

3.179

ampl

0

10

20

30

40

50

60

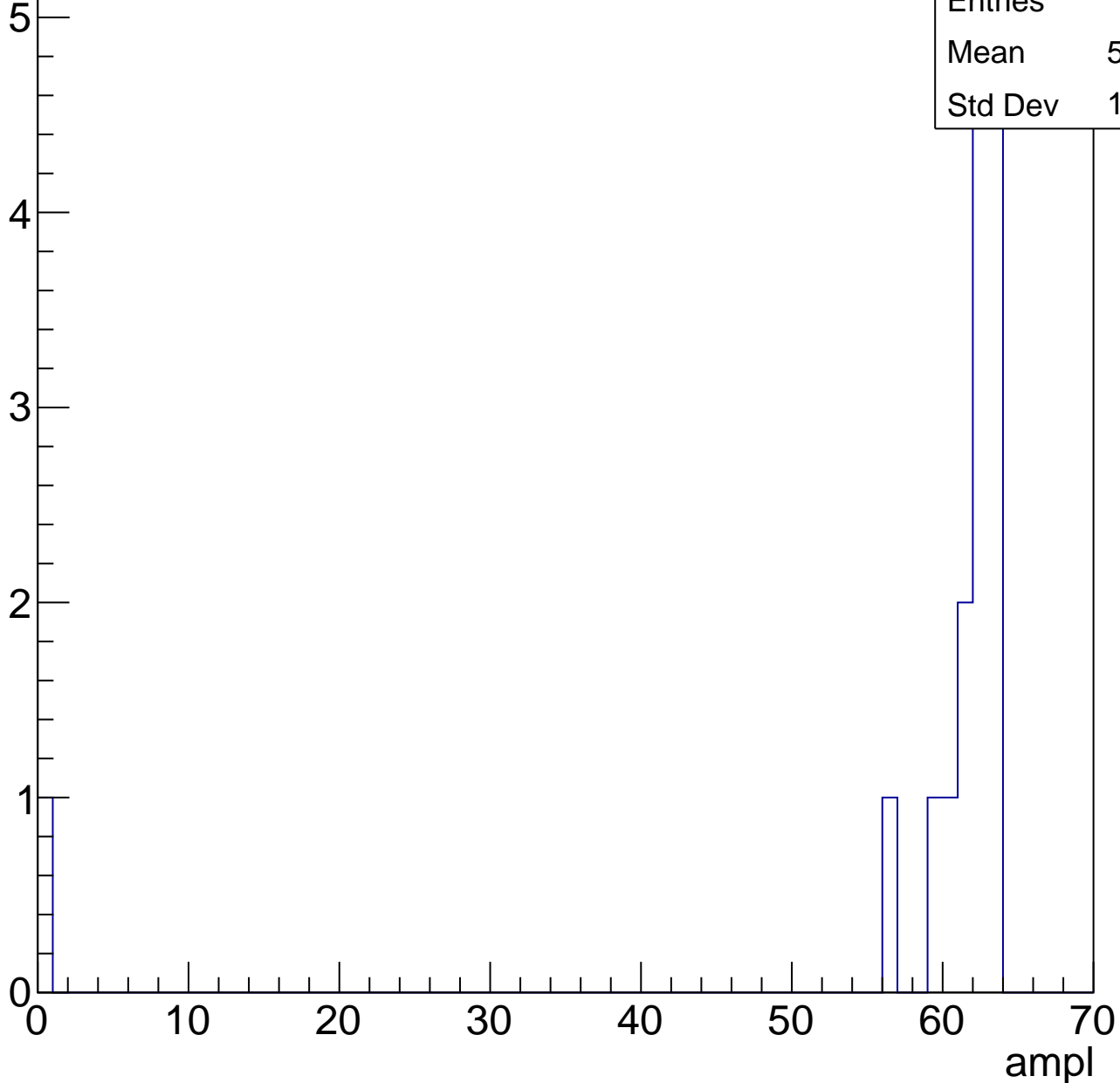
70

B1L100S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	57.62
Std Dev	14.99



B1L100S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U14-ch99, adc0

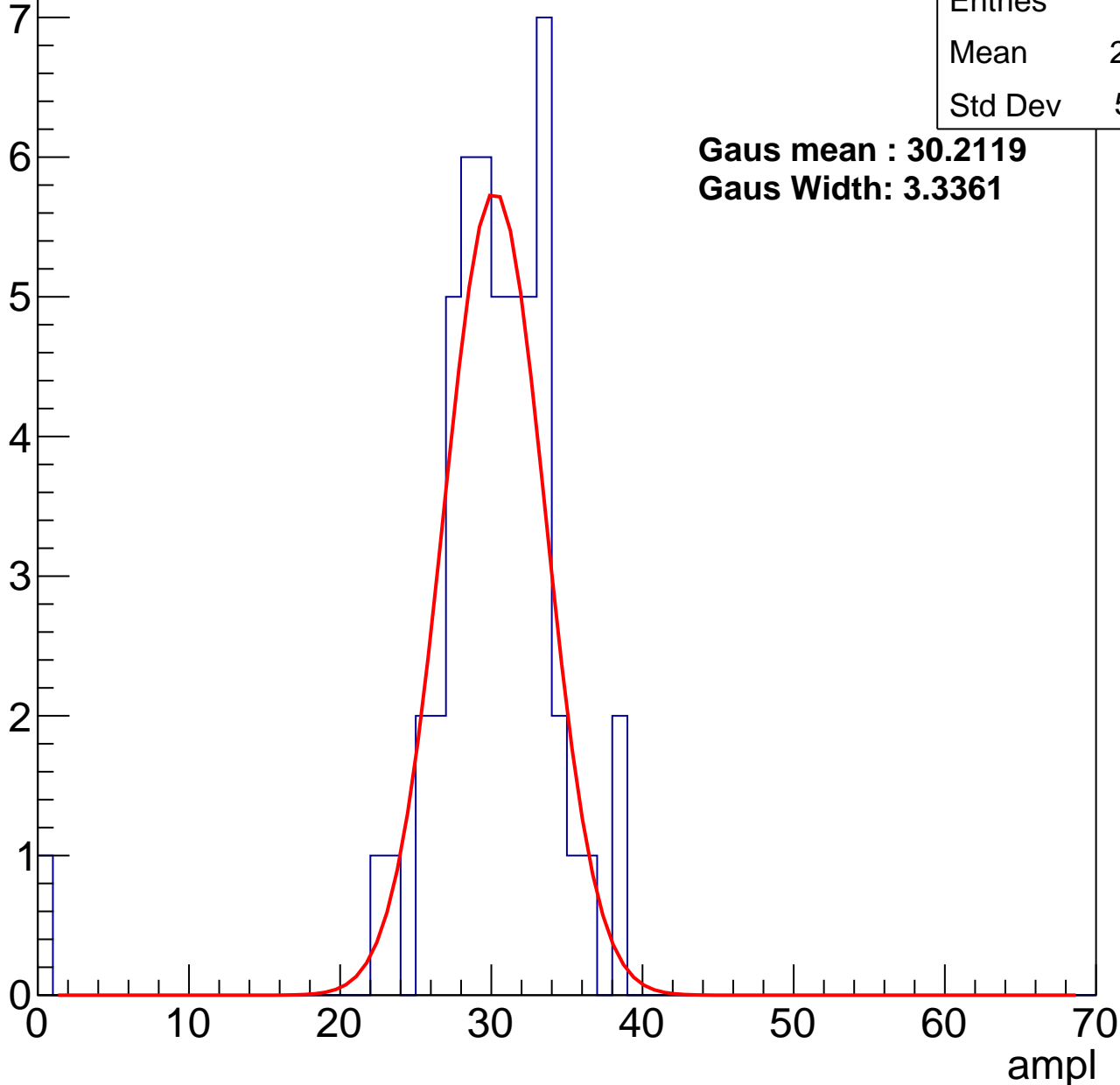
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	29.52
Std Dev	5.311

Gaus mean : 30.2119

Gaus Width: 3.3361



B1L100S, U14-ch99, adc1

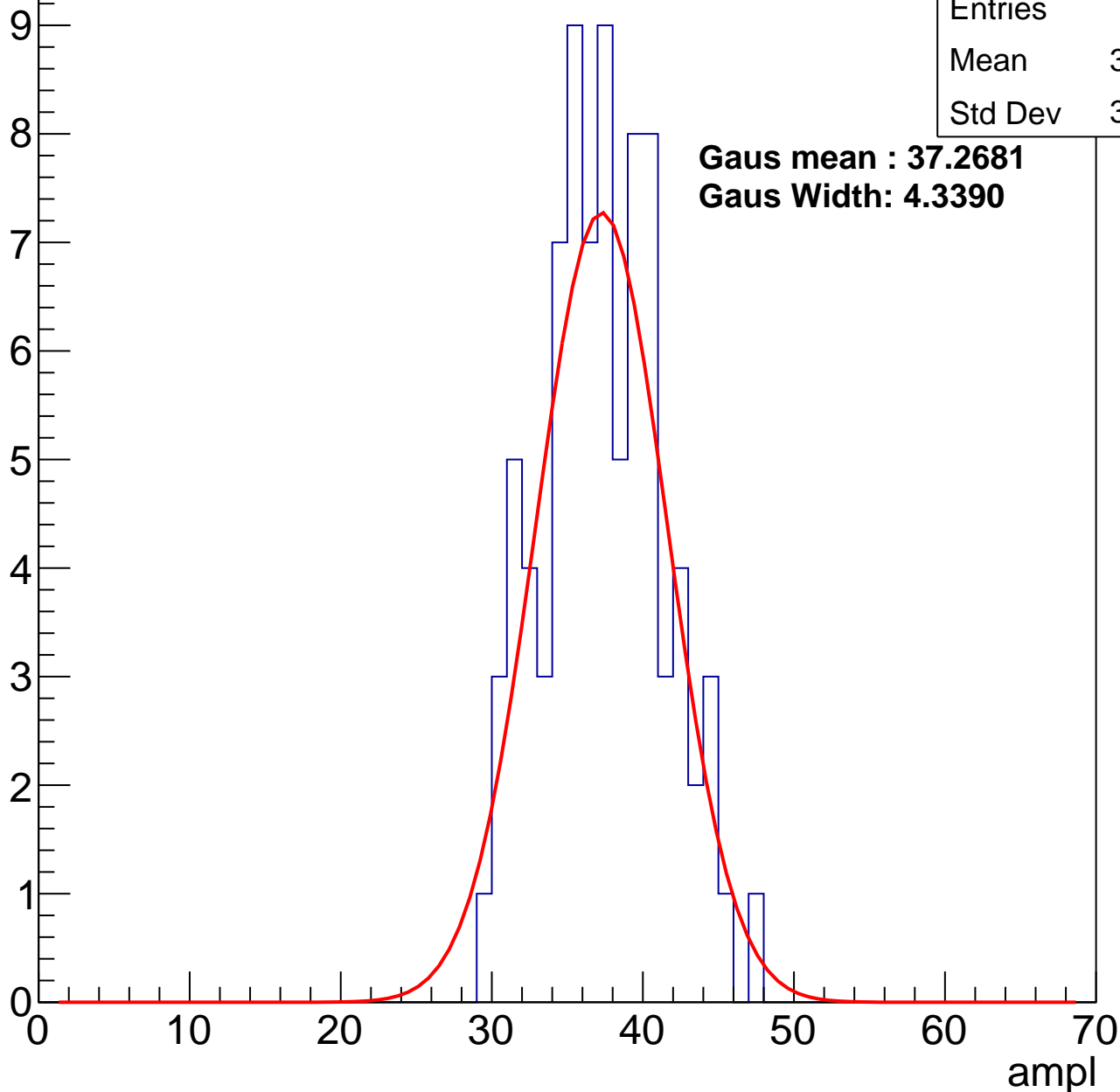
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	36.89
Std Dev	3.933

Gaus mean : 37.2681

Gaus Width: 4.3390



B1L100S, U14-ch99, adc2

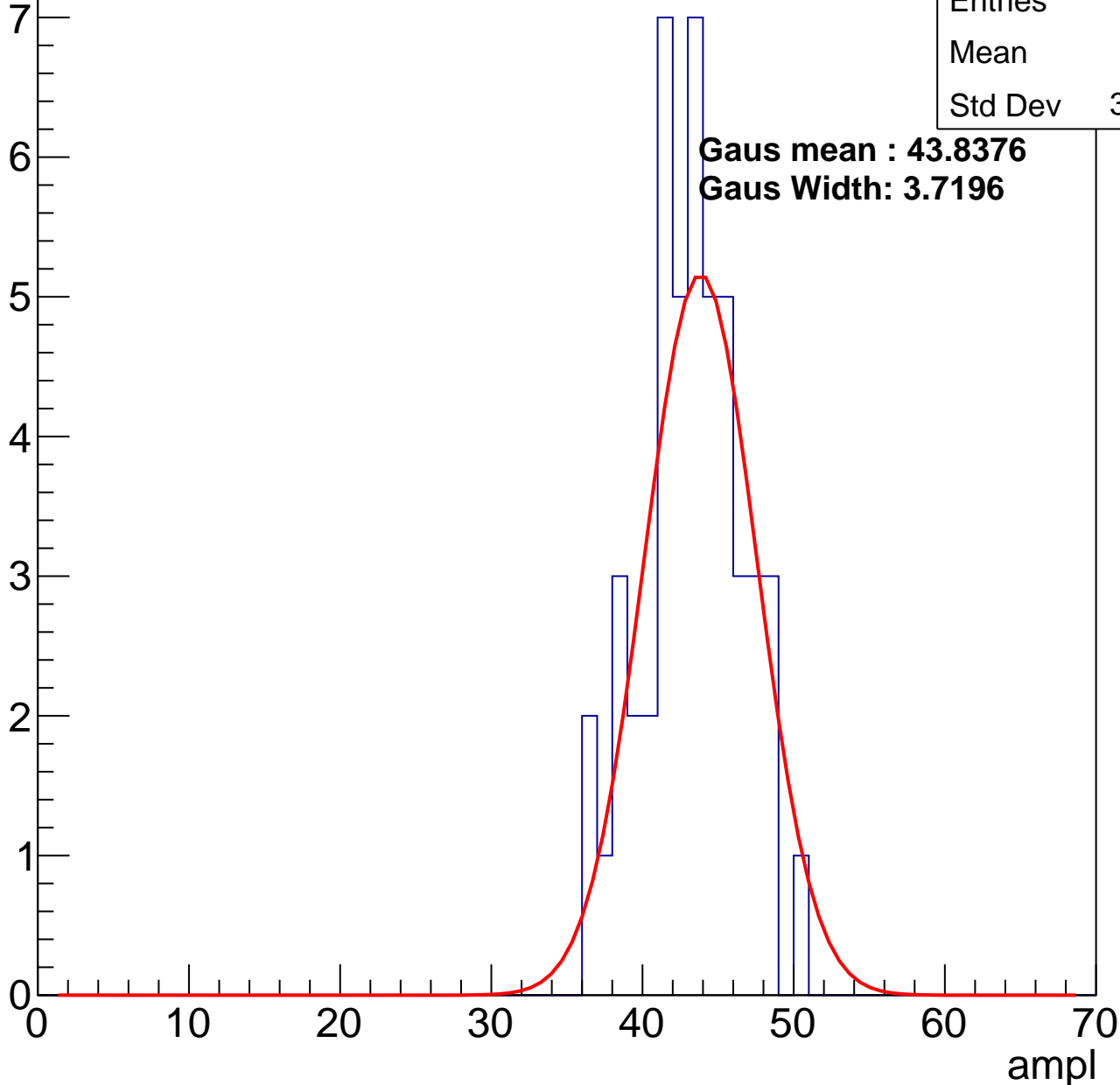
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	42.8
Std Dev	3.239

Gaus mean : 43.8376

Gaus Width: 3.7196

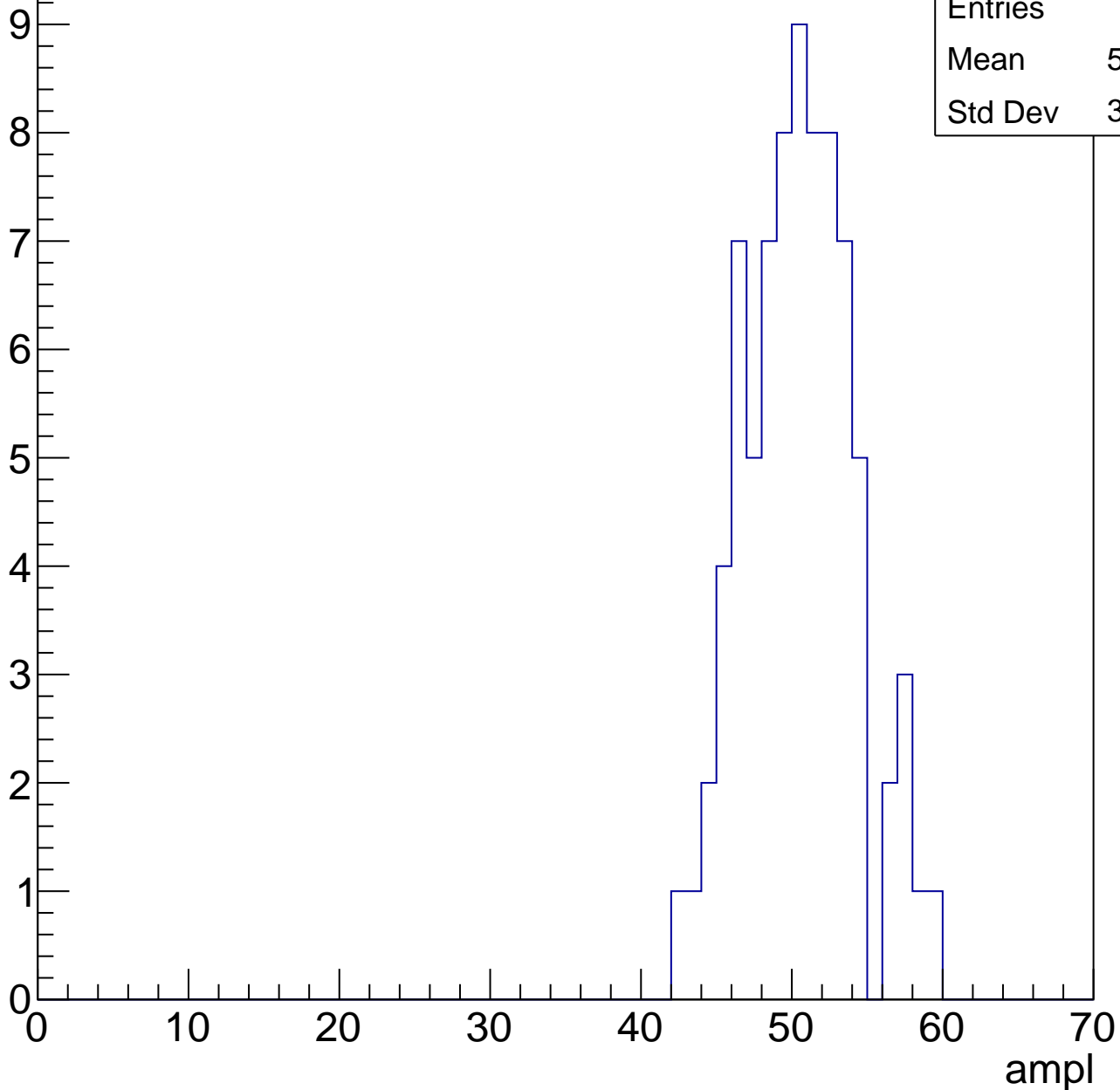


B1L100S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	50.04
Std Dev	3.602

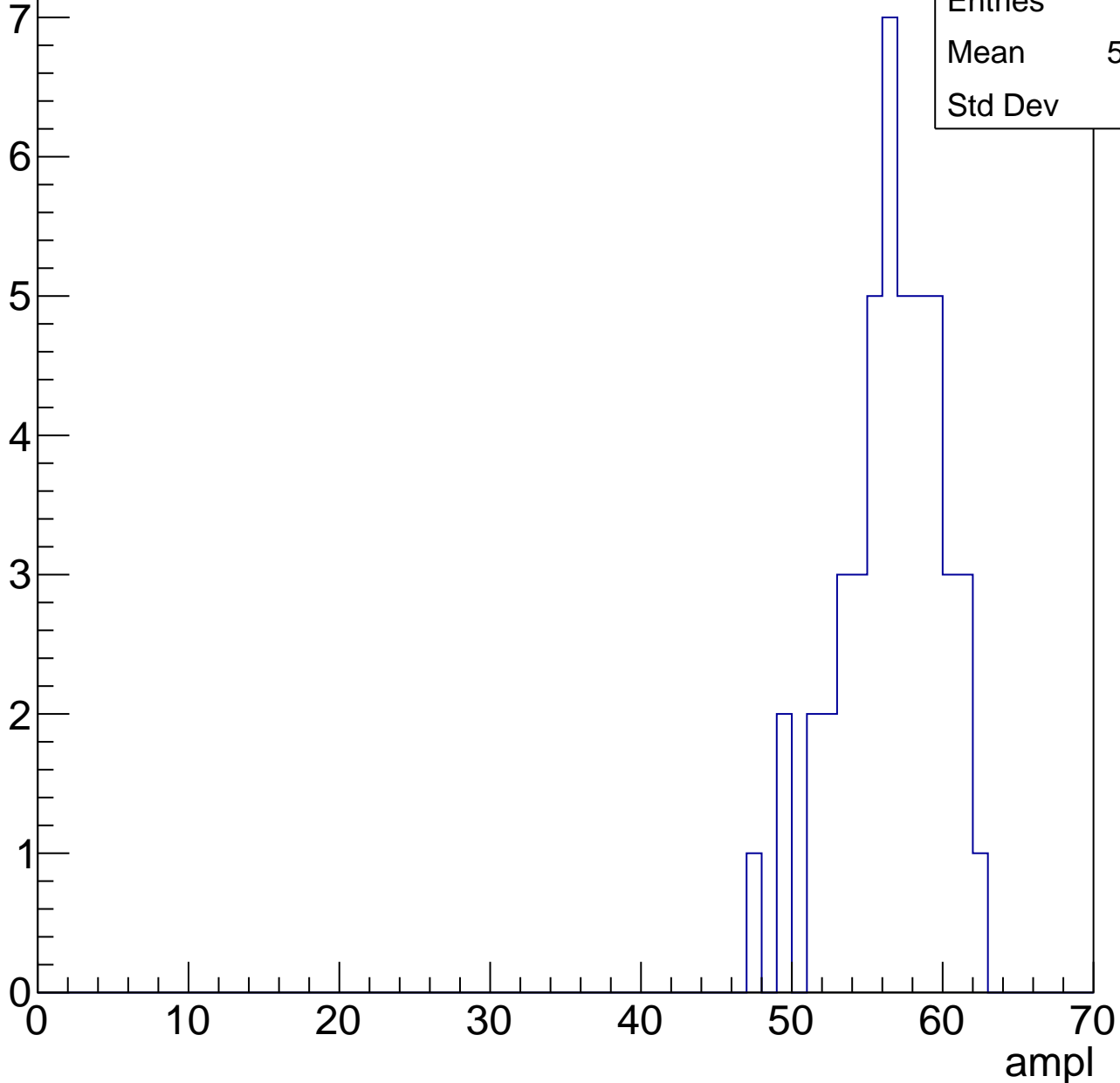


B1L100S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	56.04
Std Dev	3.37

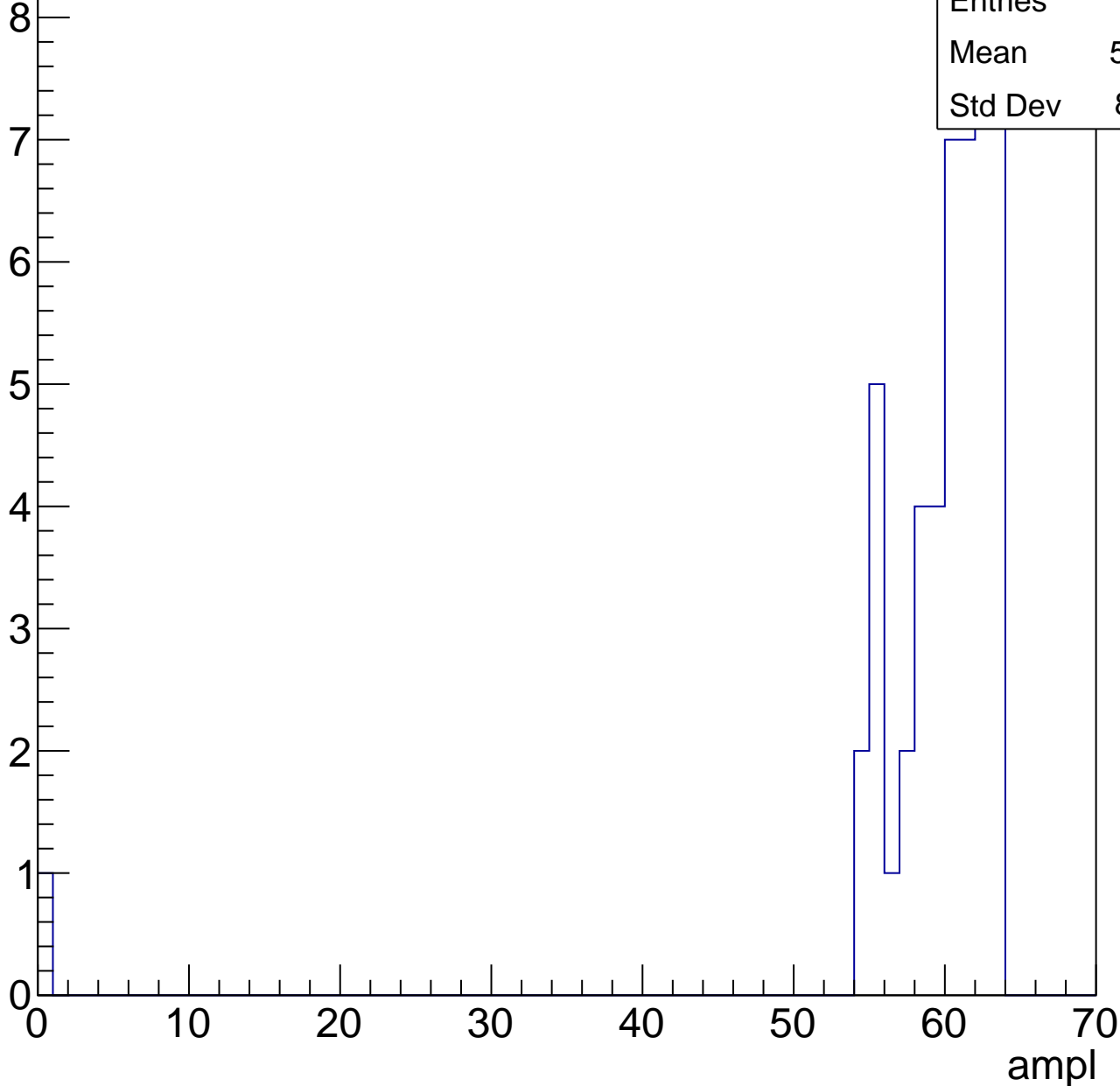


B1L100S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.53
Std Dev	8.871



B1L100S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch100, adc0

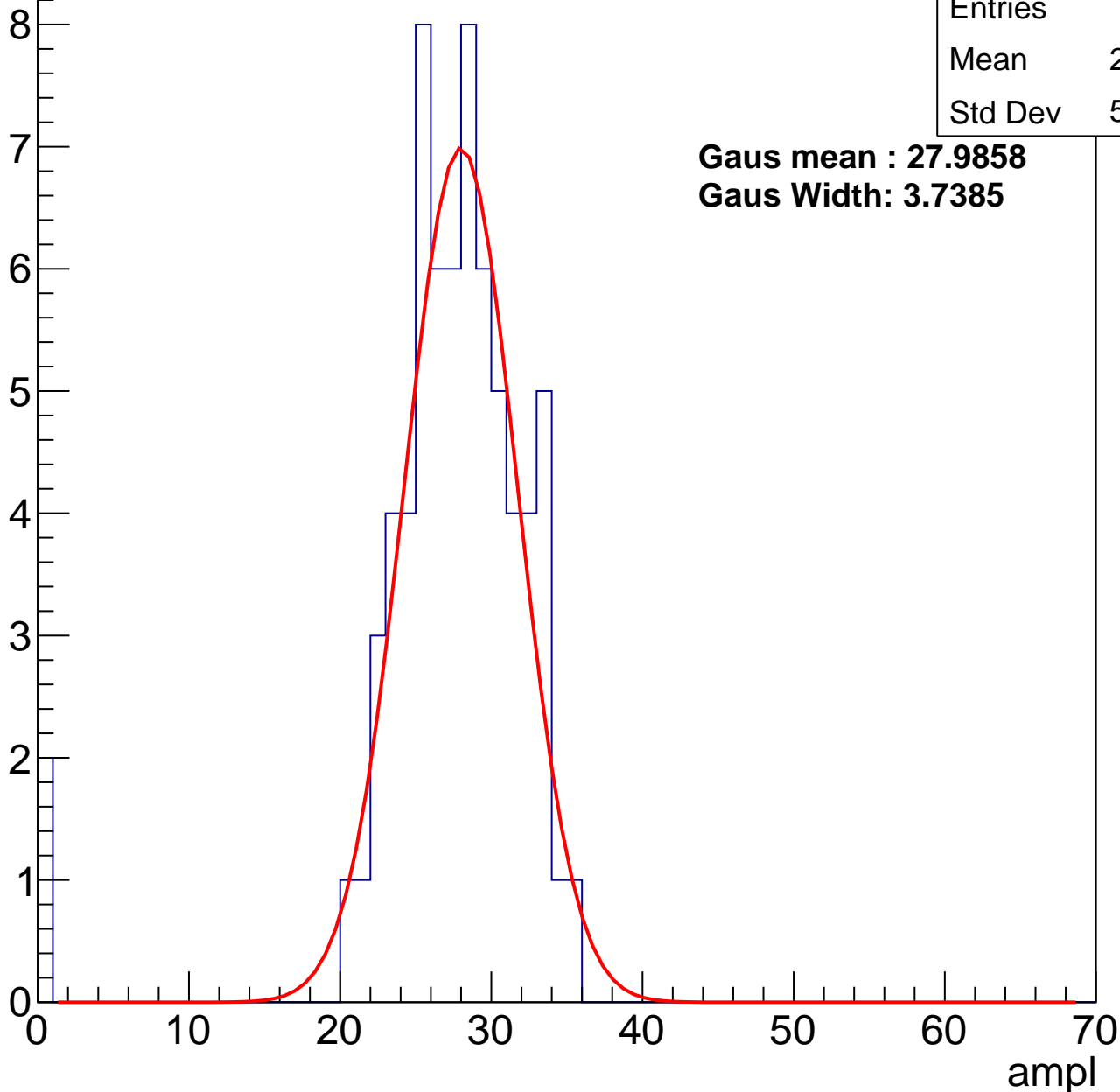
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	26.77
Std Dev	5.756

Gaus mean : 27.9858

Gaus Width: 3.7385



B1L100S, U14-ch100, adc1

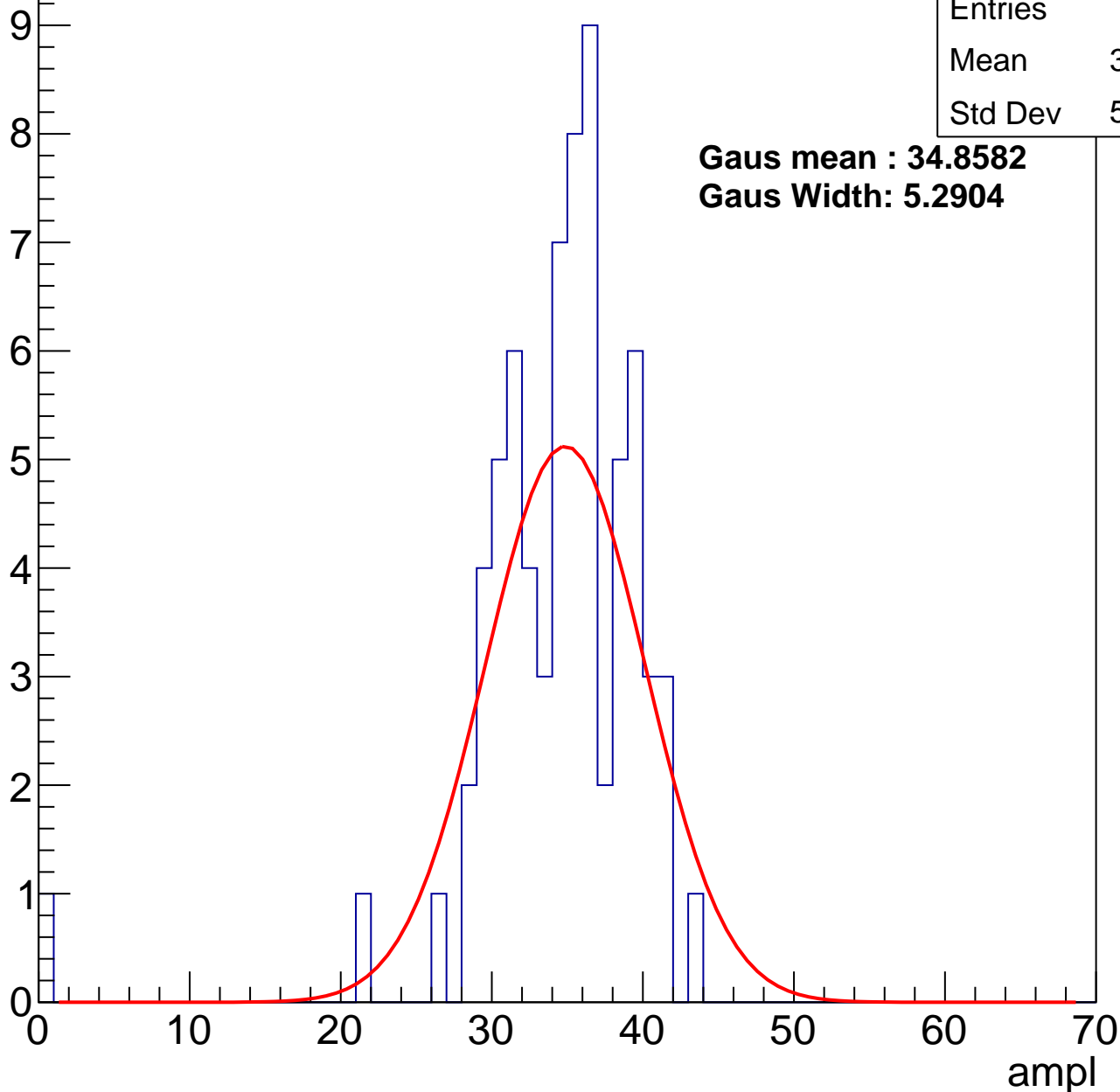
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	33.92
Std Dev	5.743

Gaus mean : 34.8582

Gaus Width: 5.2904



B1L100S, U14-ch100, adc2

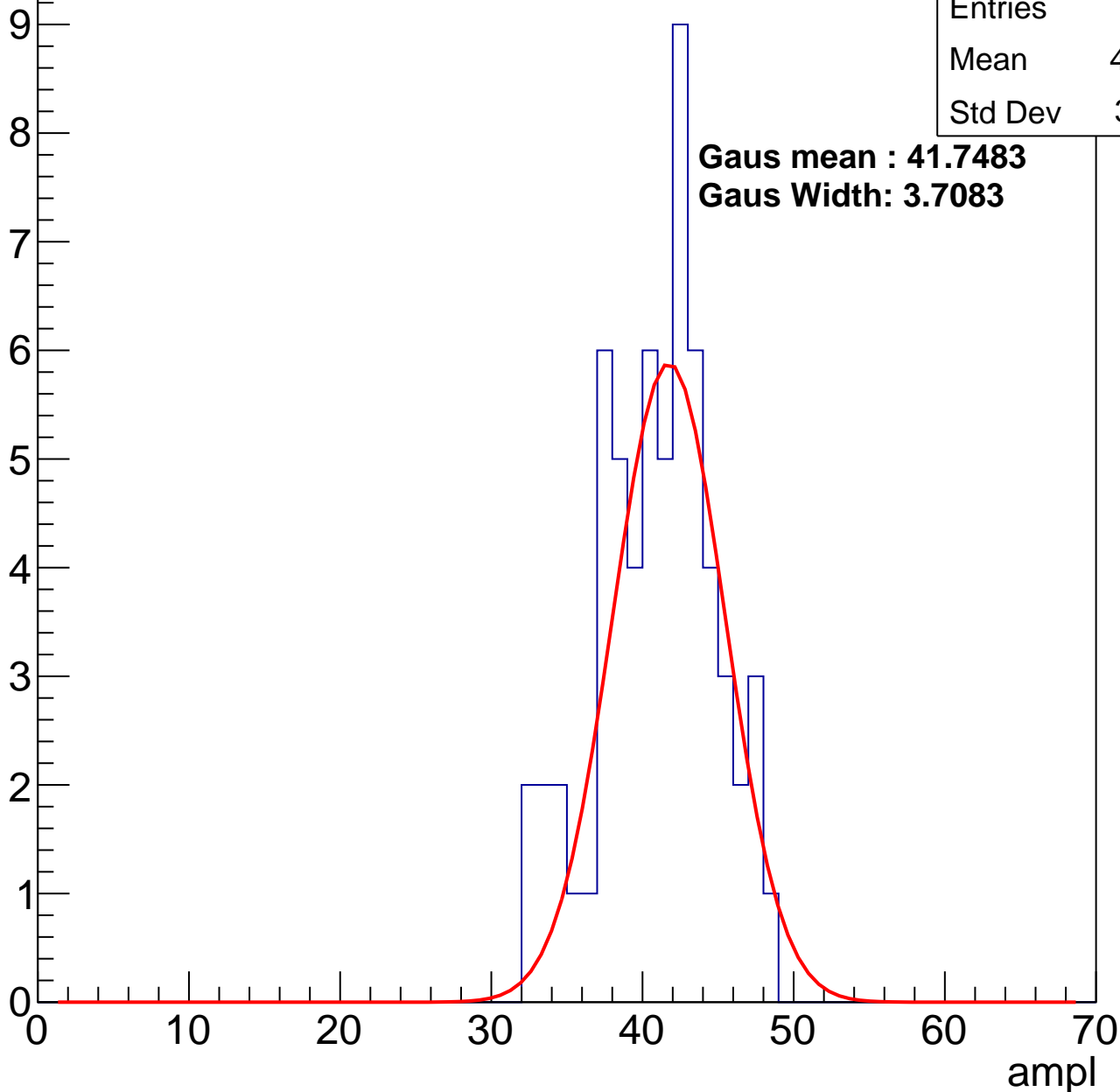
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	40.48
Std Dev	3.851

Gaus mean : 41.7483

Gaus Width: 3.7083

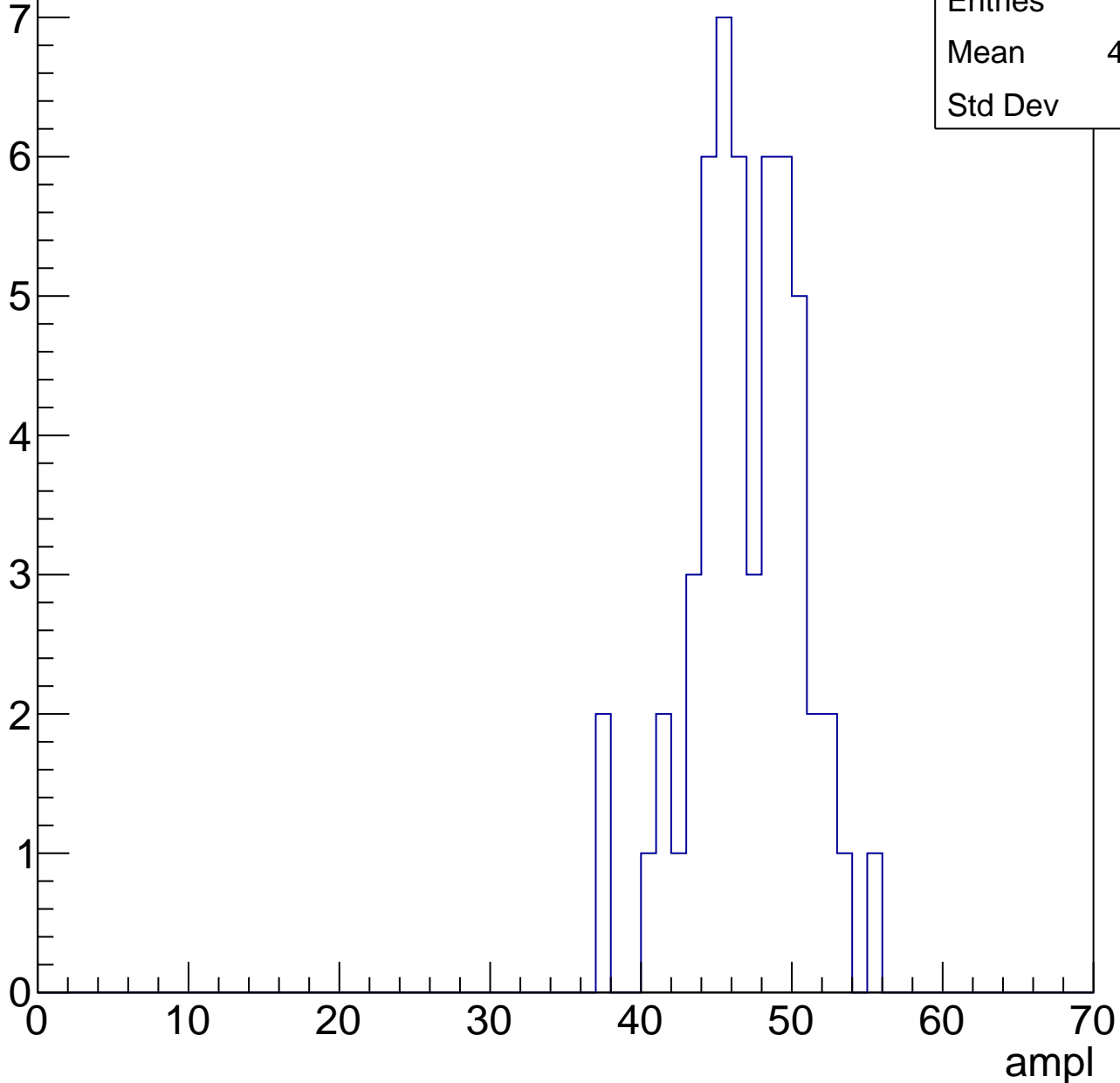


B1L100S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	46.46
Std Dev	3.66

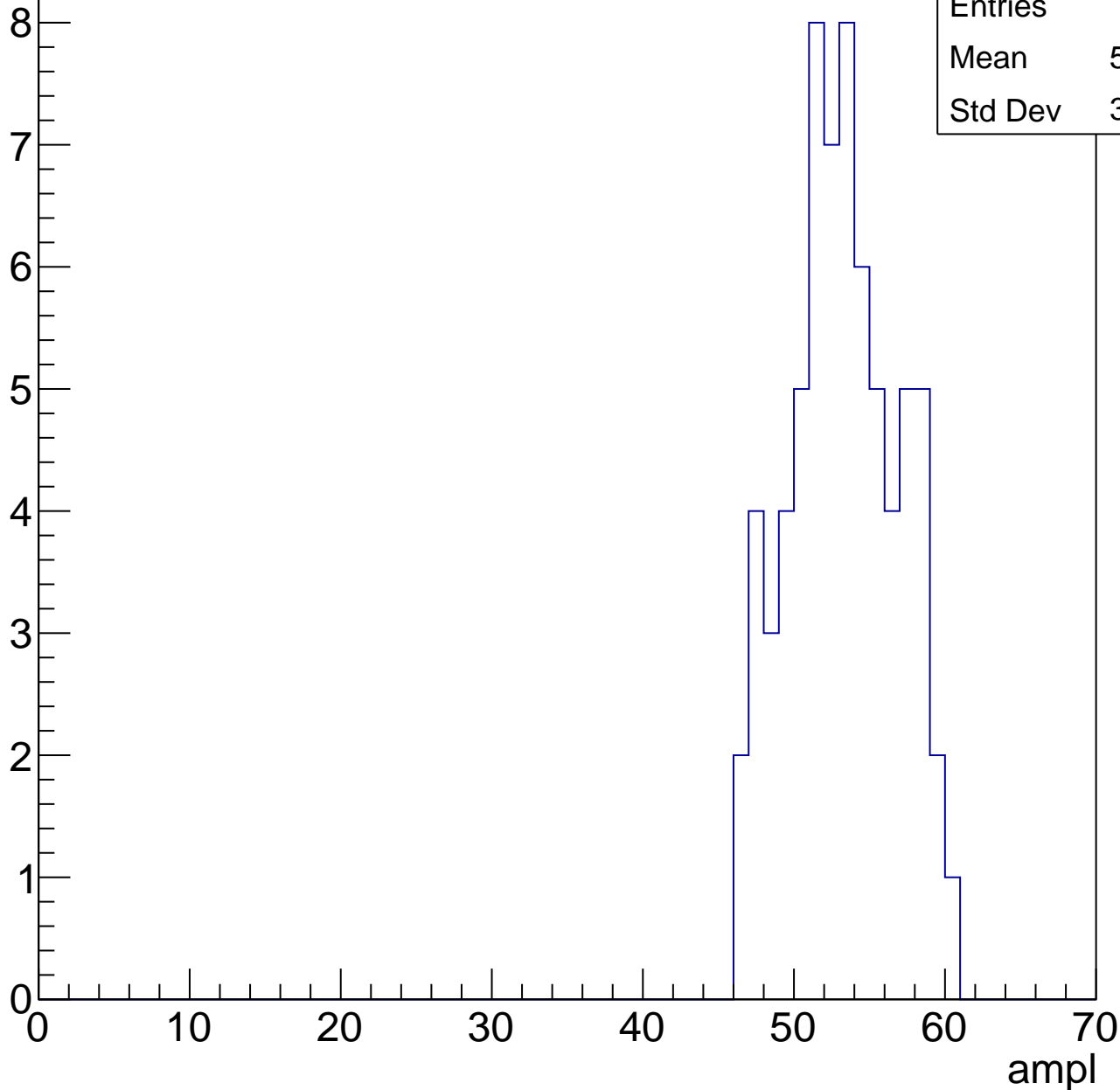


B1L100S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	52.78
Std Dev	3.509

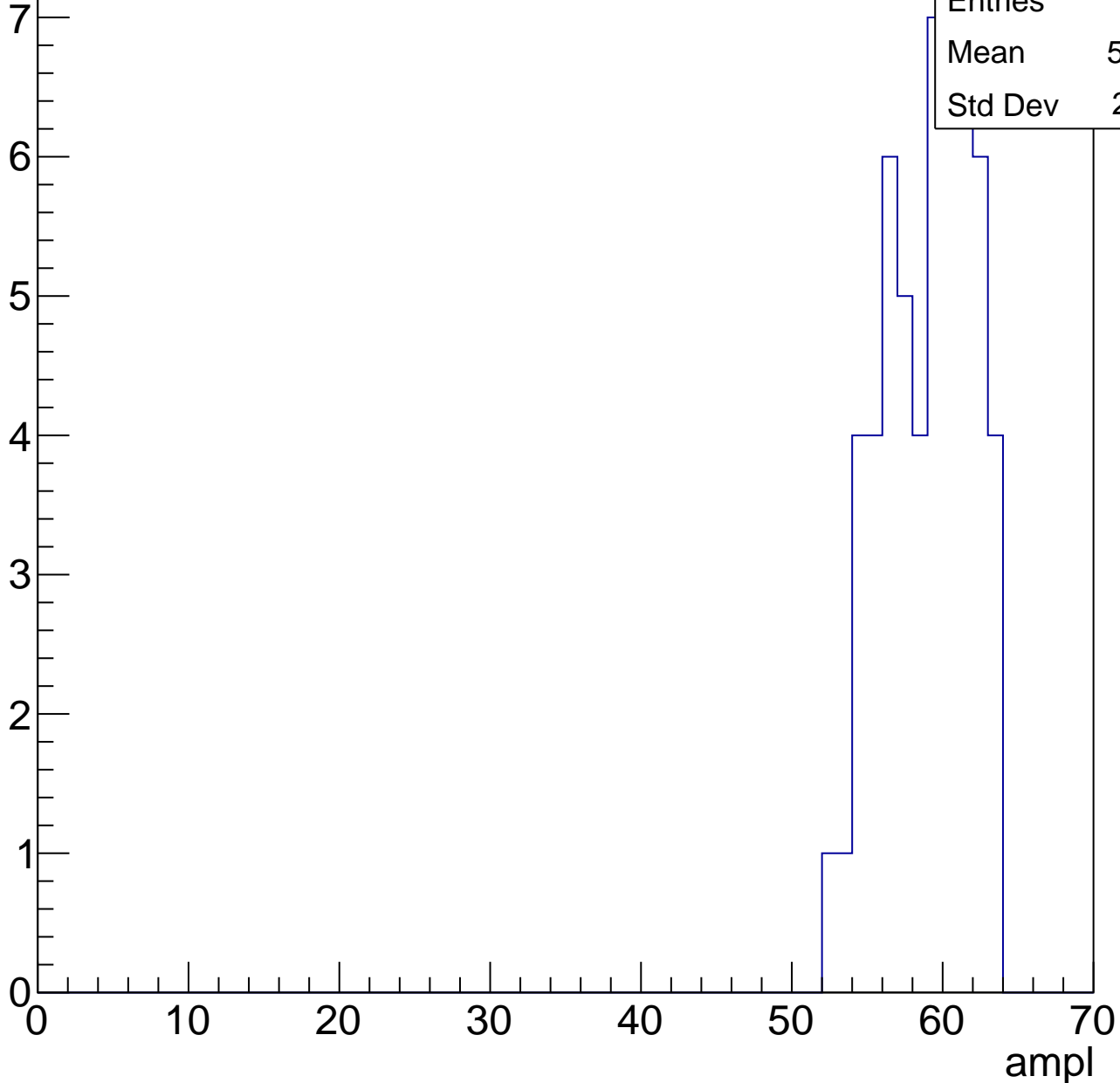


B1L100S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	58.54
Std Dev	2.891

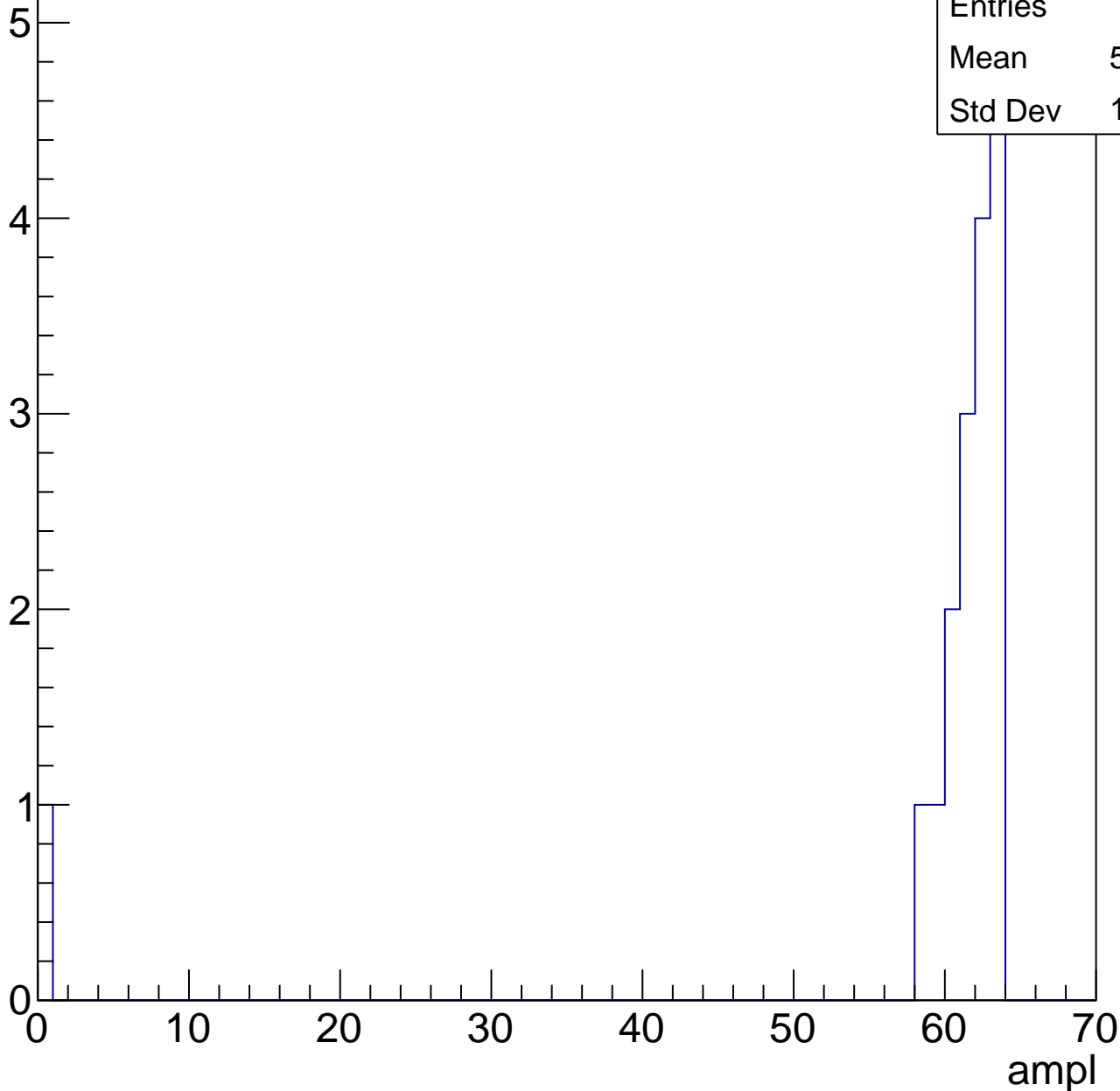


B1L100S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.82
Std Dev	14.53



B1L100S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U14-ch101, adc0

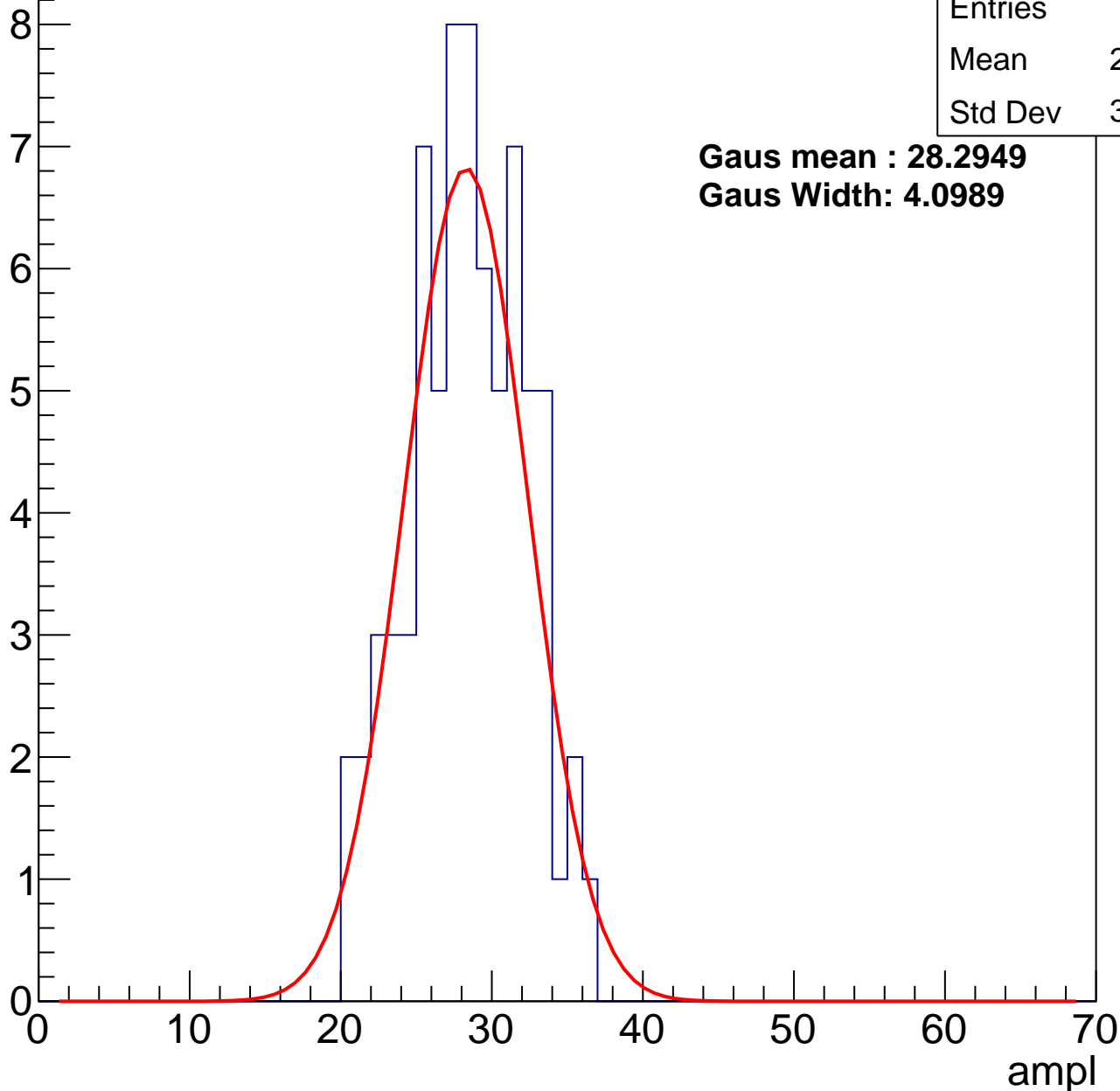
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	27.95
Std Dev	3.774

Gaus mean : 28.2949

Gaus Width: 4.0989



B1L100S, U14-ch101, adc1

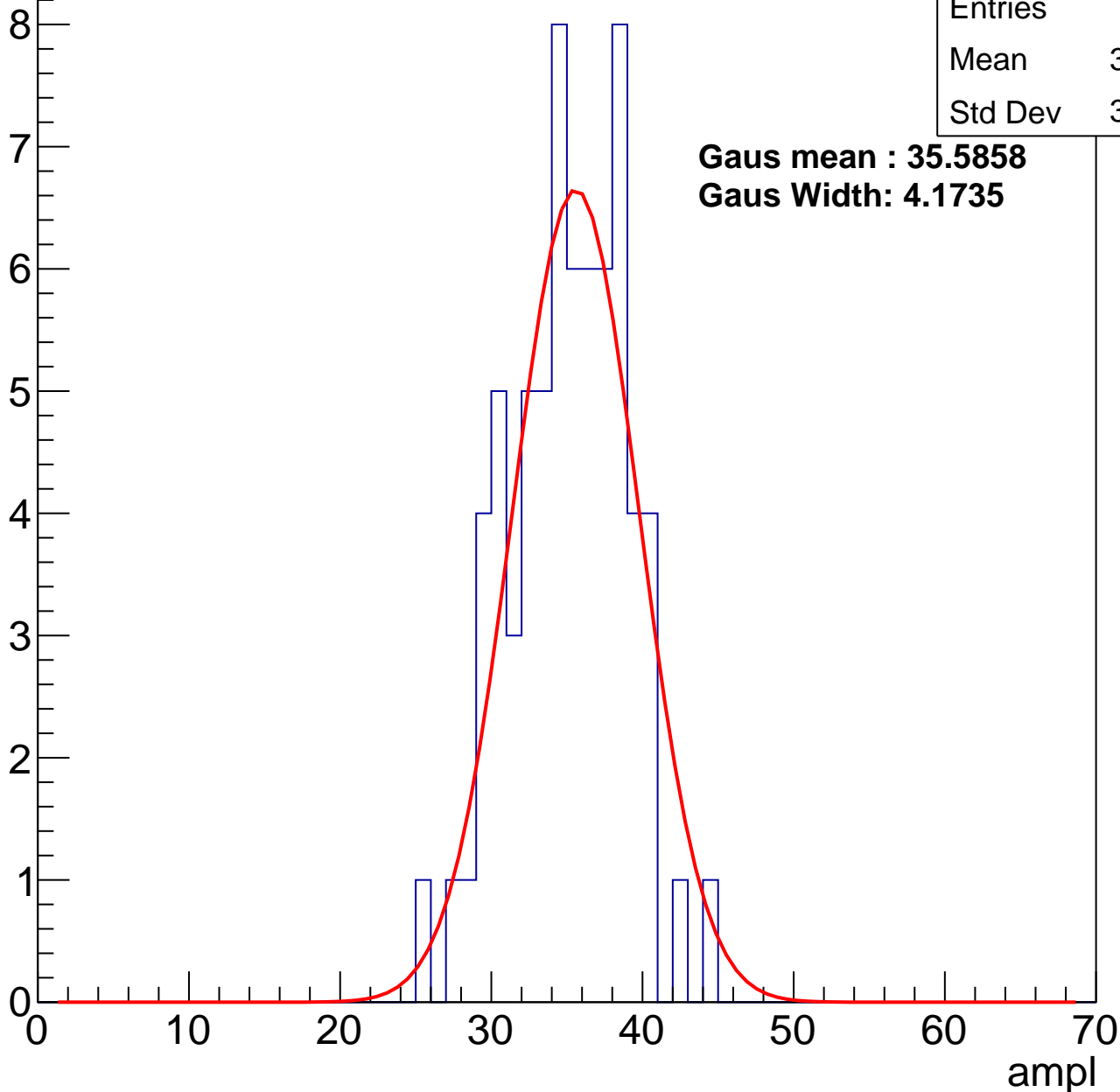
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.64
Std Dev	3.788

Gaus mean : 35.5858

Gaus Width: 4.1735



B1L100S, U14-ch101, adc2

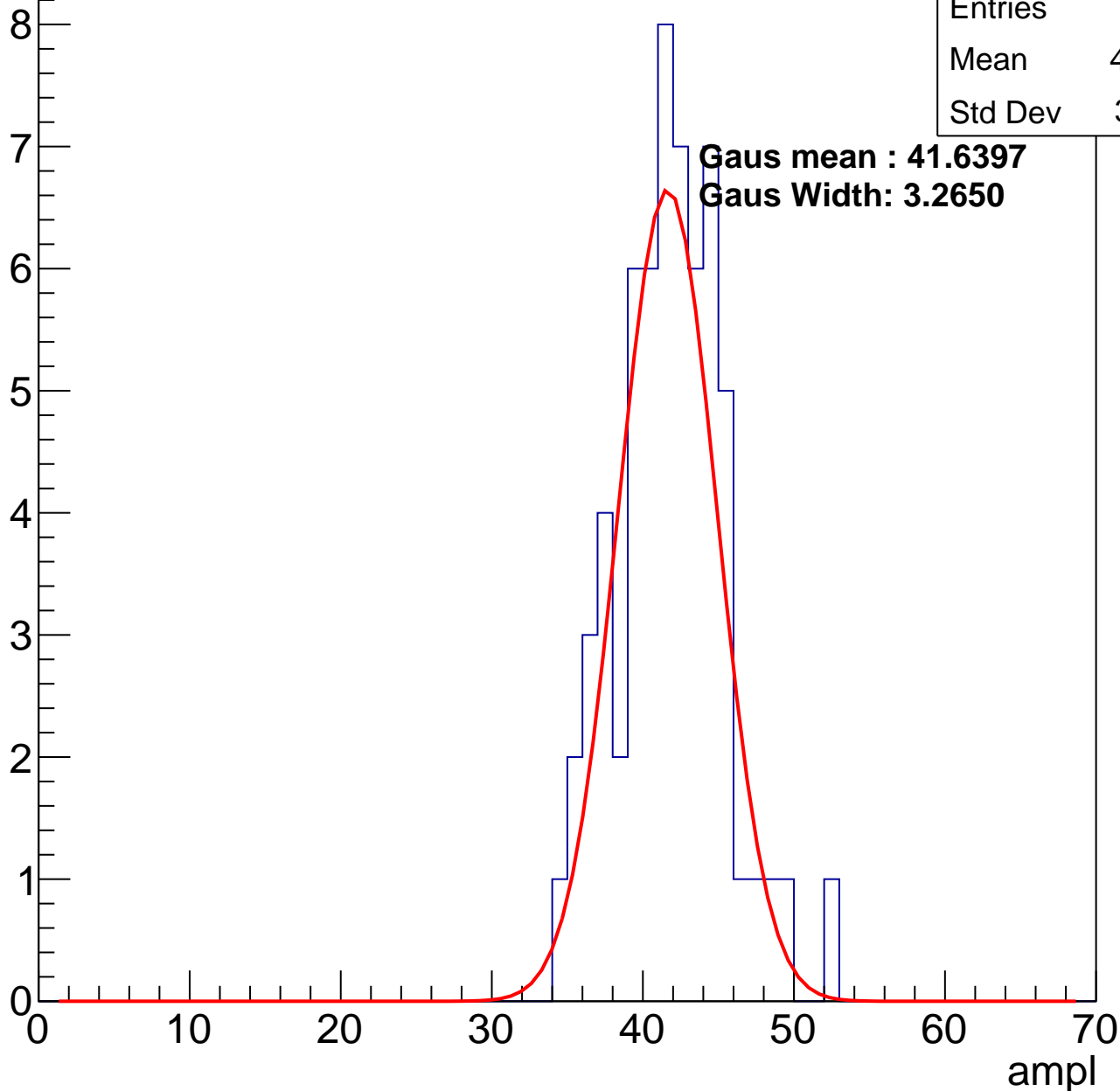
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.37
Std Dev	3.521

Gaus mean : 41.6397

Gaus Width: 3.2650

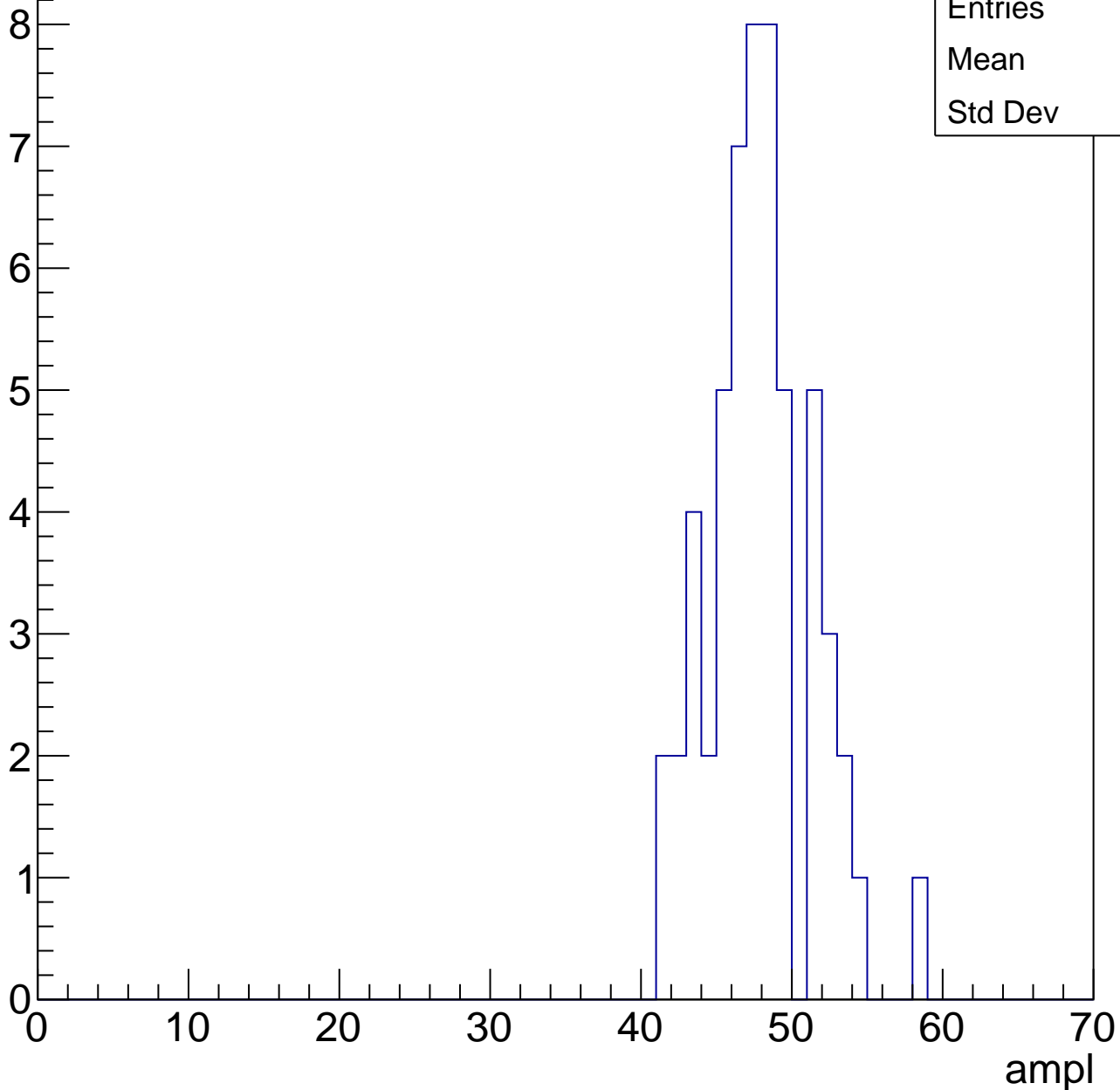


B1L100S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

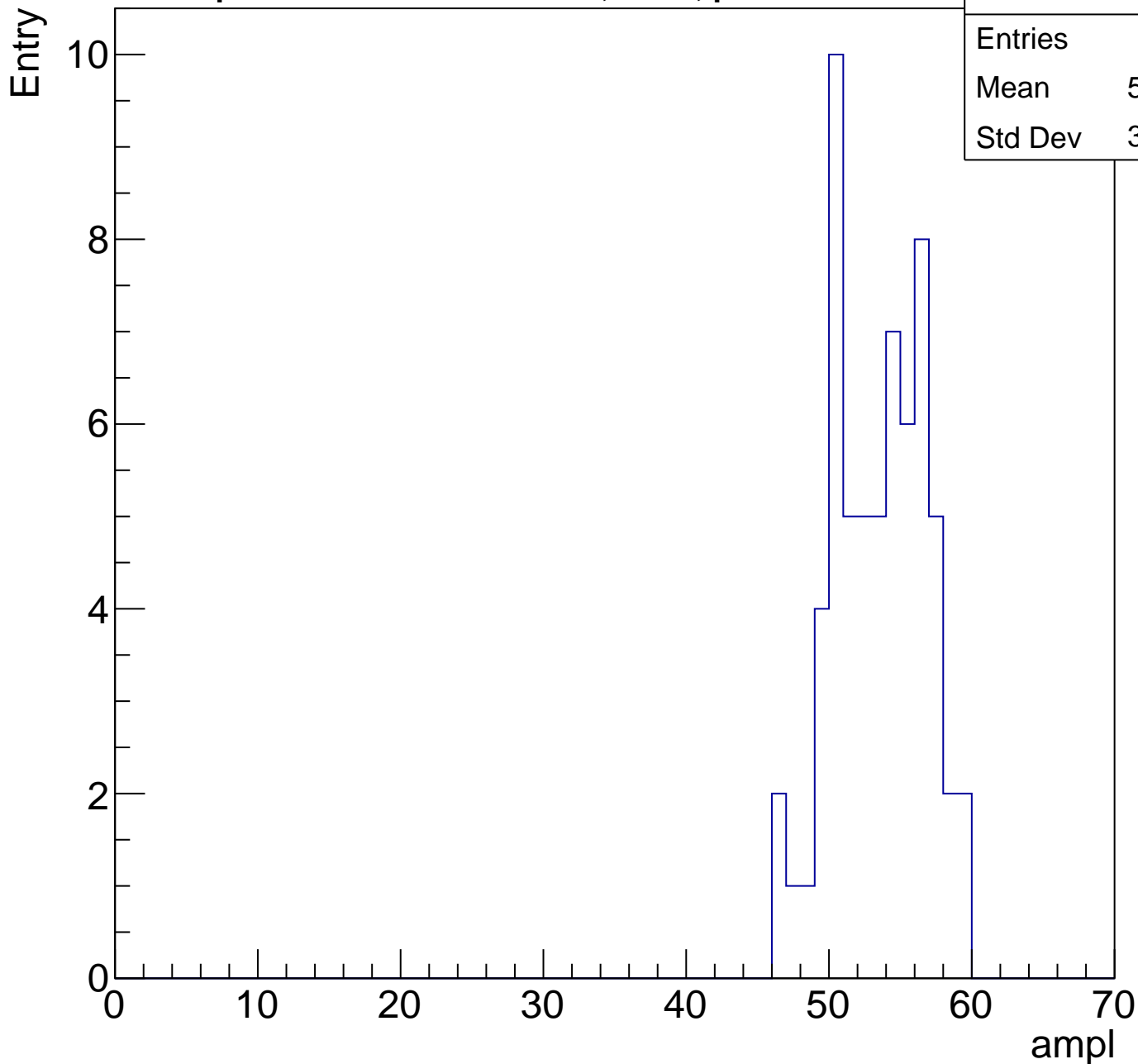
Entries	55
Mean	47.4
Std Dev	3.43



B1L100S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	52.98
Std Dev	3.195

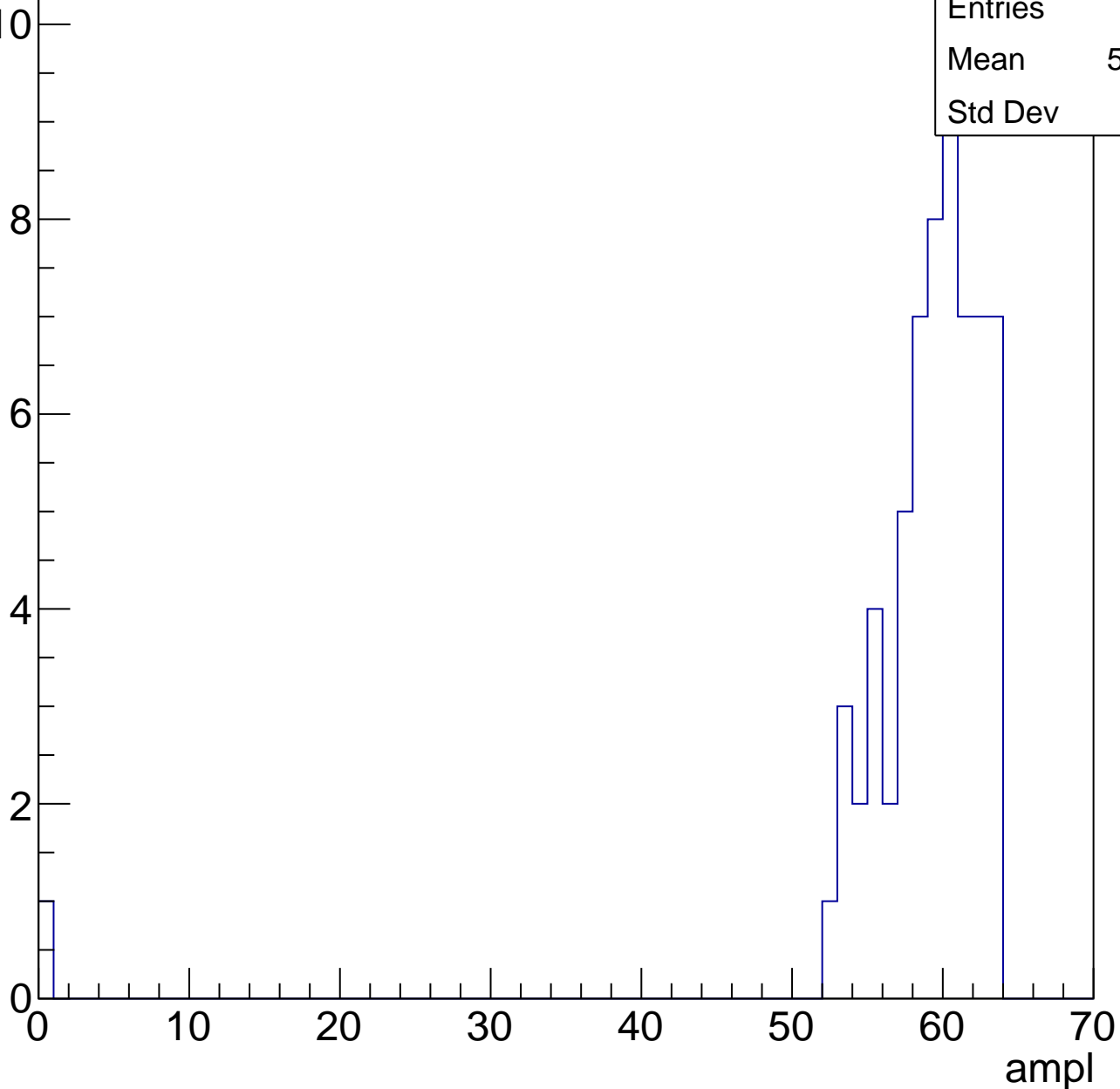


B1L100S, U14-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	58.06
Std Dev	7.86



B1L100S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

8

Mean

61.88

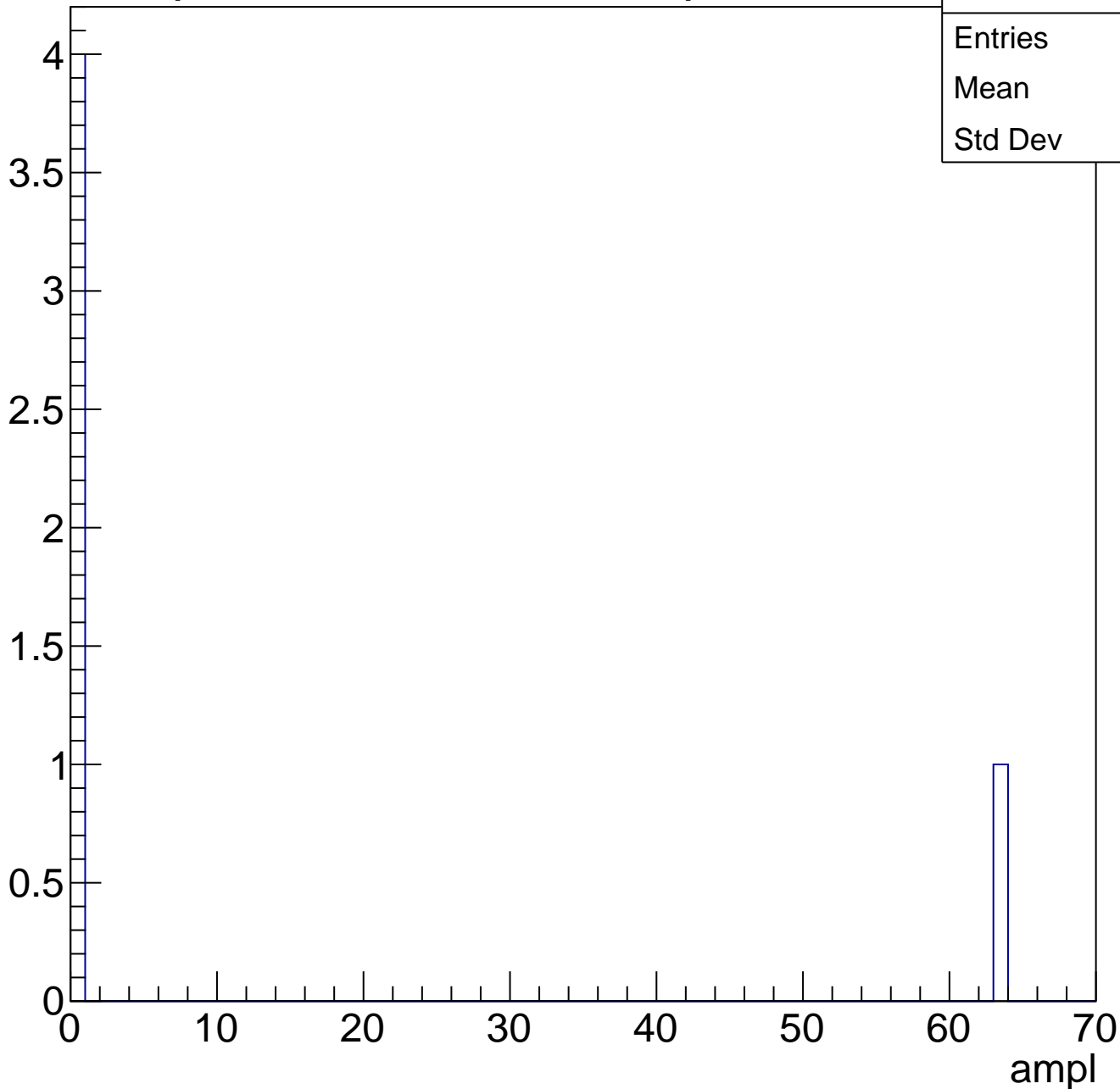
Std Dev

0.7806

B1L100S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch102, adc0

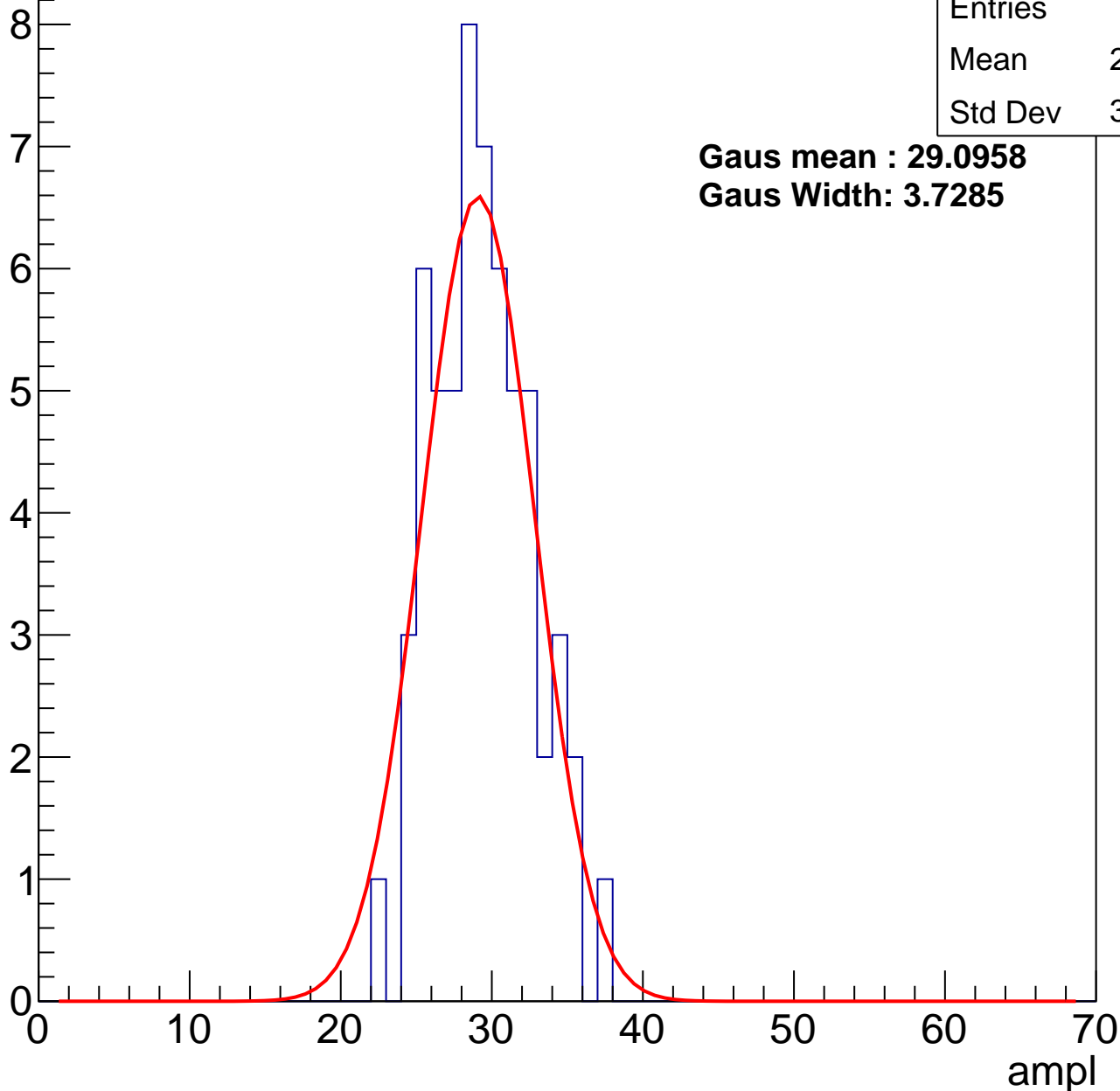
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.92
Std Dev	3.206

Gaus mean : 29.0958

Gaus Width: 3.7285



B1L100S, U14-ch102, adc1

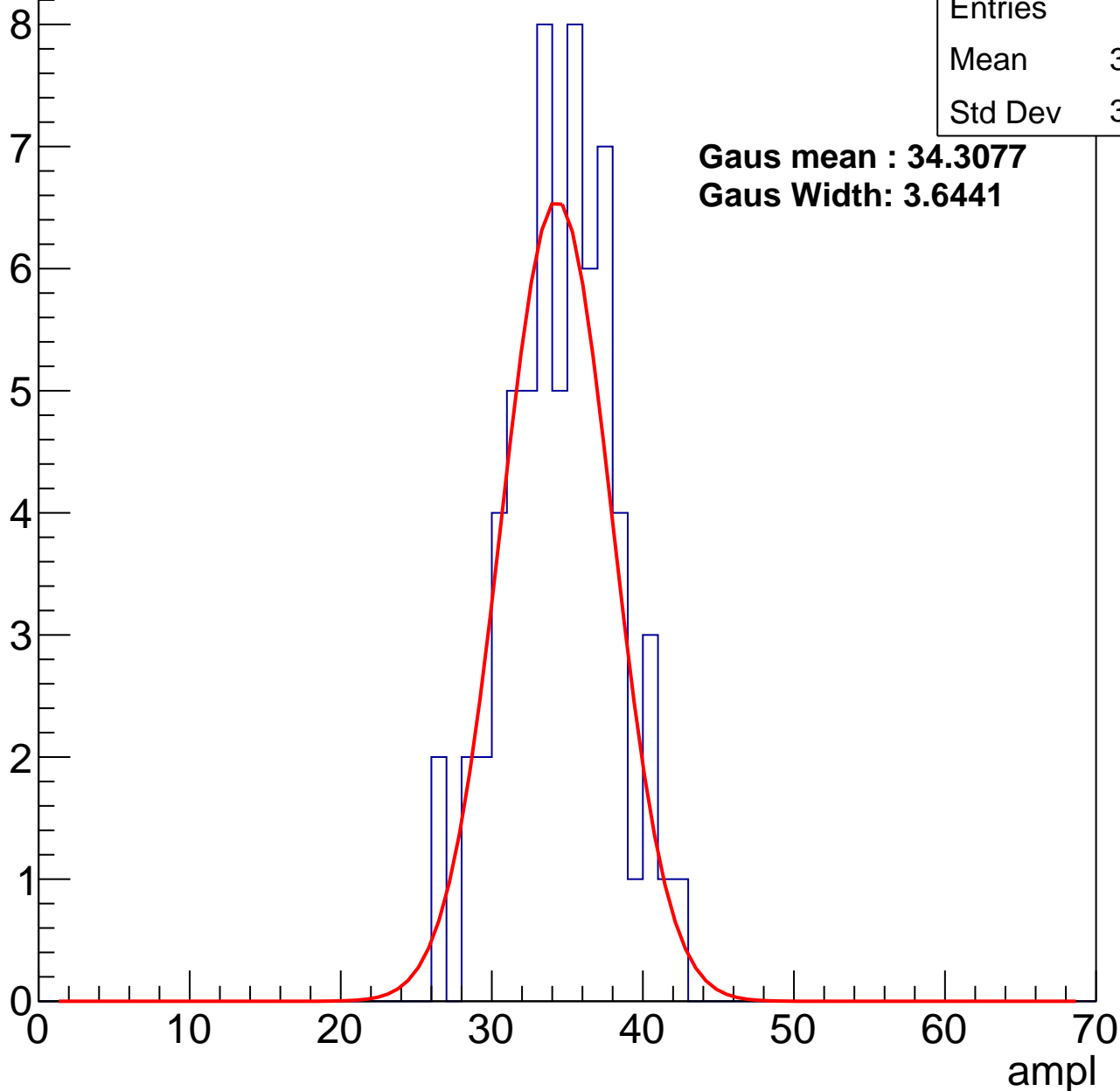
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	34.12
Std Dev	3.516

Gaus mean : 34.3077

Gaus Width: 3.6441



B1L100S, U14-ch102, adc2

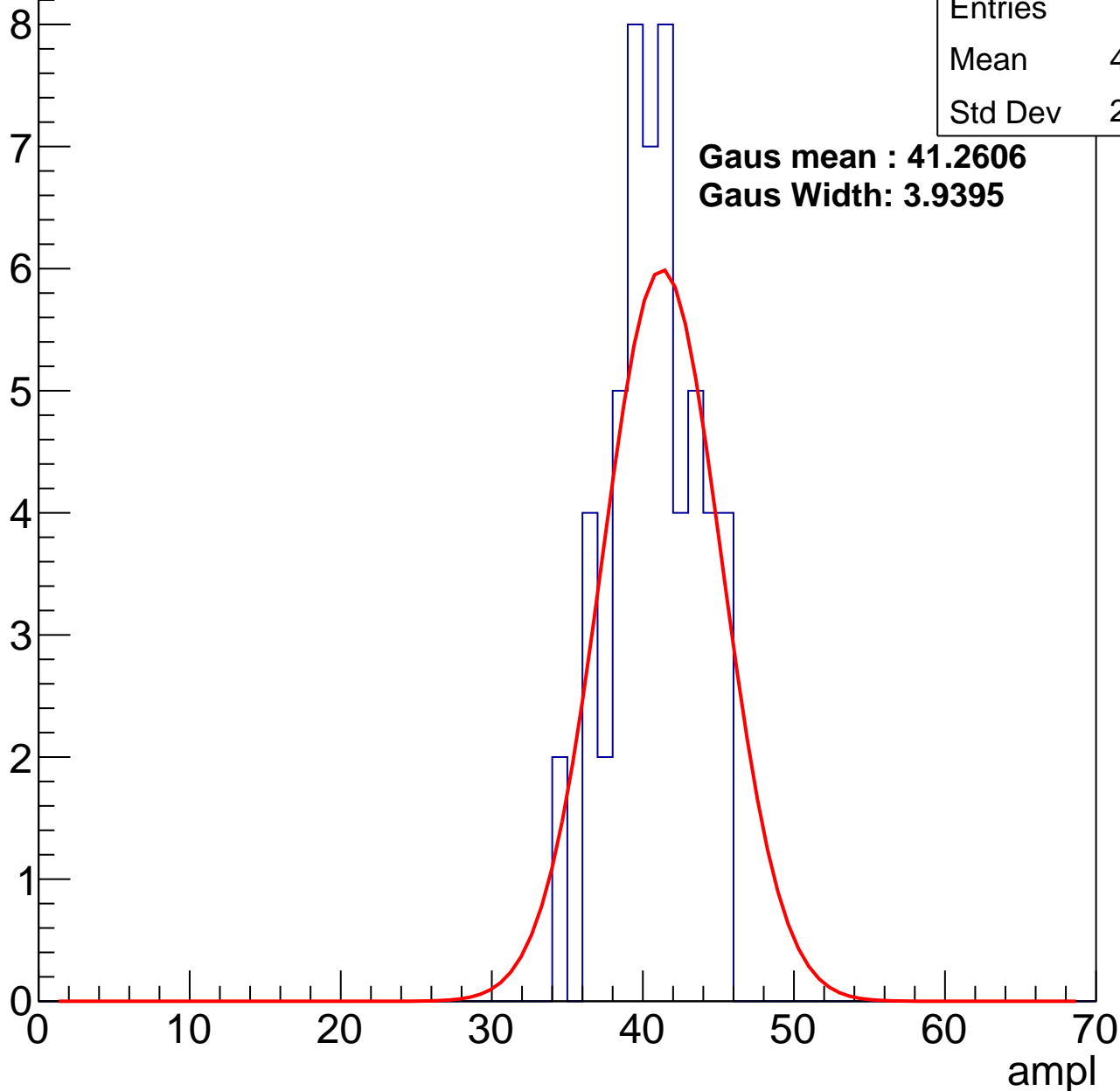
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	40.28
Std Dev	2.784

Gaus mean : 41.2606

Gaus Width: 3.9395

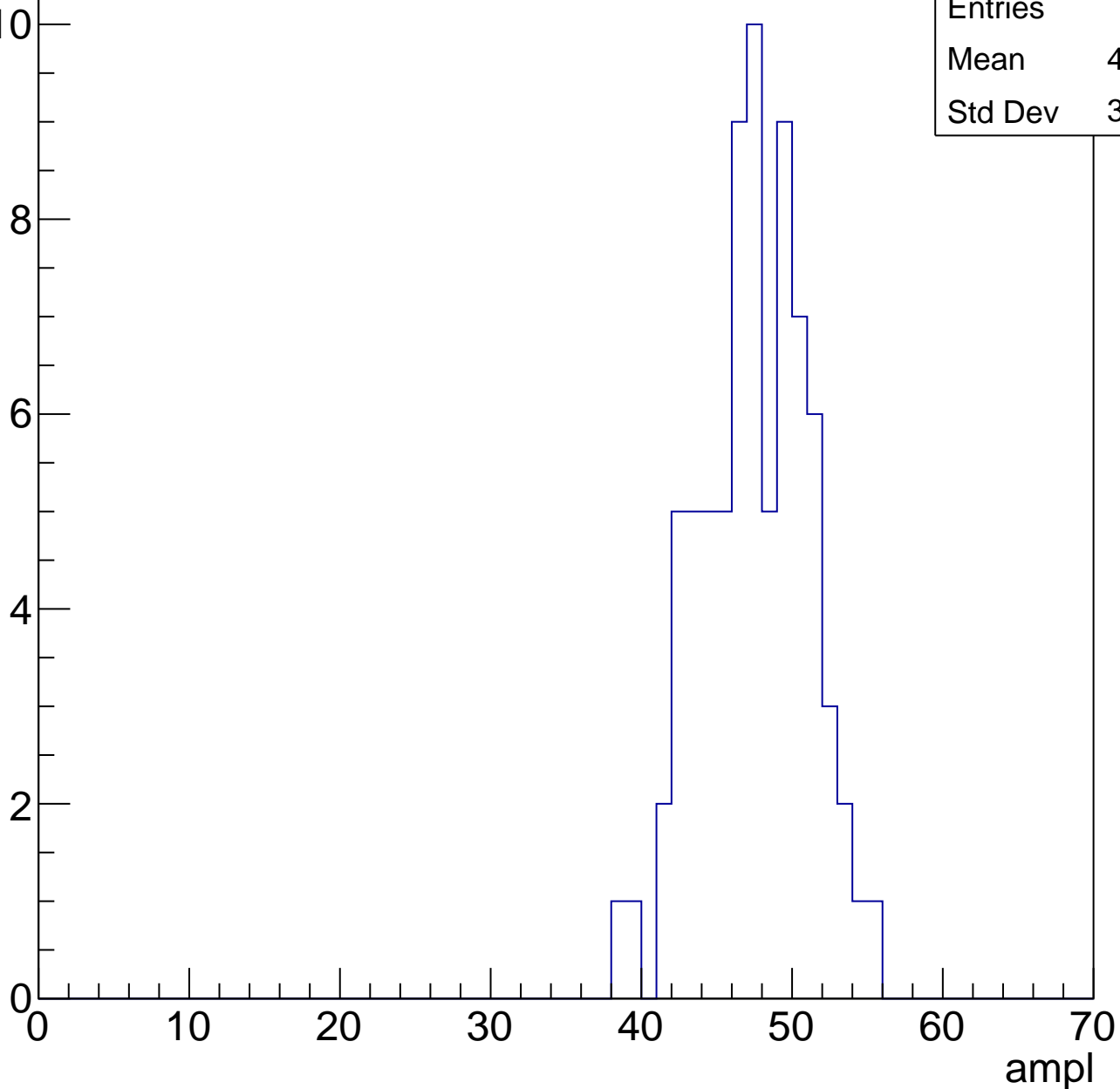


B1L100S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	47.03
Std Dev	3.523

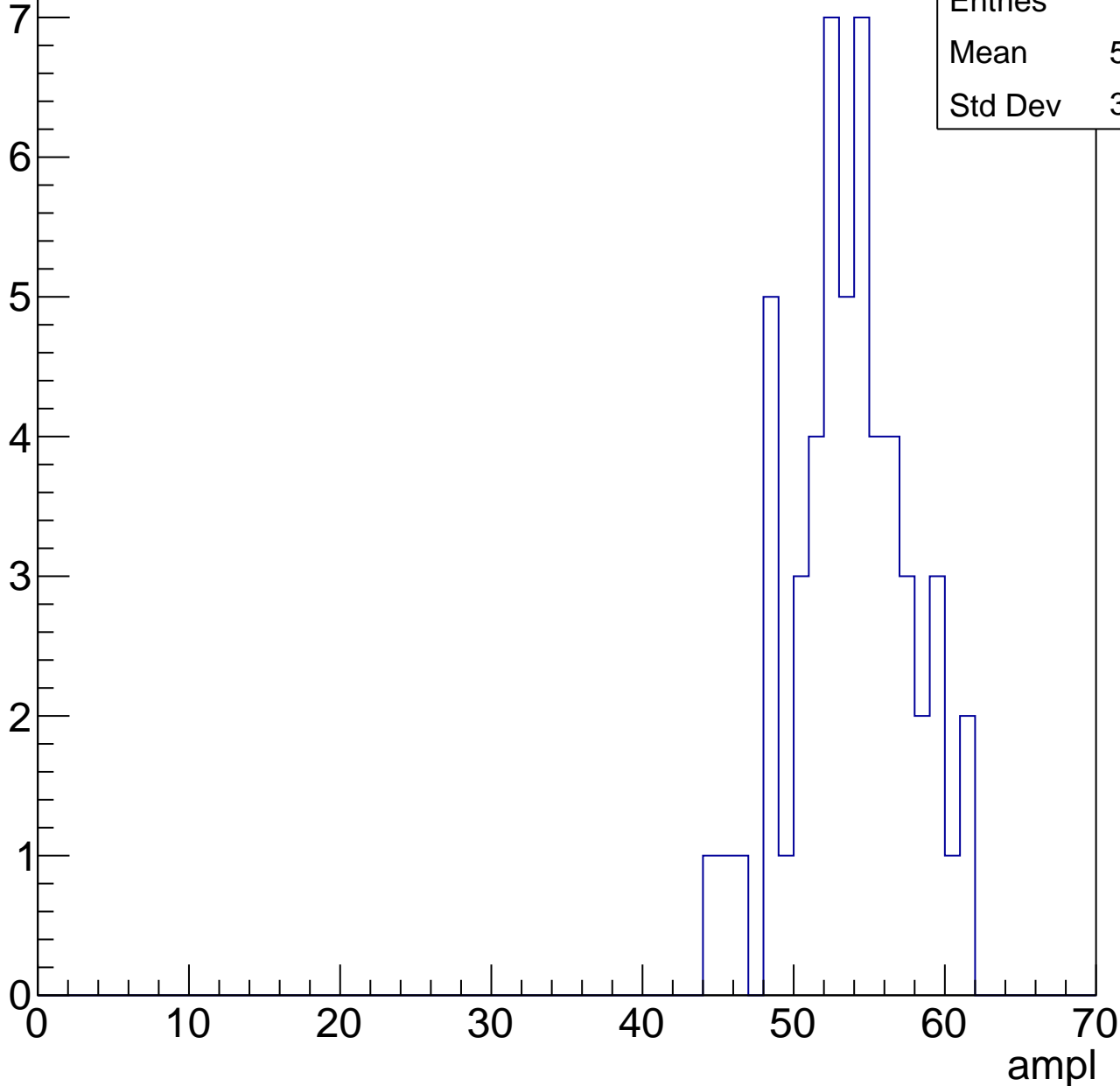


B1L100S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

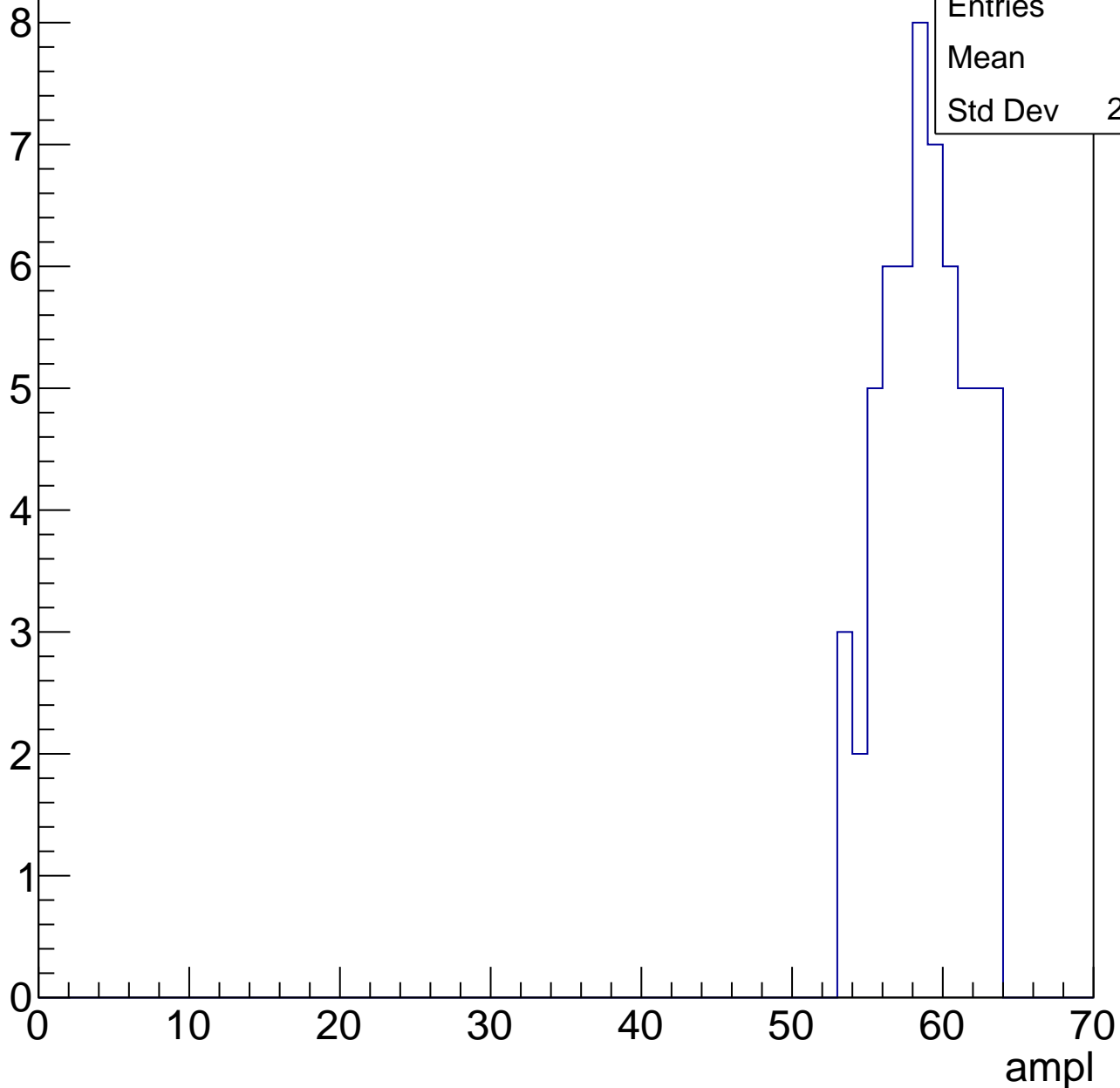
Entries	54
Mean	53.24
Std Dev	3.906



B1L100S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

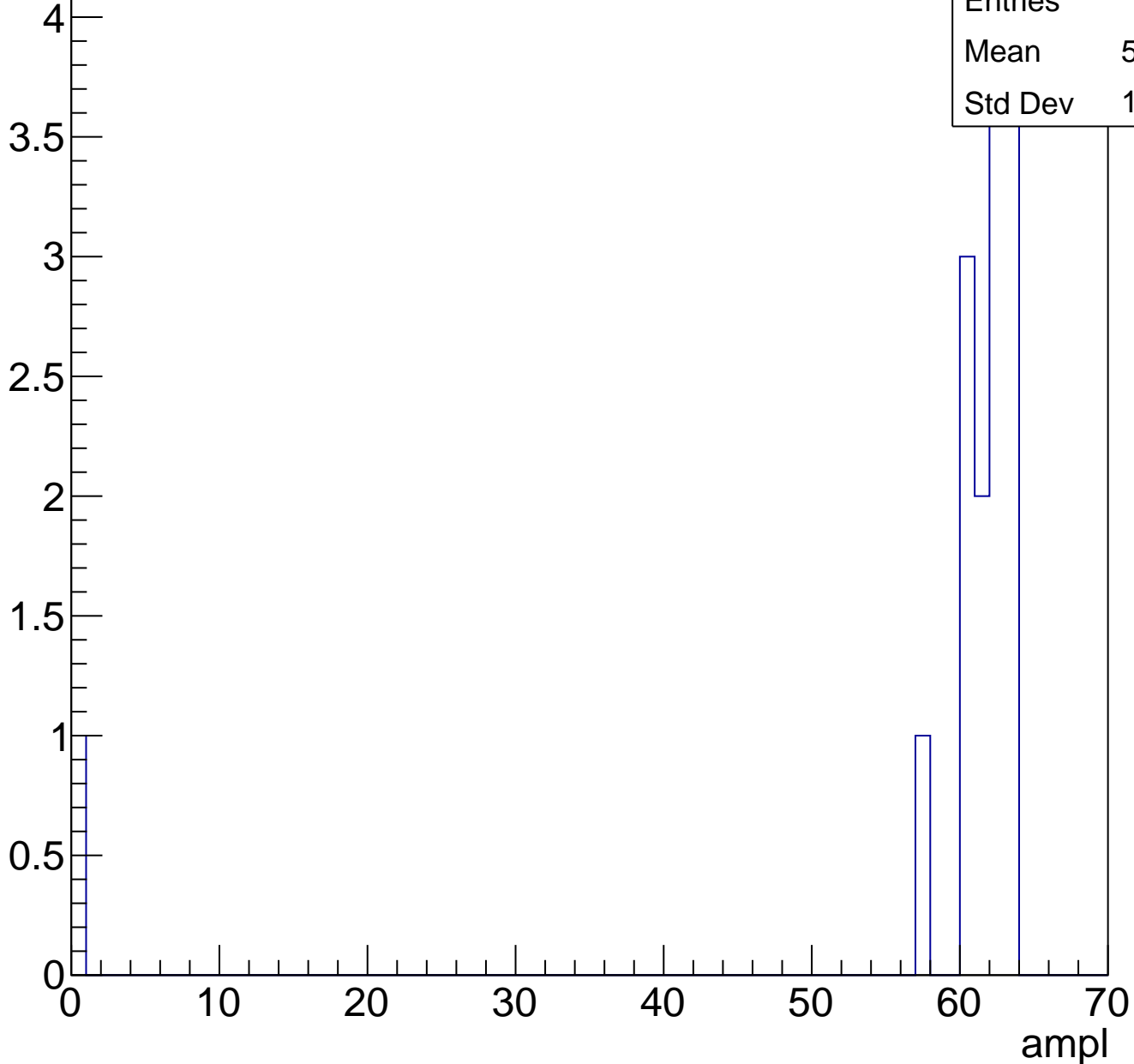


Entries	58
Mean	58.4
Std Dev	2.797

B1L100S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

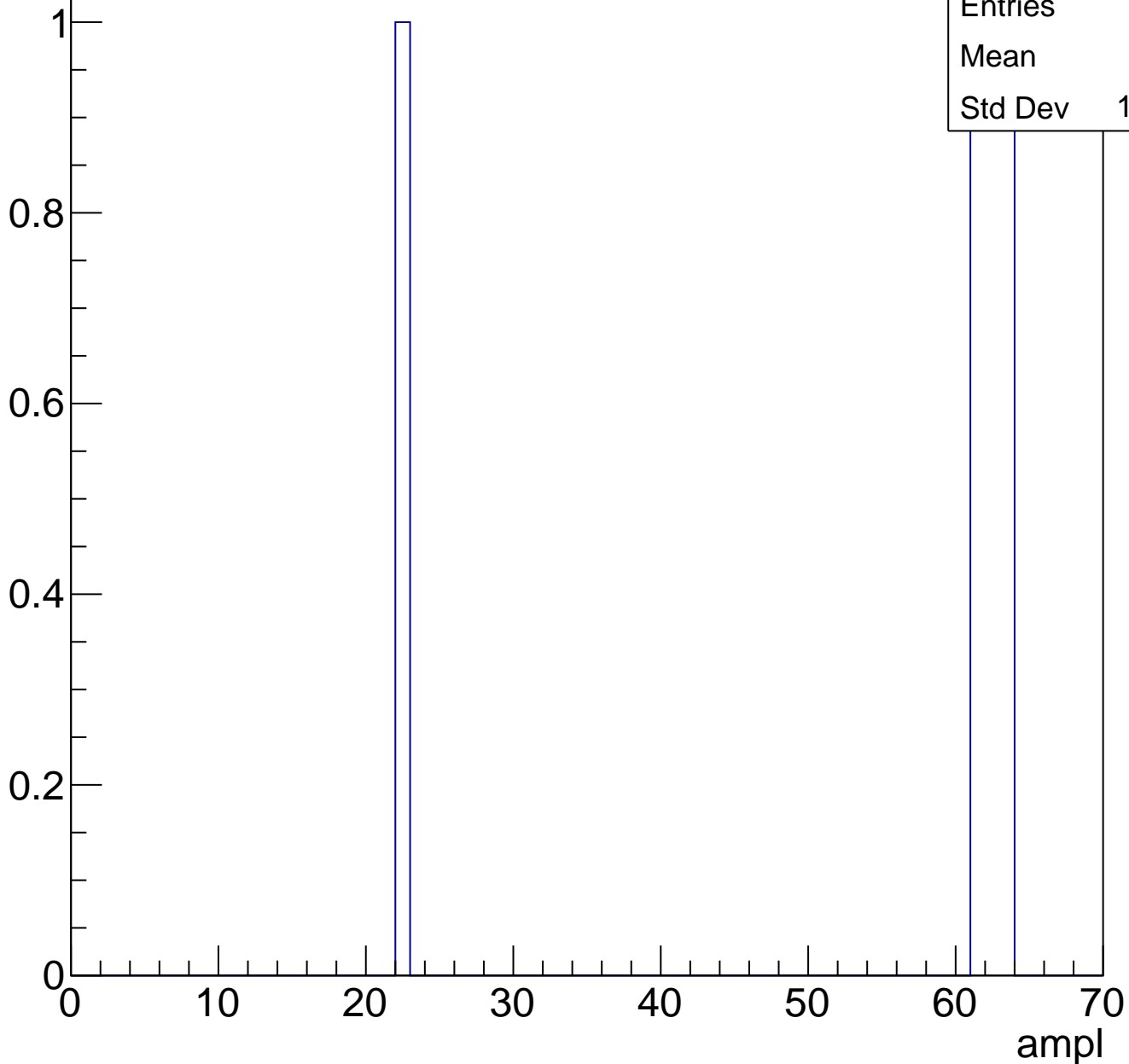
Entry



B1L100S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch103, adc0

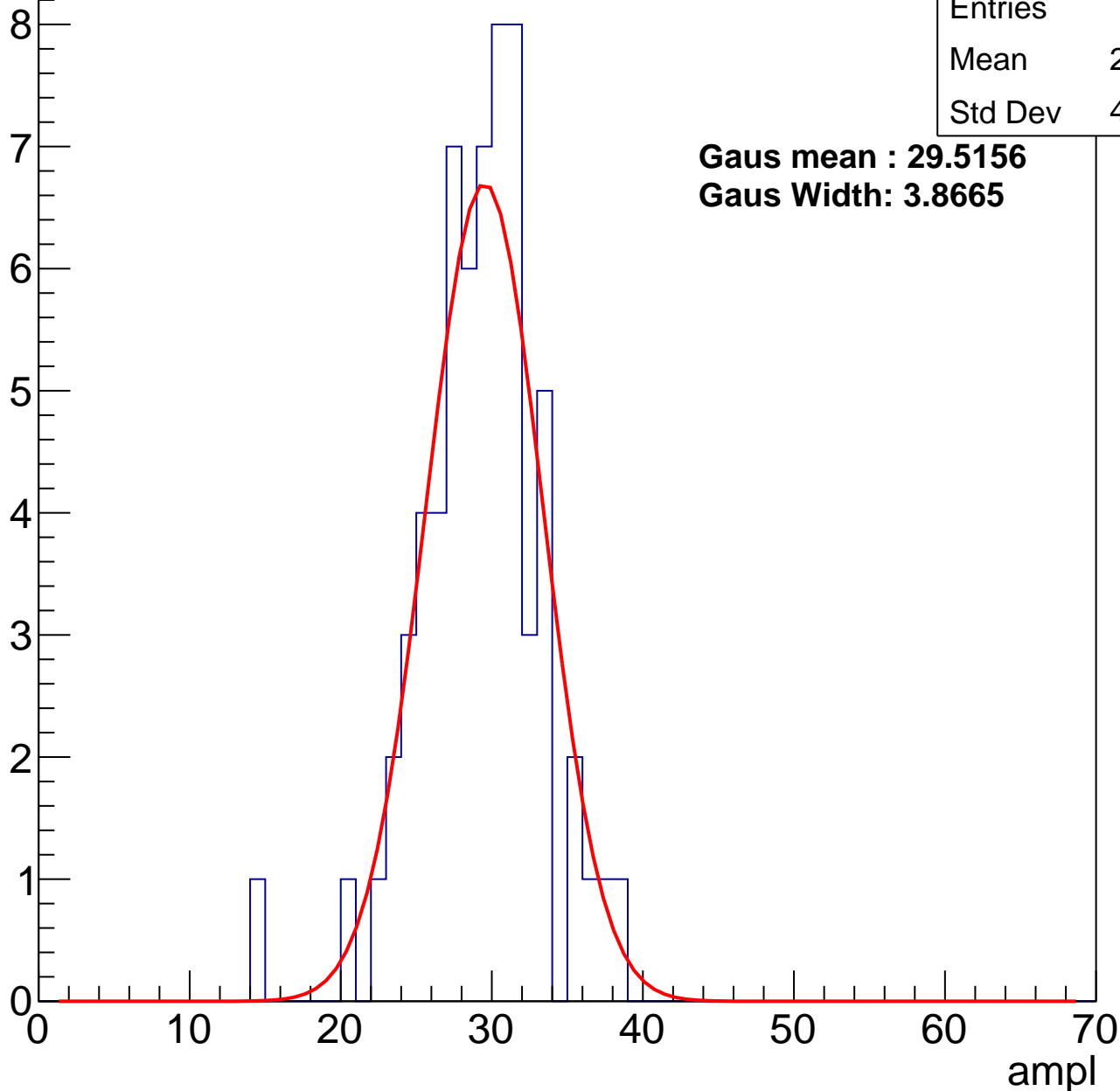
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.74
Std Dev	4.009

Gaus mean : 29.5156

Gaus Width: 3.8665



B1L100S, U14-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	35.06
Std Dev	3.926

Gaus mean : 35.9411

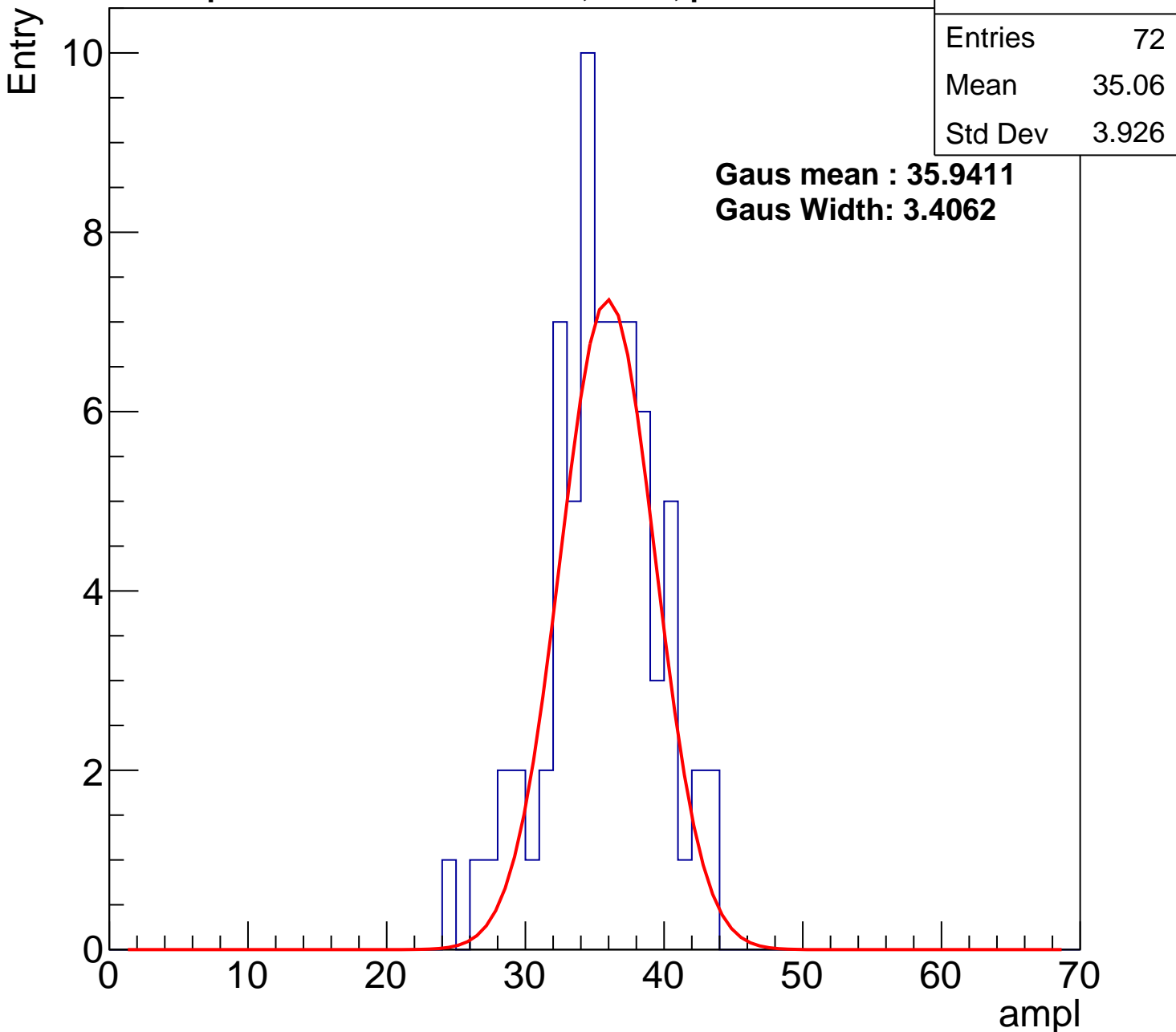
Gaus Width: 3.4062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

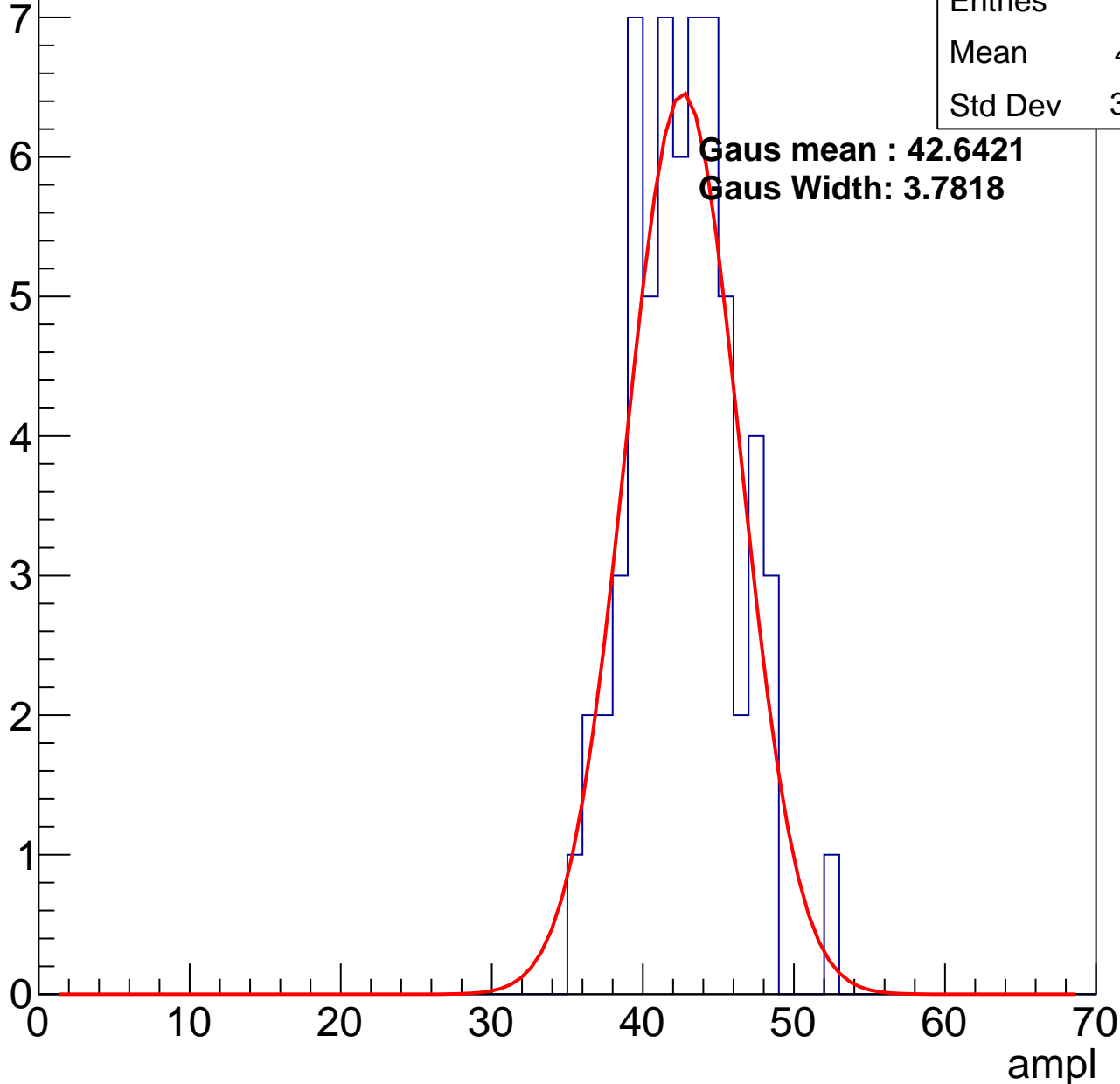


B1L100S, U14-ch103, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.21
Std Dev	3.423

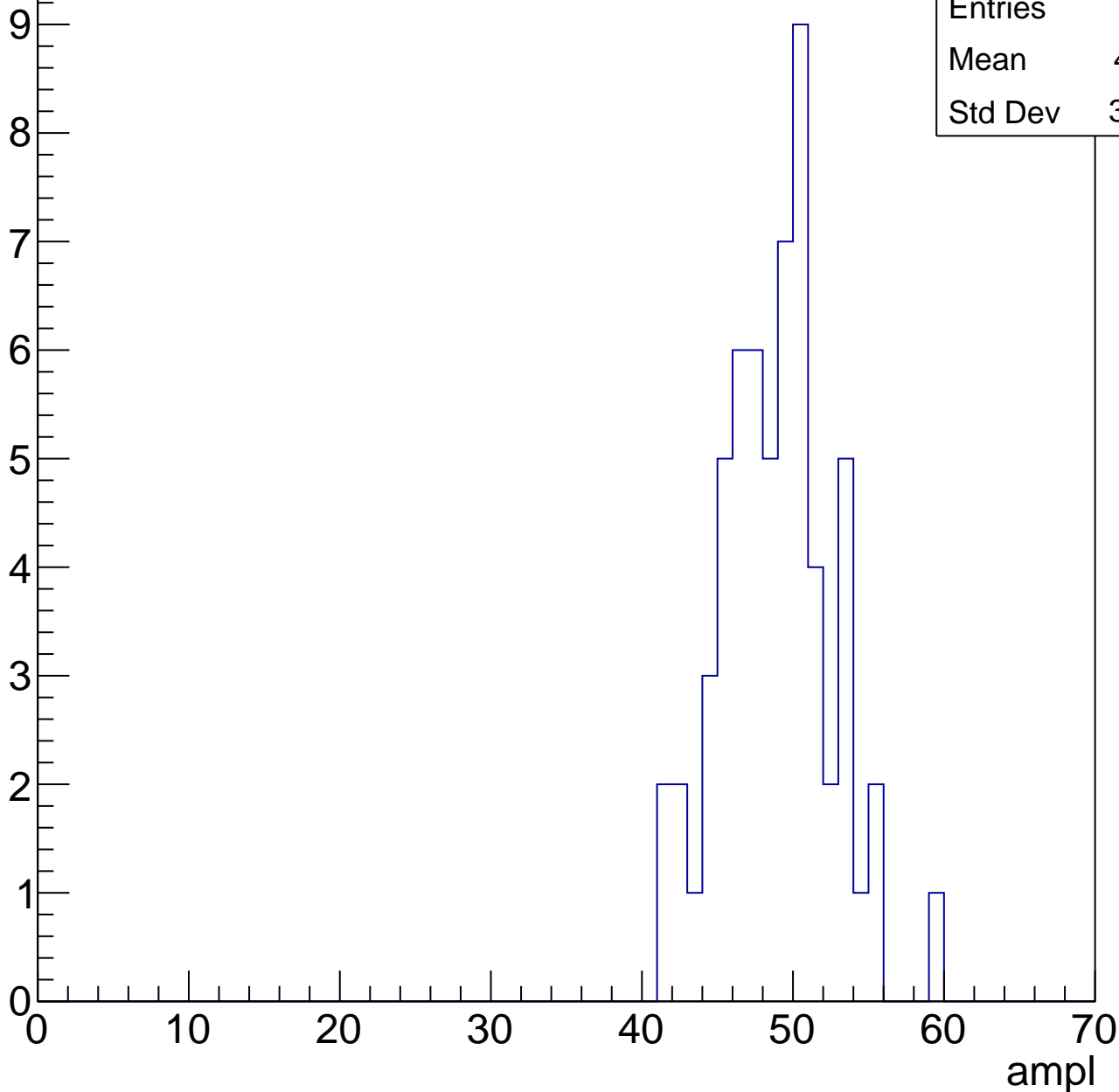


B1L100S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48.41
Std Dev	3.619

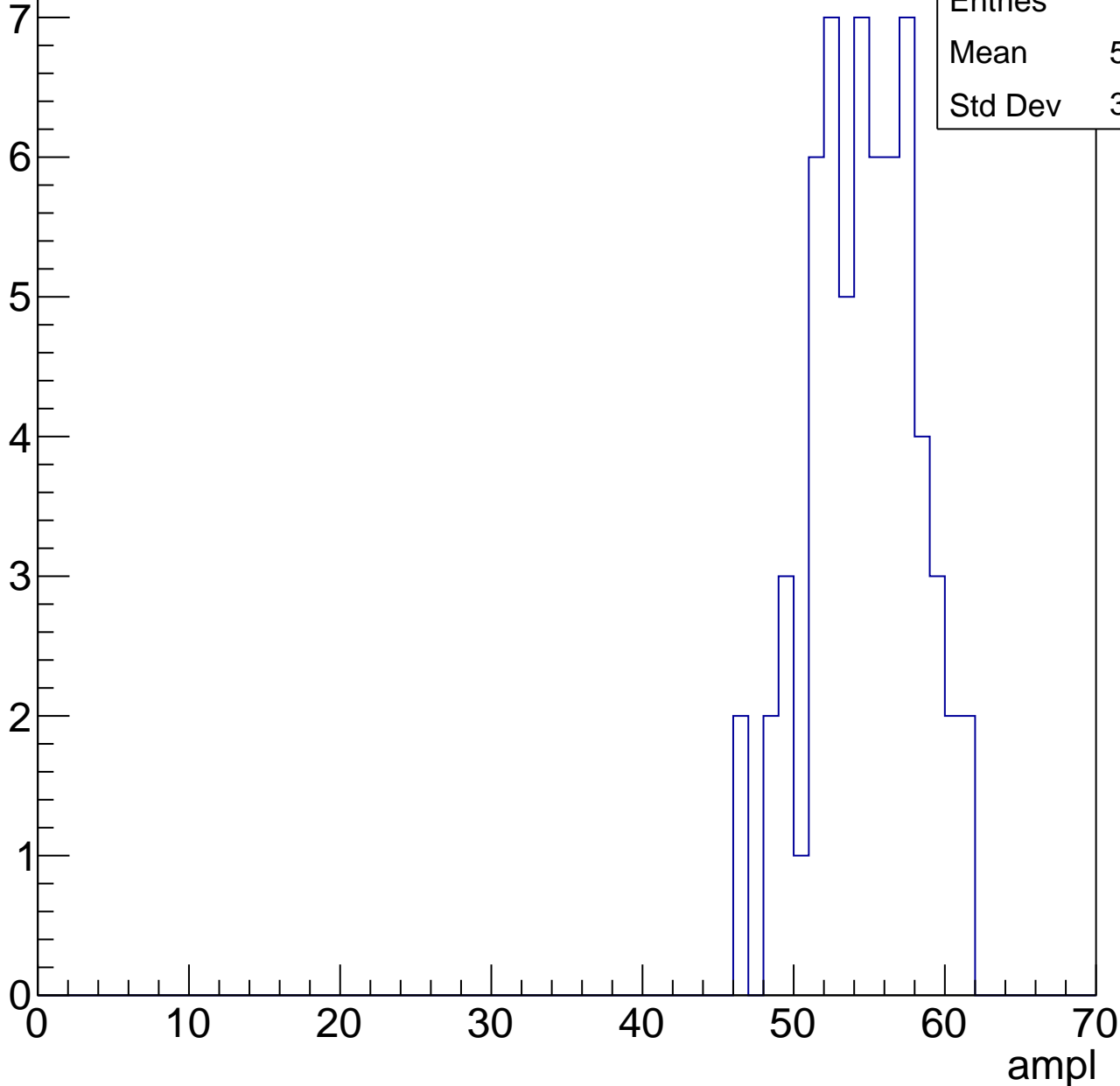


B1L100S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

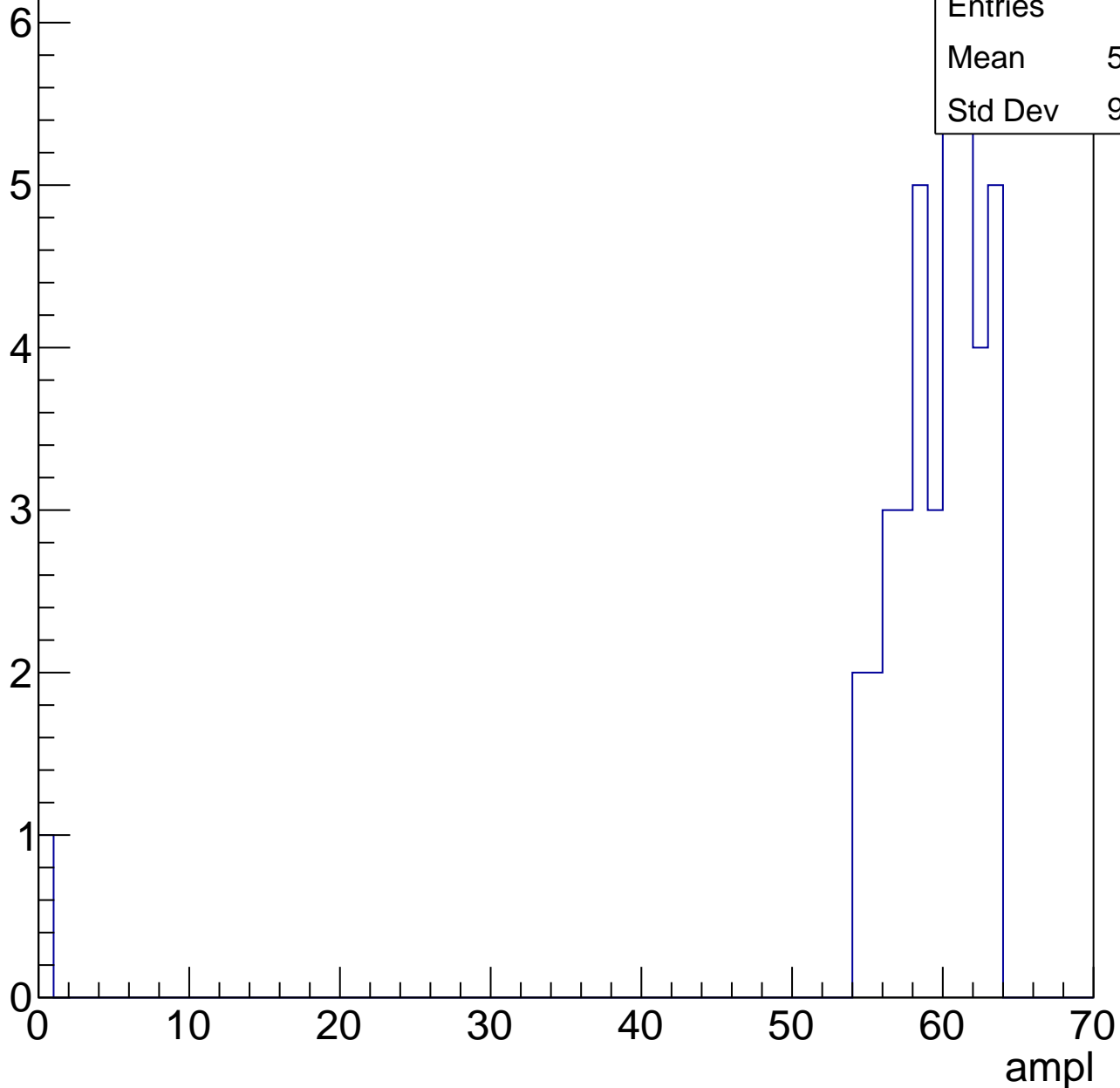
Entries	63
Mean	54.19
Std Dev	3.513



B1L100S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

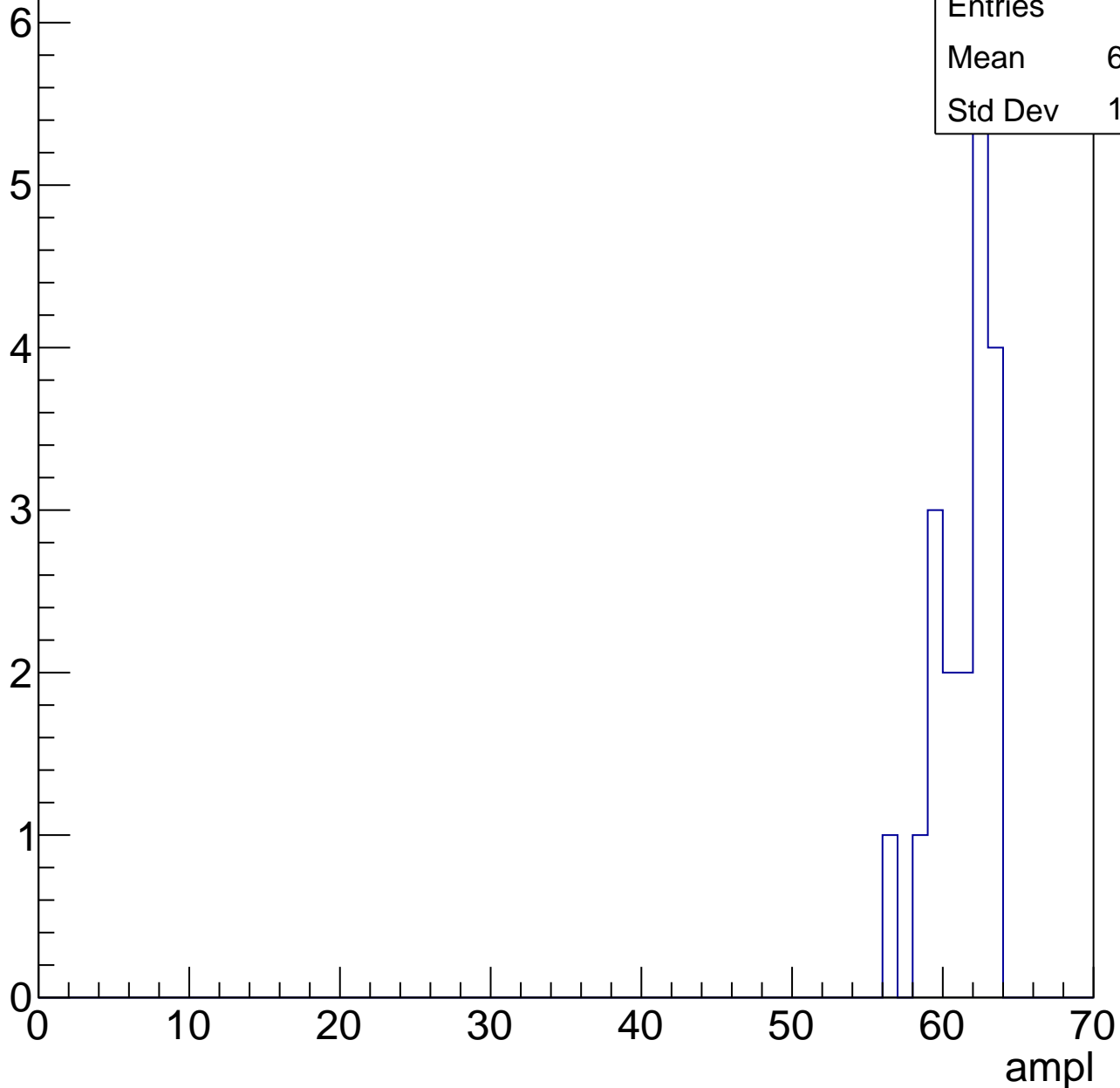


B1L100S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	60.89
Std Dev	1.917



B1L100S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch104, adc0

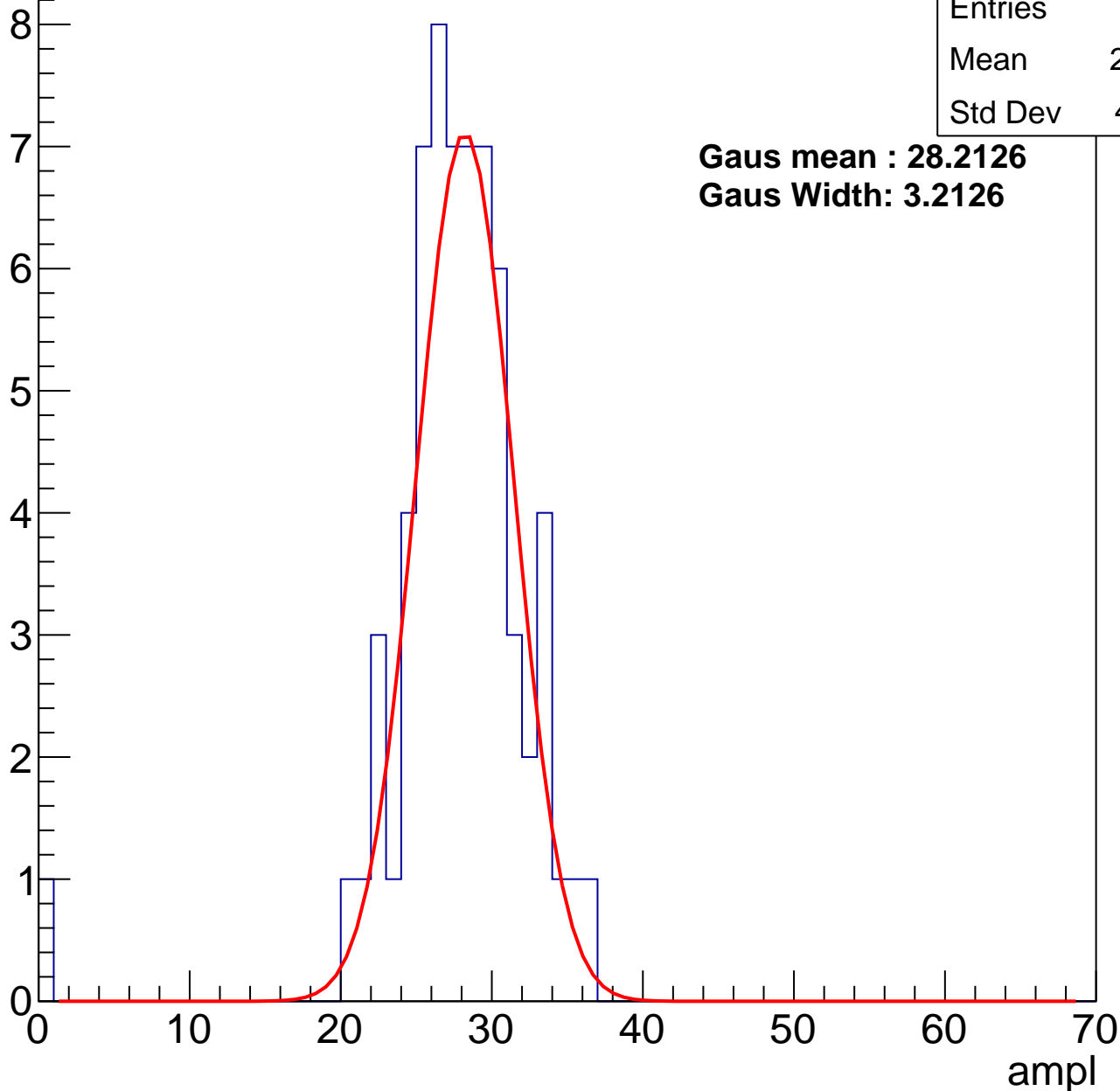
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.25
Std Dev	4.801

Gaus mean : 28.2126

Gaus Width: 3.2126



B1L100S, U14-ch104, adc1

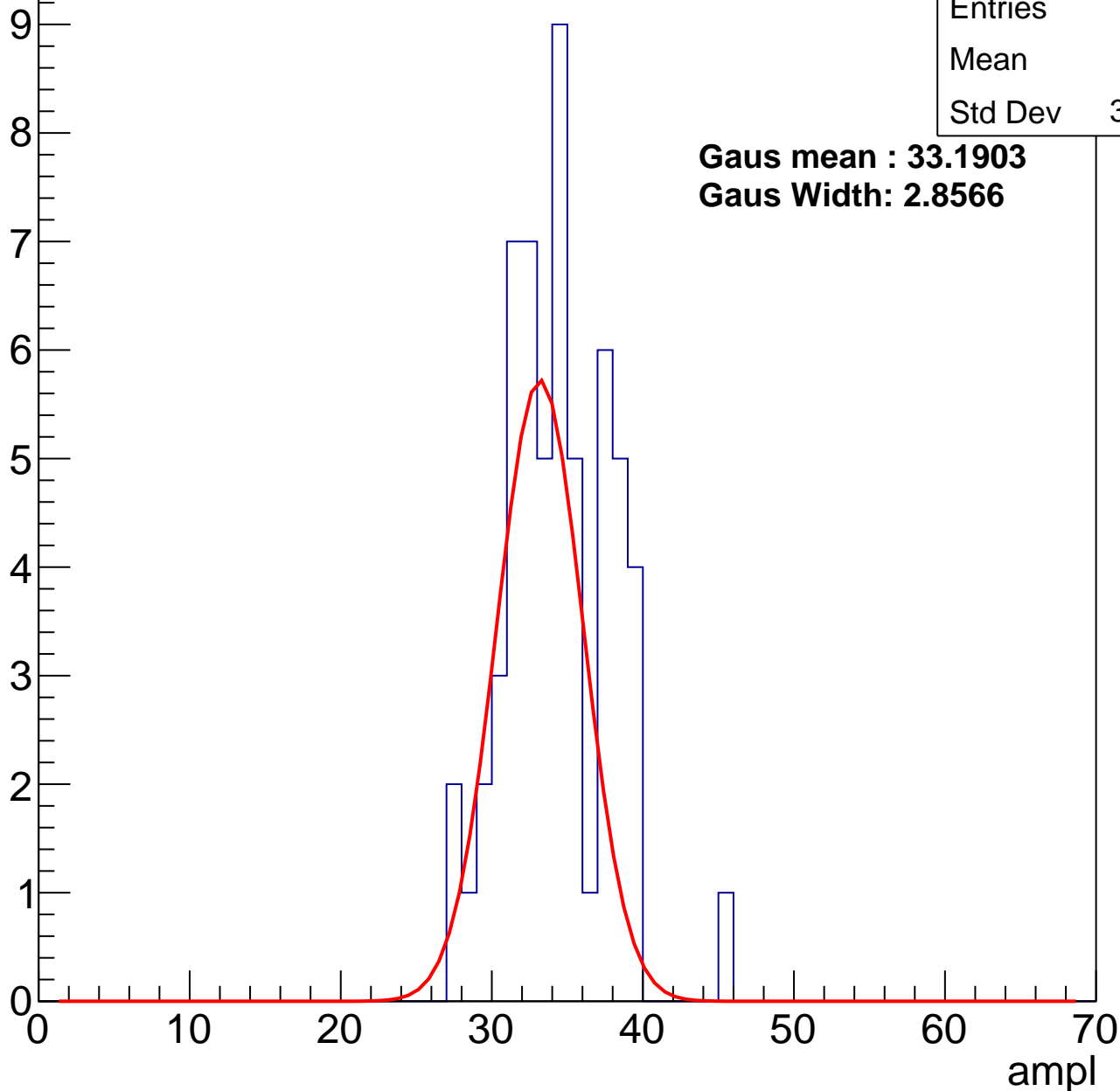
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	33.9
Std Dev	3.453

Gaus mean : 33.1903

Gaus Width: 2.8566



B1L100S, U14-ch104, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	40.36
Std Dev	3.439

Gaus mean : 40.8386

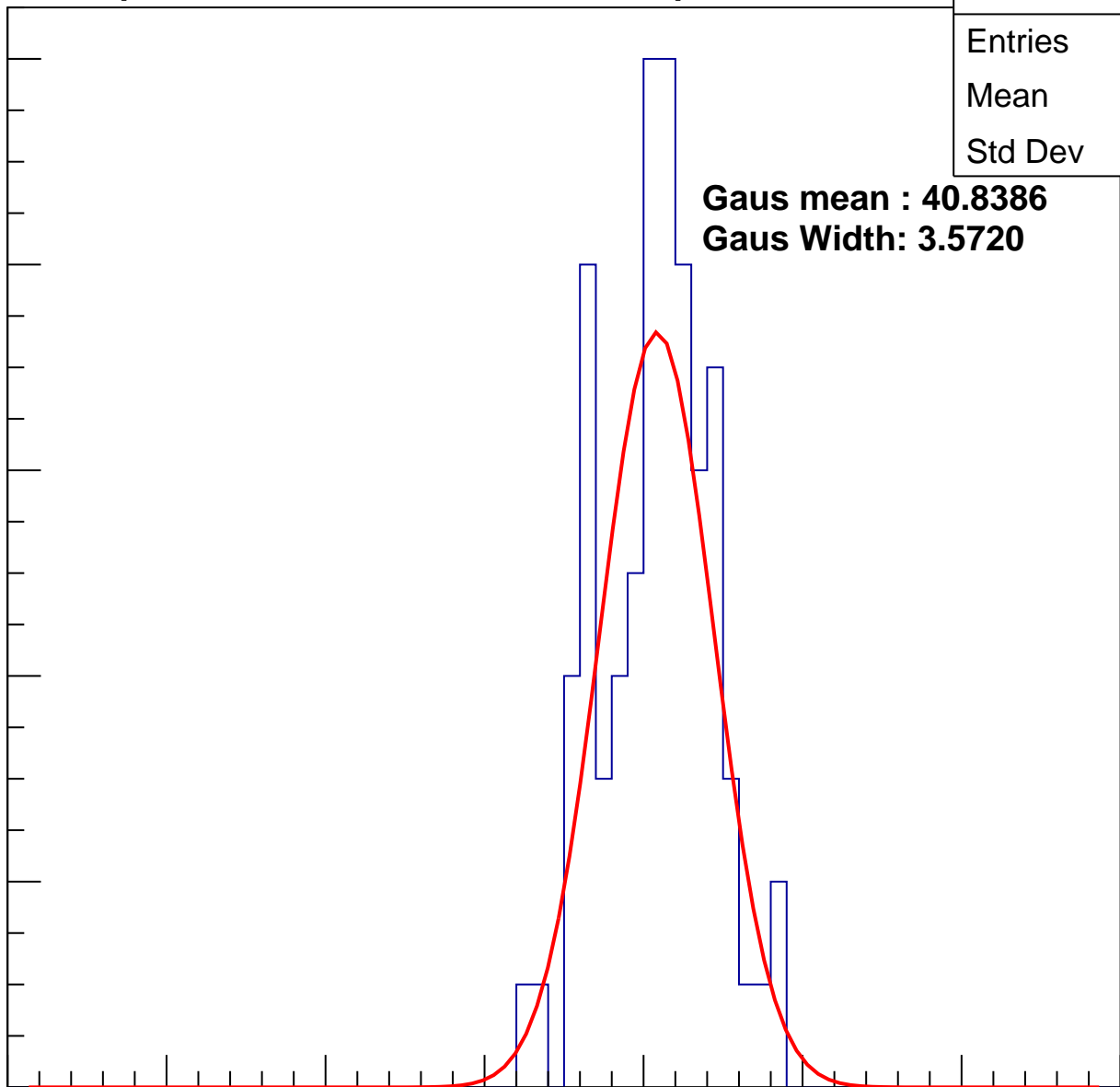
Gaus Width: 3.5720

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

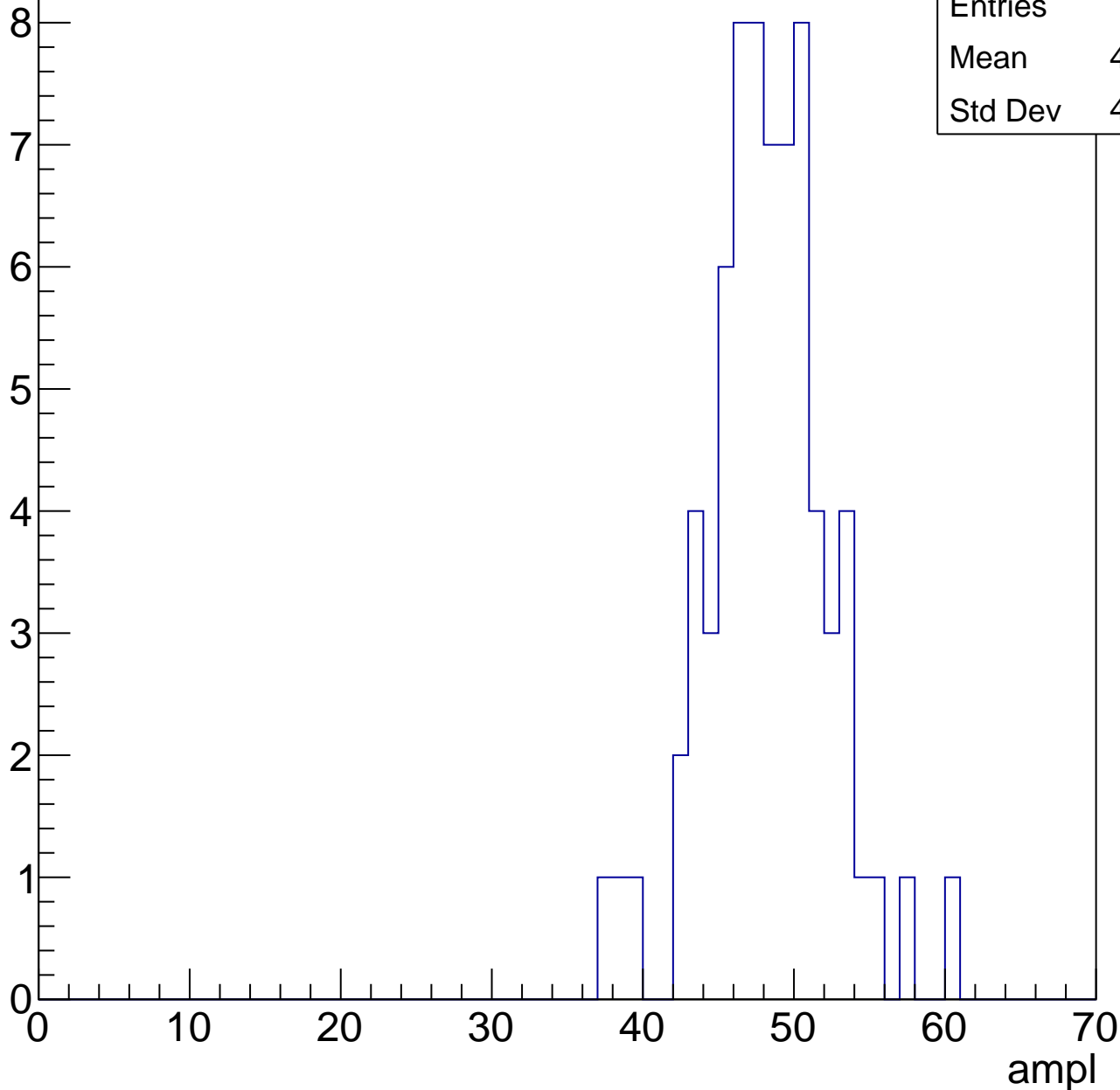


B1L100S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	47.79
Std Dev	4.017

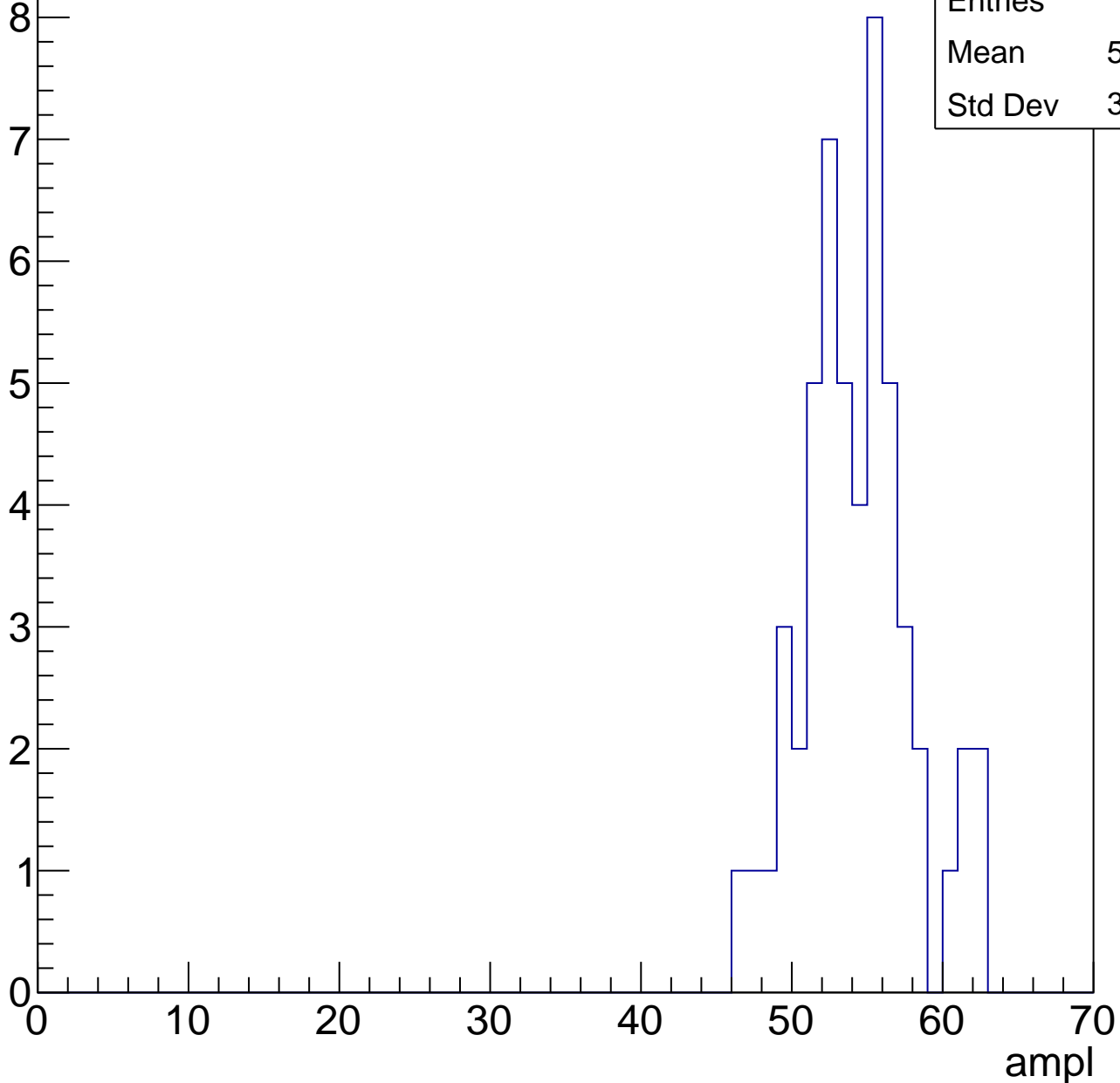


B1L100S, U14-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	53.87
Std Dev	3.622



B1L100S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	57.64
Std Dev	8.121

Entry

10

8

6

4

2

0

0

10

20

30

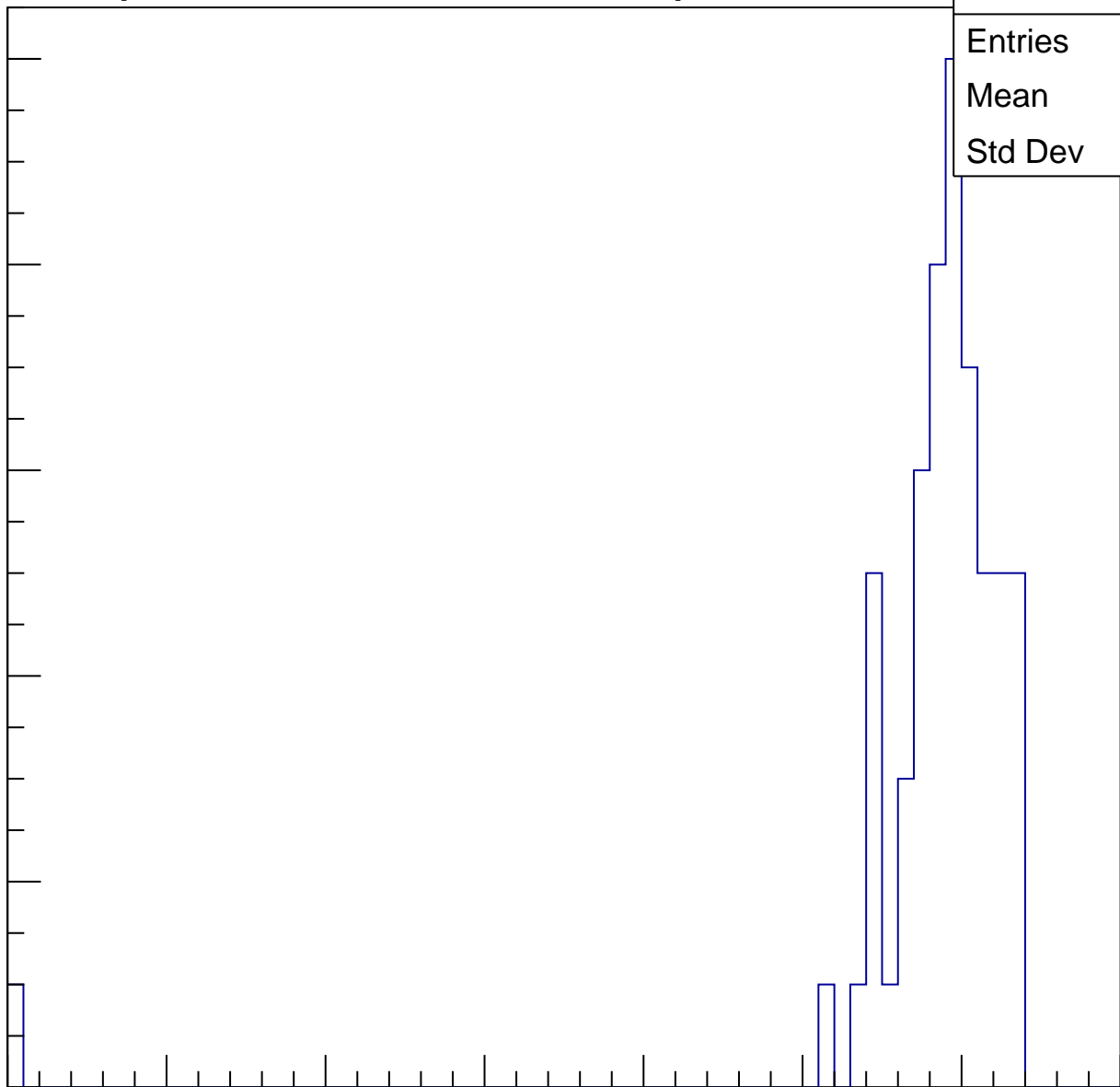
40

50

60

70

ampl



B1L100S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

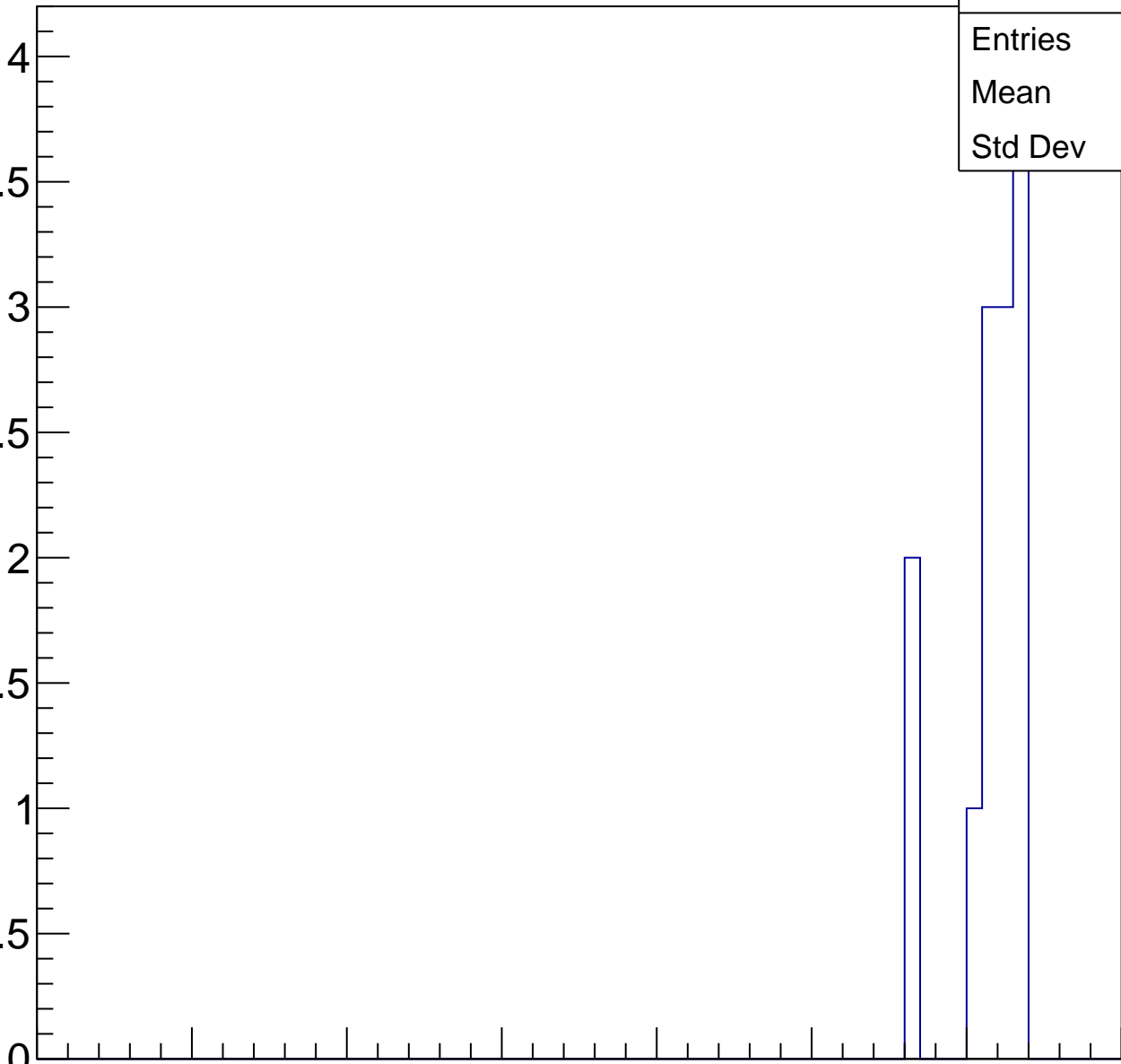
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	13
Mean	61
Std Dev	2.32

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch105, adc0

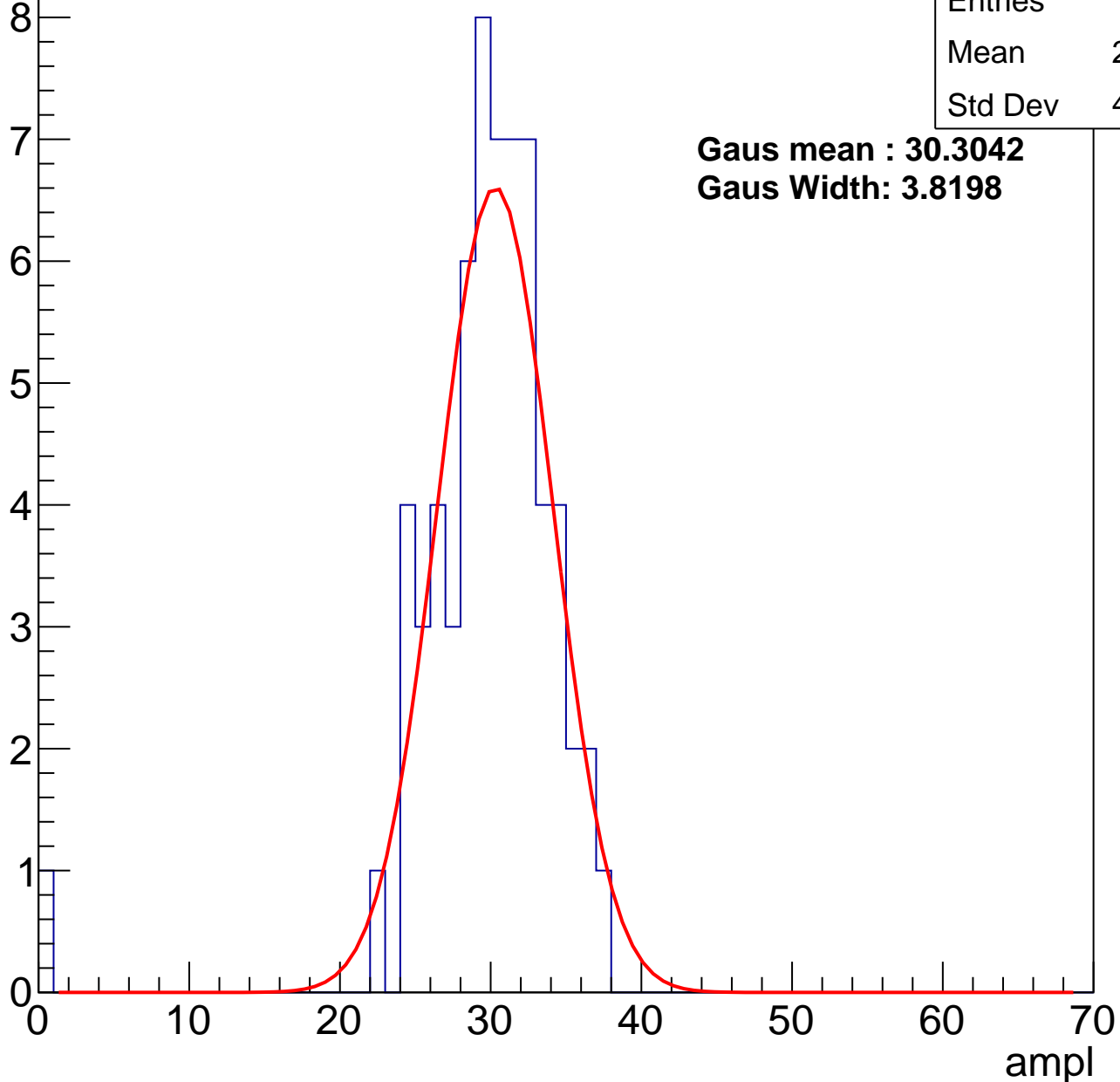
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.31
Std Dev	4.971

Gaus mean : 30.3042

Gaus Width: 3.8198



B1L100S, U14-ch105, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	36.51
Std Dev	3.623

Gaus mean : 36.9136

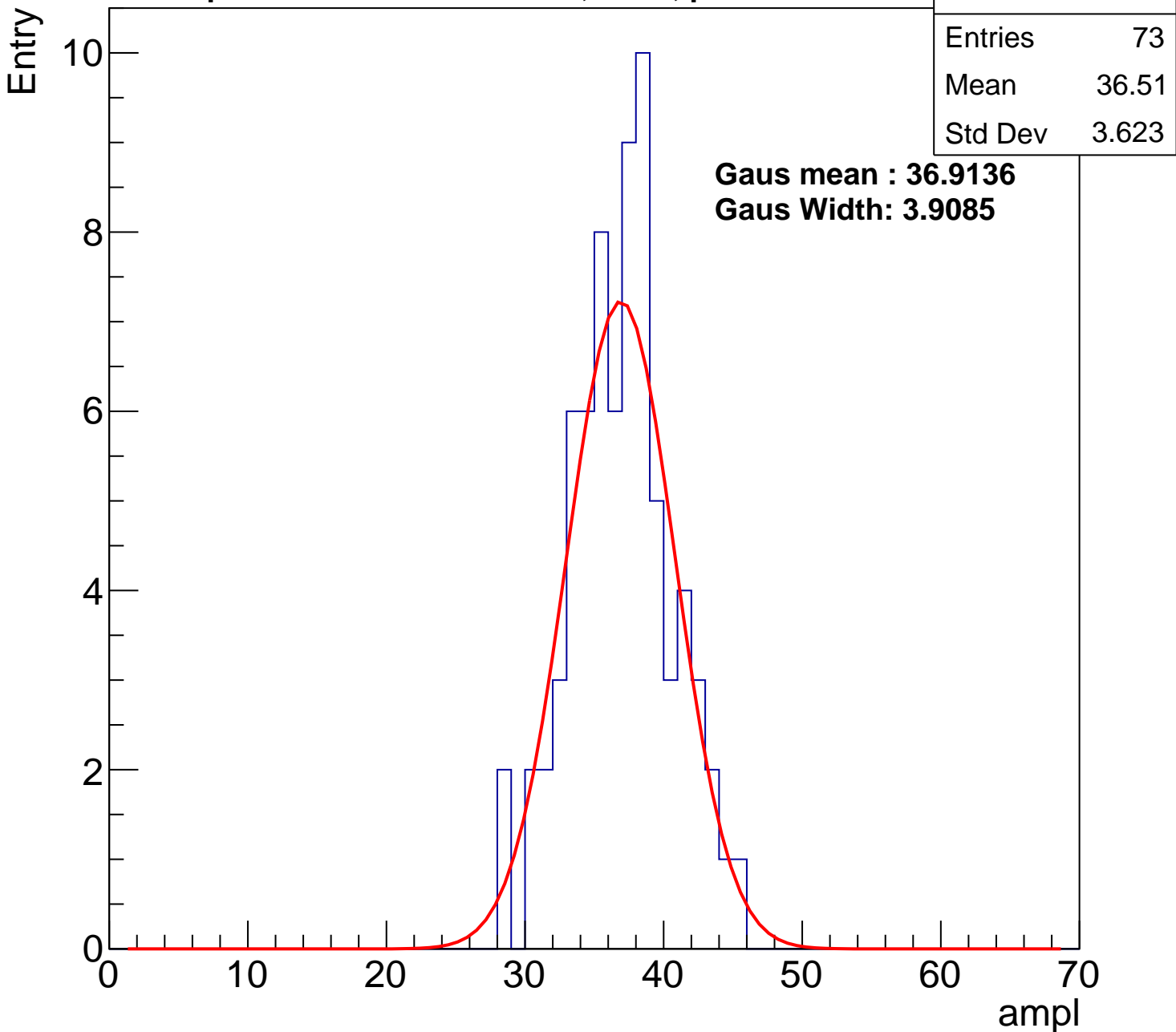
Gaus Width: 3.9085

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch105, adc2

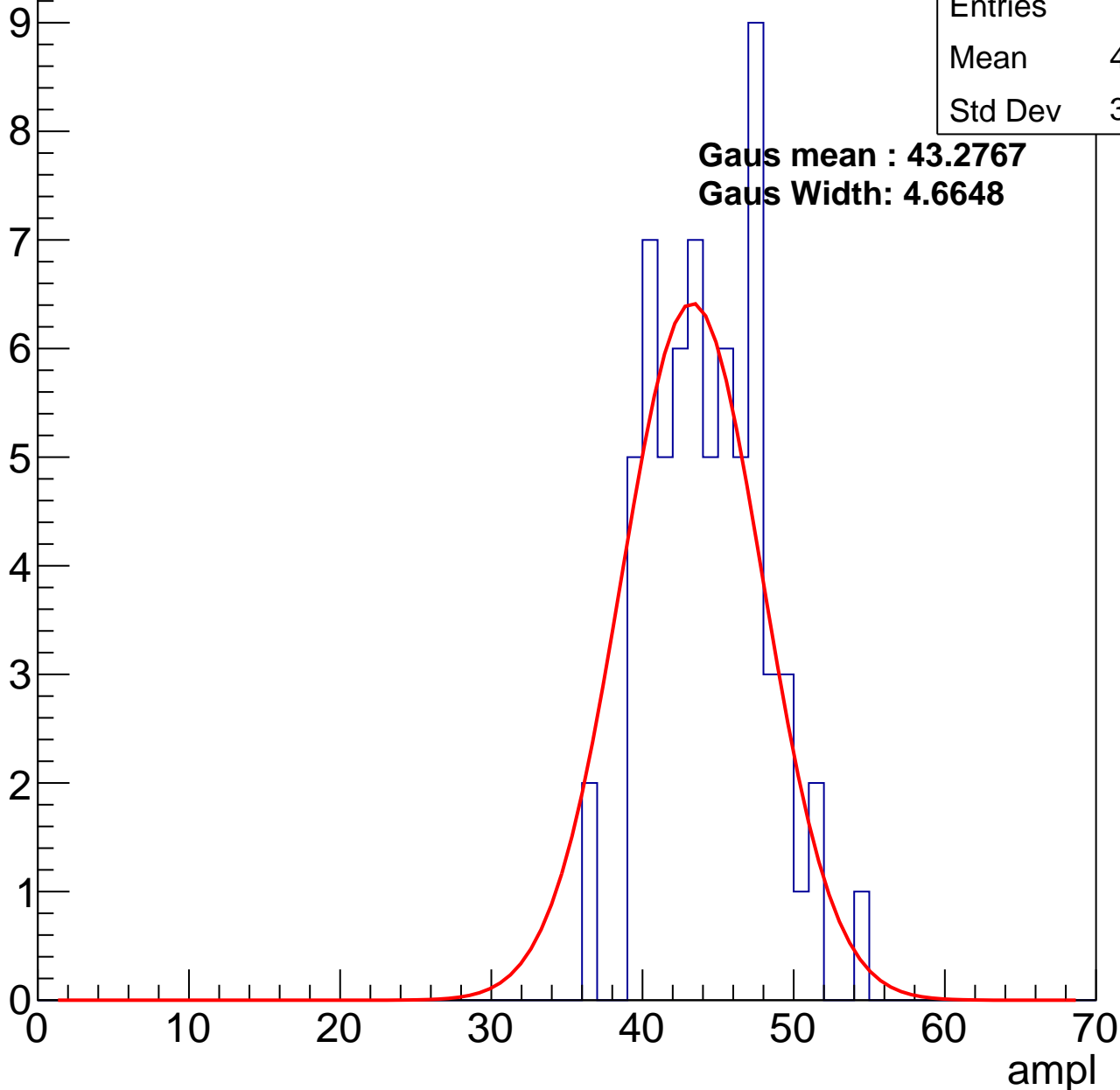
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.96
Std Dev	3.675

Gaus mean : 43.2767

Gaus Width: 4.6648

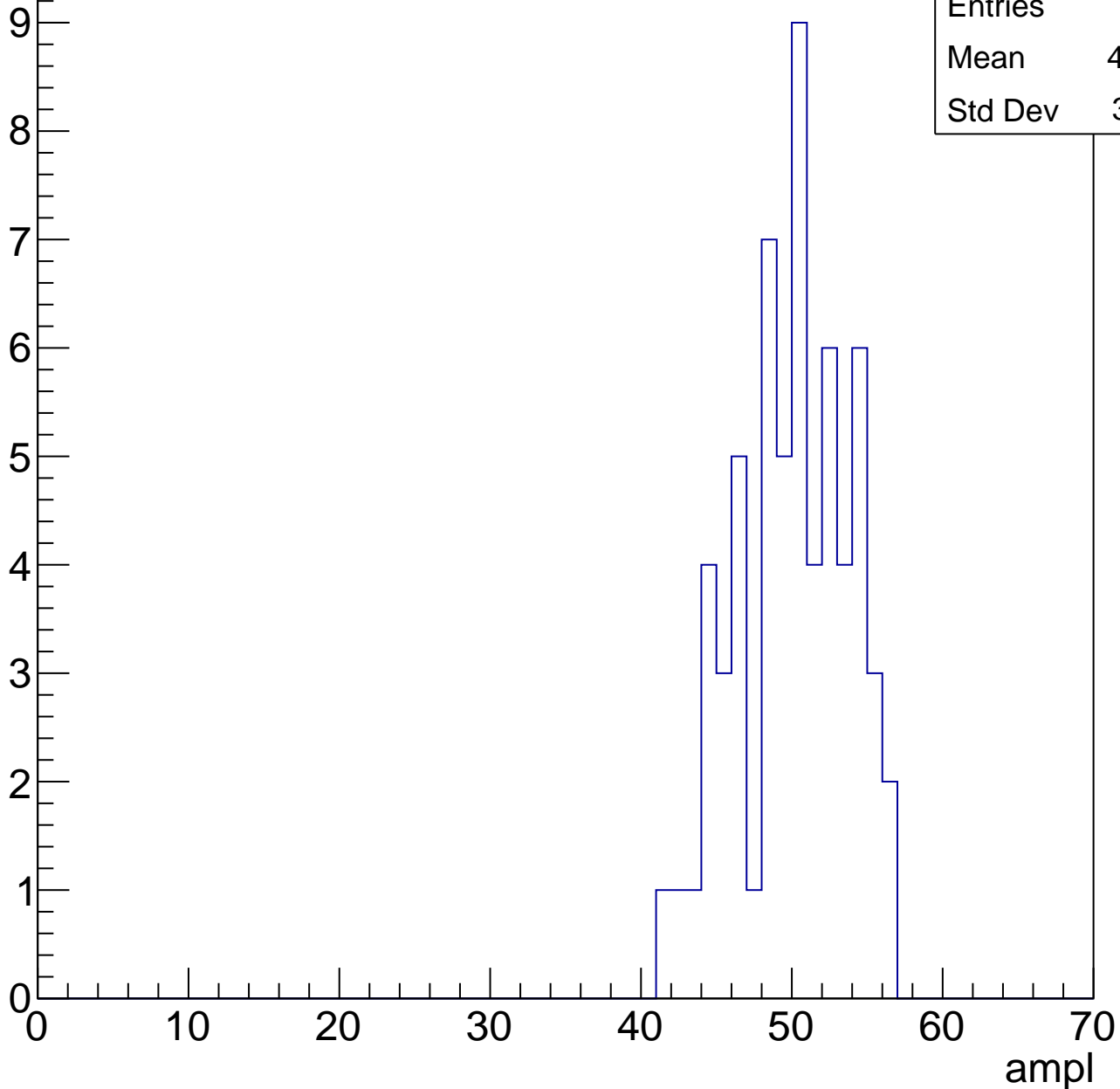


B1L100S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

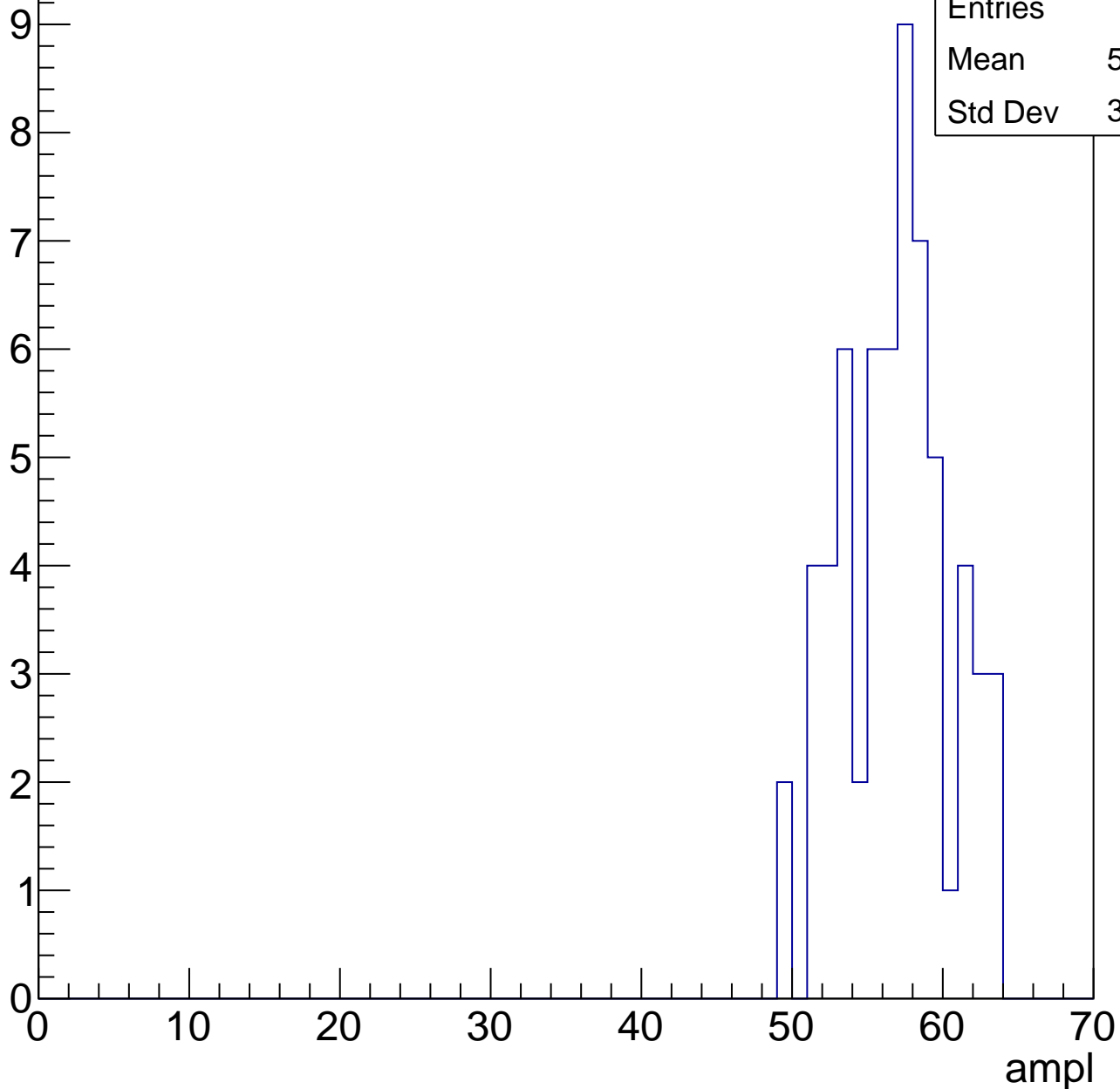
Entries	62
Mean	49.58
Std Dev	3.661



B1L100S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



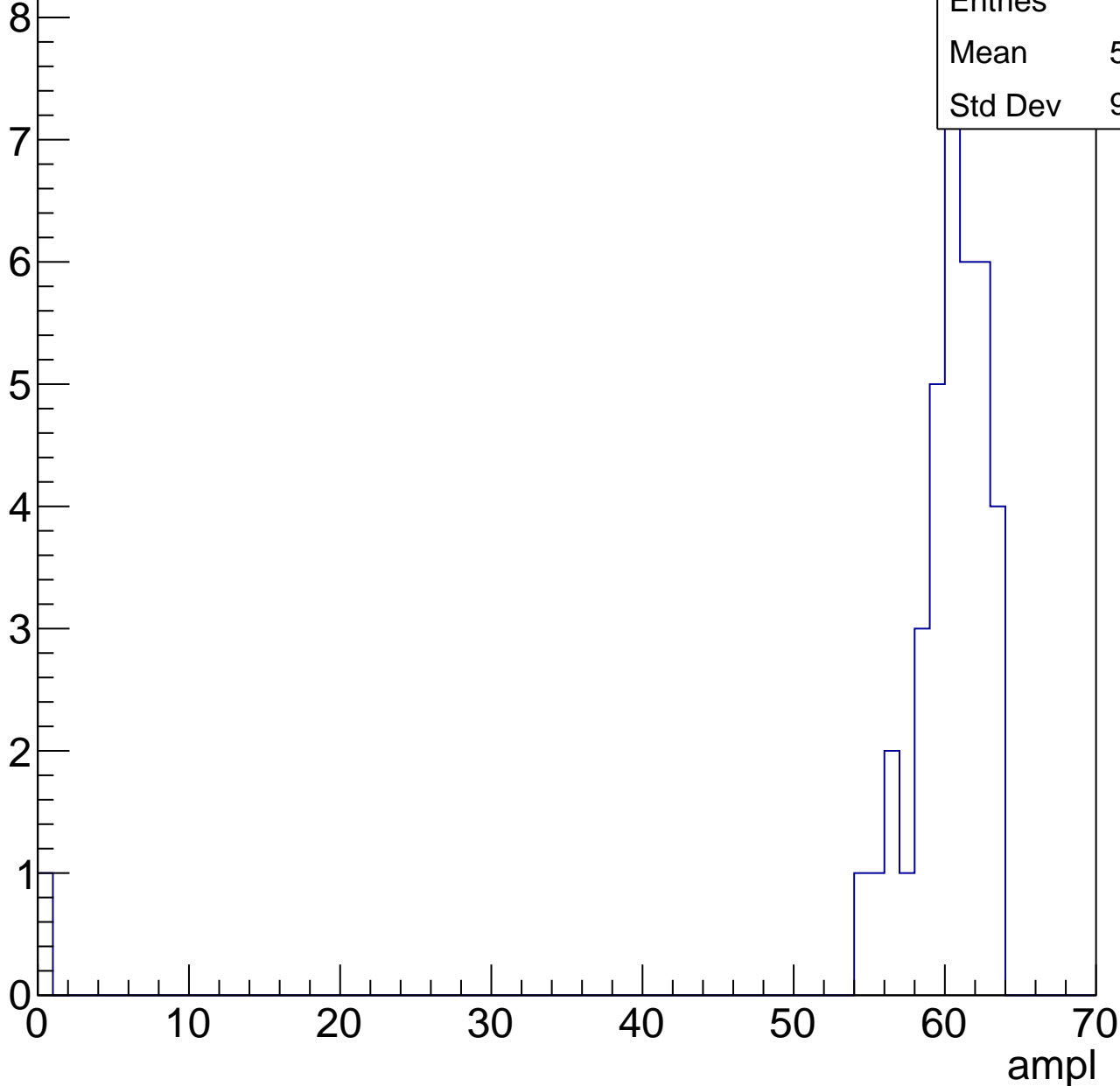
Entries	62
Mean	56.37
Std Dev	3.534

B1L100S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.34
Std Dev	9.842



B1L100S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	62.2
Std Dev	1.166

B1L100S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch106, adc0

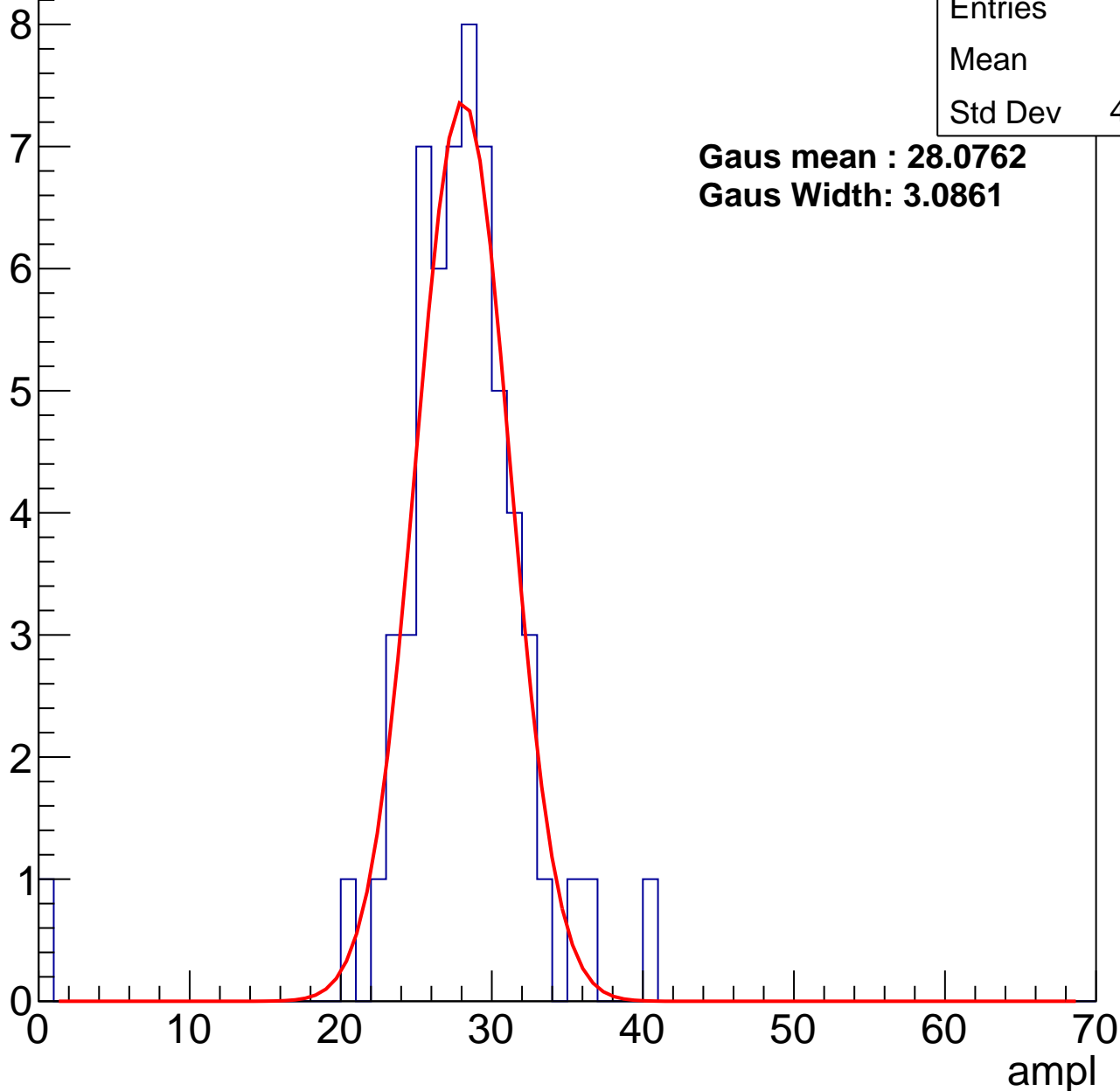
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	27.4
Std Dev	4.954

Gaus mean : 28.0762

Gaus Width: 3.0861



B1L100S, U14-ch106, adc1

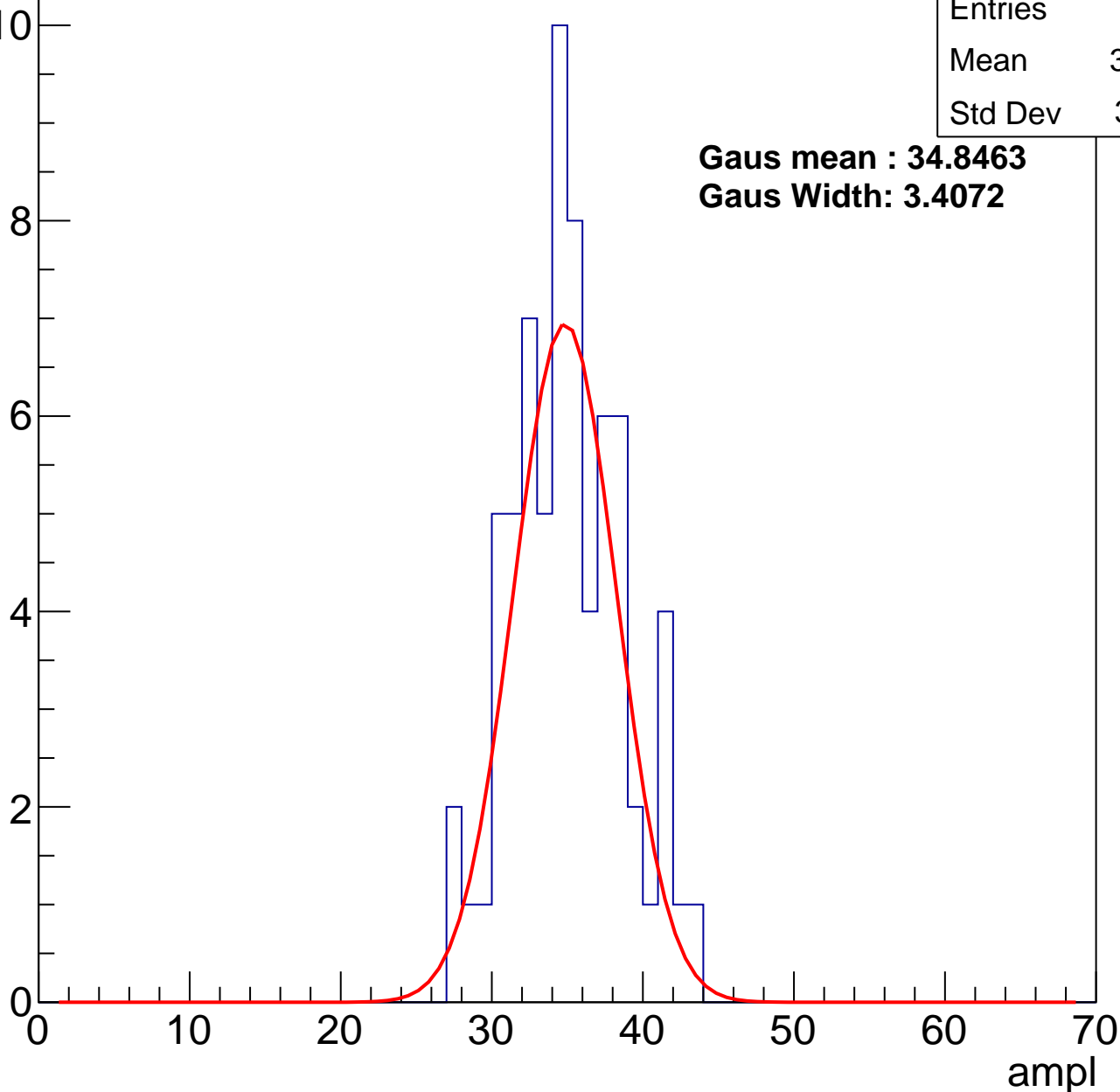
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.58
Std Dev	3.601

Gaus mean : 34.8463

Gaus Width: 3.4072



B1L100S, U14-ch106, adc2

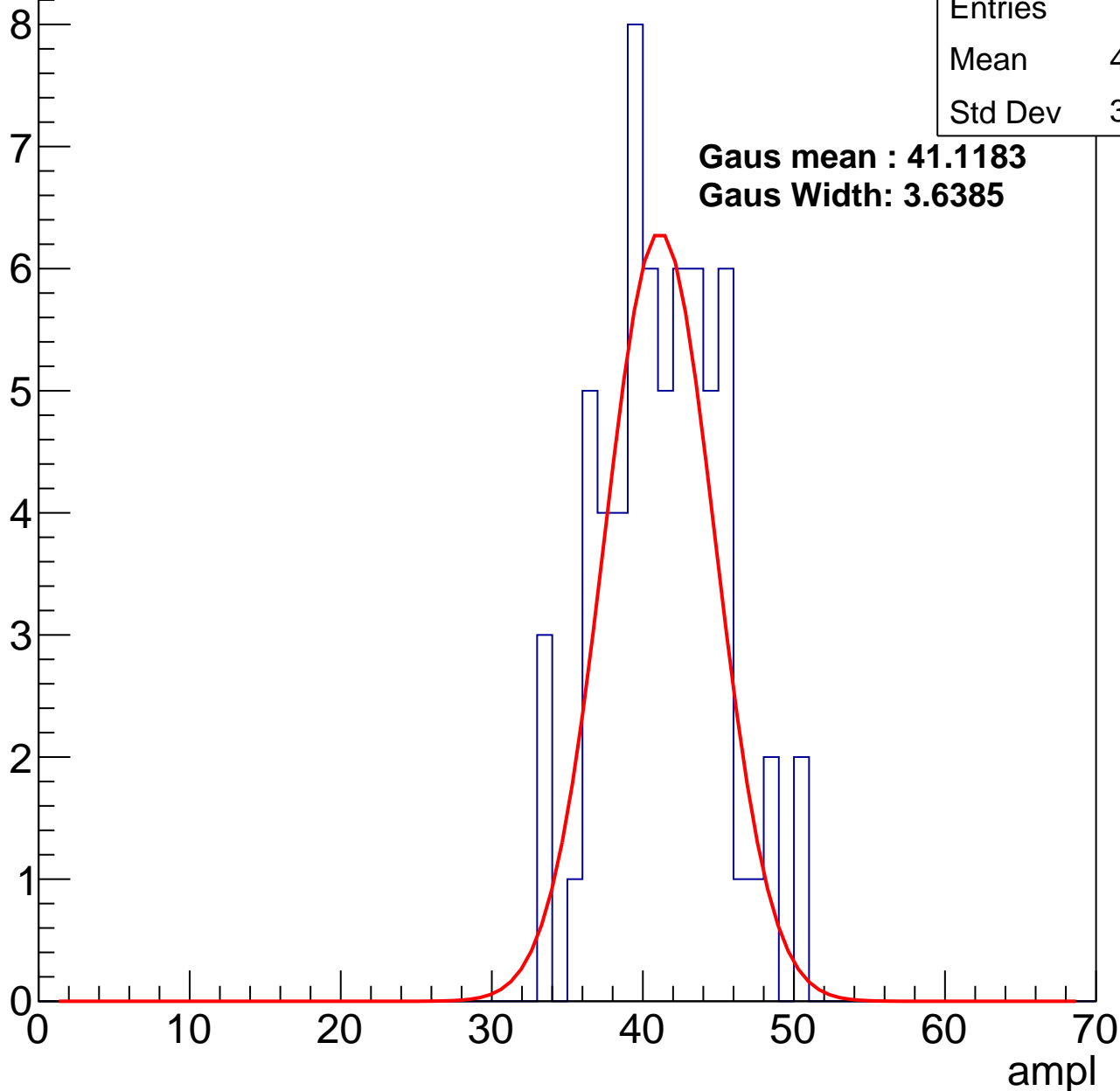
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	40.92
Std Dev	3.888

Gaus mean : 41.1183

Gaus Width: 3.6385

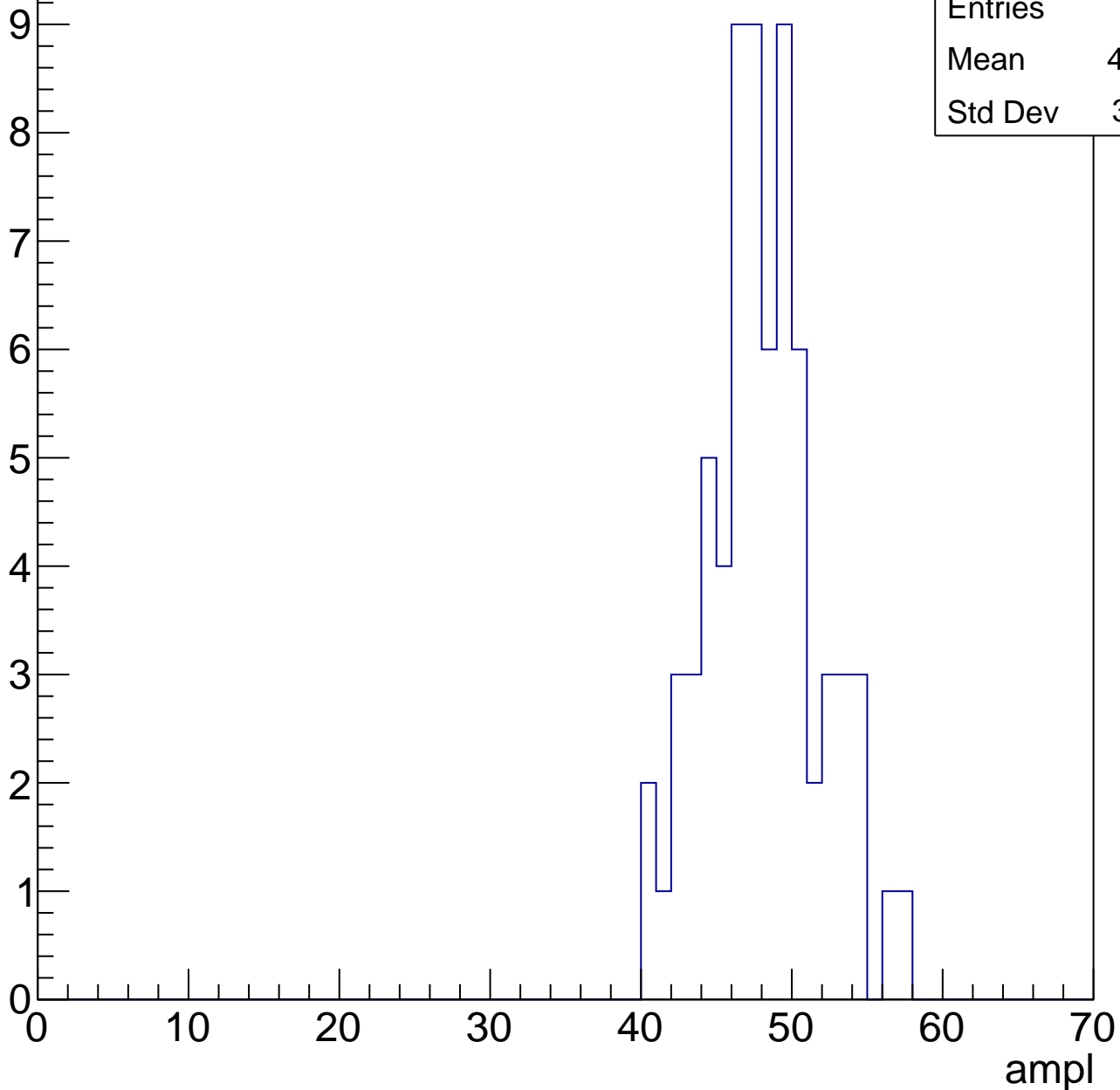


B1L100S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.63
Std Dev	3.661

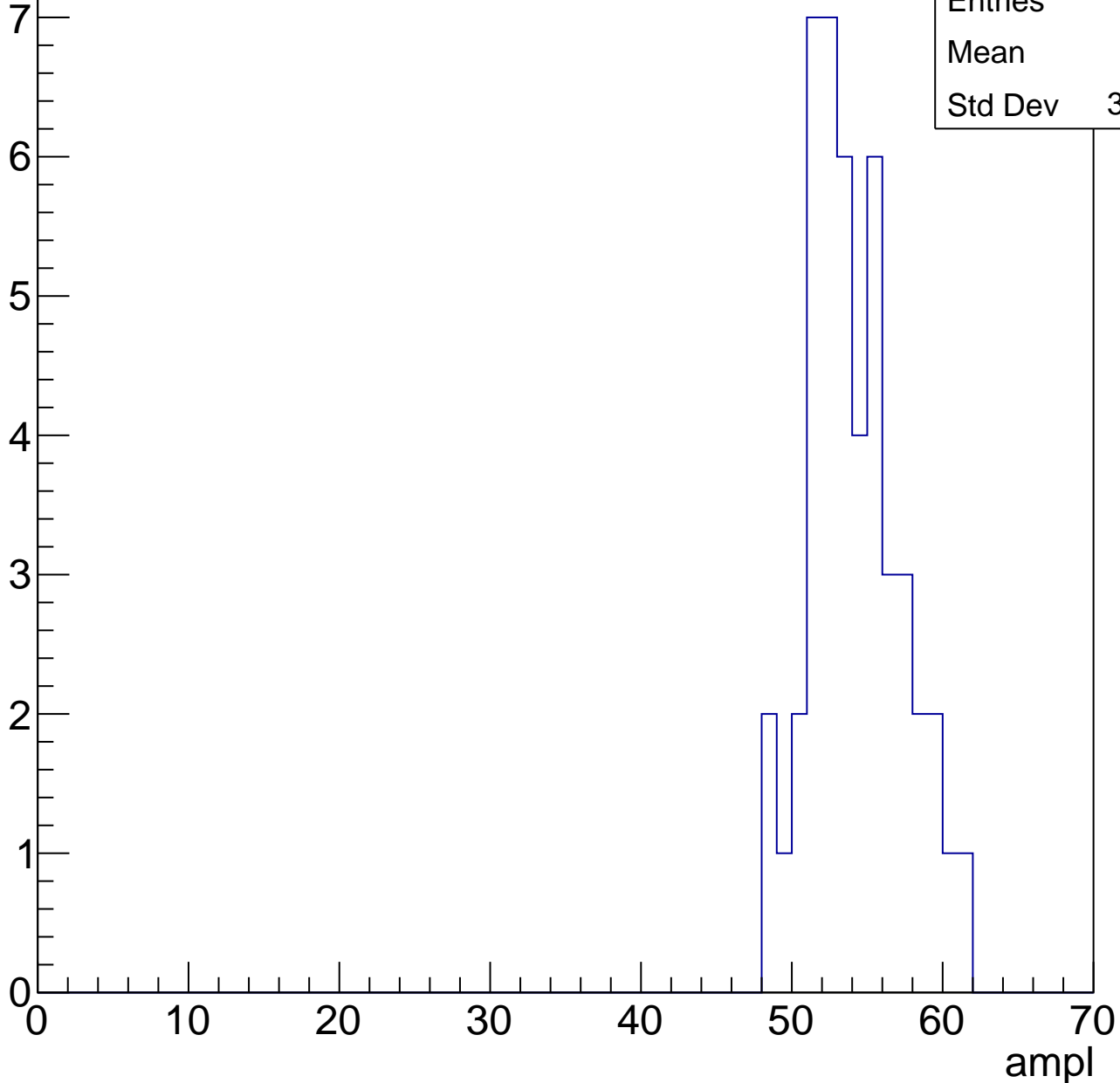


B1L100S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	53.7
Std Dev	3.038

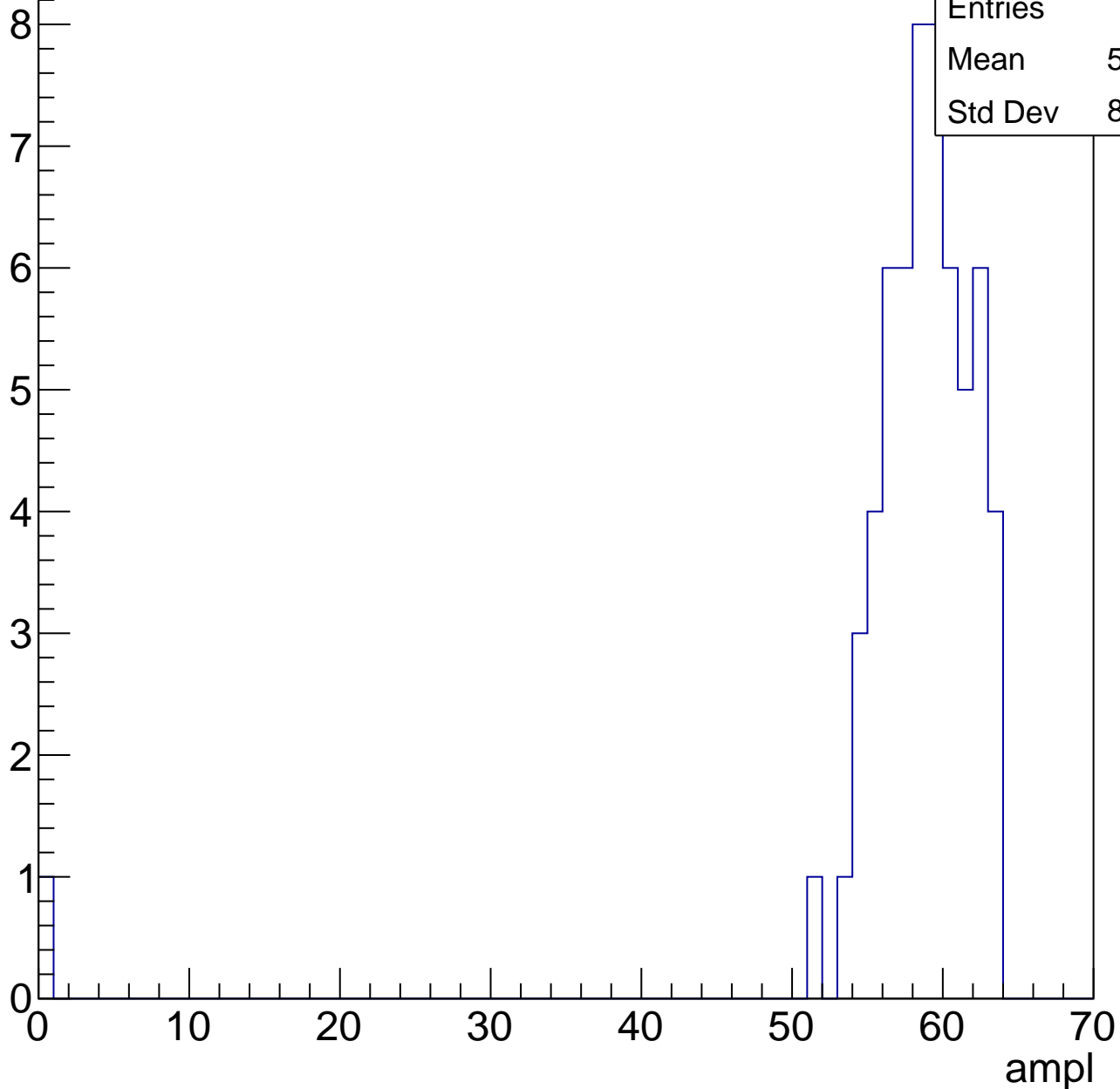


B1L100S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	57.44
Std Dev	8.032

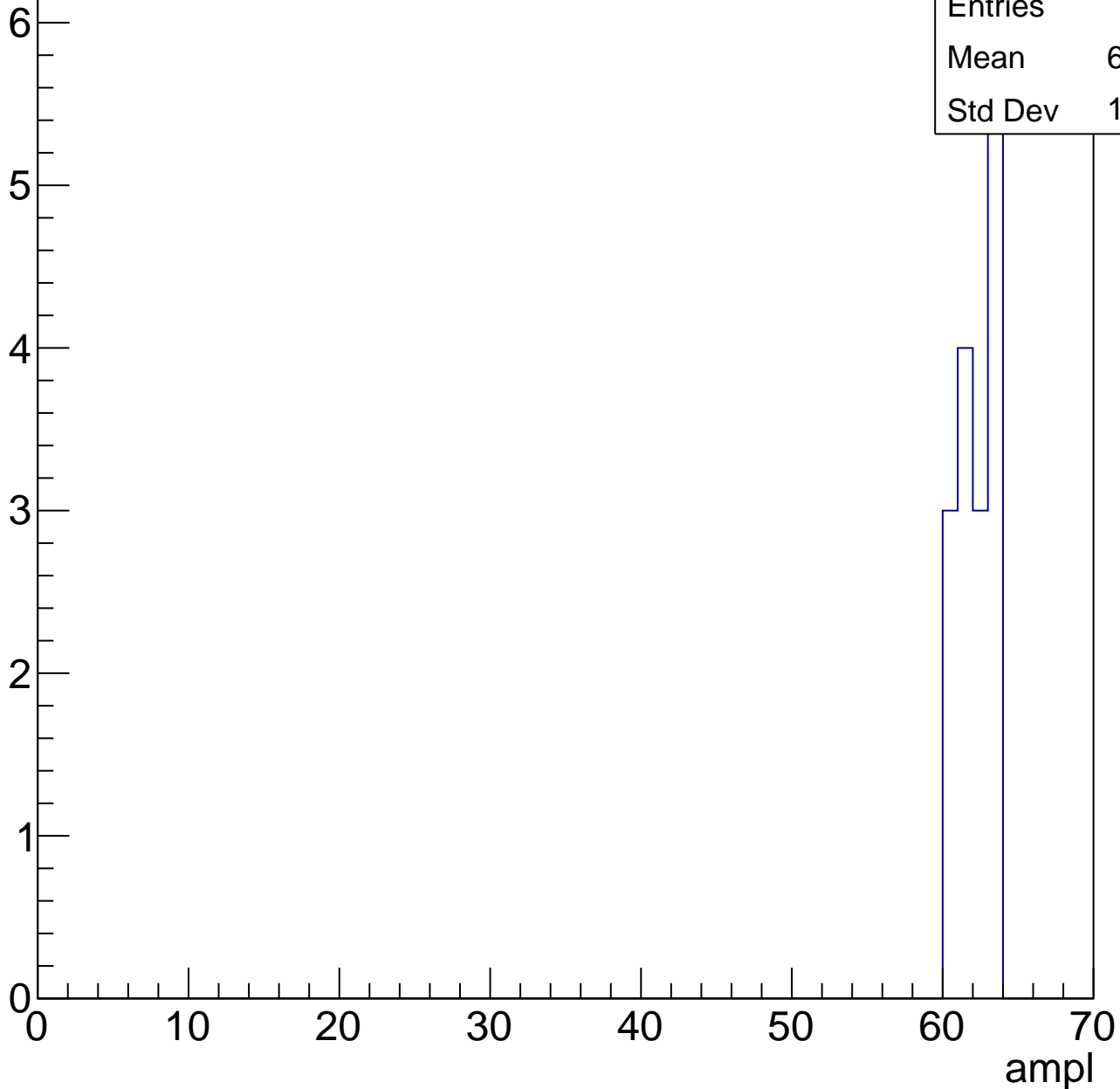


B1L100S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

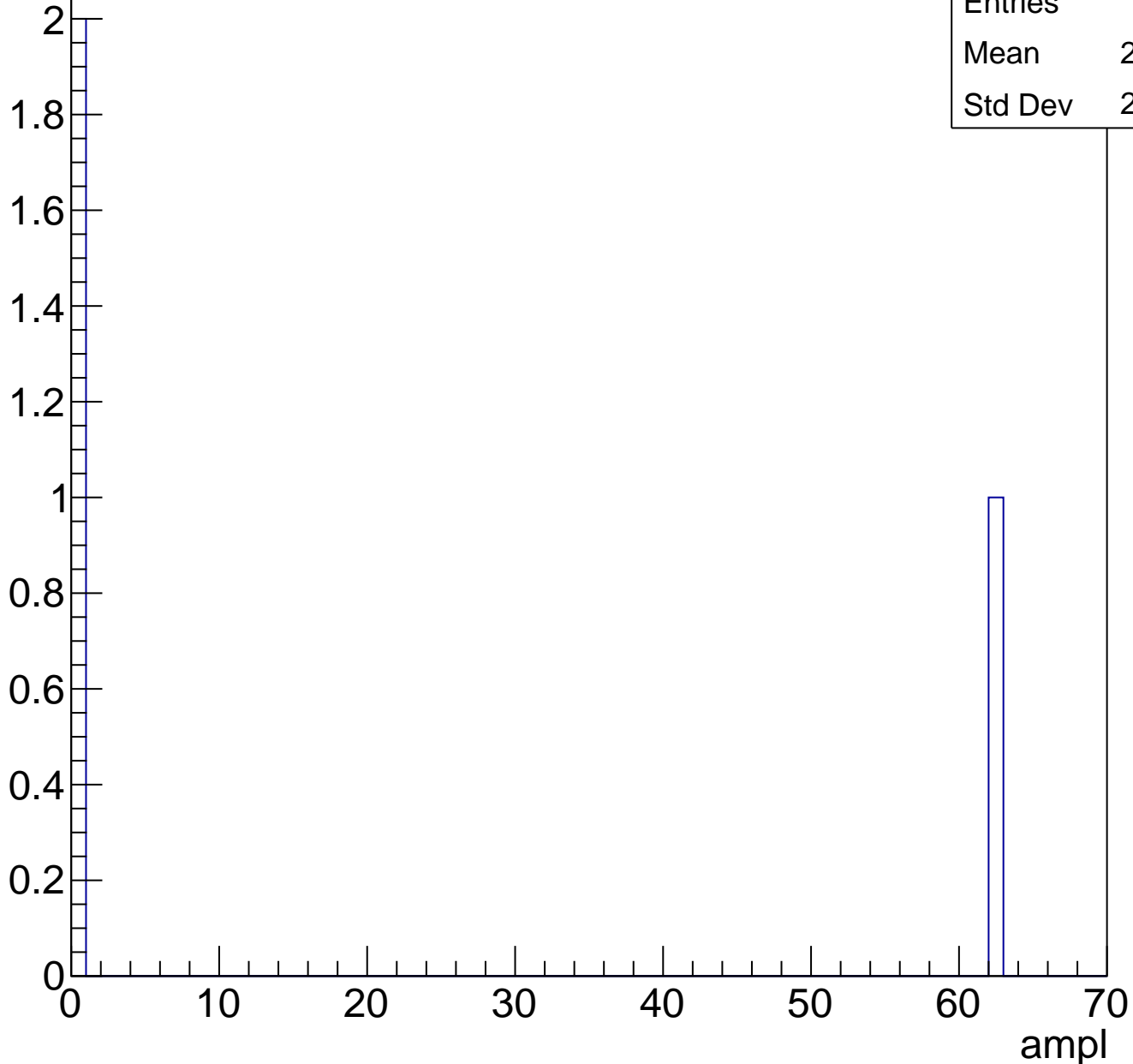
Entries	16
Mean	61.75
Std Dev	1.146



B1L100S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L100S, U14-ch107, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	28.58
Std Dev	4.856

Gaus mean : 29.0359

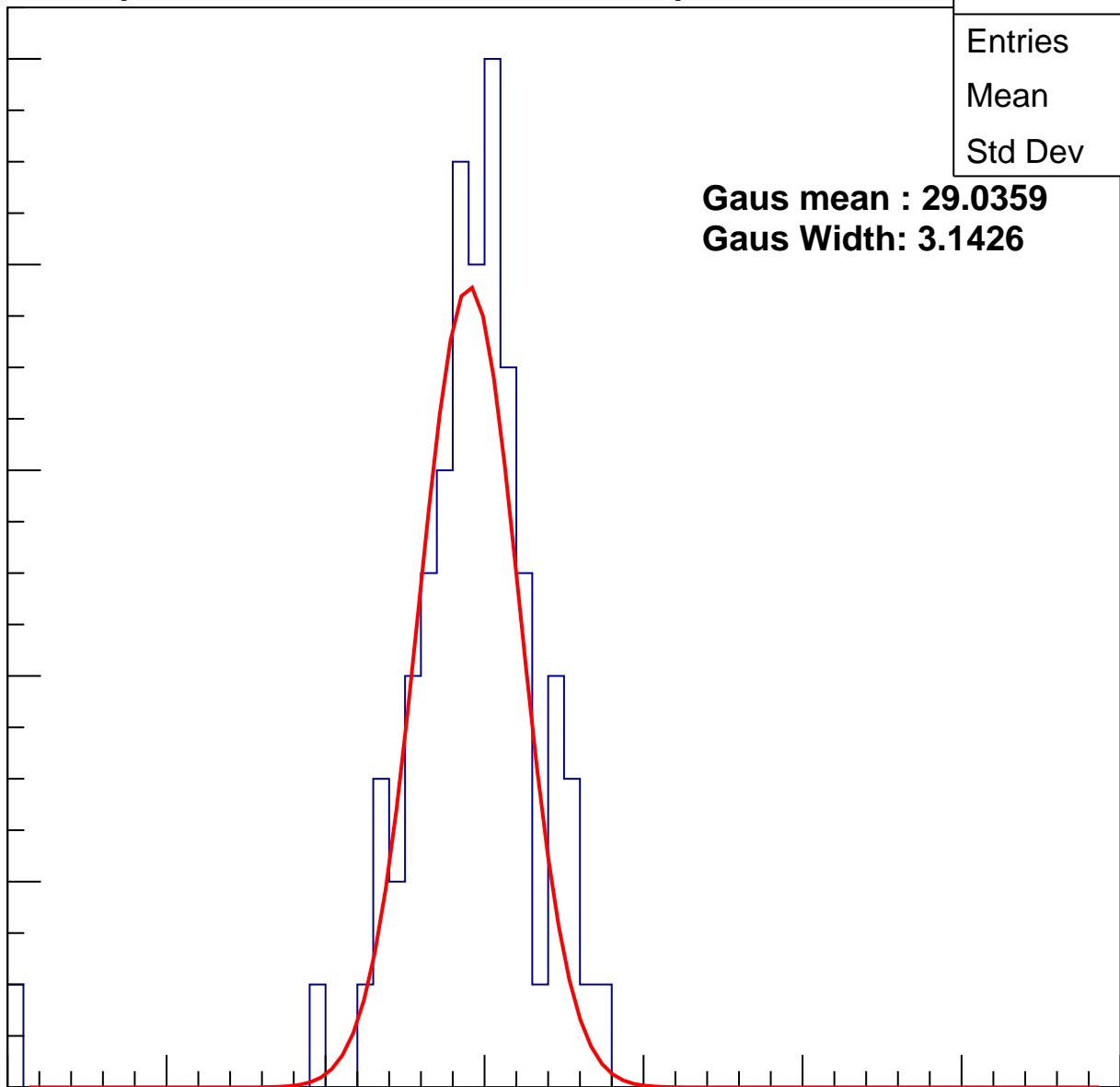
Gaus Width: 3.1426

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch107, adc1

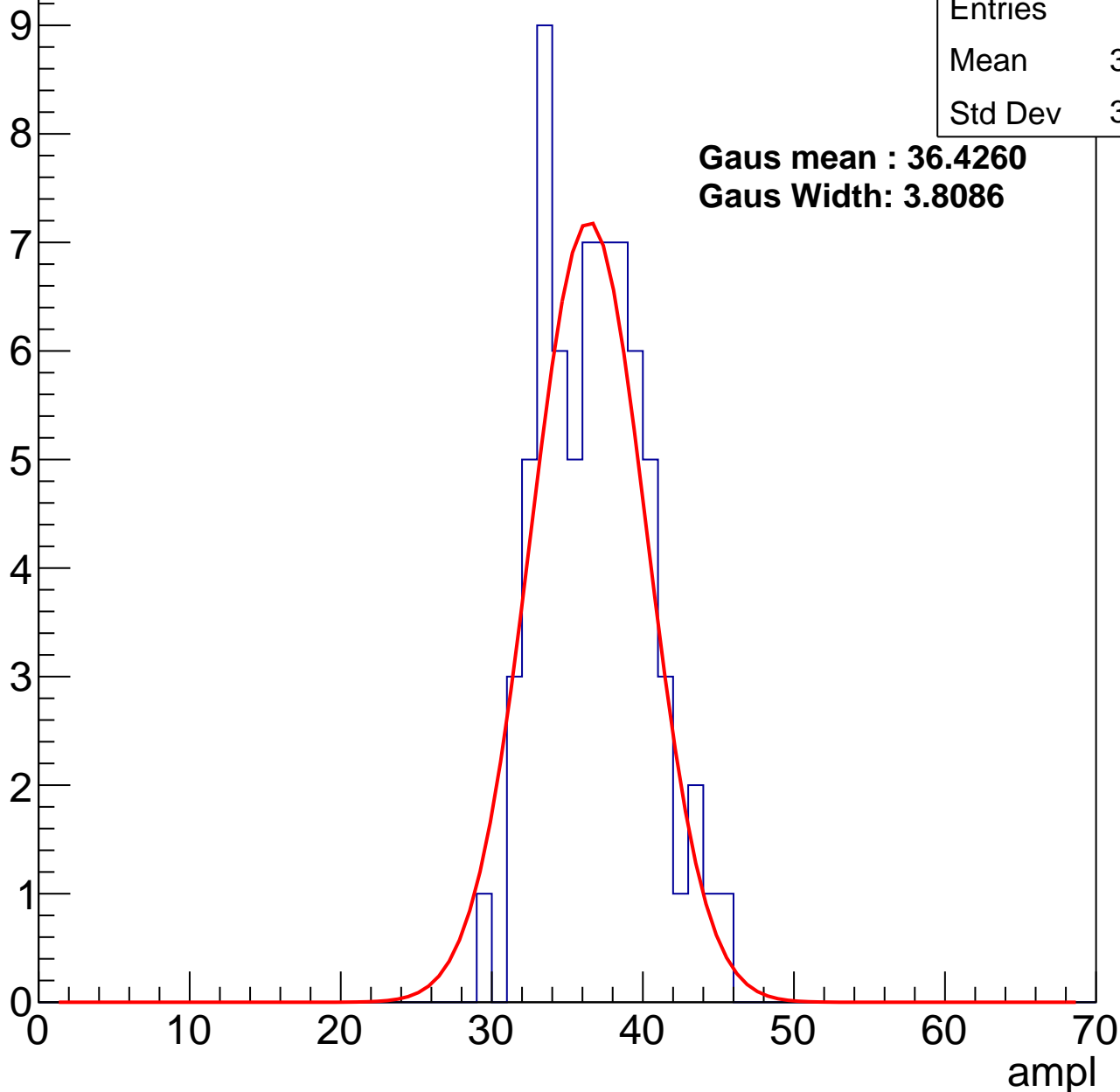
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.36
Std Dev	3.464

Gaus mean : 36.4260

Gaus Width: 3.8086



B1L100S, U14-ch107, adc2

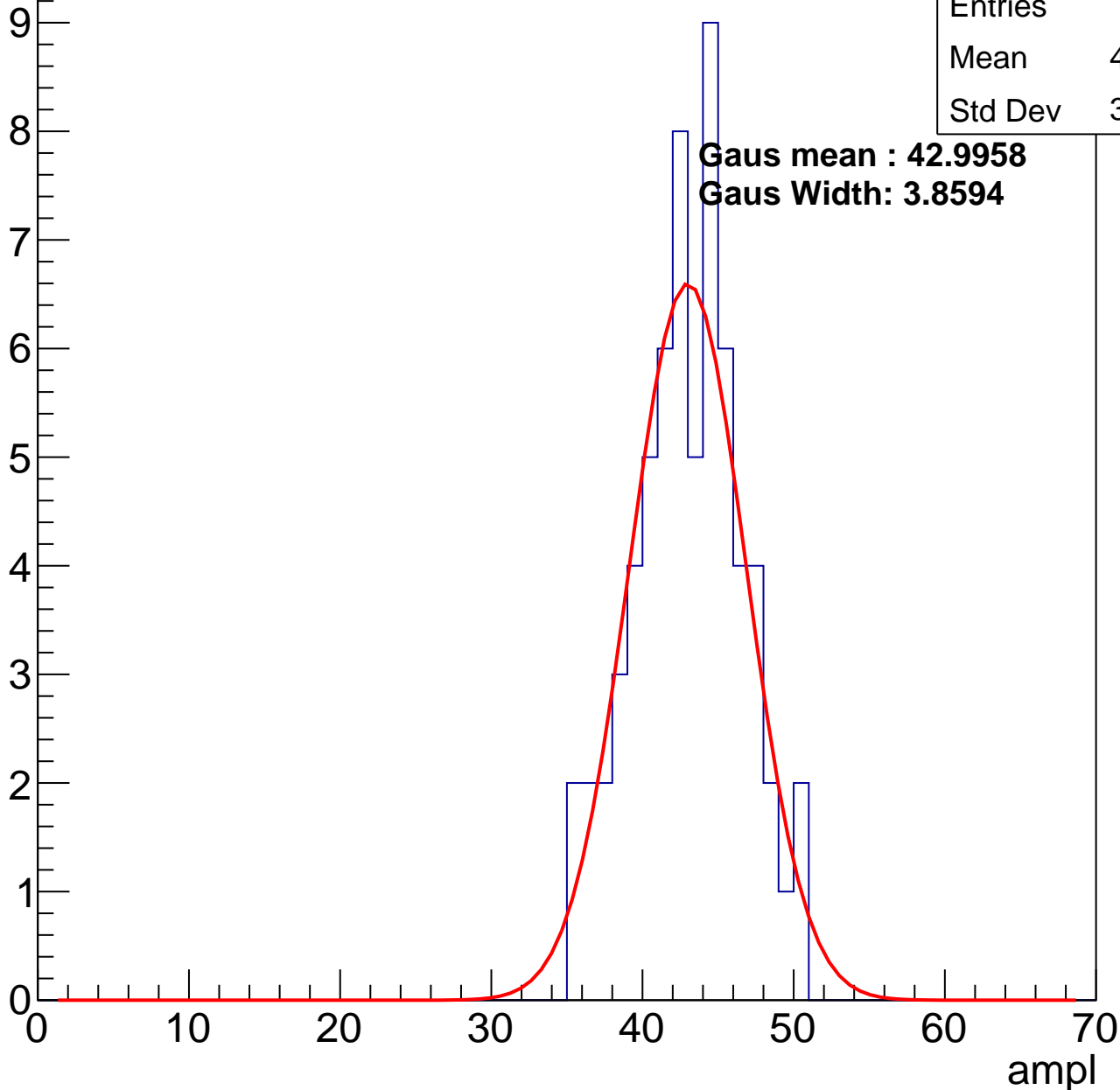
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.55
Std Dev	3.548

Gaus mean : 42.9958

Gaus Width: 3.8594

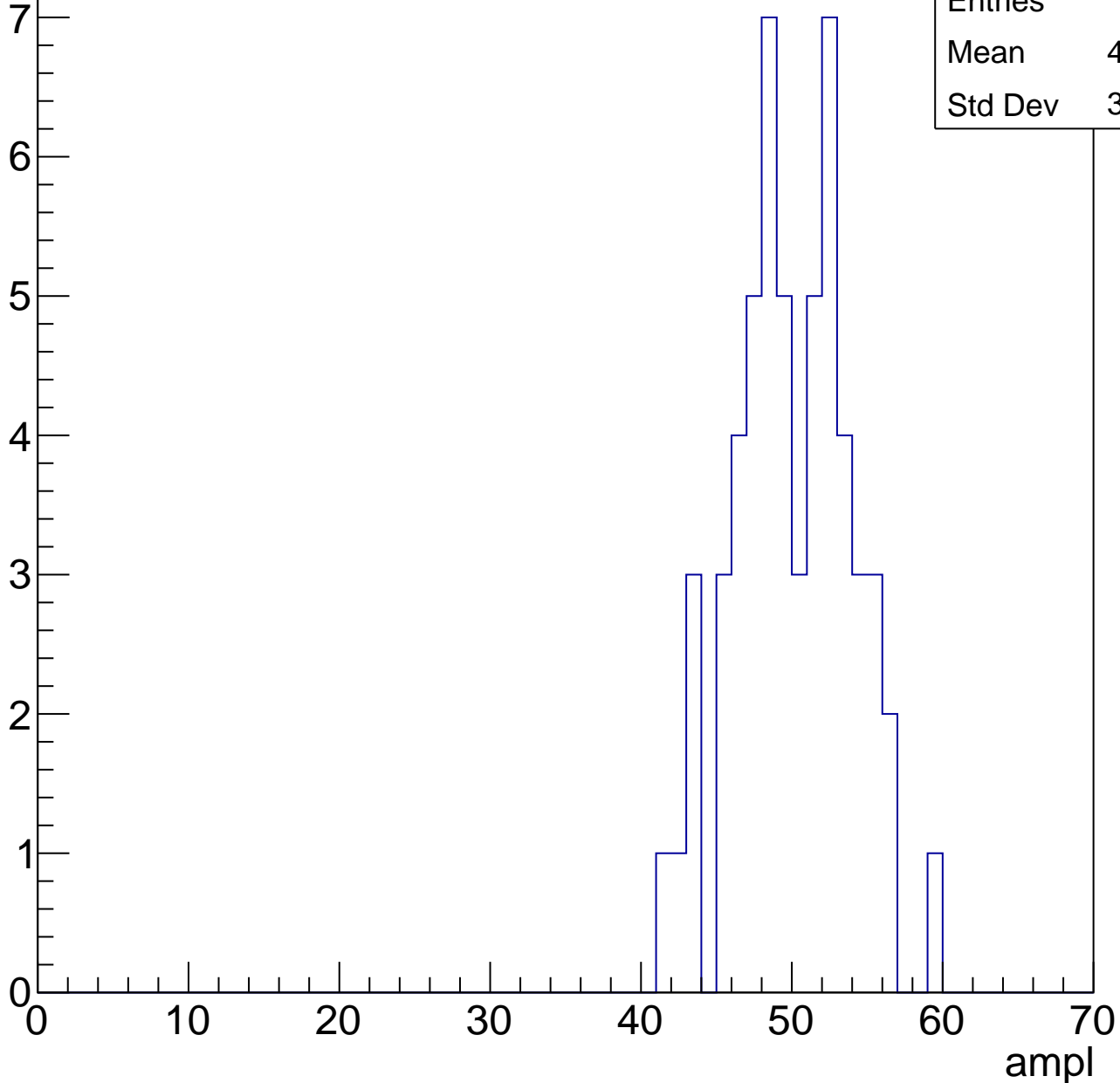


B1L100S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

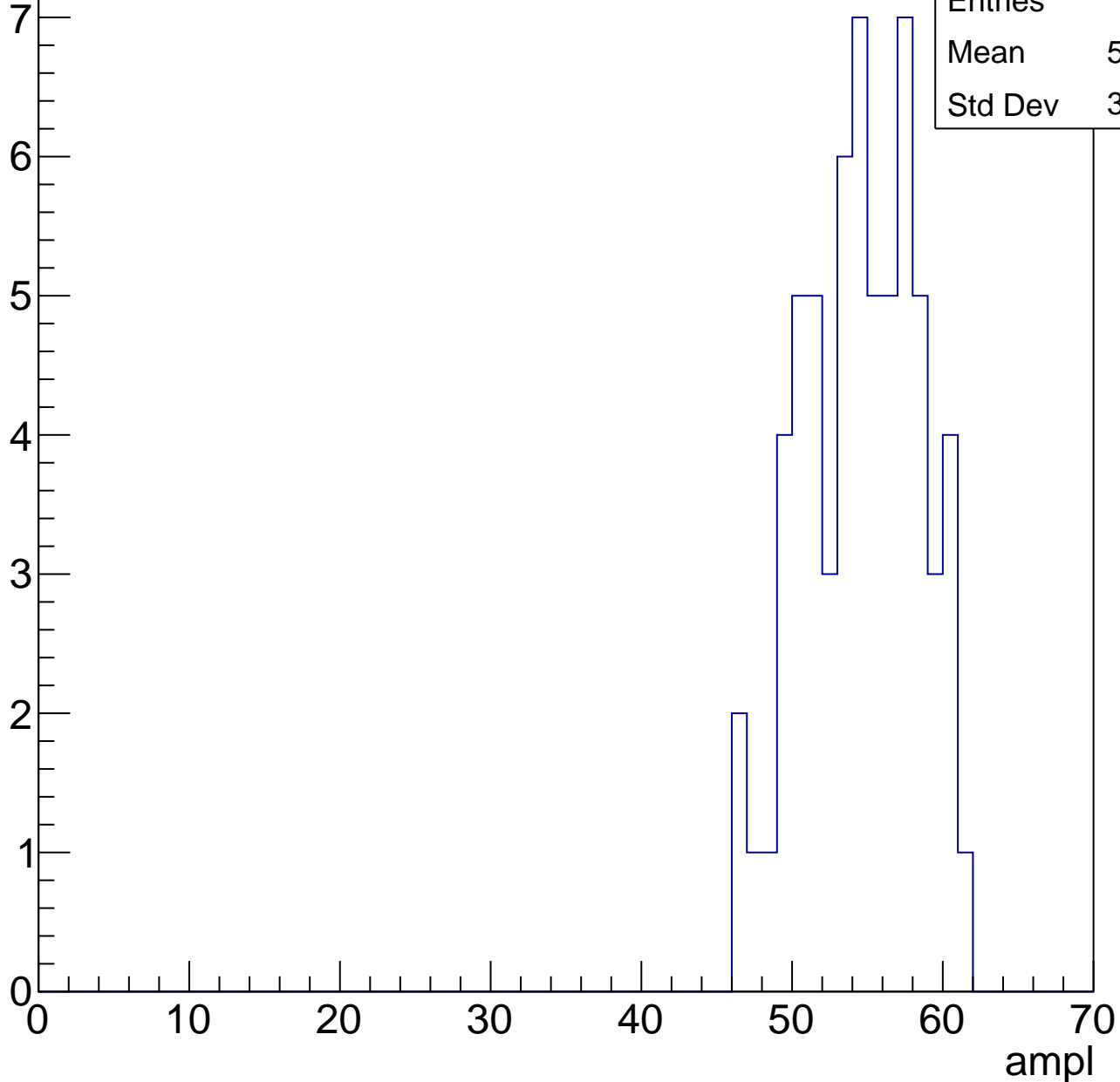
Entries	57
Mean	49.58
Std Dev	3.848



B1L100S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



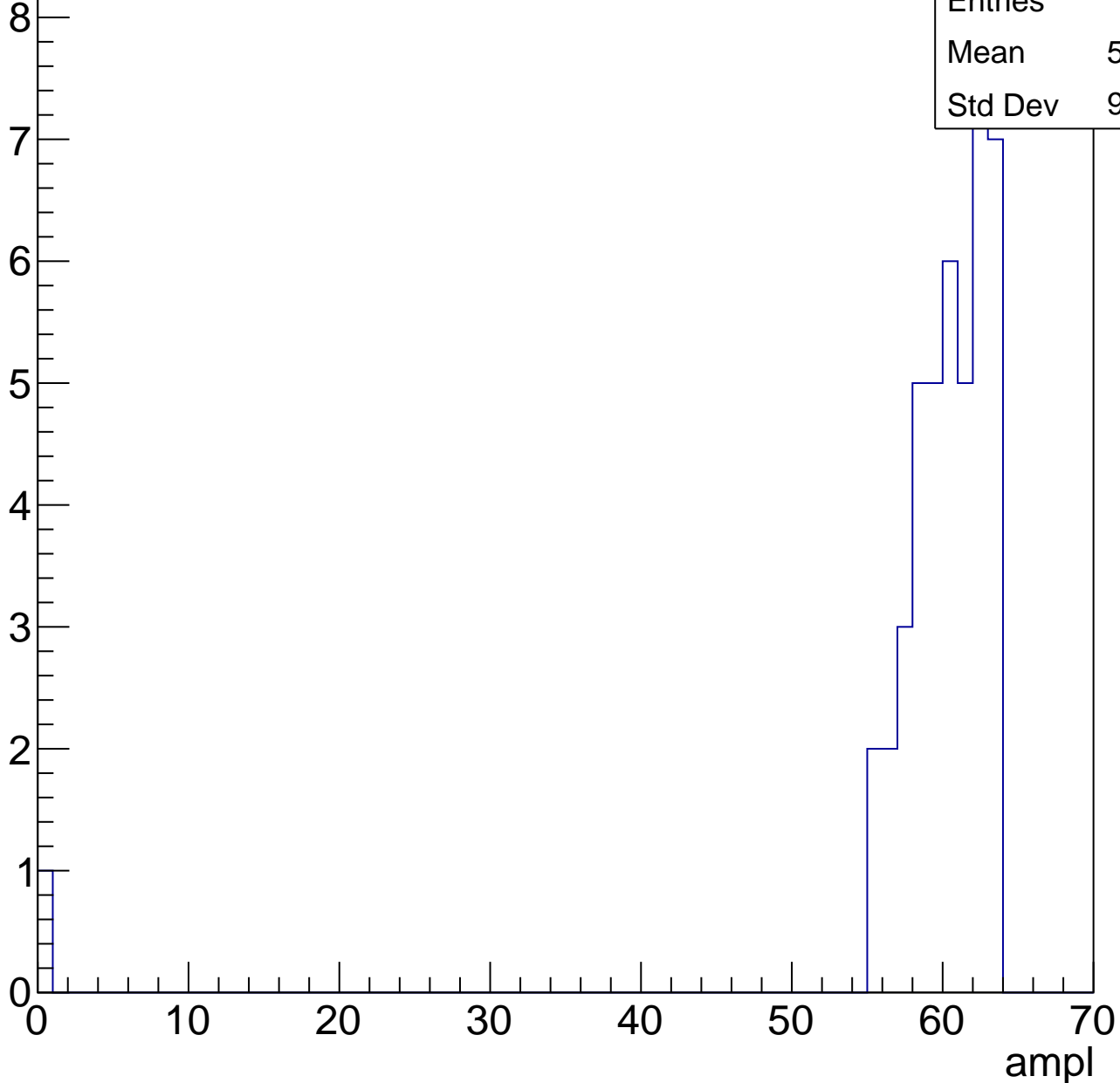
Entries	64
Mean	54.09
Std Dev	3.732

B1L100S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

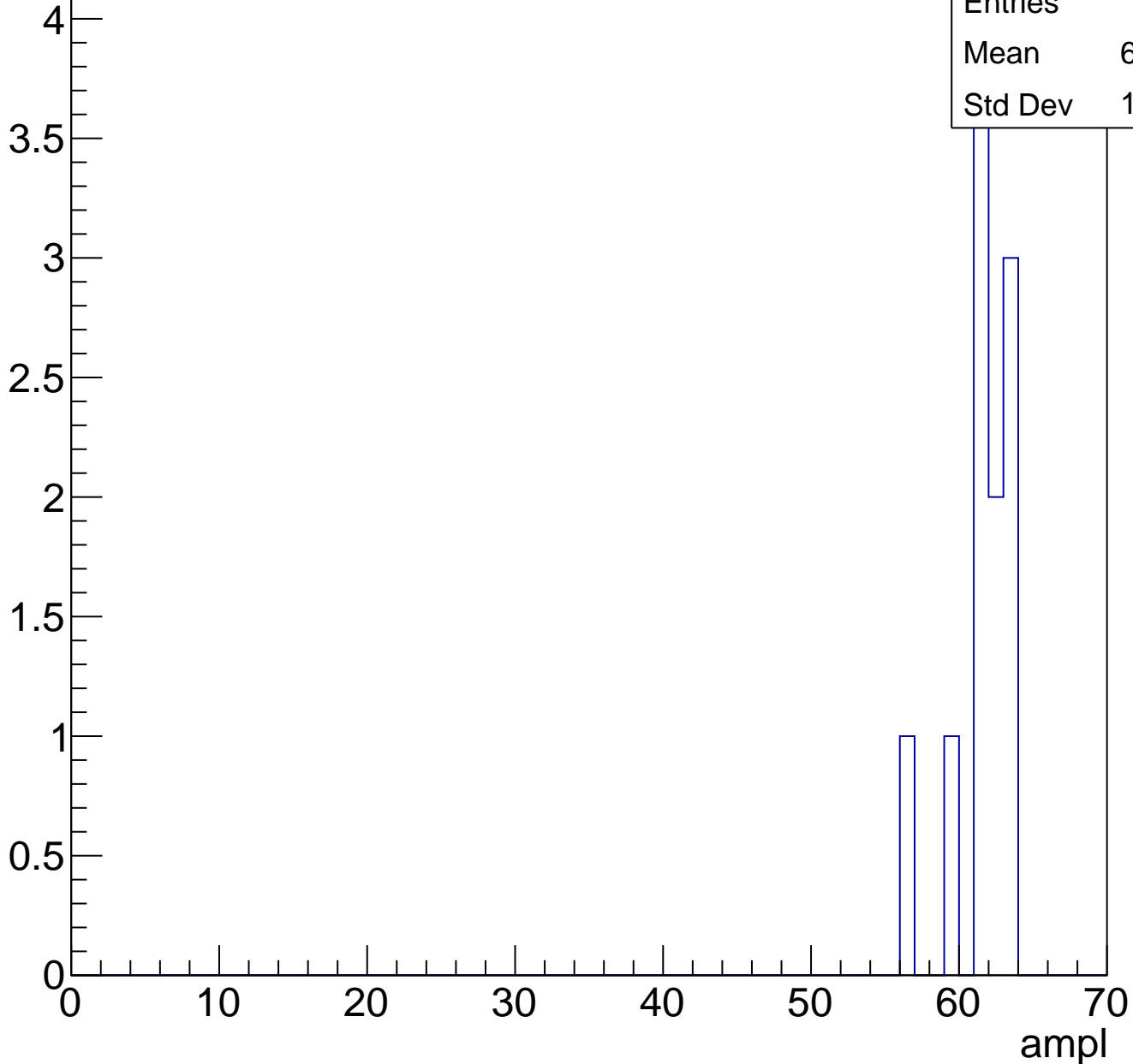
Entries	44
Mean	58.64
Std Dev	9.235



B1L100S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch108, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	93
Mean	26.97
Std Dev	6.329

Gaus mean : 28.4401

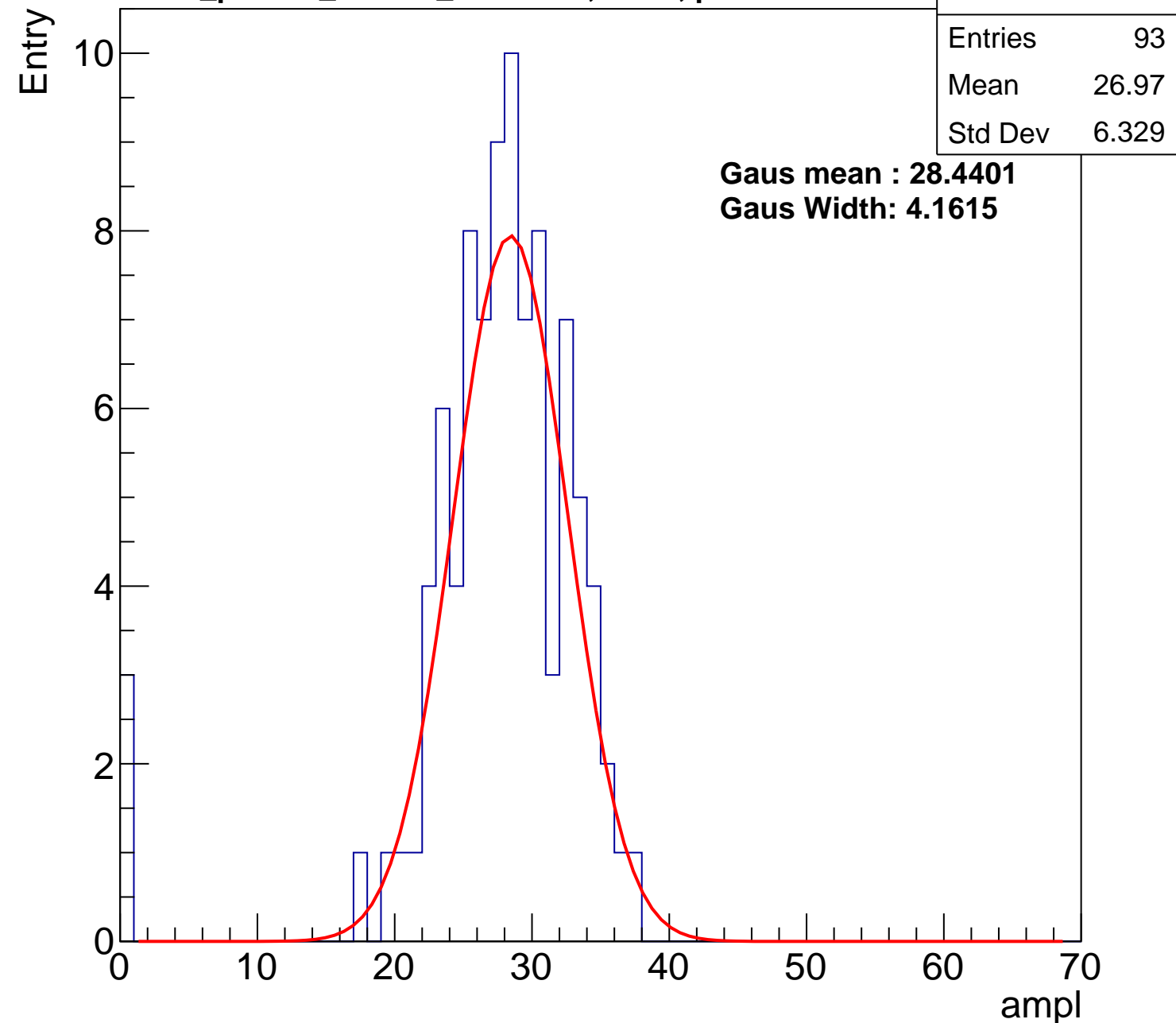
Gaus Width: 4.1615

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



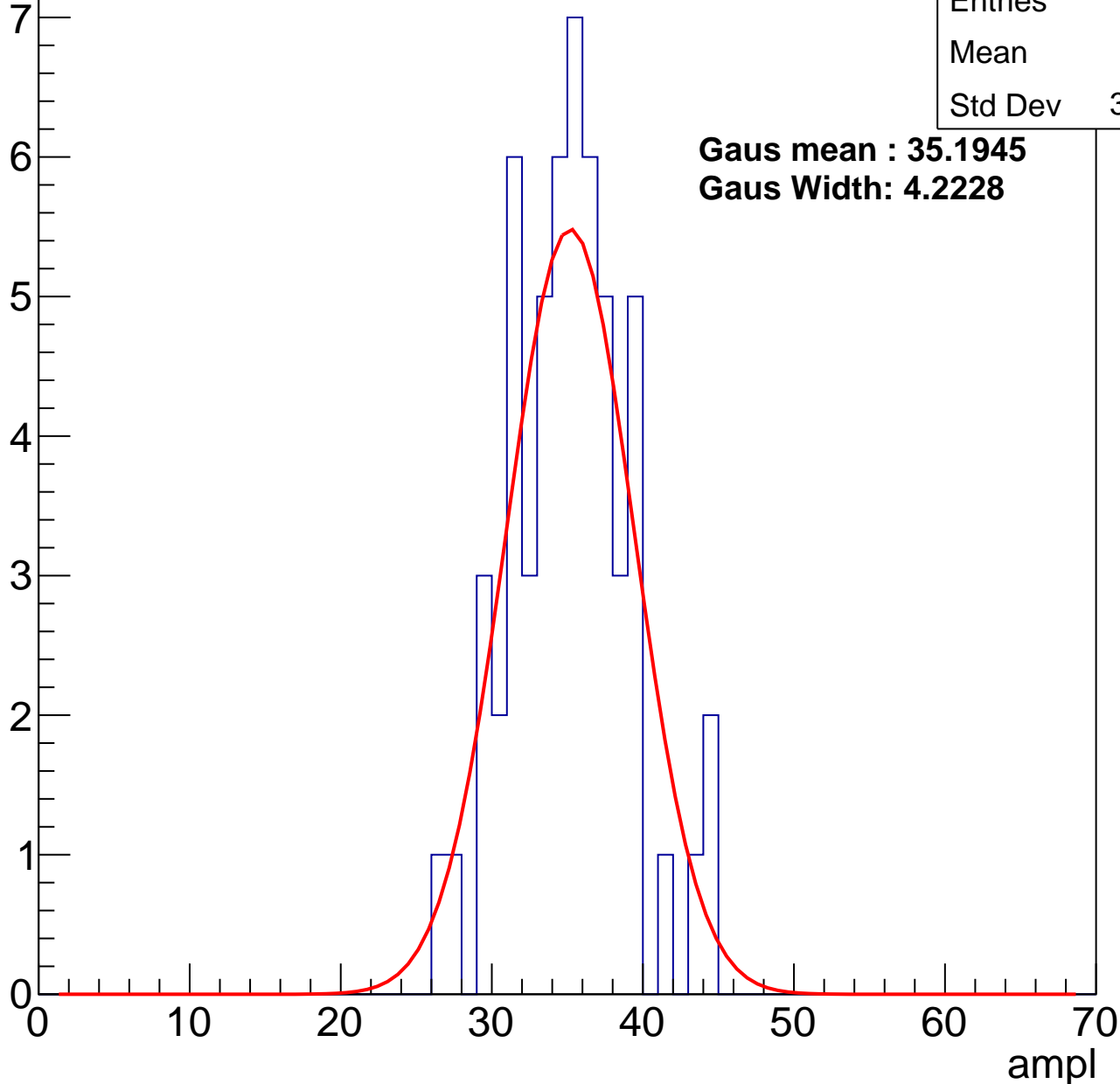
B1L100S, U14-ch108, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.7
Std Dev	3.843

Gaus mean : 35.1945
Gaus Width: 4.2228



B1L100S, U14-ch108, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	41.56
Std Dev	3.431

Gaus mean : 42.0530

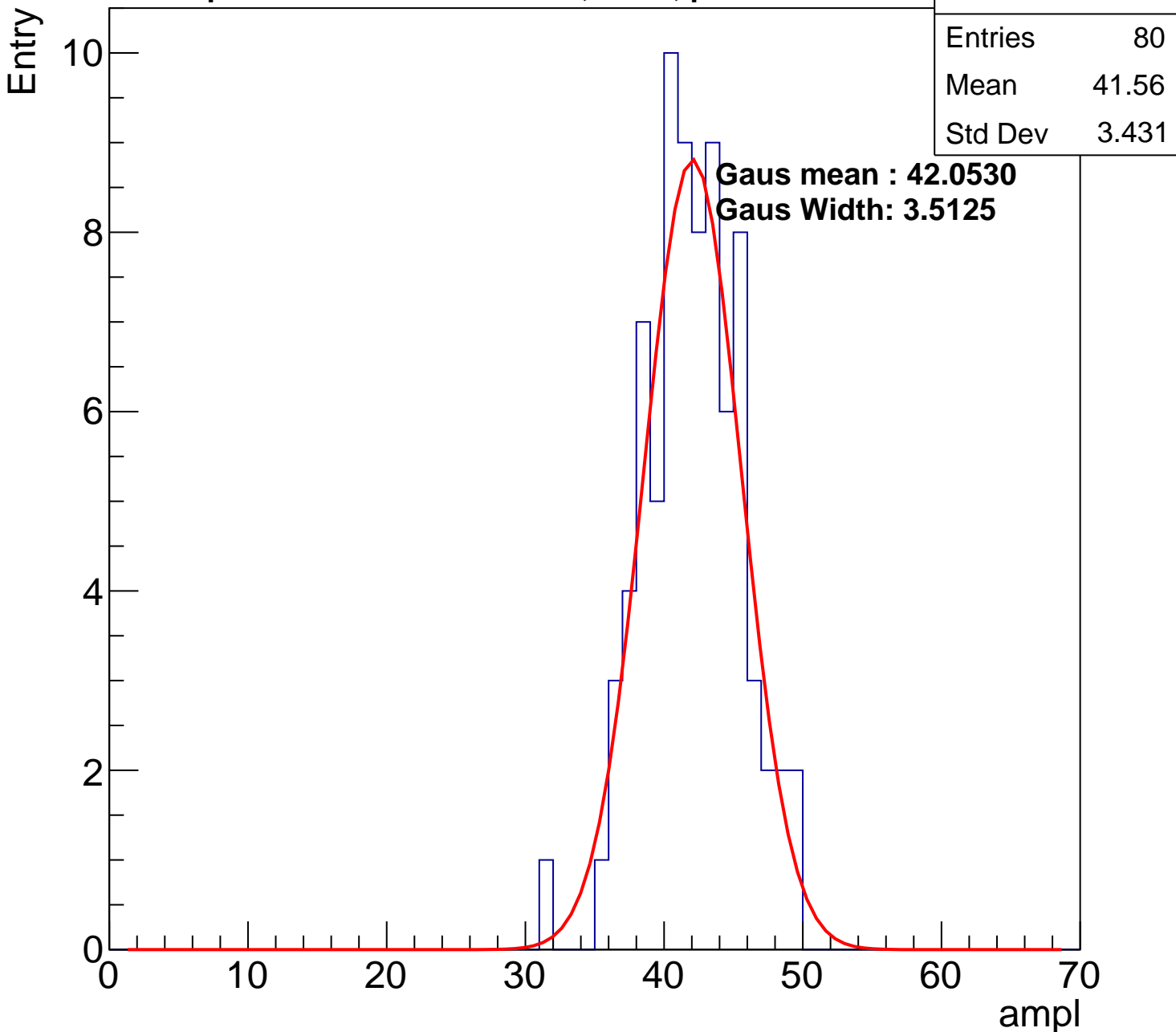
Gaus Width: 3.5125

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

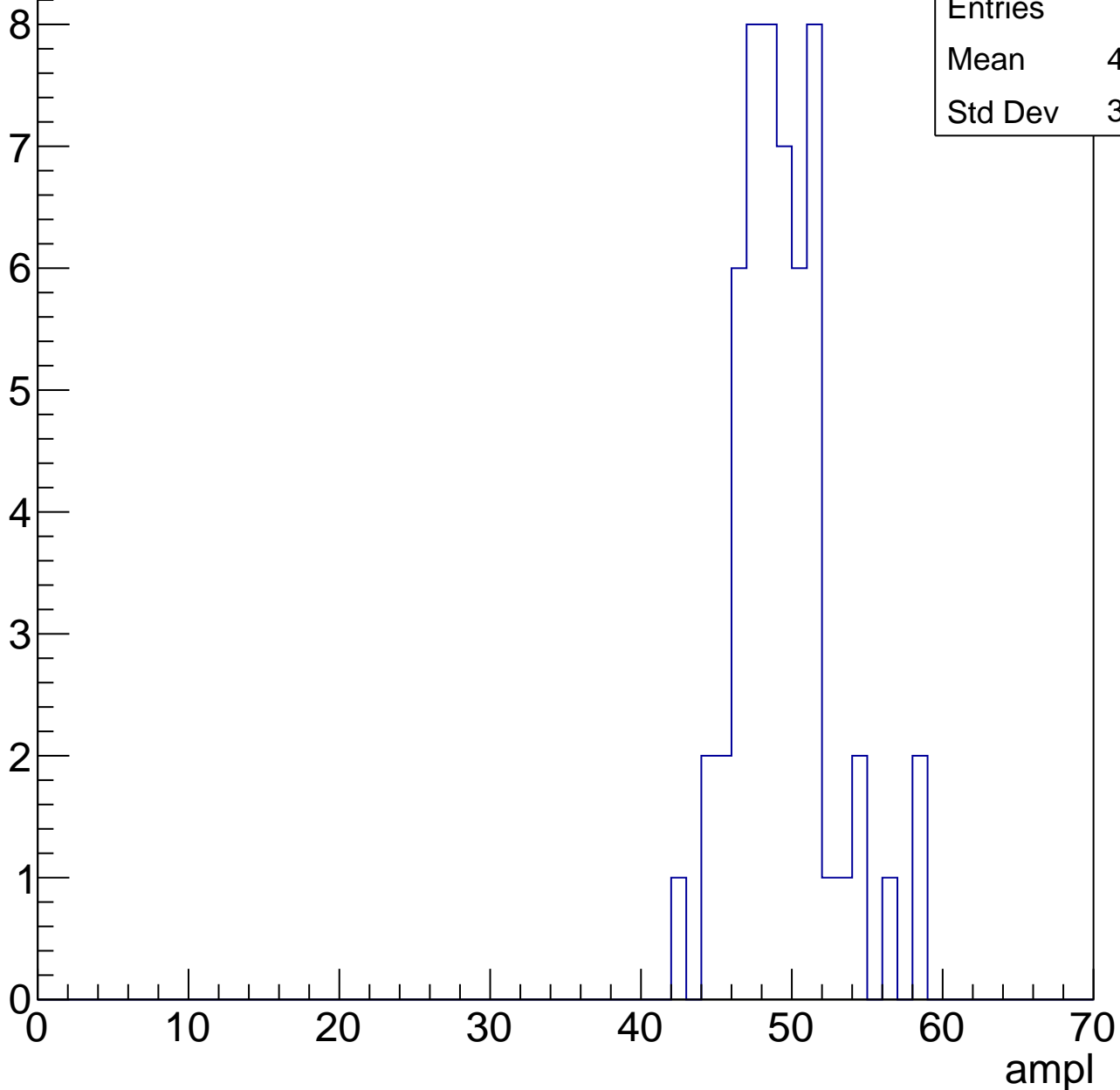


B1L100S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	48.95
Std Dev	3.159

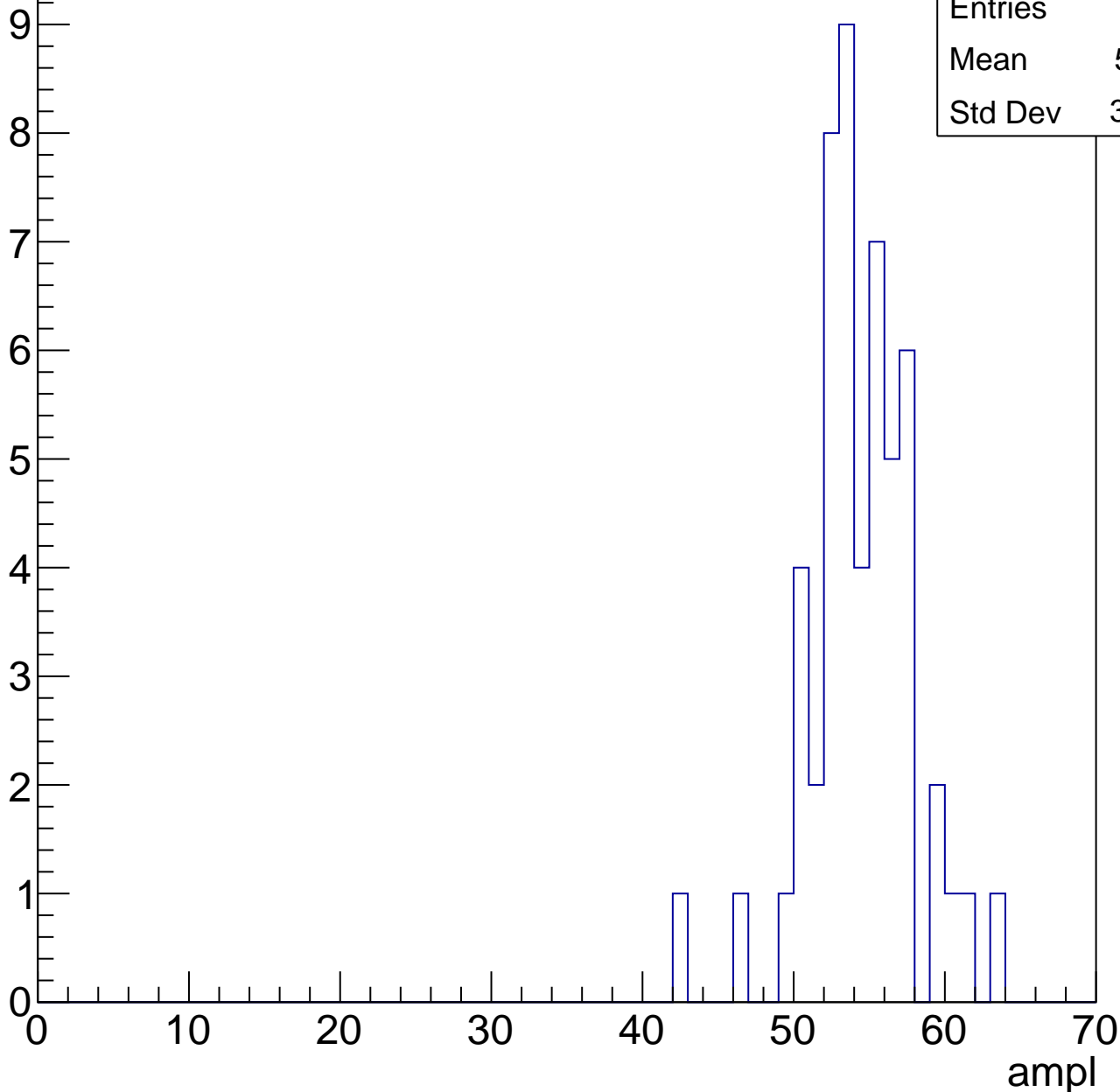


B1L100S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	53.91
Std Dev	3.509

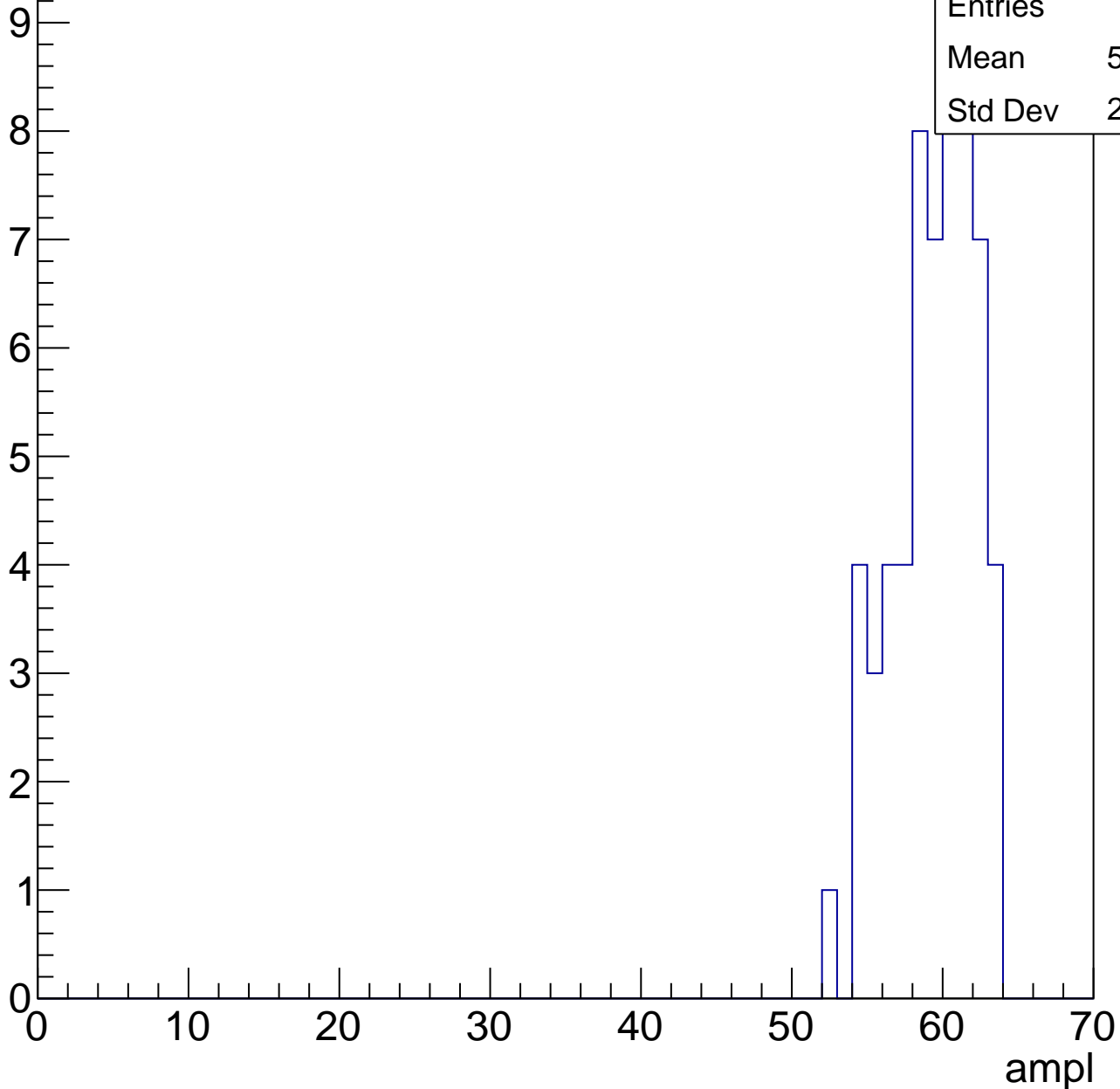


B1L100S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	58.93
Std Dev	2.693

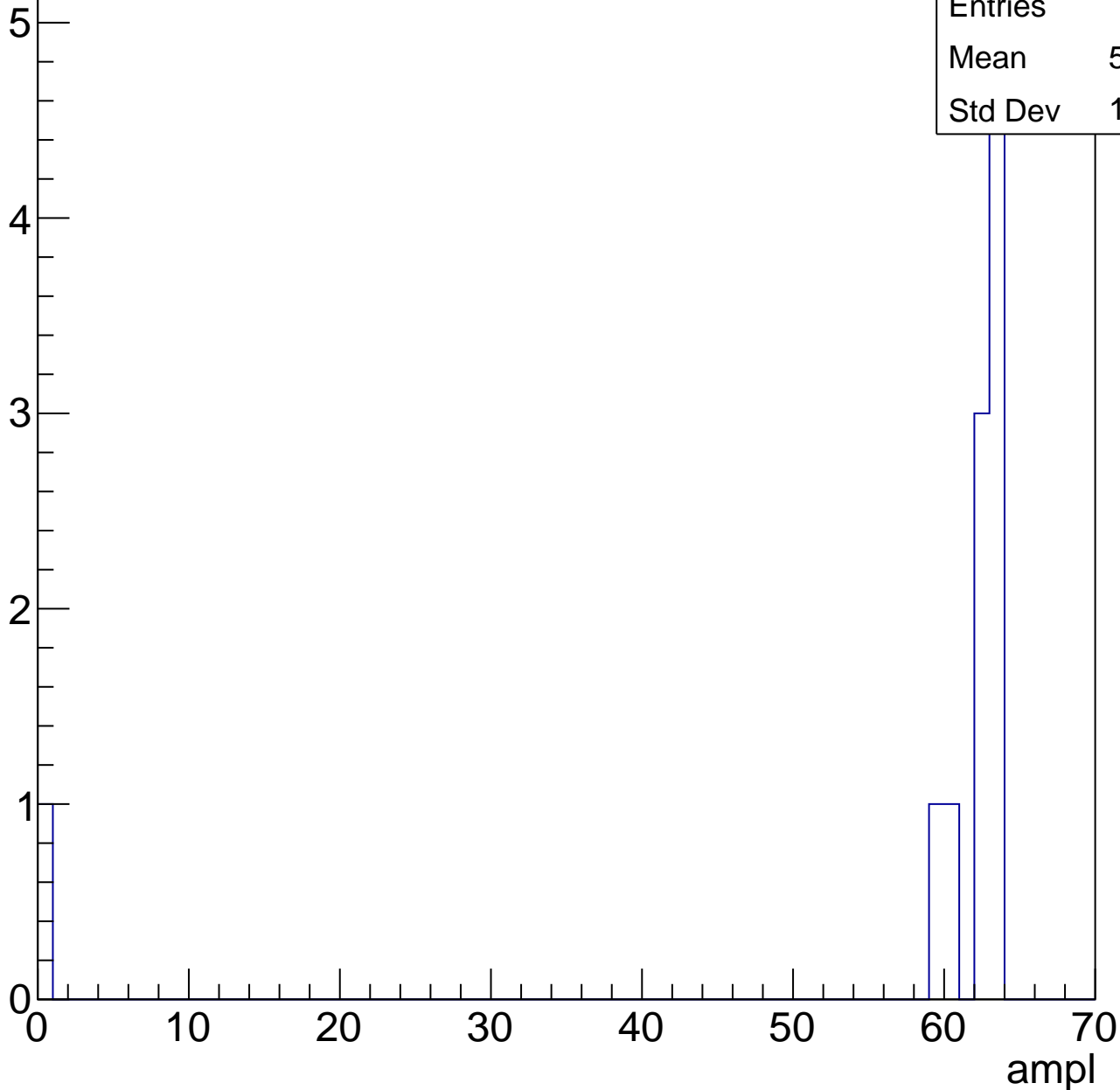


B1L100S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	56.36
Std Dev	17.87



B1L100S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch109, adc0

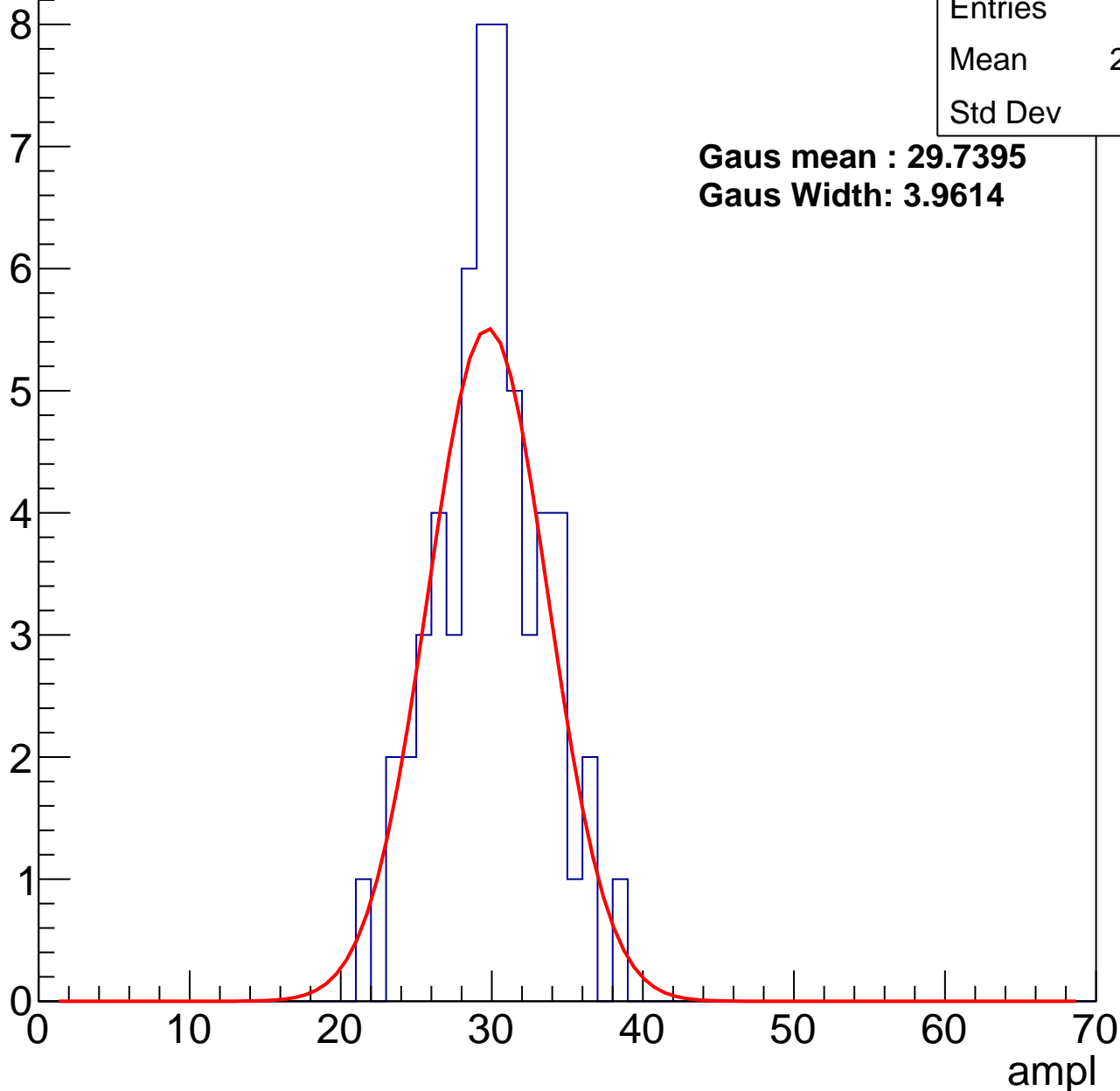
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	29.46
Std Dev	3.53

Gaus mean : 29.7395

Gaus Width: 3.9614



B1L100S, U14-ch109, adc1

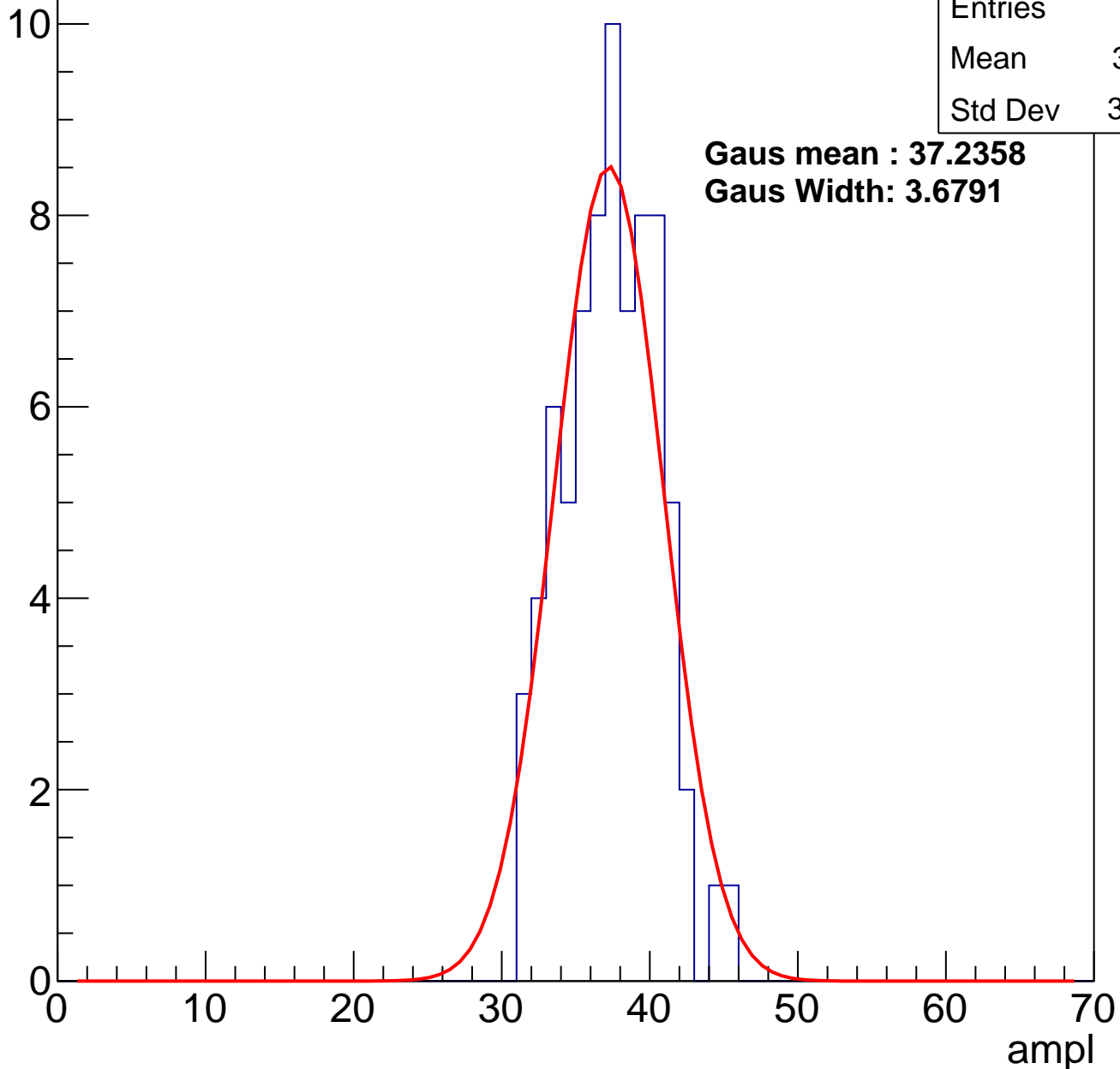
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	36.91
Std Dev	3.138

Gaus mean : 37.2358

Gaus Width: 3.6791

Entry



B1L100S, U14-ch109, adc2

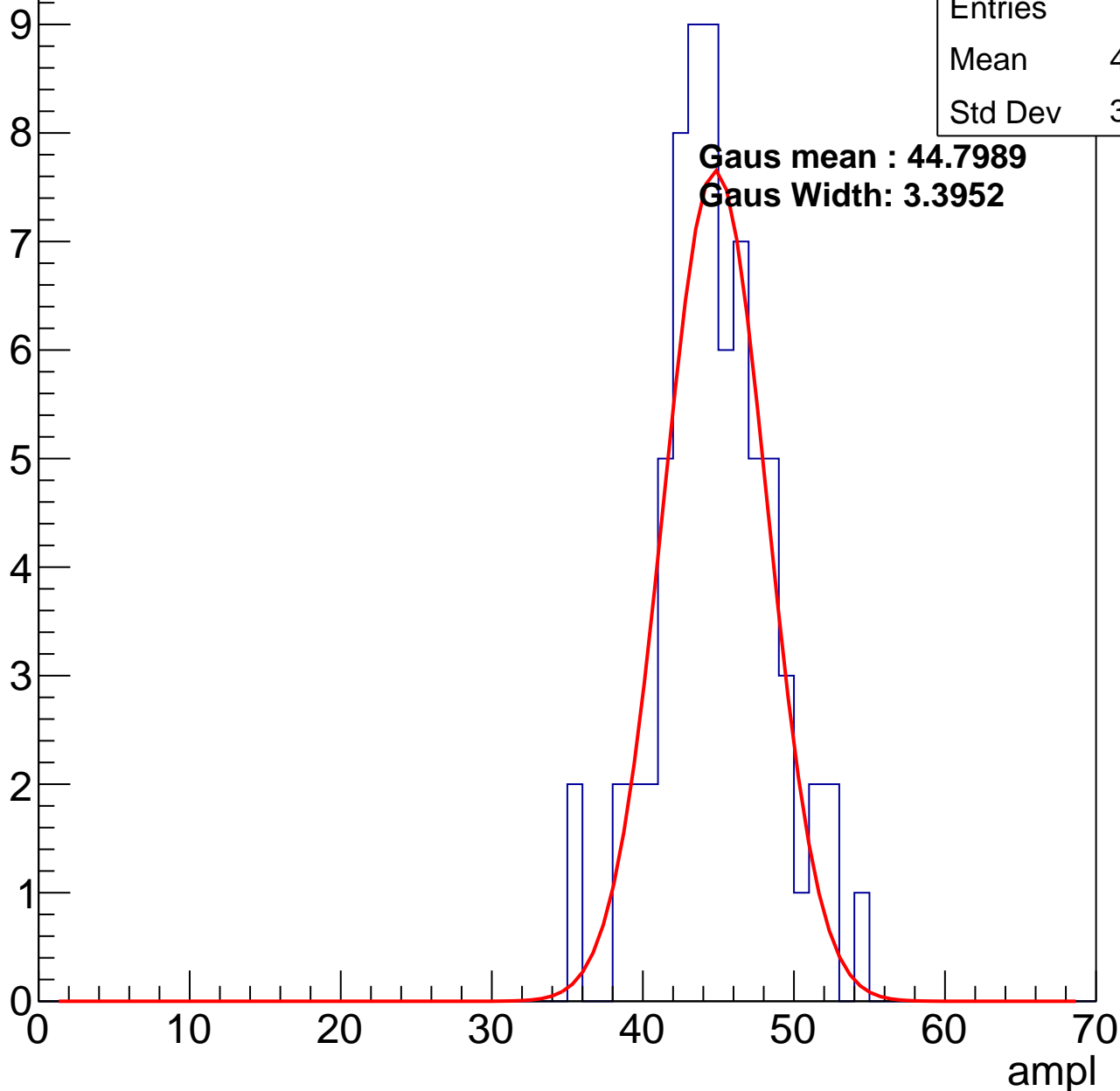
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.39
Std Dev	3.728

Gaus mean : 44.7989

Gaus Width: 3.3952

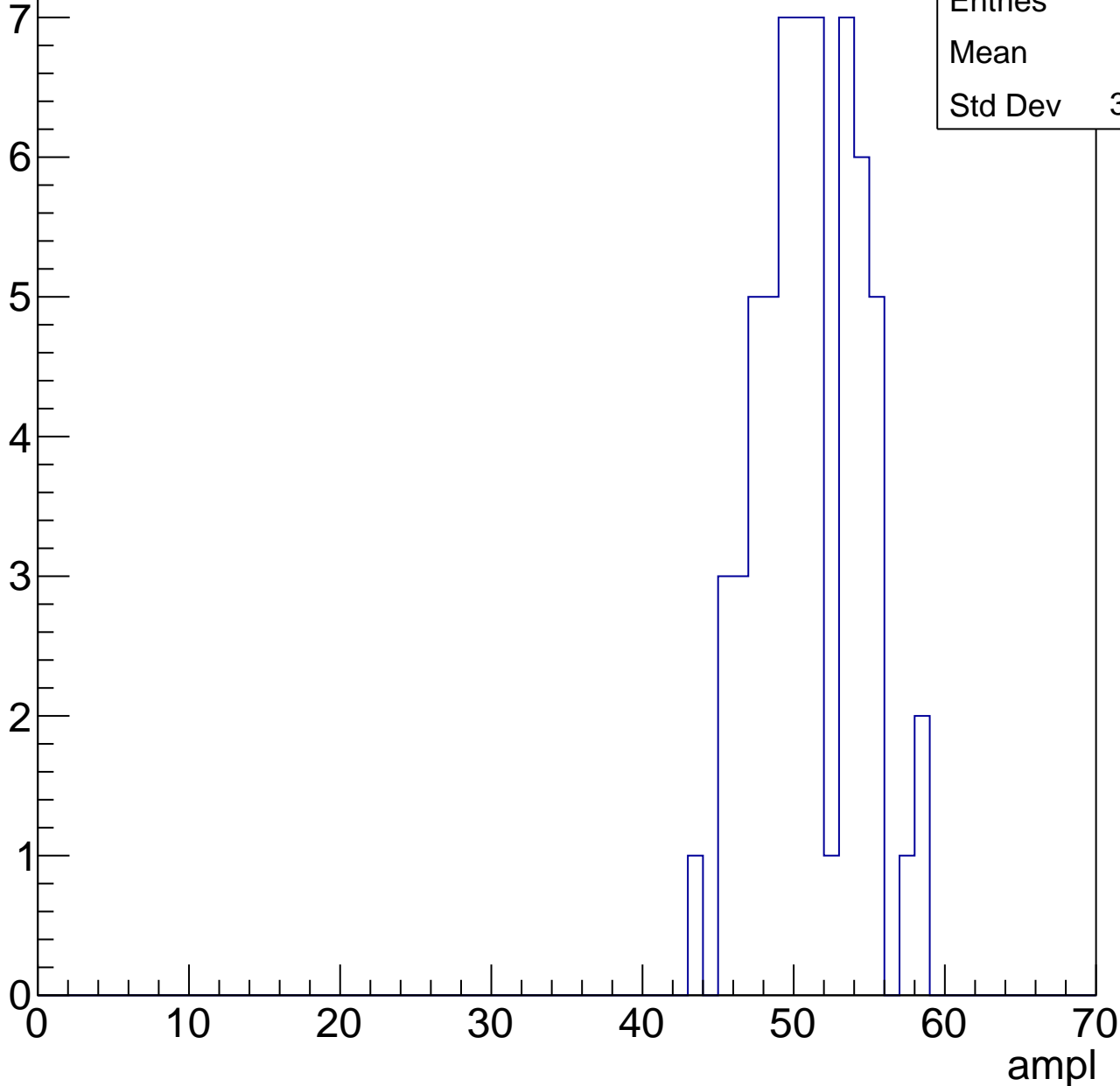


B1L100S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	50.6
Std Dev	3.402

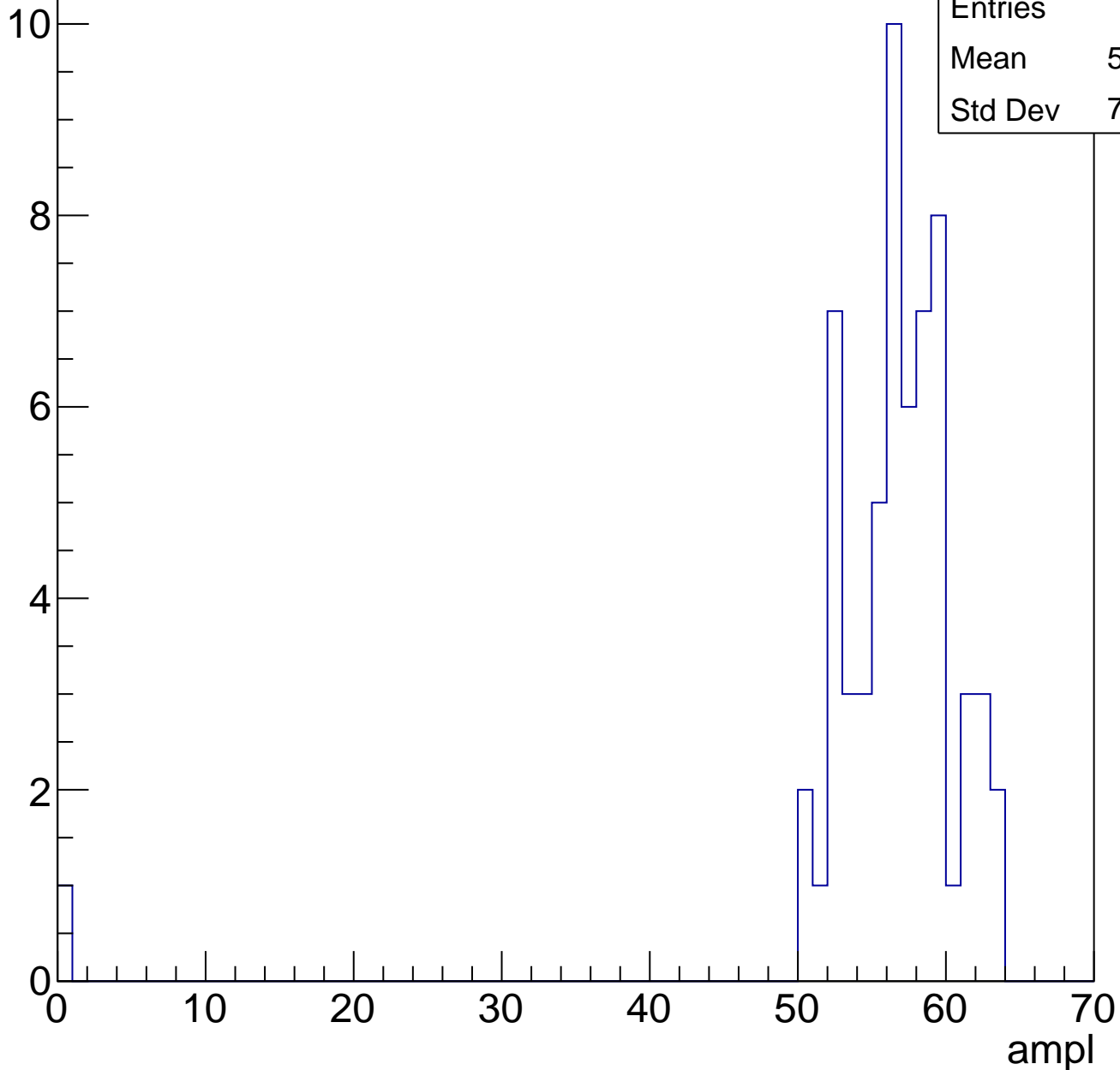


B1L100S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	55.58
Std Dev	7.809

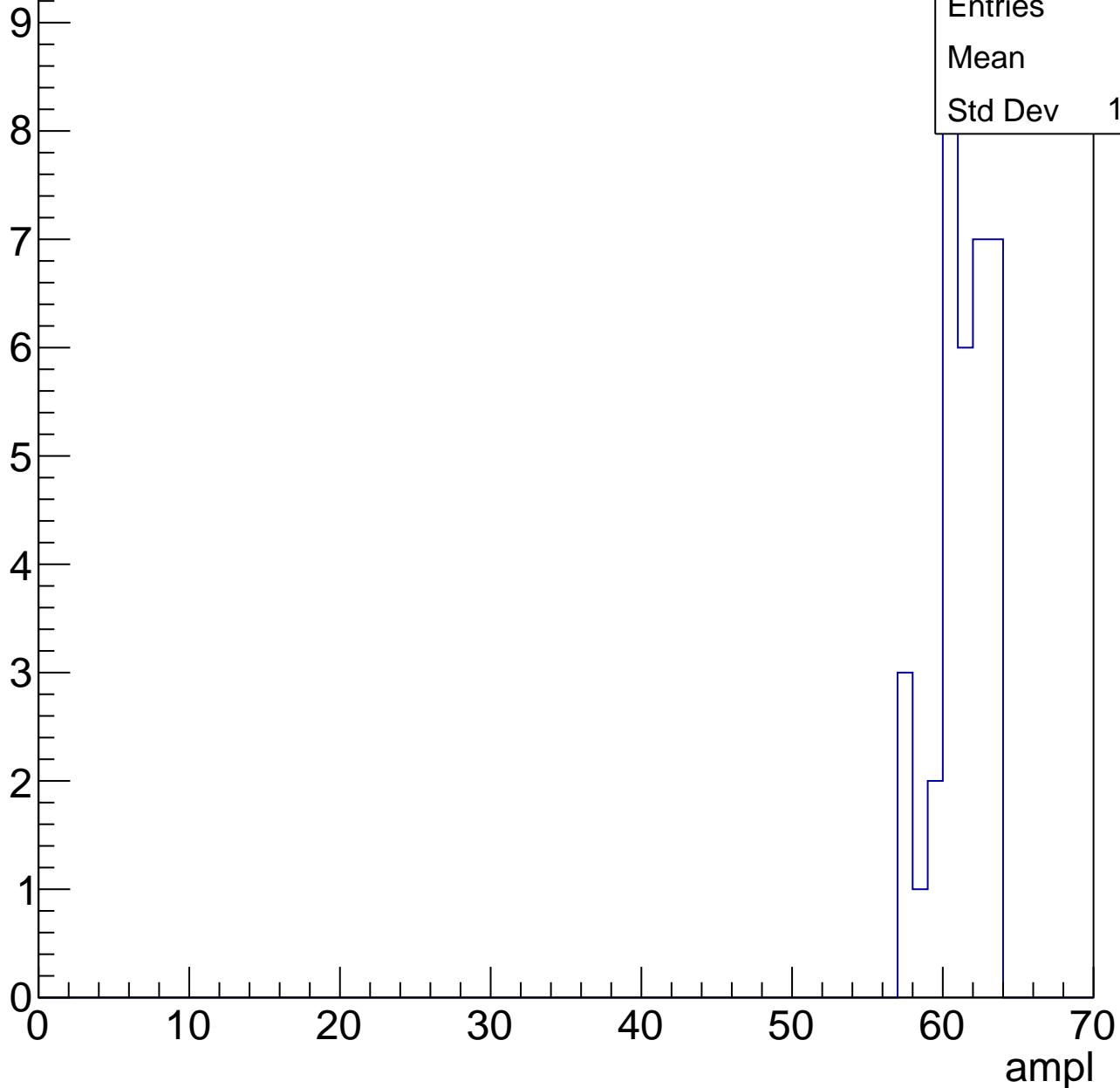
Entry



B1L100S, U14-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

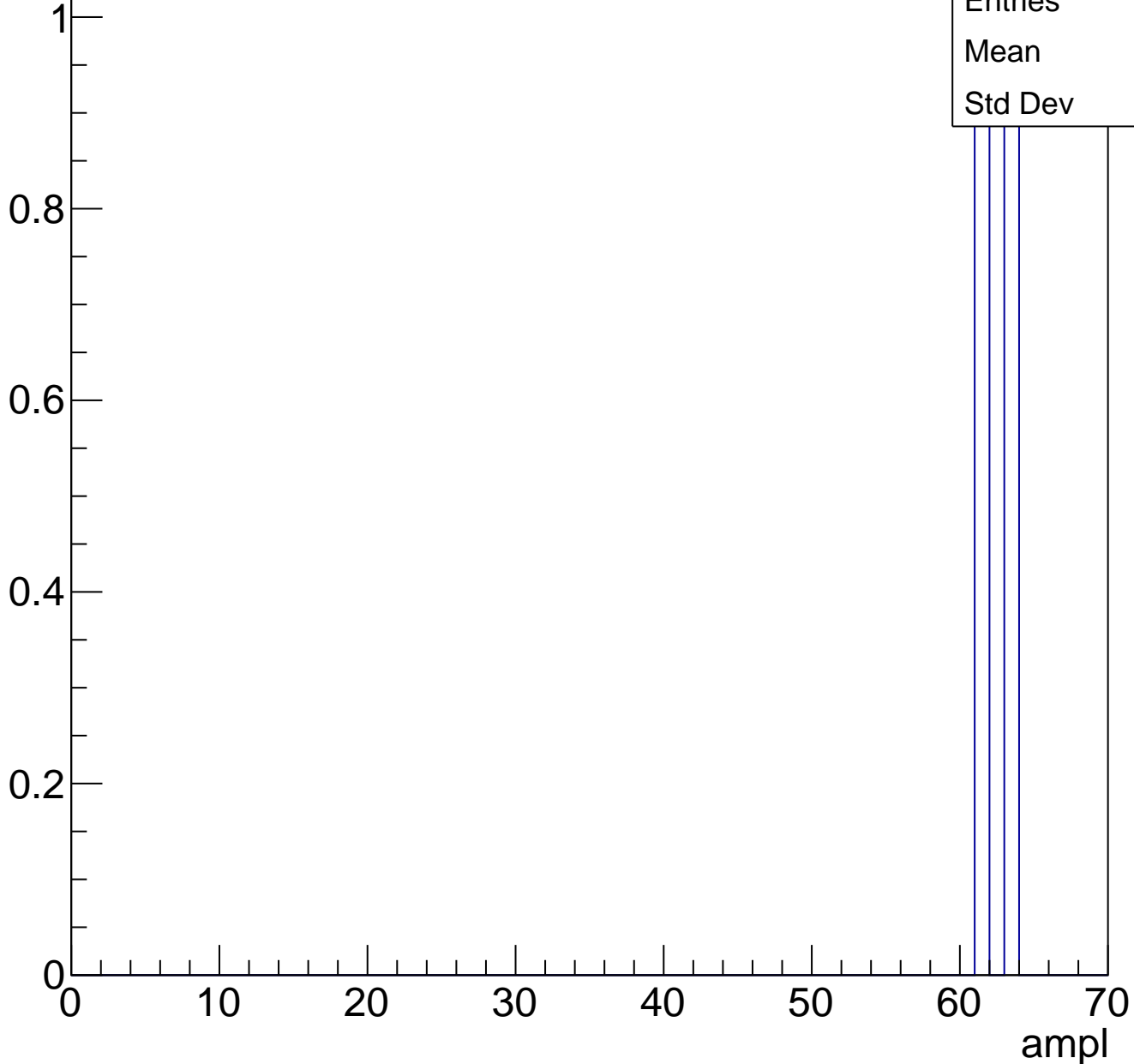
Entry



B1L100S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch110, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	28.1
Std Dev	6.098

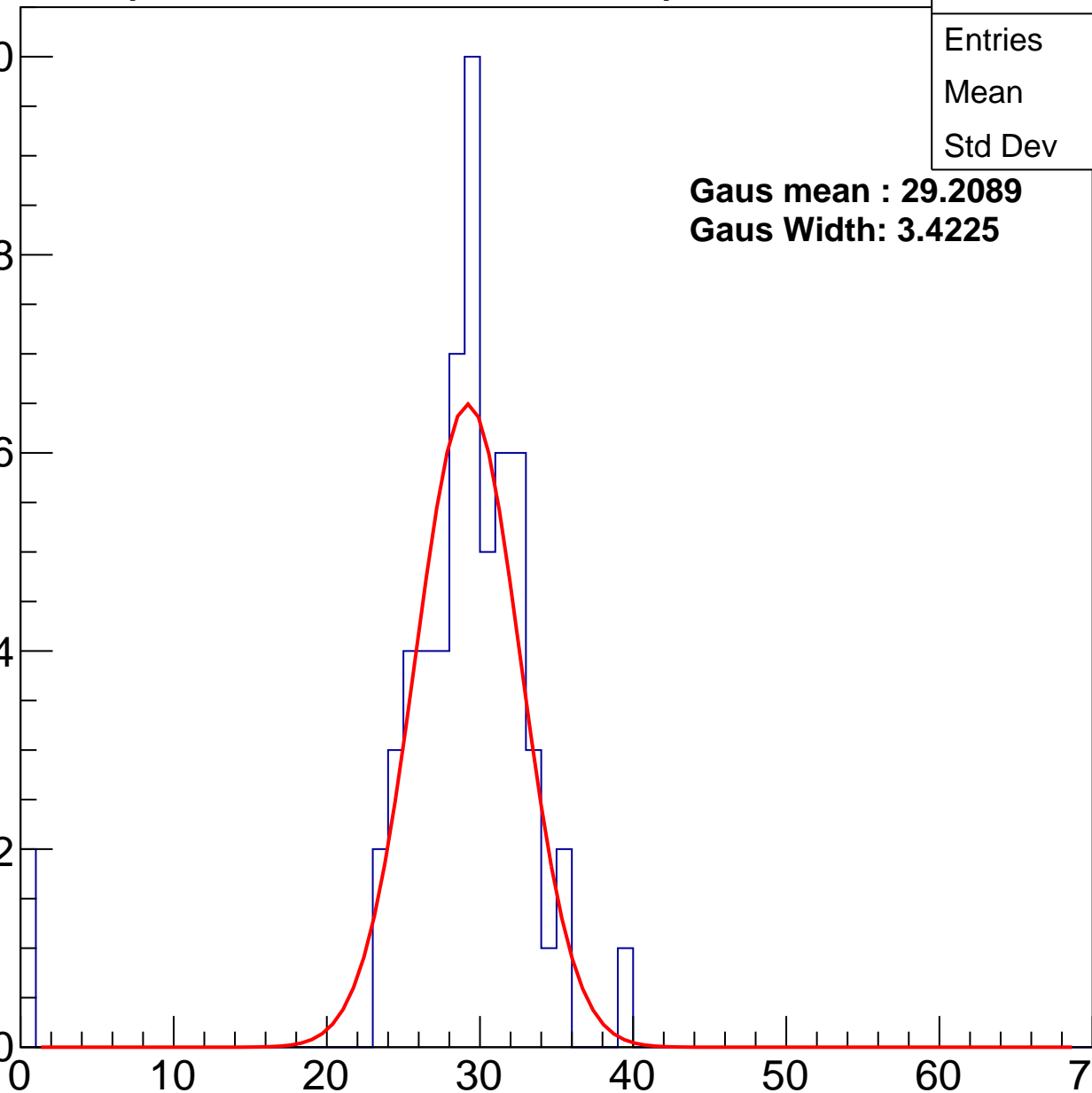
Gaus mean : 29.2089

Gaus Width: 3.4225

Entry

10
8
6
4
2
0

ampl



B1L100S, U14-ch110, adc1

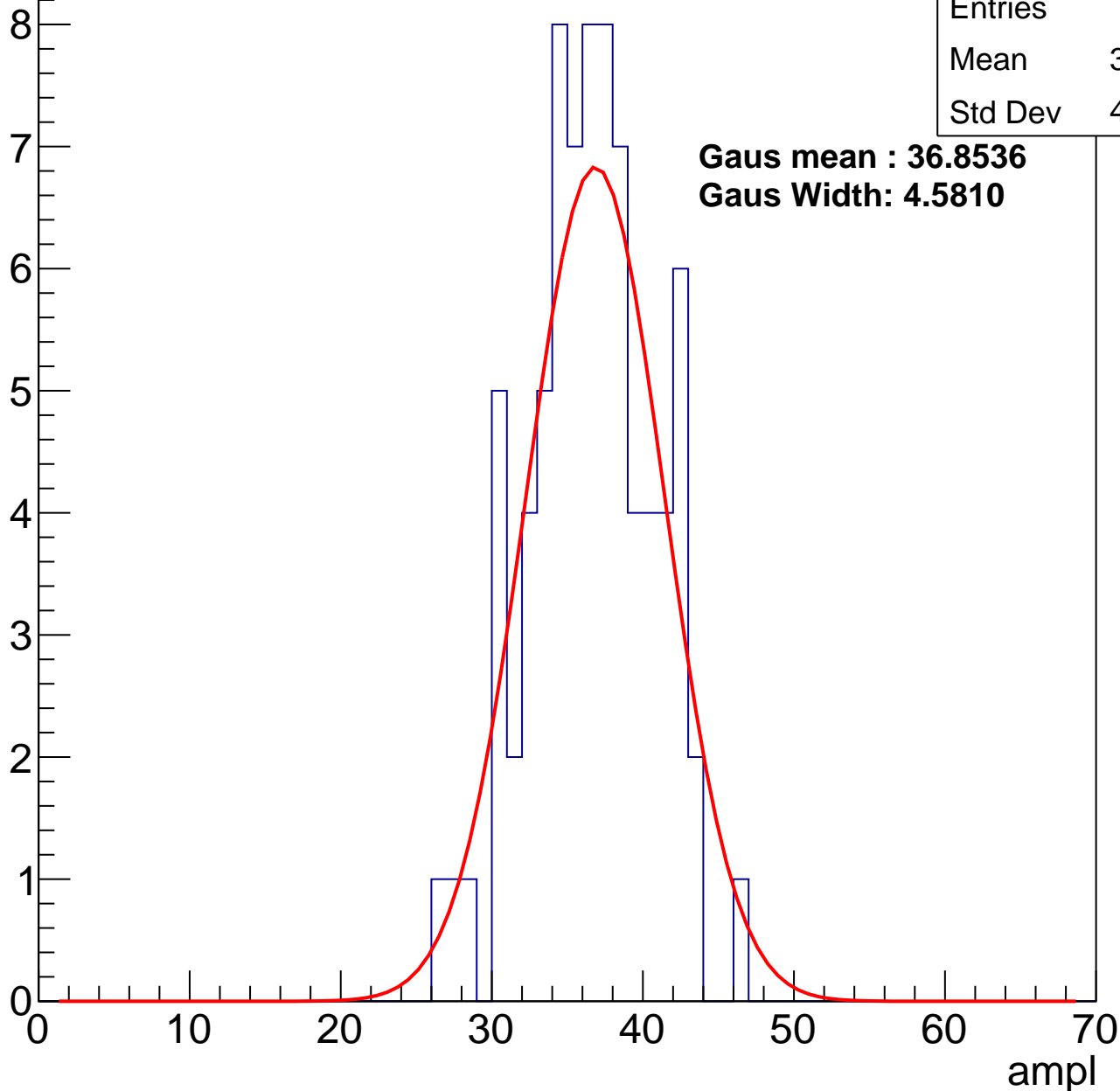
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.12
Std Dev	4.038

Gaus mean : 36.8536

Gaus Width: 4.5810



B1L100S, U14-ch110, adc2

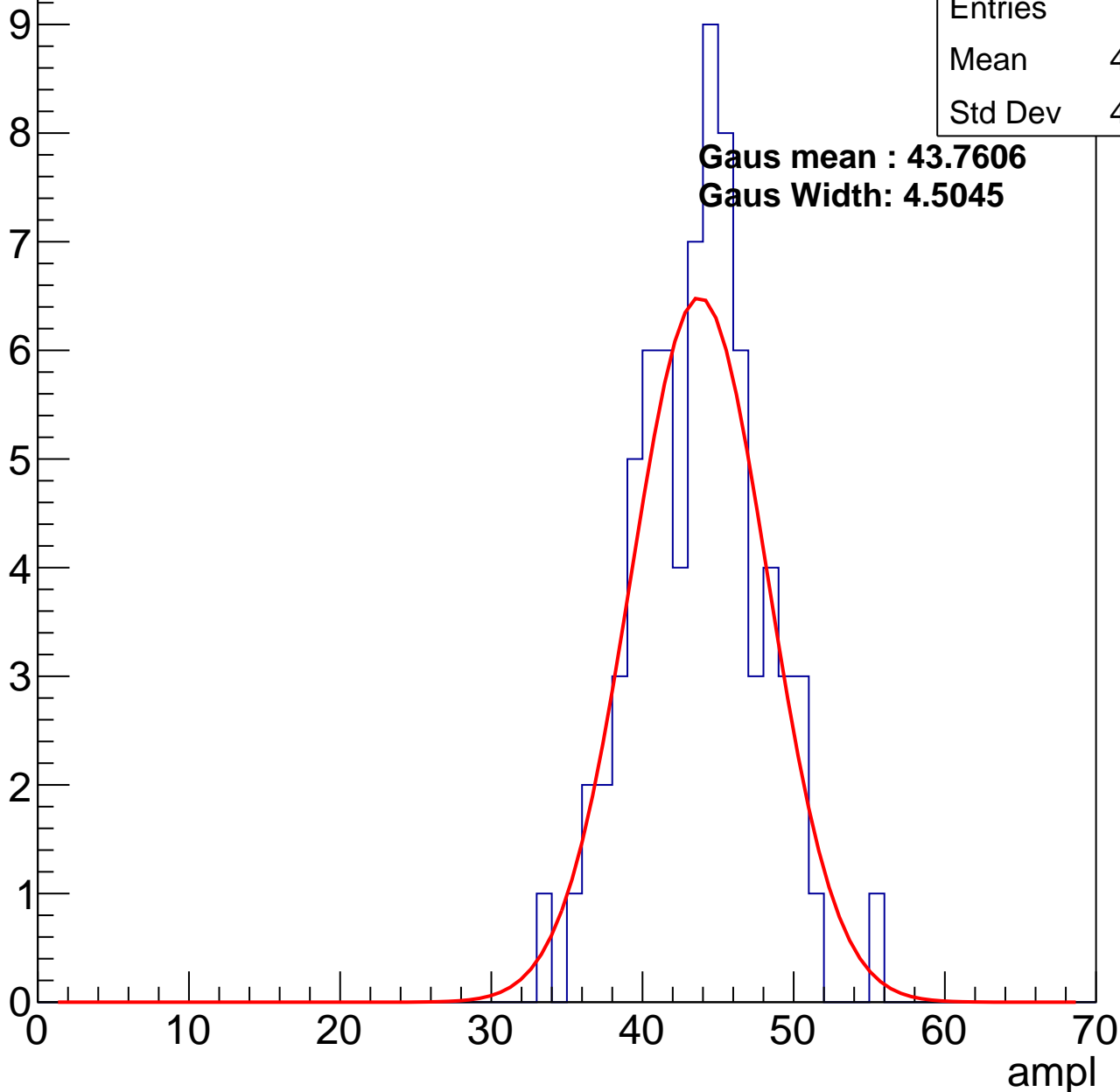
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	43.28
Std Dev	4.117

Gaus mean : 43.7606

Gaus Width: 4.5045

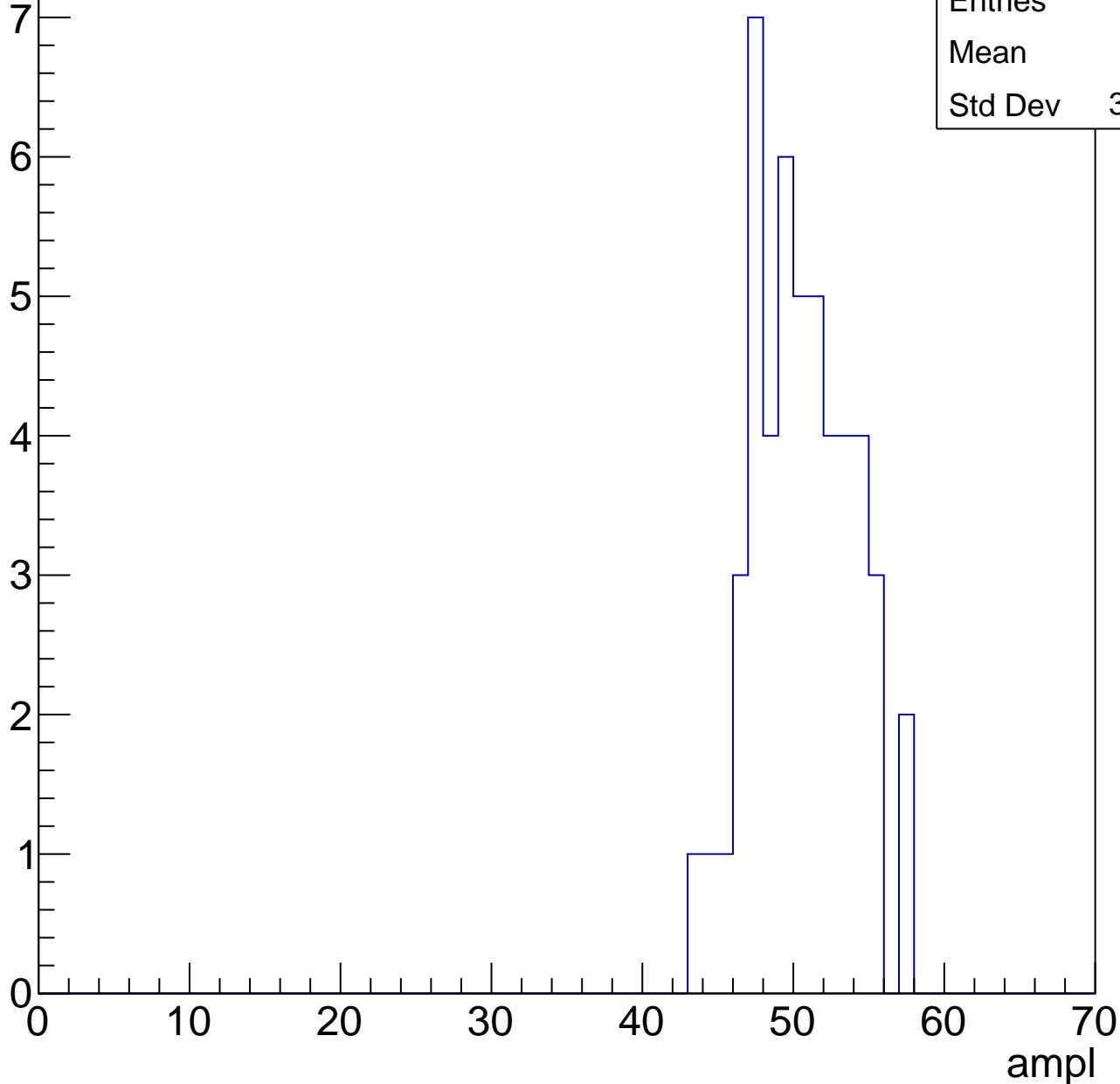


B1L100S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	50.1
Std Dev	3.276



B1L100S, U14-ch110, adc4

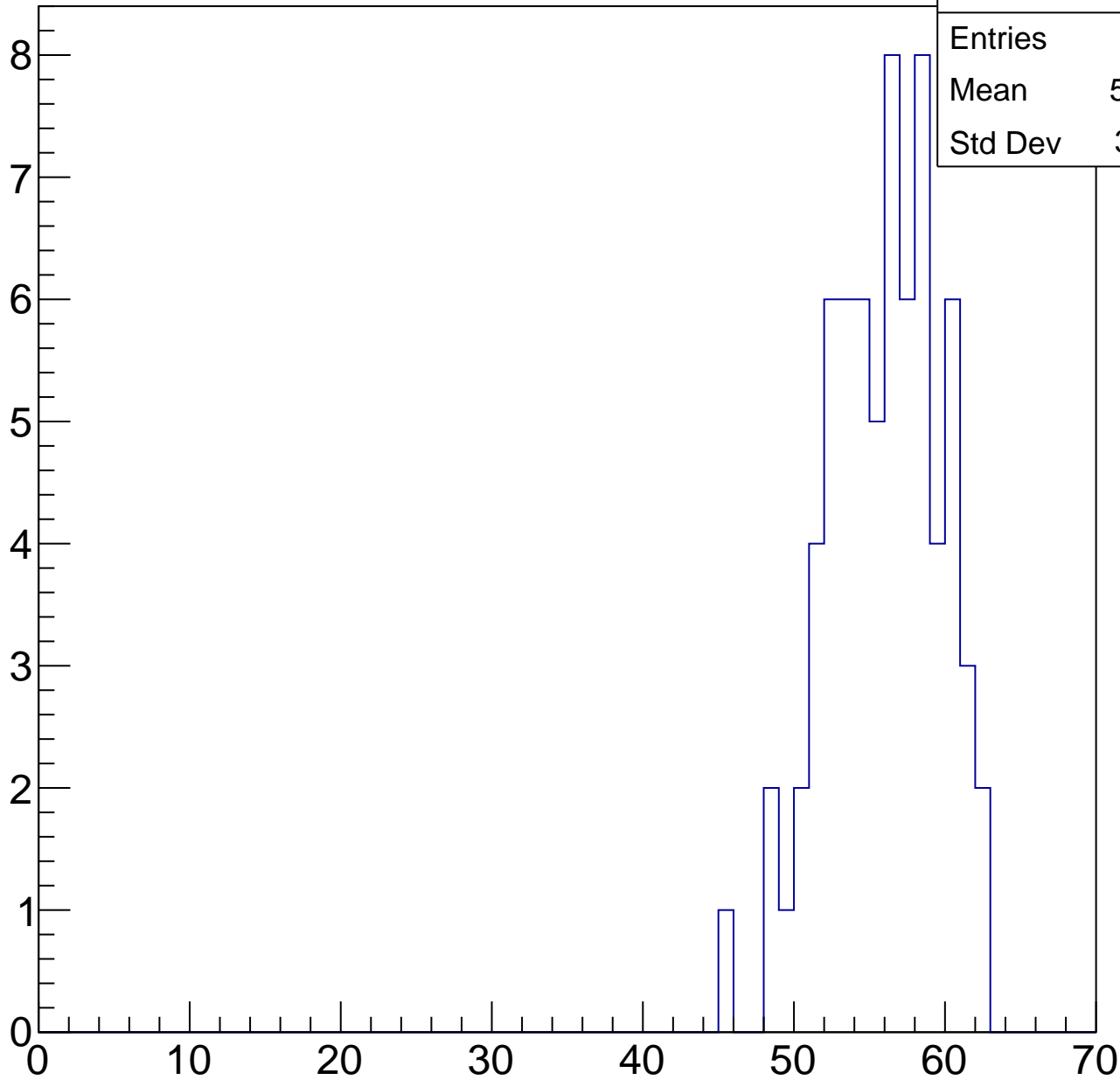
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	70
Mean	55.43
Std Dev	3.671

ampl

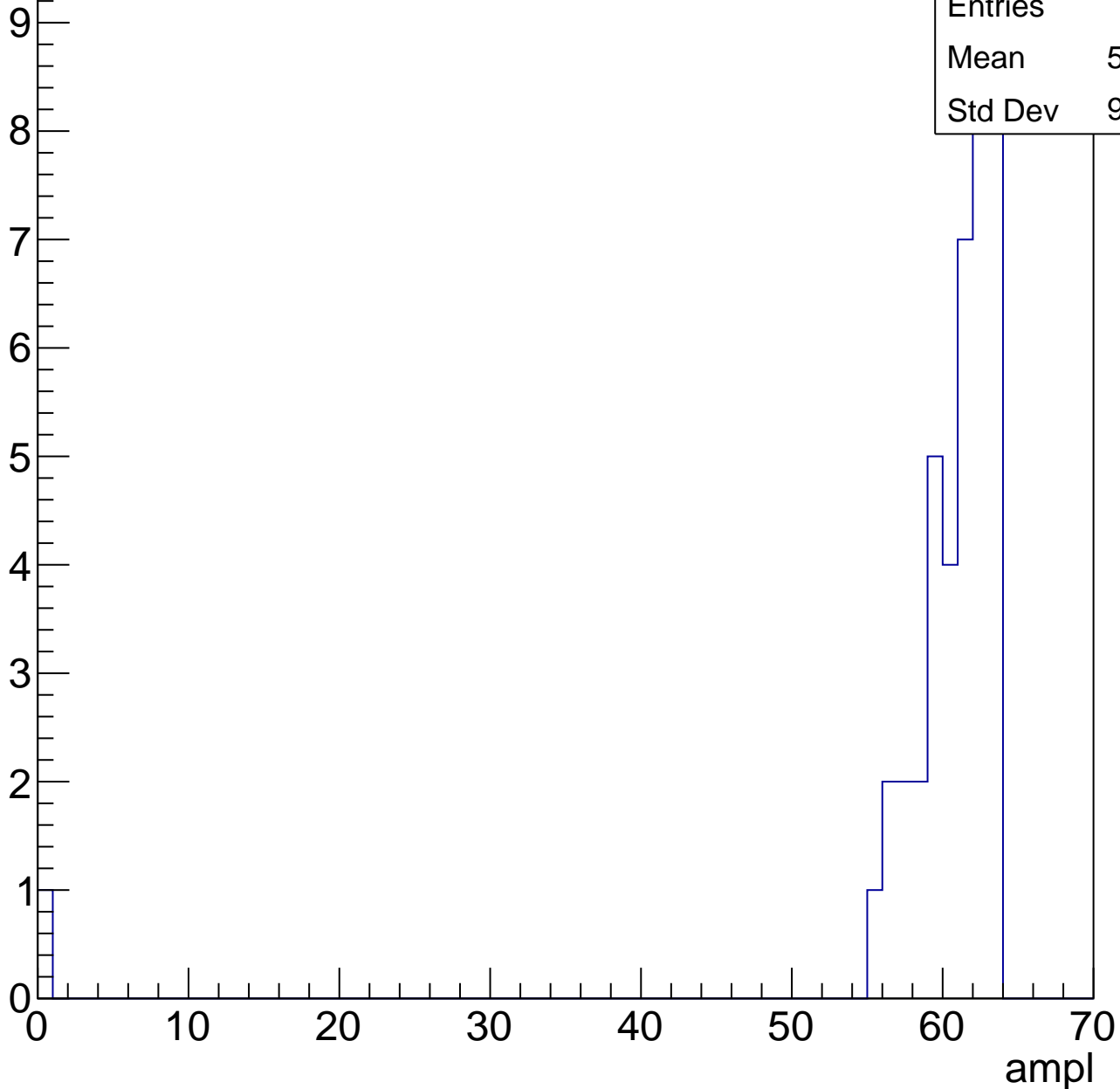


B1L100S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	59.07
Std Dev	9.593



B1L100S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch111, adc0

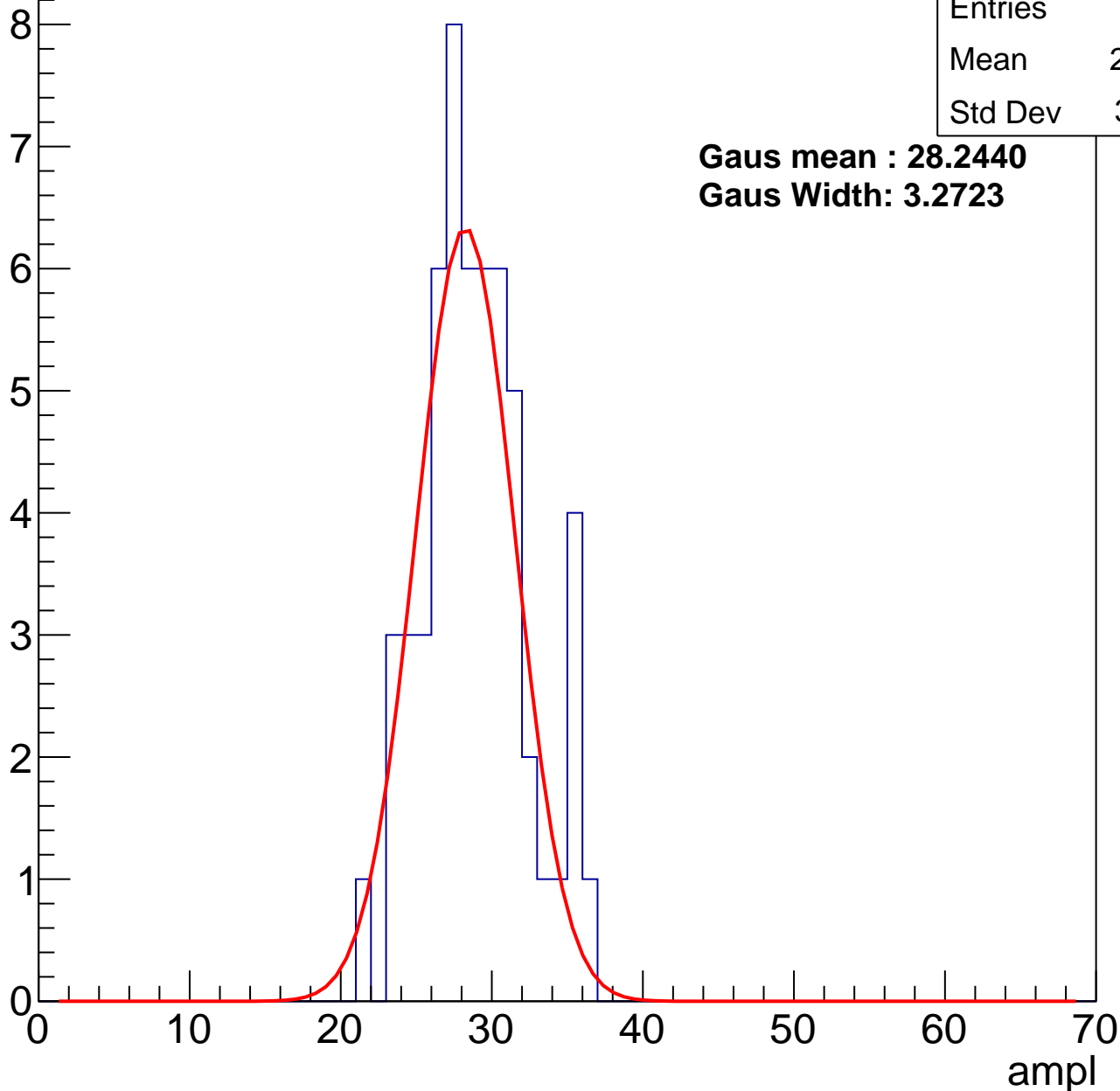
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	28.45
Std Dev	3.401

Gaus mean : 28.2440

Gaus Width: 3.2723



B1L100S, U14-ch111, adc1

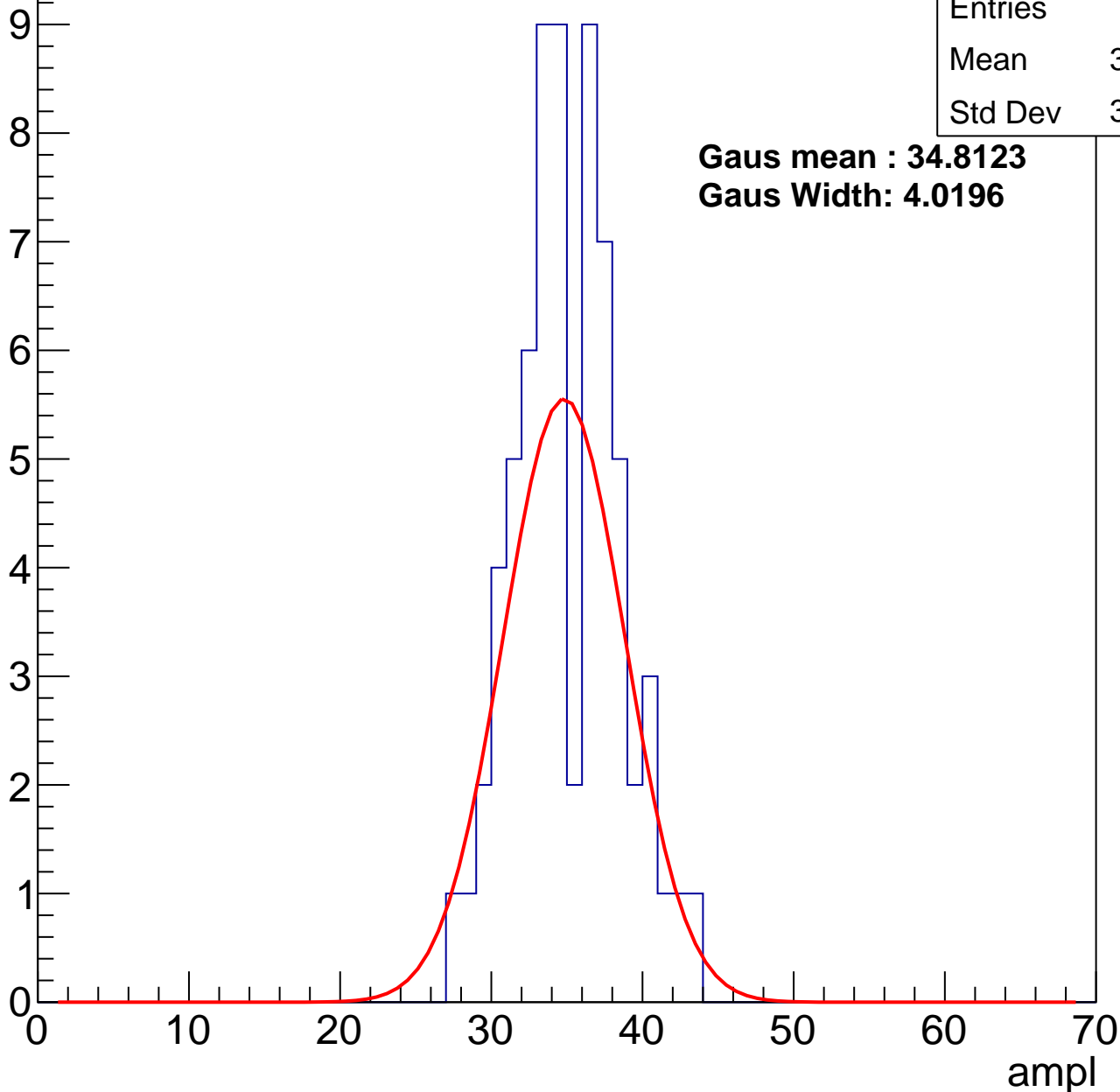
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	34.56
Std Dev	3.406

Gaus mean : 34.8123

Gaus Width: 4.0196



B1L100S, U14-ch111, adc2

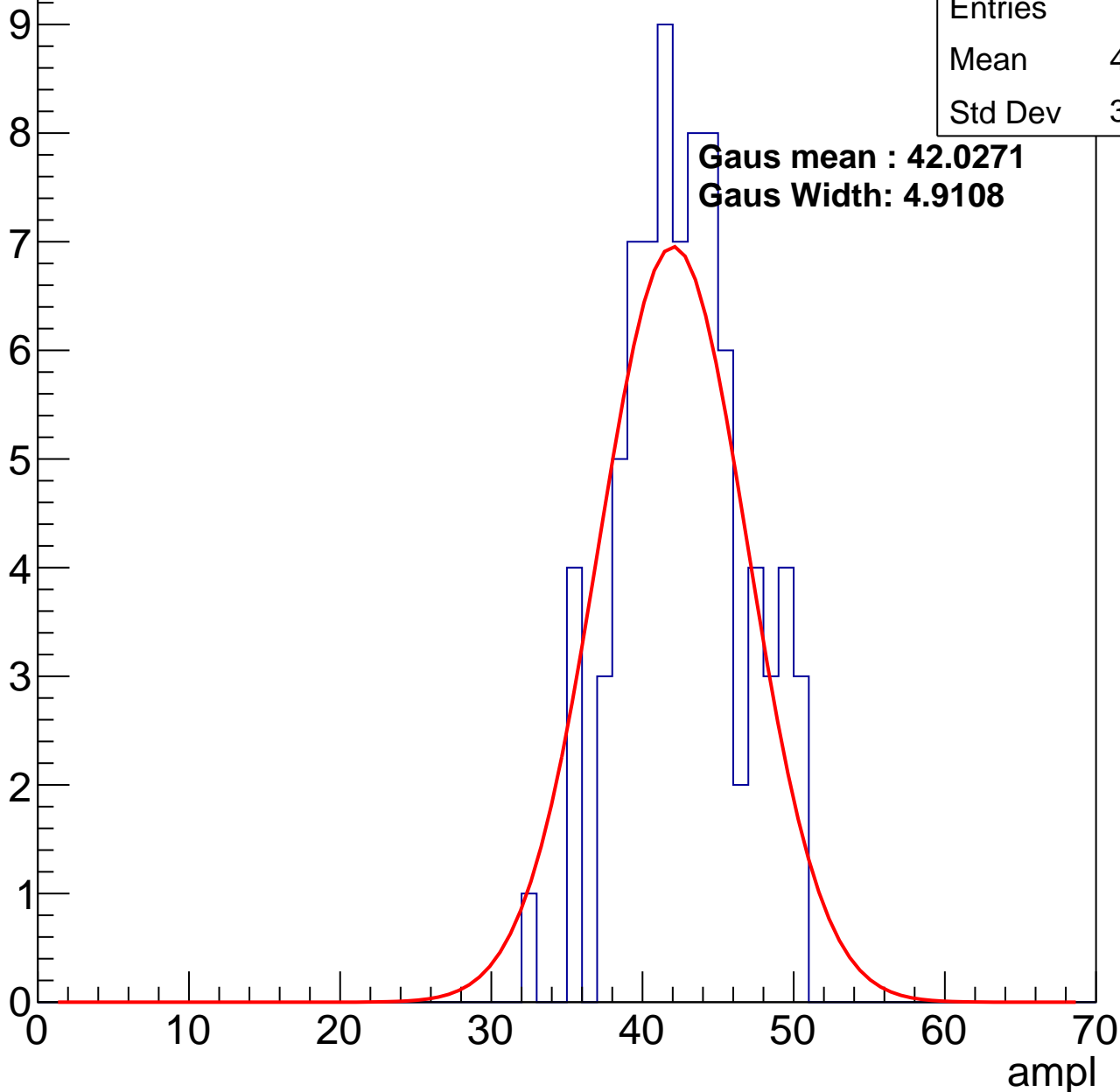
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	42.28
Std Dev	3.954

Gaus mean : 42.0271

Gaus Width: 4.9108

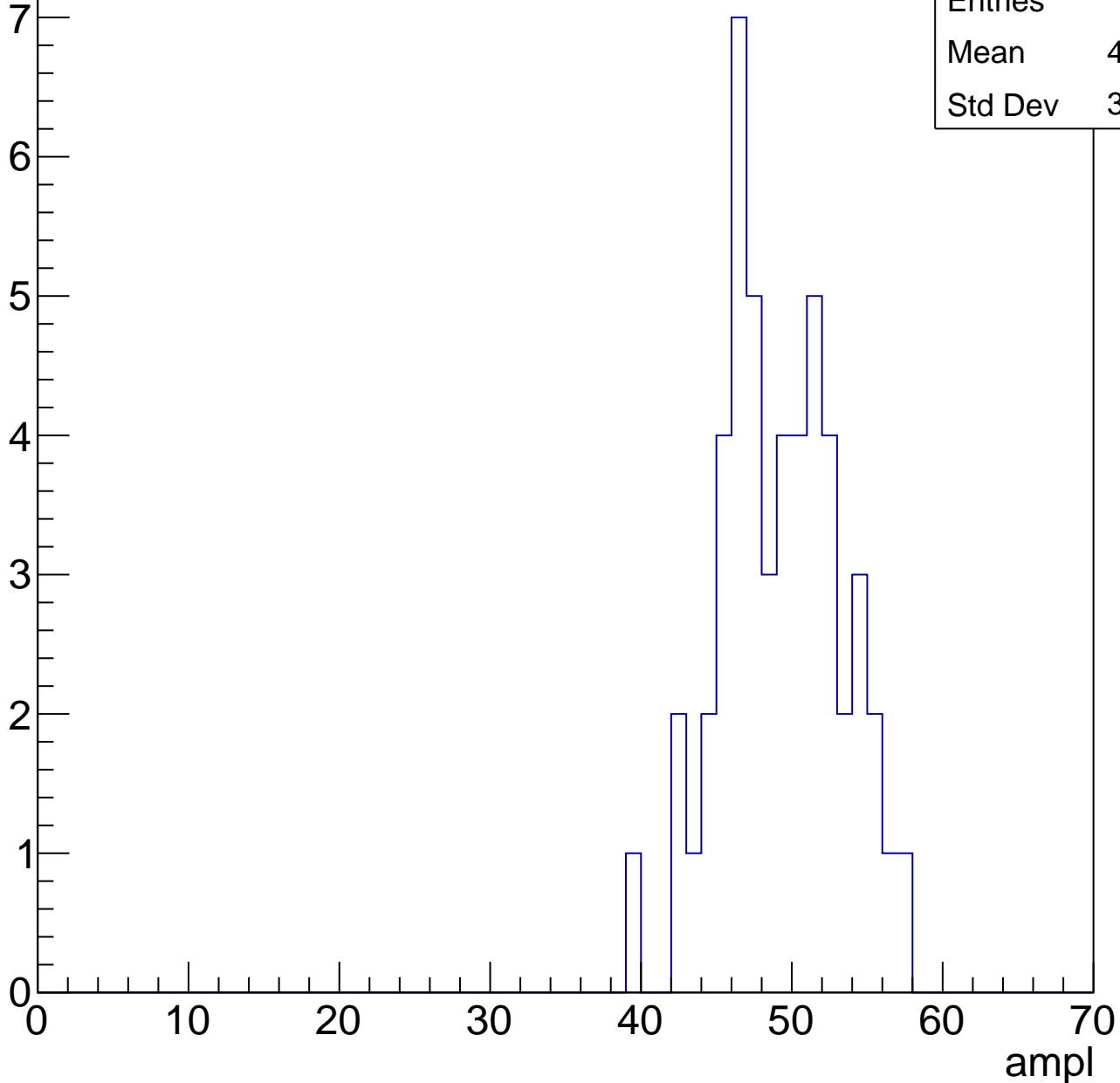


B1L100S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	48.73
Std Dev	3.916

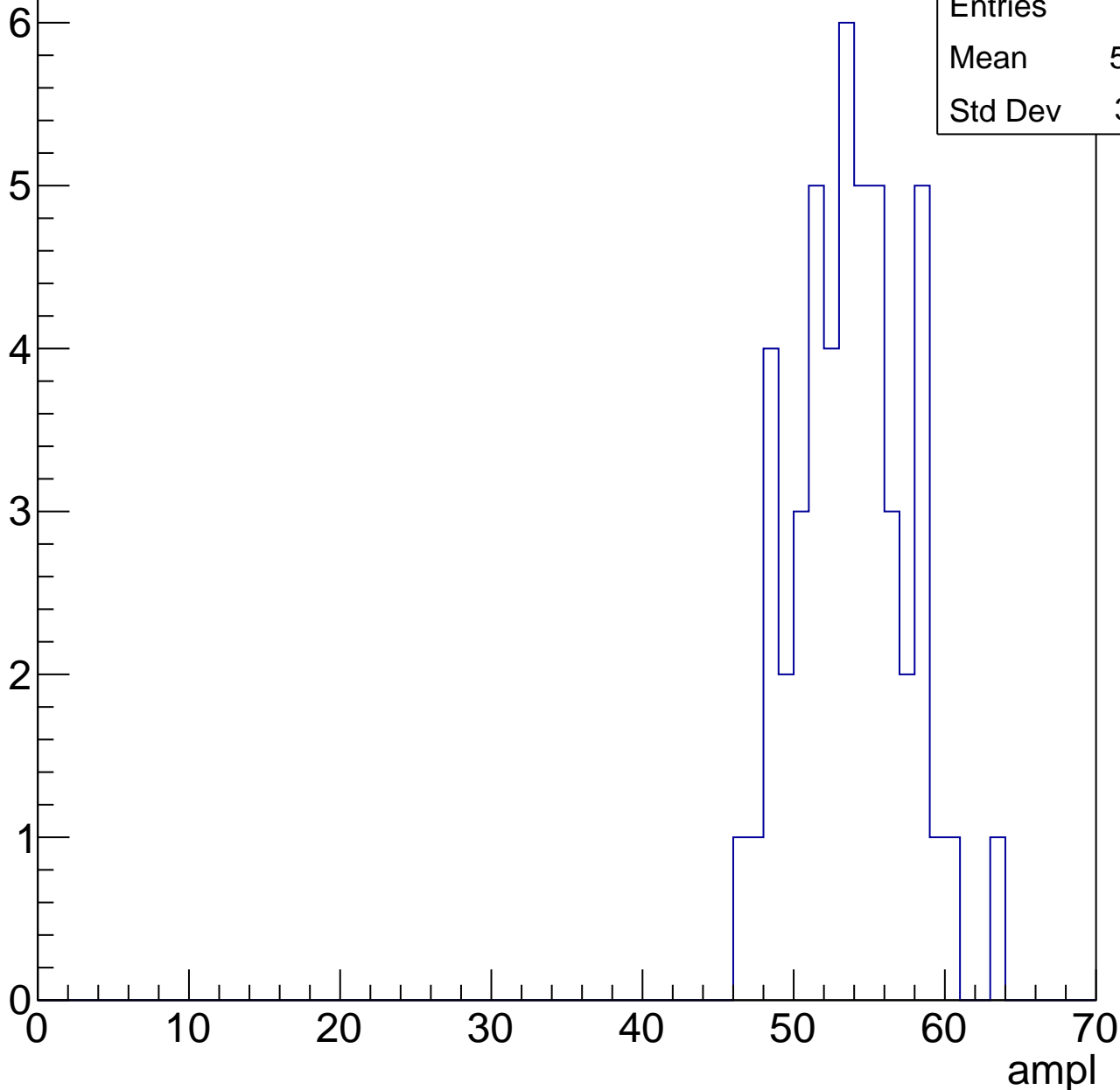


B1L100S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	53.33
Std Dev	3.661

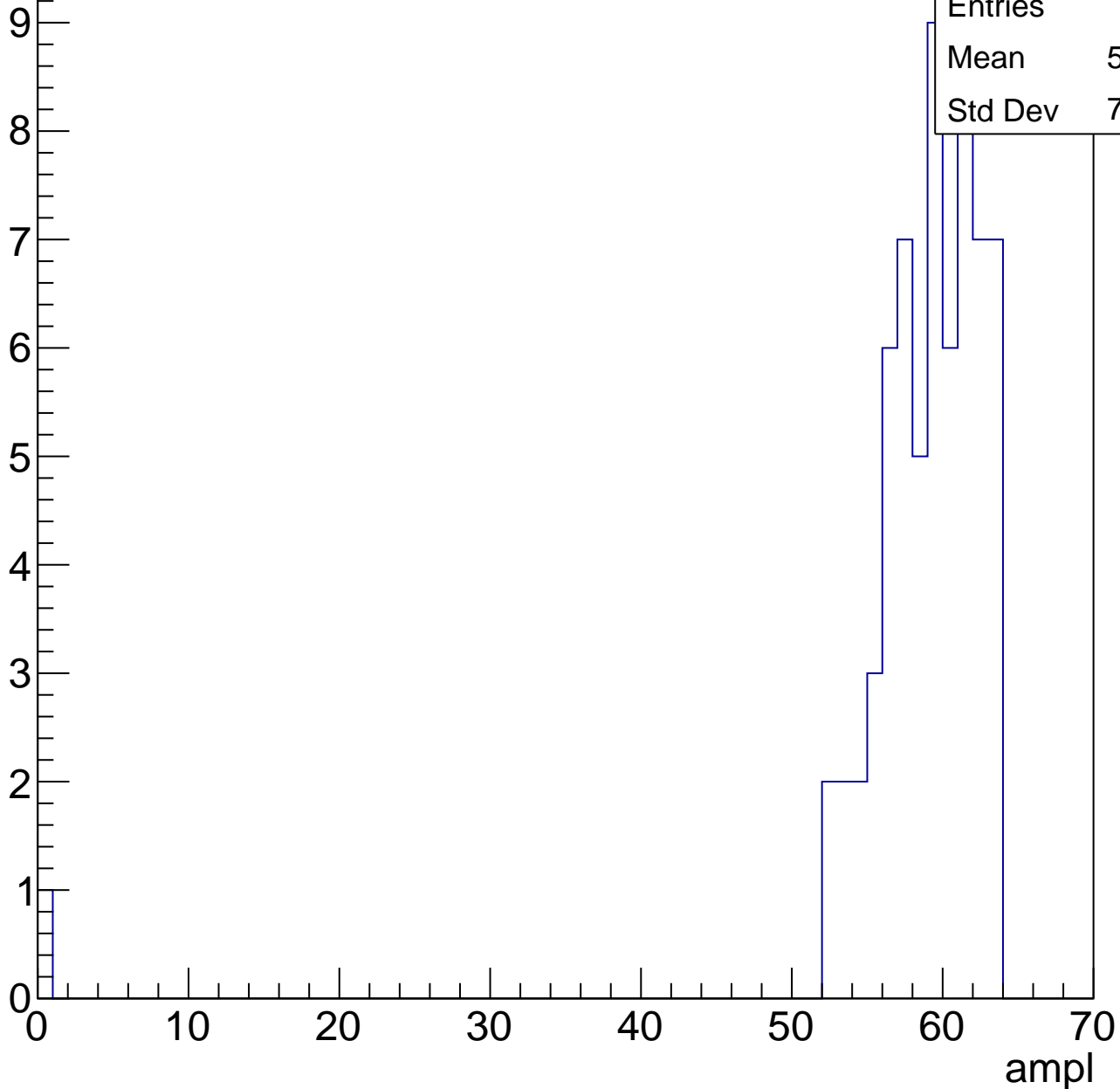


B1L100S, U14-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.92
Std Dev	7.762



B1L100S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.25

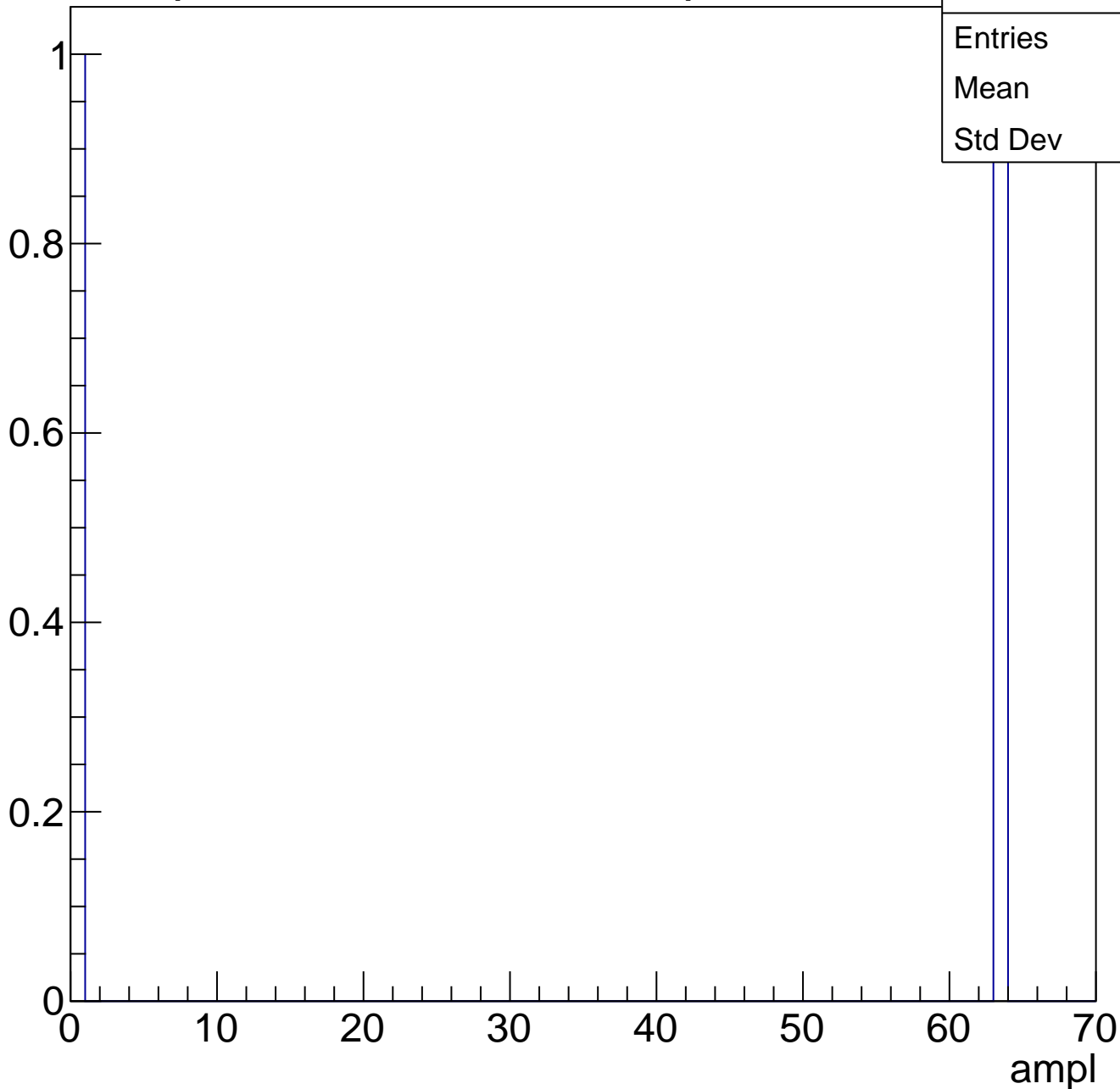
Std Dev

1.09

B1L100S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch112, adc0

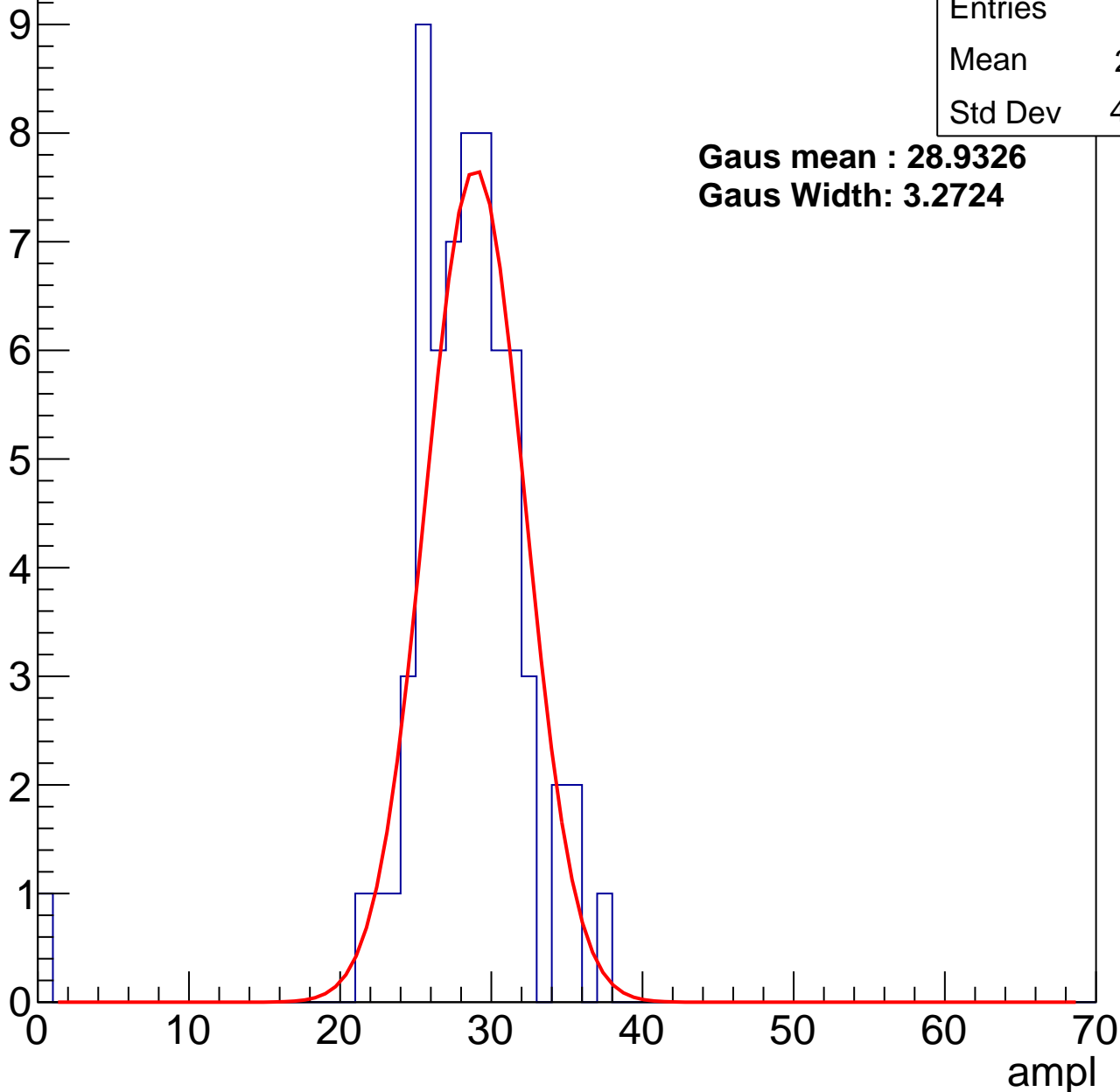
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.71
Std Dev	4.696

Gaus mean : 28.9326

Gaus Width: 3.2724



B1L100S, U14-ch112, adc1

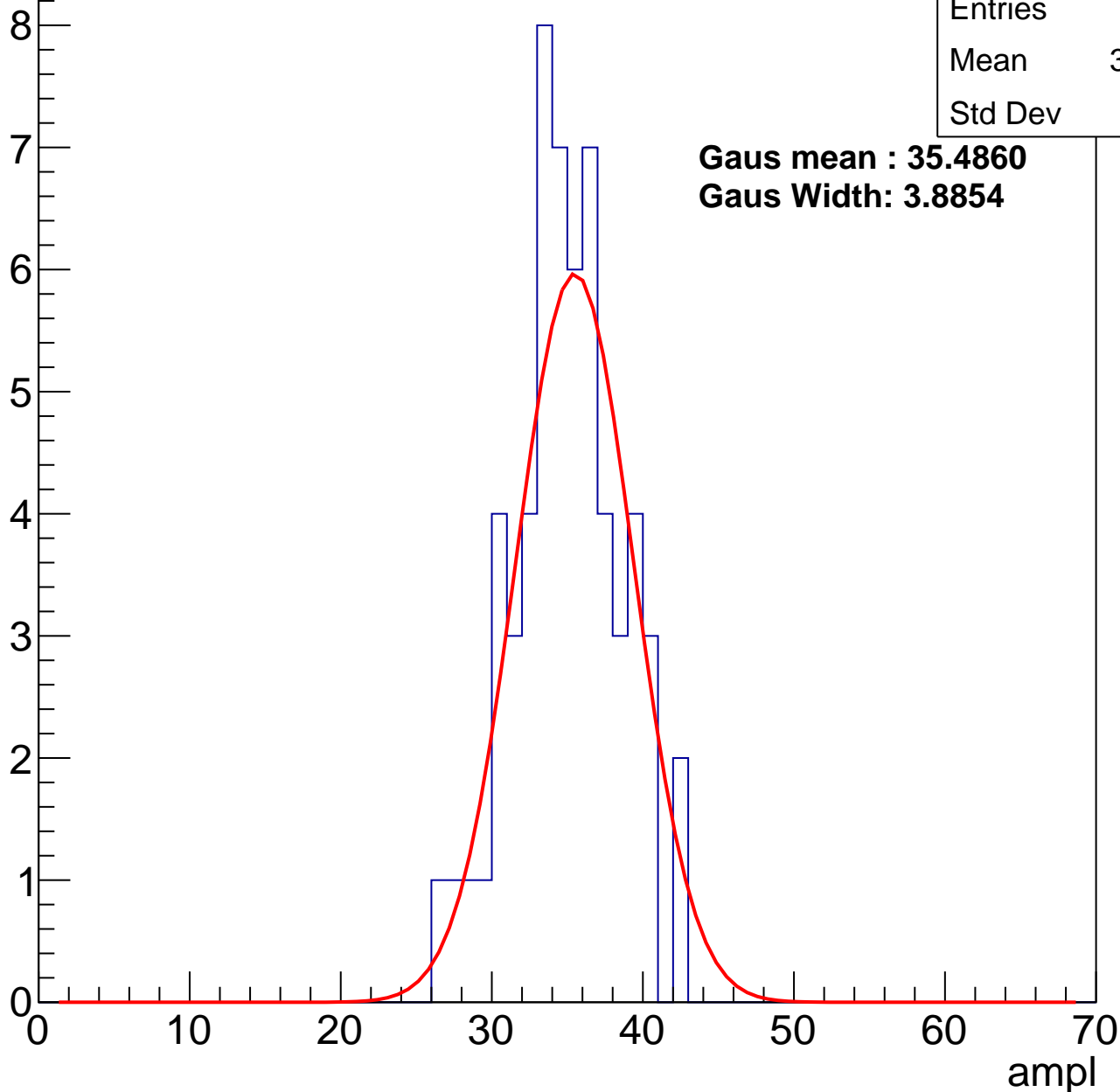
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.53
Std Dev	3.51

Gaus mean : 35.4860

Gaus Width: 3.8854



B1L100S, U14-ch112, adc2

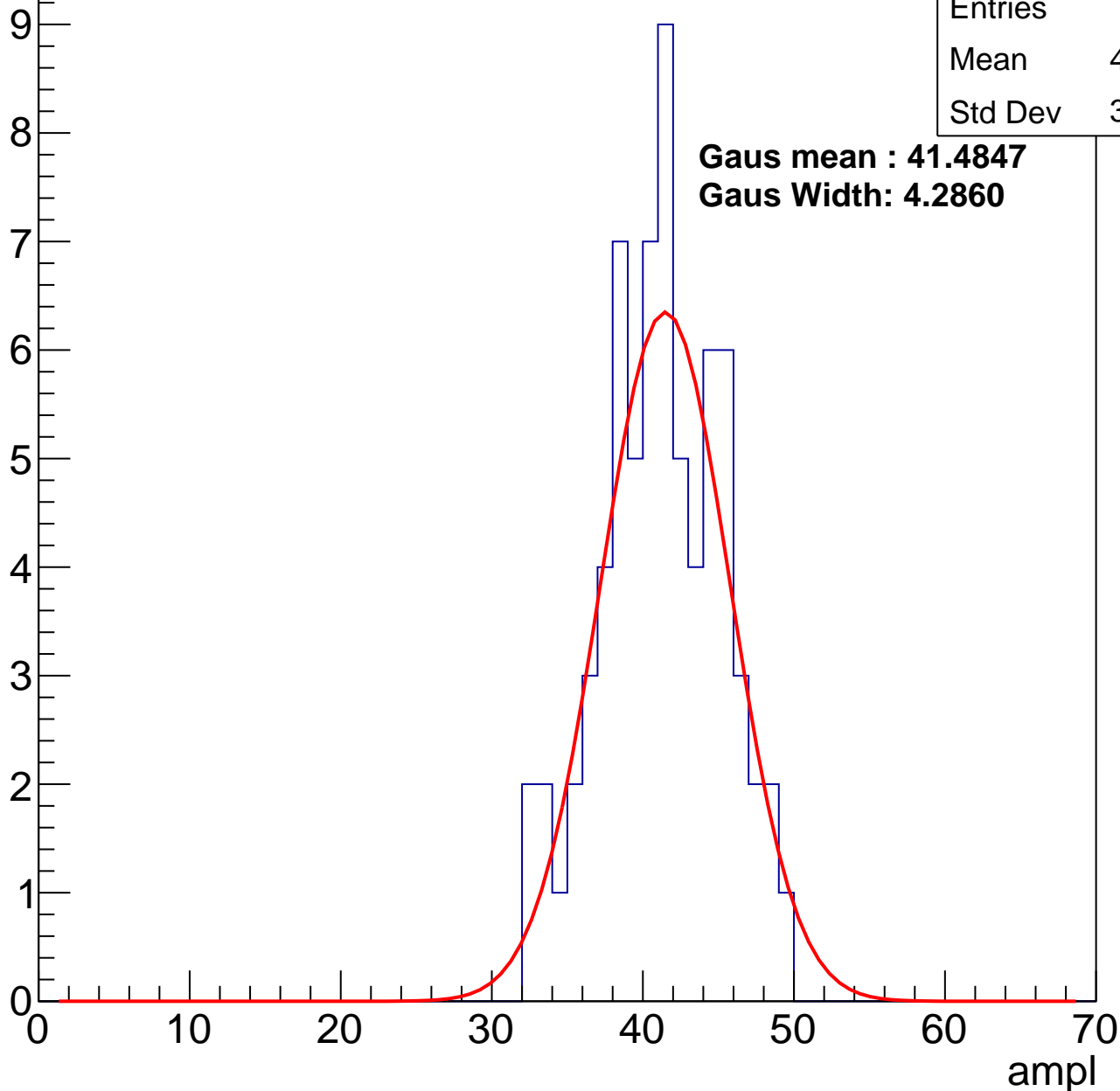
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	40.75
Std Dev	3.964

Gaus mean : 41.4847

Gaus Width: 4.2860

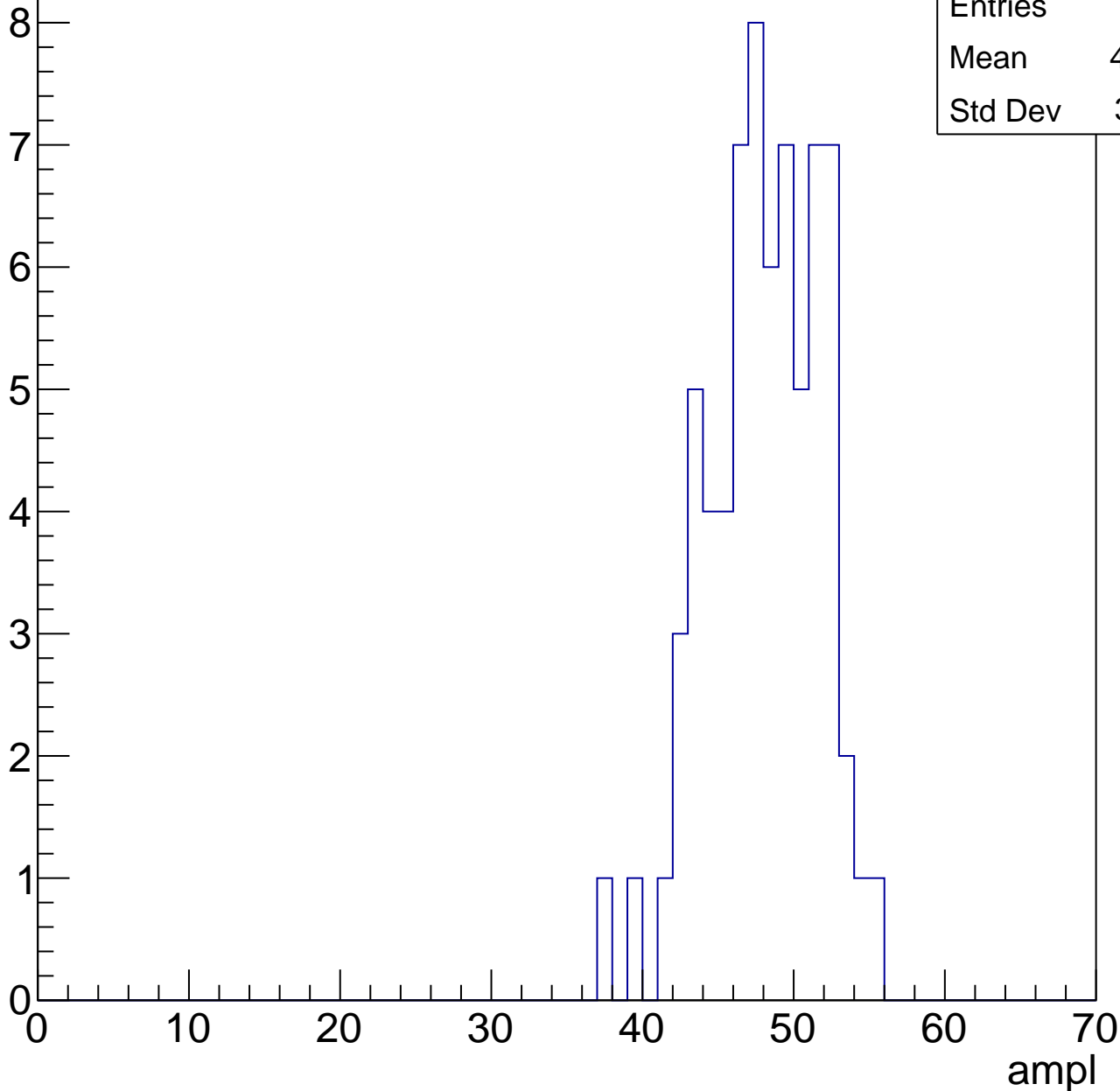


B1L100S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.56
Std Dev	3.671

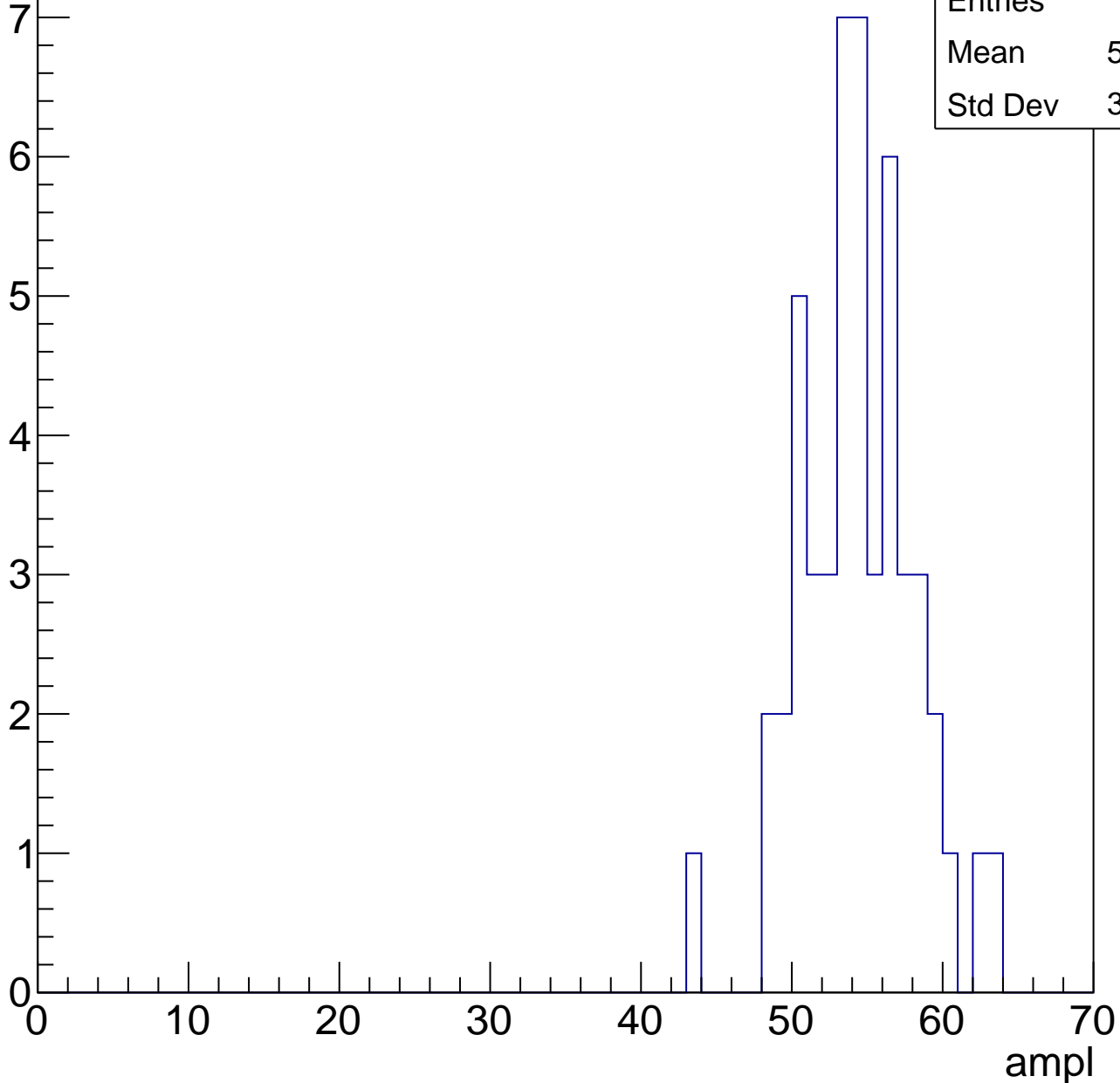


B1L100S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	53.88
Std Dev	3.745



B1L100S, U14-ch112, adc5

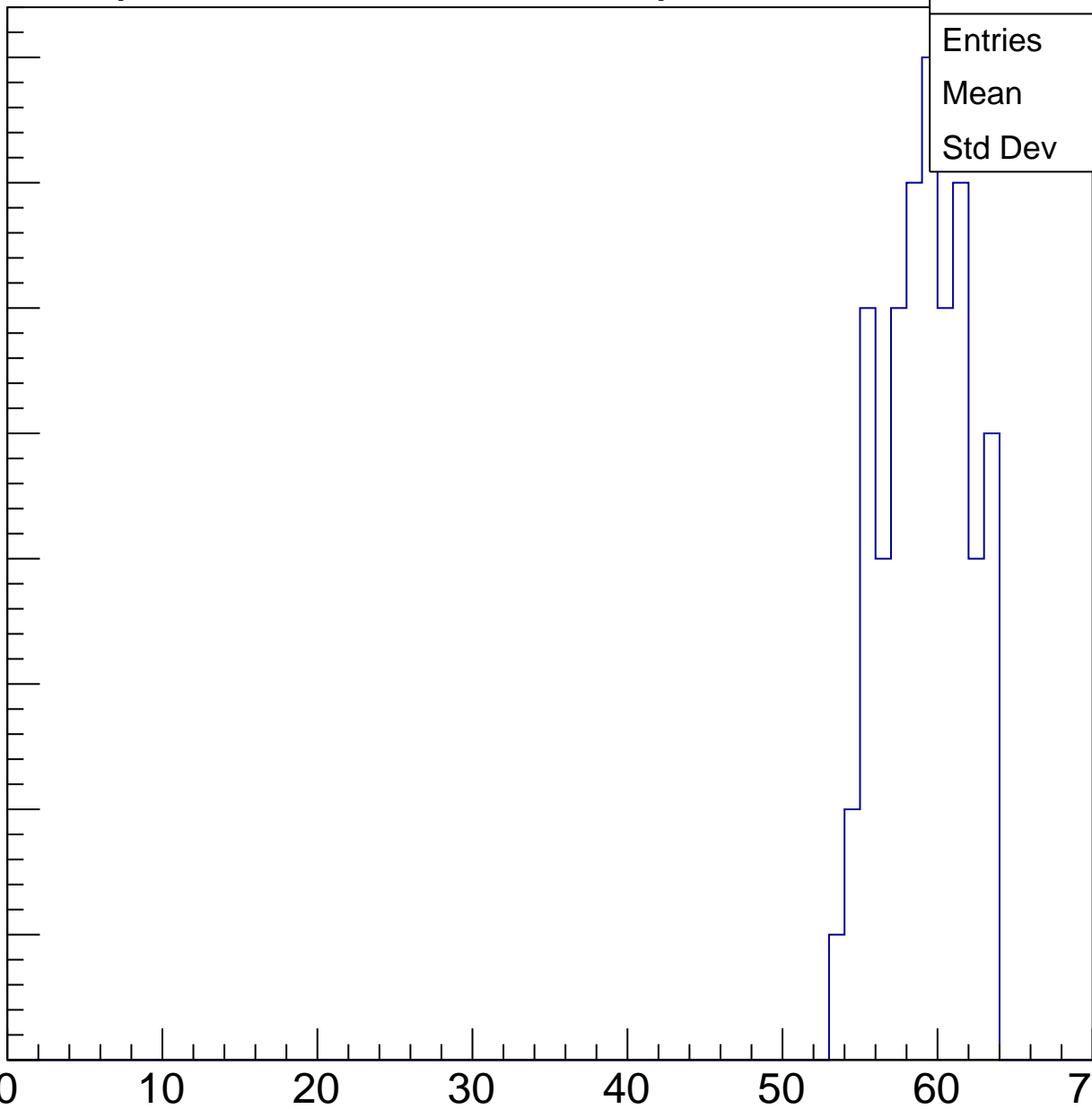
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.66
Std Dev	2.648

ampl

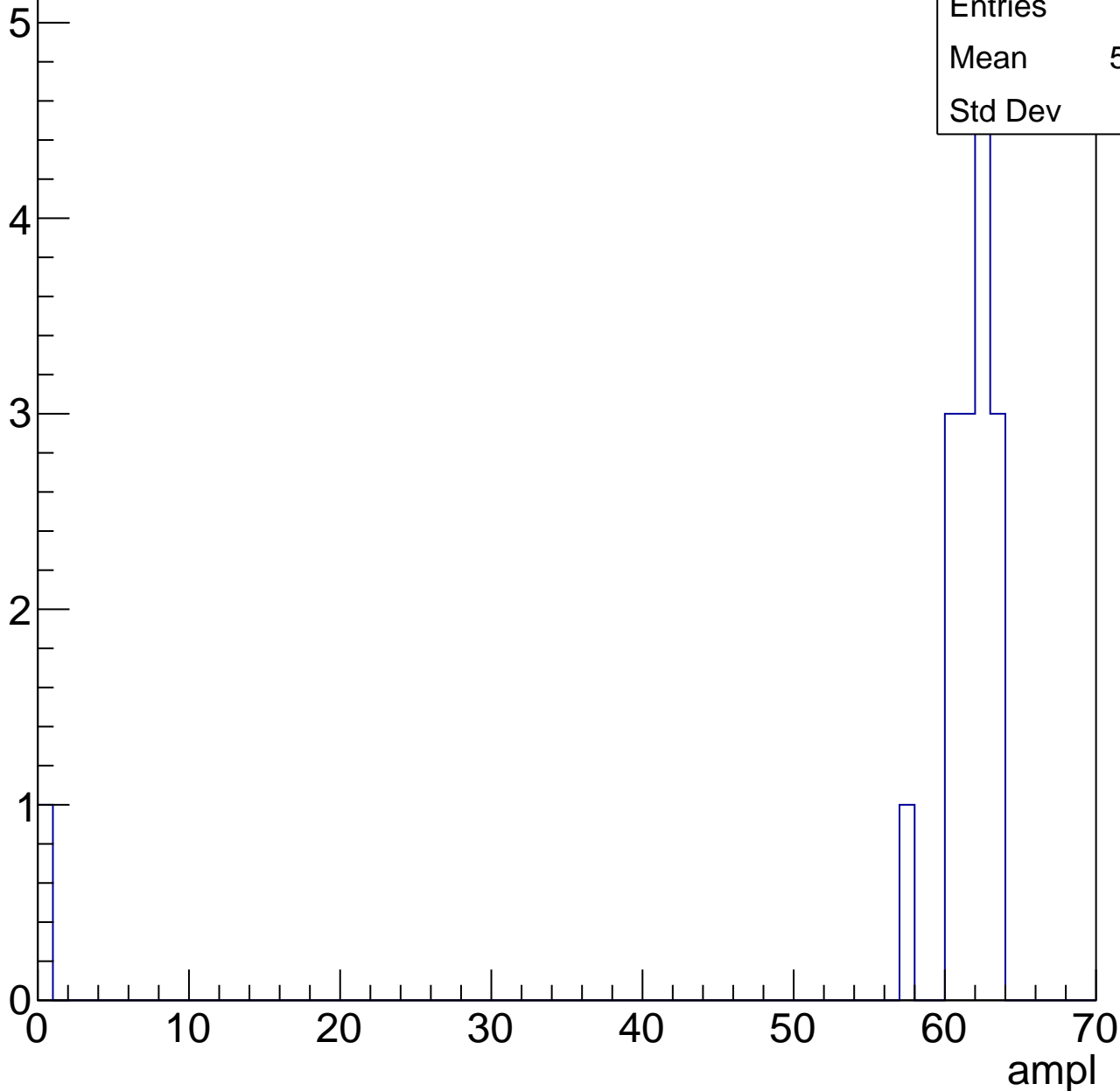


B1L100S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	57.44
Std Dev	14.9



B1L100S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch113, adc0

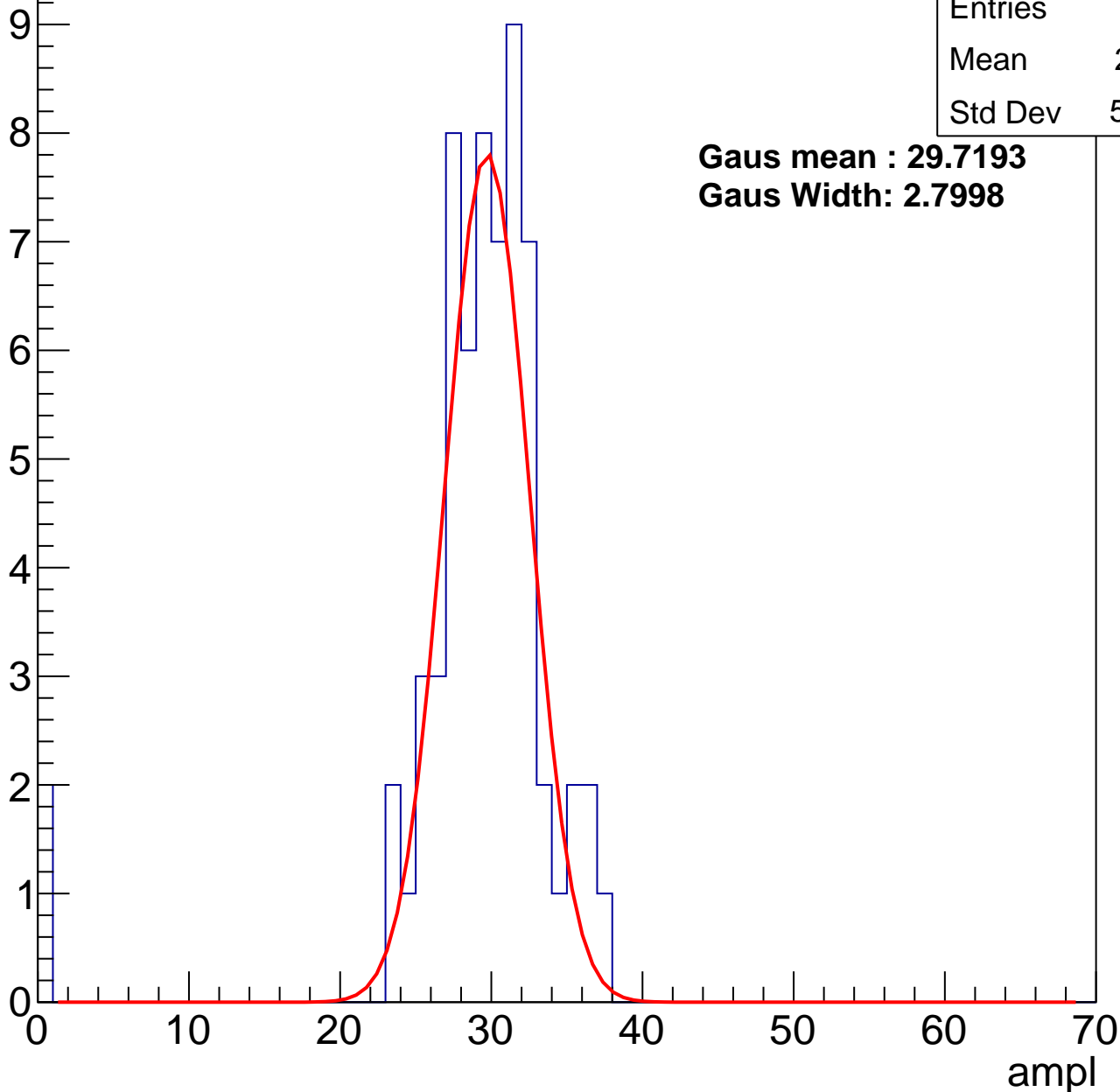
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.61
Std Dev	5.968

Gaus mean : 29.7193

Gaus Width: 2.7998



B1L100S, U14-ch113, adc1

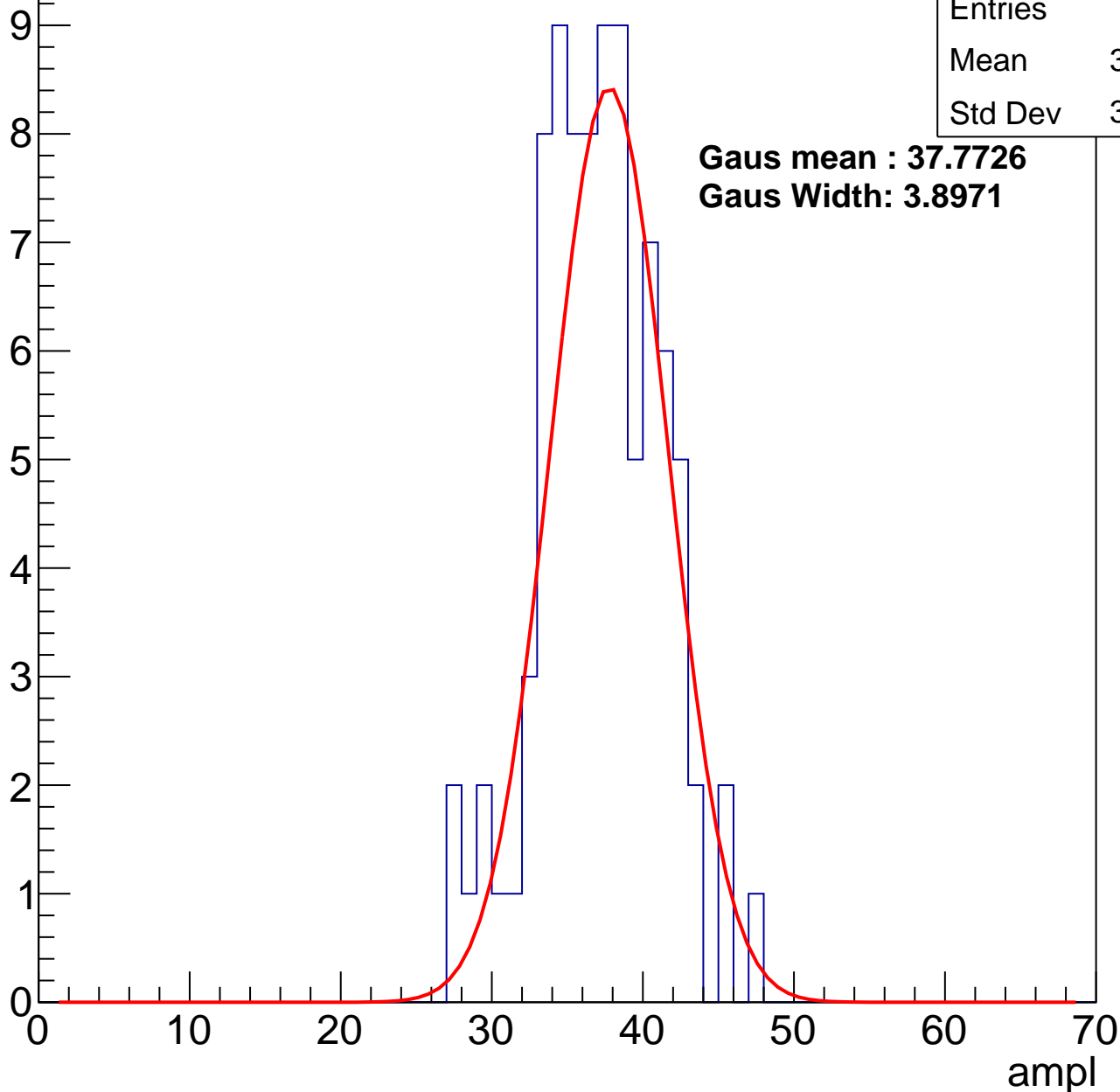
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	36.67
Std Dev	3.997

Gaus mean : 37.7726

Gaus Width: 3.8971



B1L100S, U14-ch113, adc2

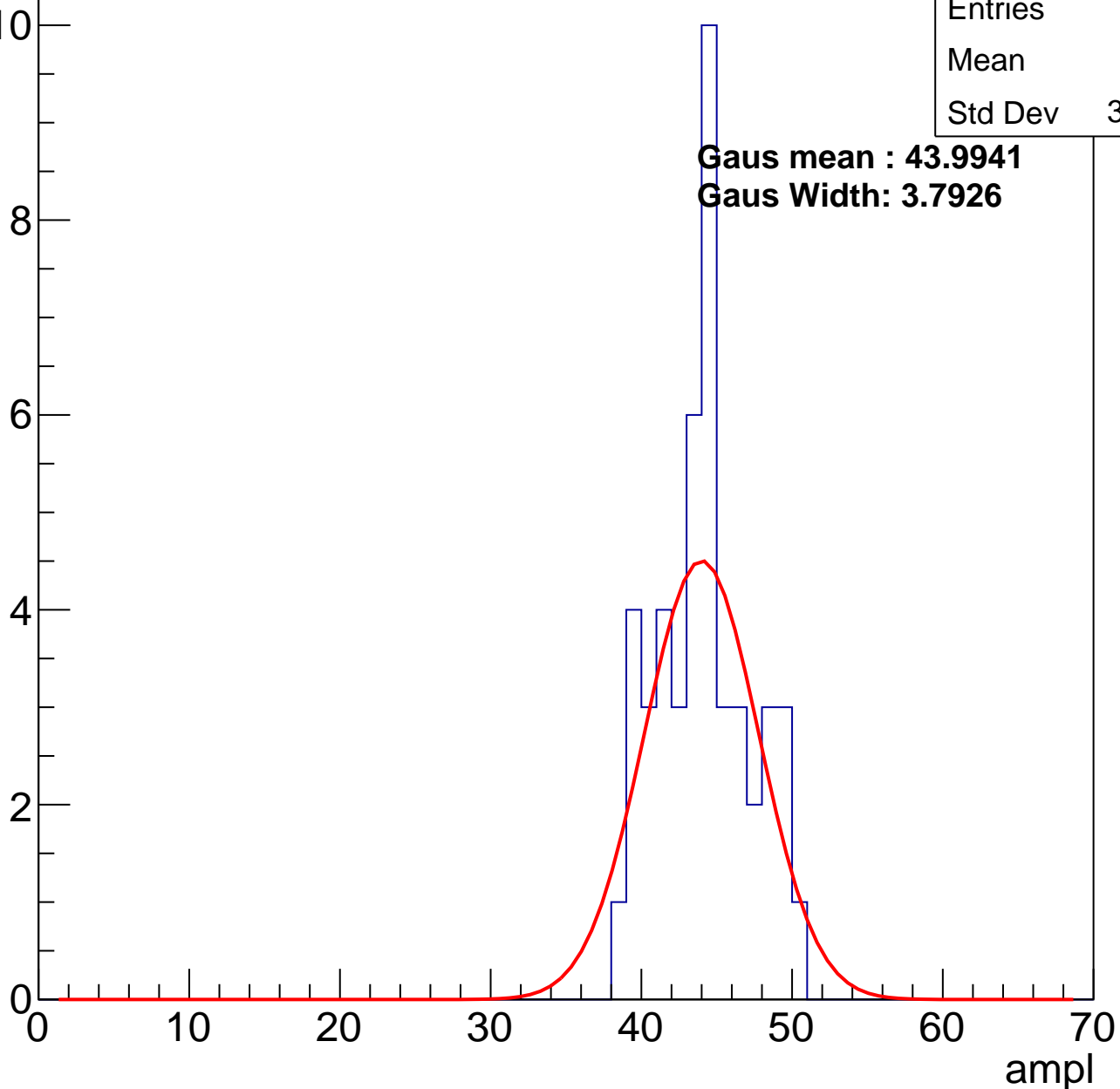
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	43.7
Std Dev	3.042

Gaus mean : 43.9941

Gaus Width: 3.7926

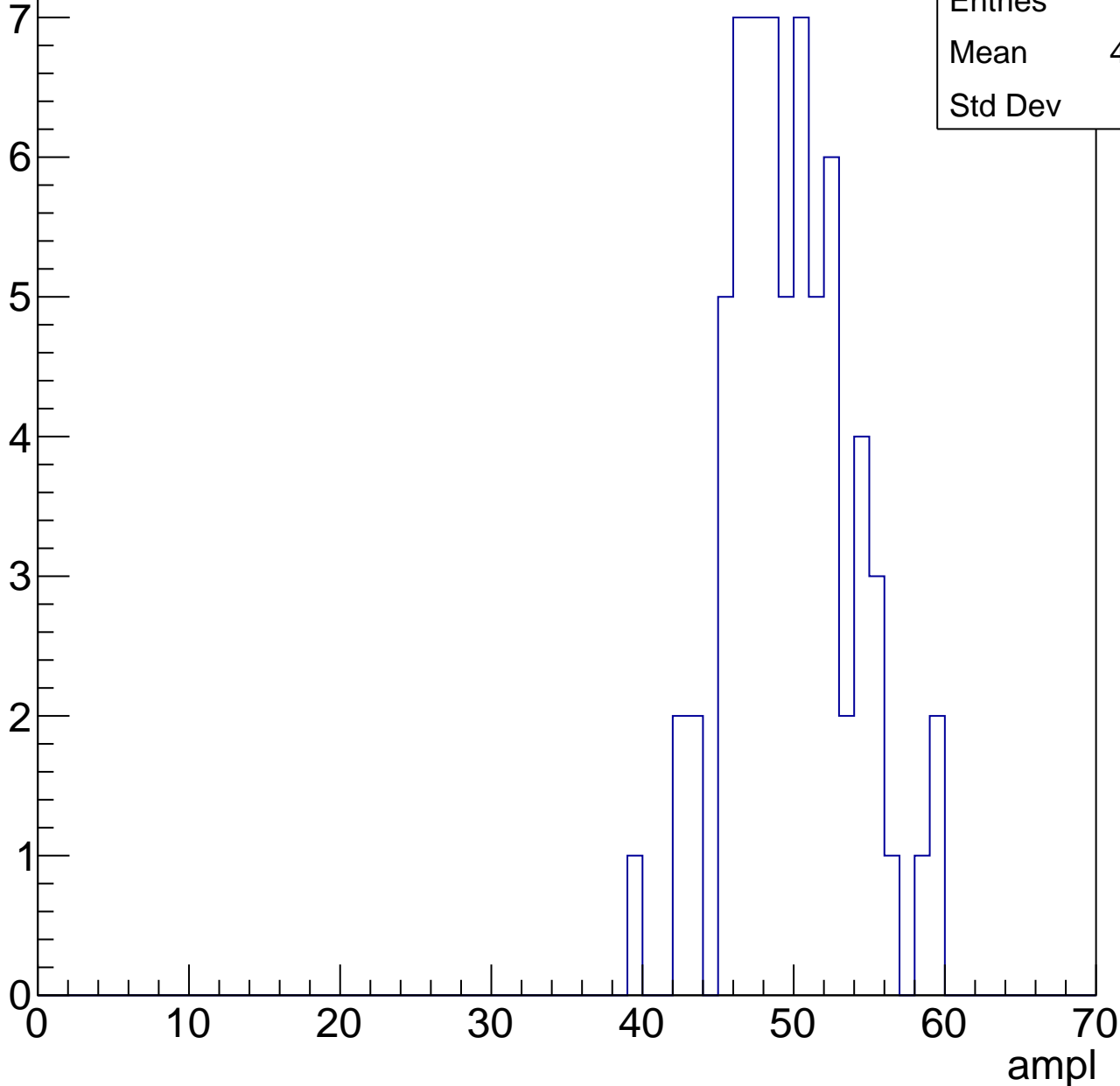


B1L100S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	49.28
Std Dev	4.04

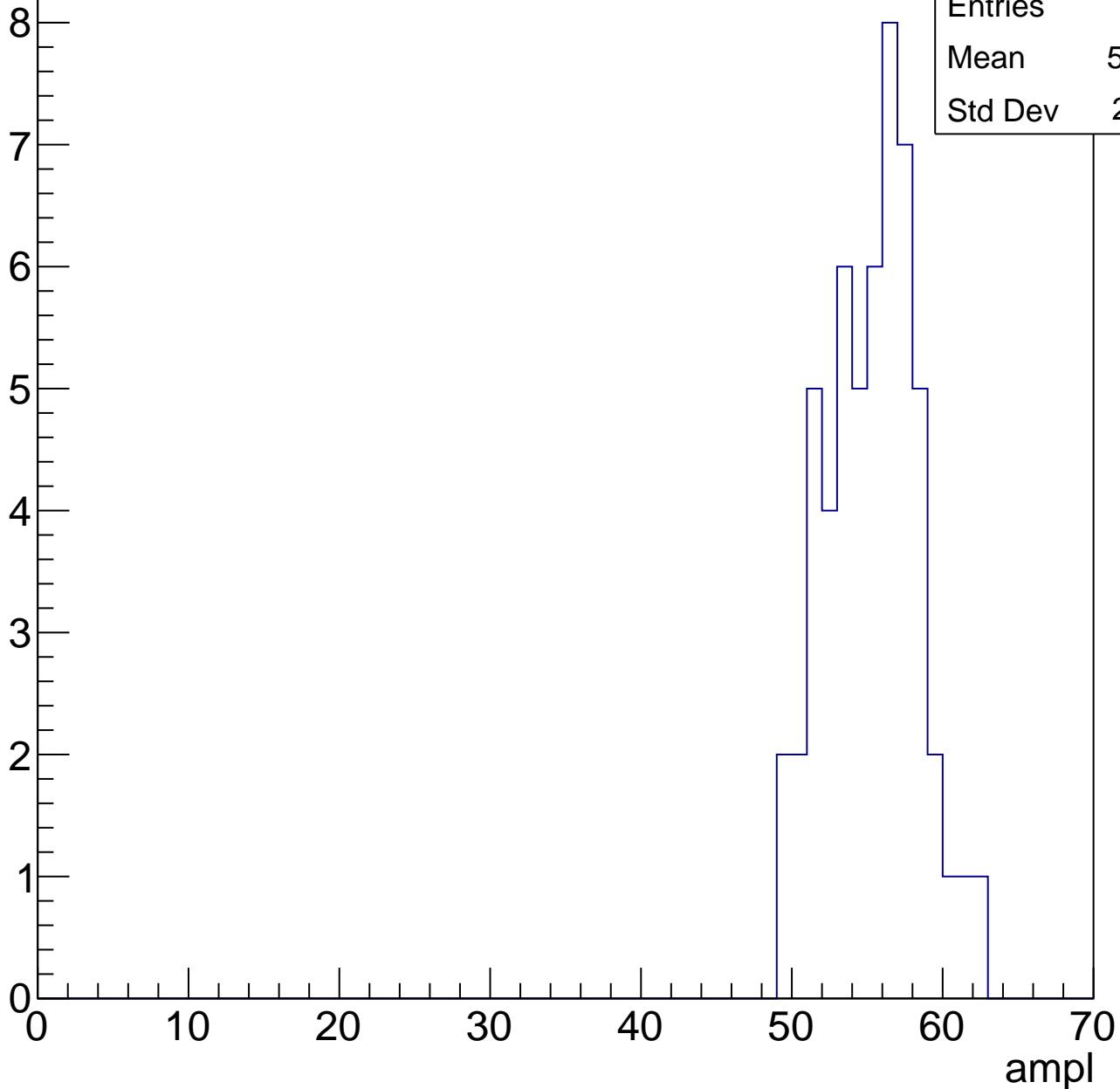


B1L100S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

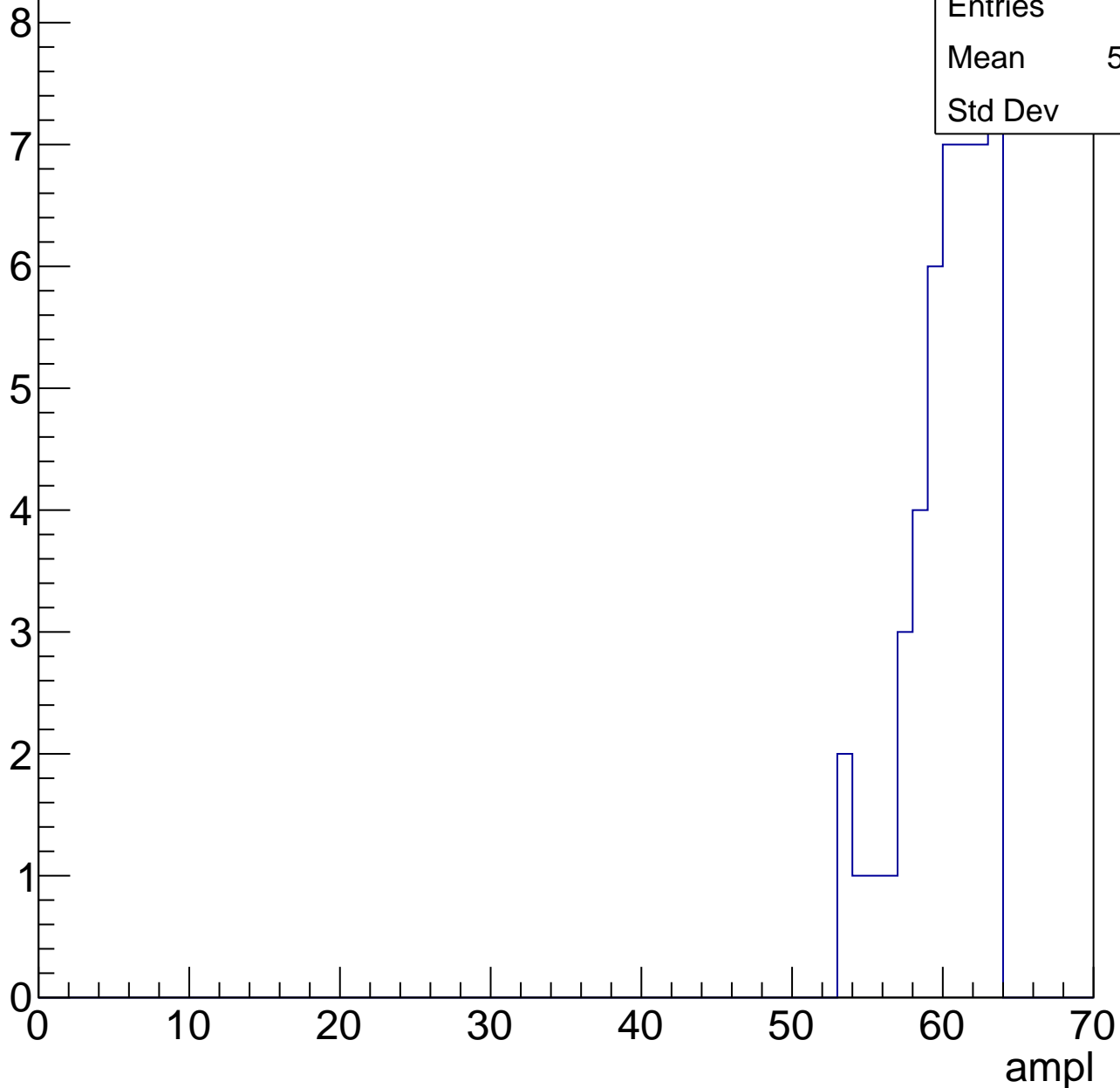
Entries	55
Mean	54.85
Std Dev	2.981



B1L100S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.67
Std Dev	19.36

0 10 20 30 40 50 60 70

ampl

B1L100S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch114, adc0

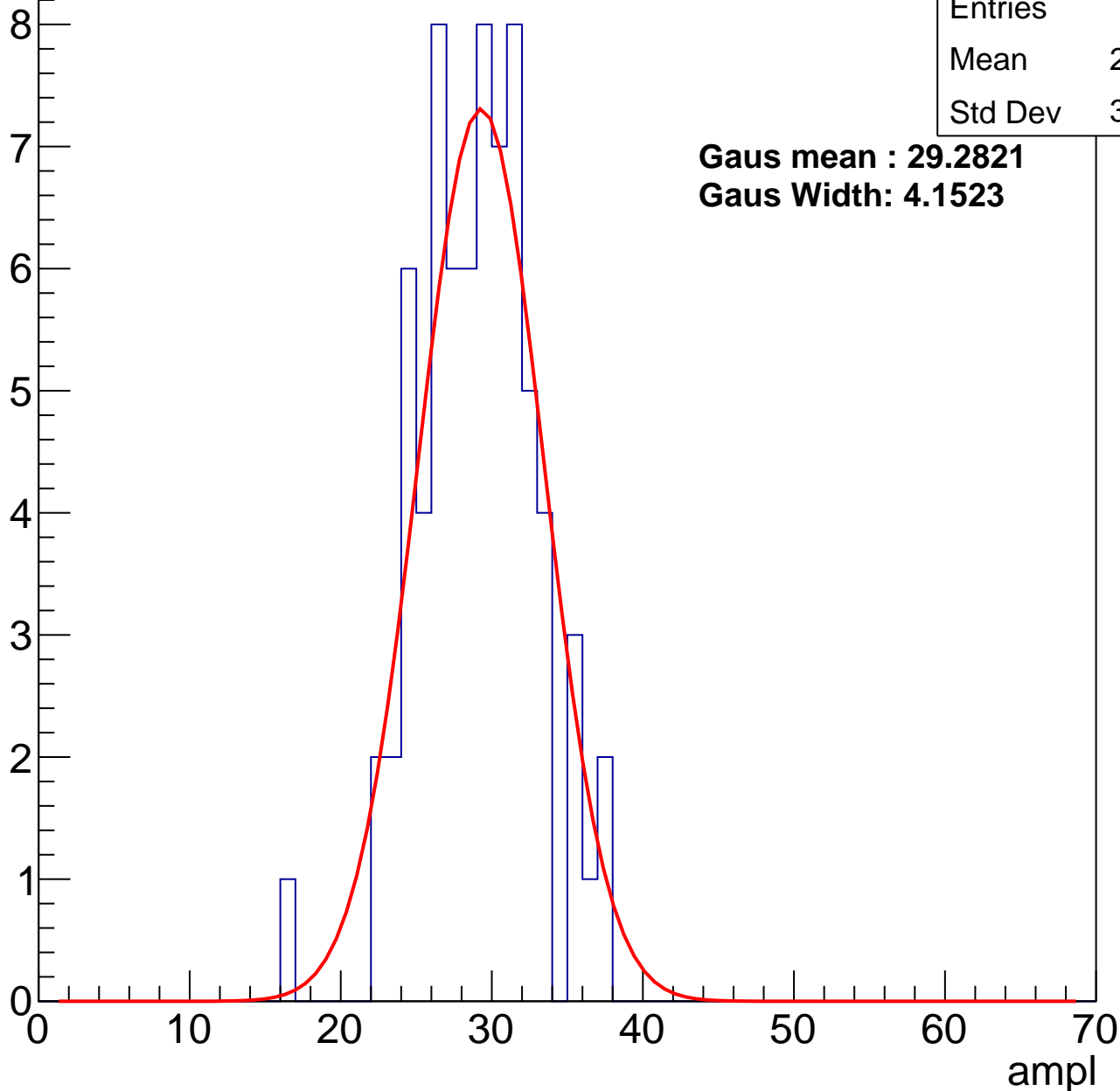
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.56
Std Dev	3.836

Gaus mean : 29.2821

Gaus Width: 4.1523



B1L100S, U14-ch114, adc1

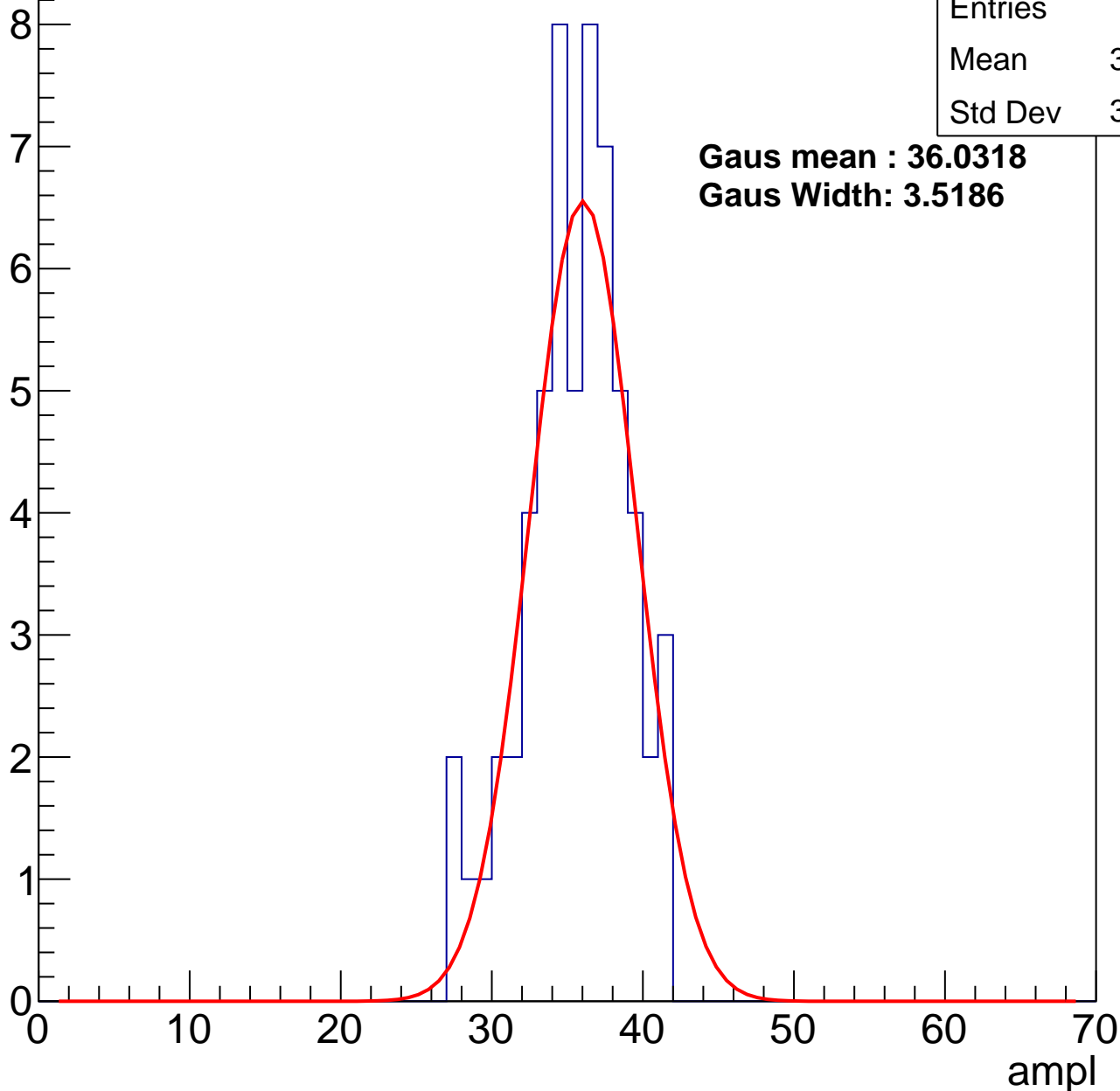
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.07
Std Dev	3.349

Gaus mean : 36.0318

Gaus Width: 3.5186



B1L100S, U14-ch114, adc2

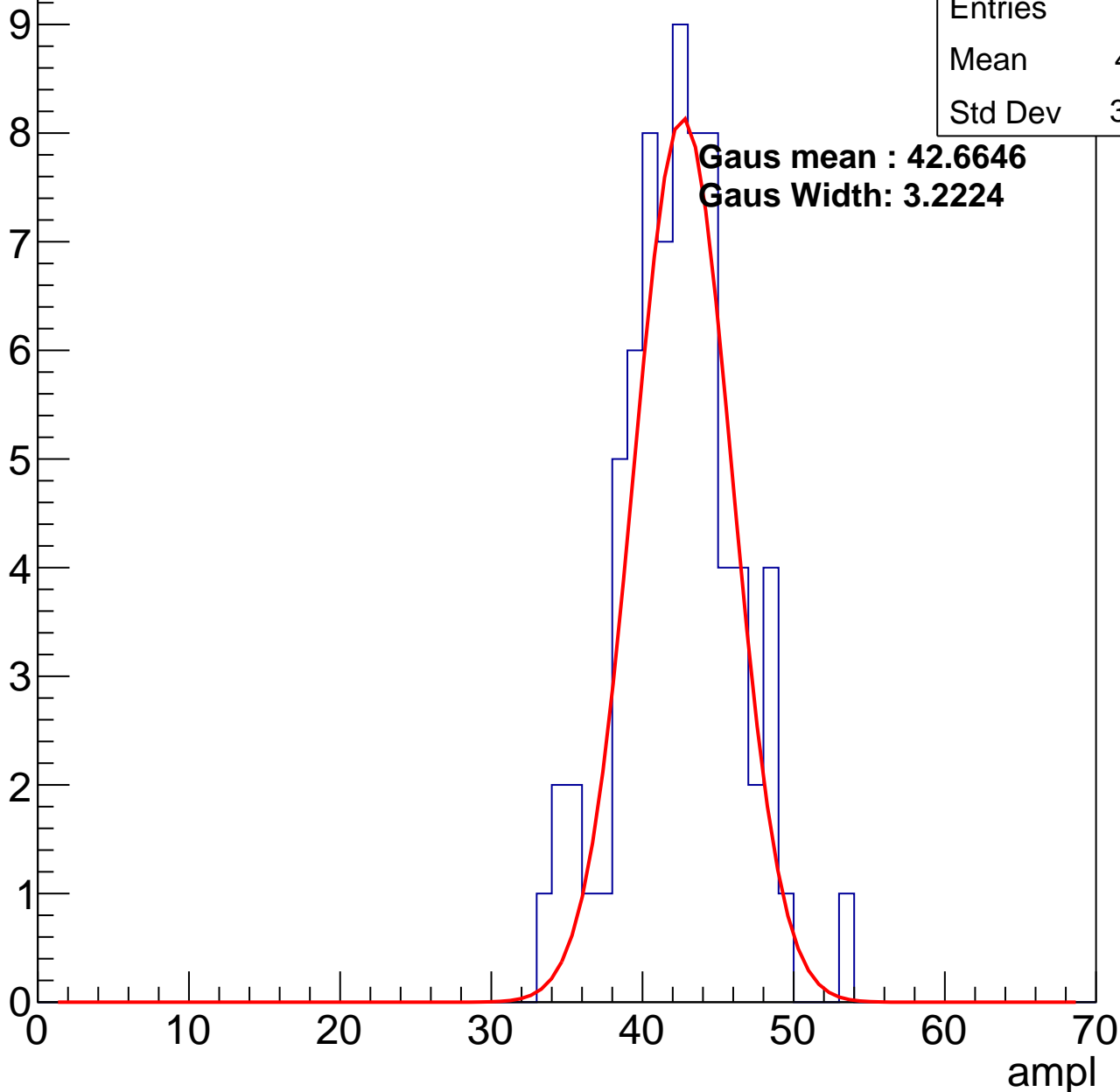
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	41.91
Std Dev	3.757

Gaus mean : 42.6646

Gaus Width: 3.2224

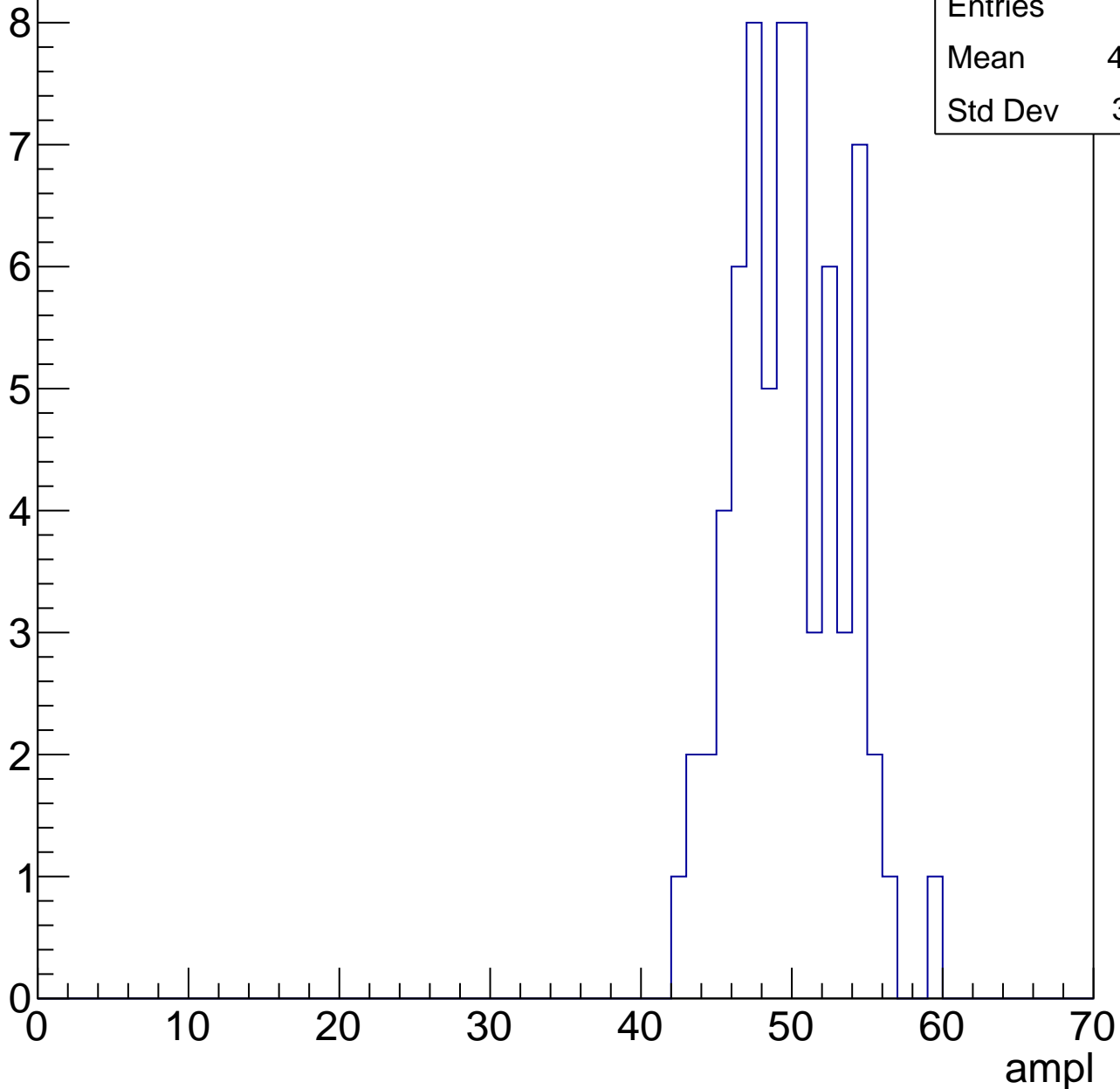


B1L100S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	49.36
Std Dev	3.531

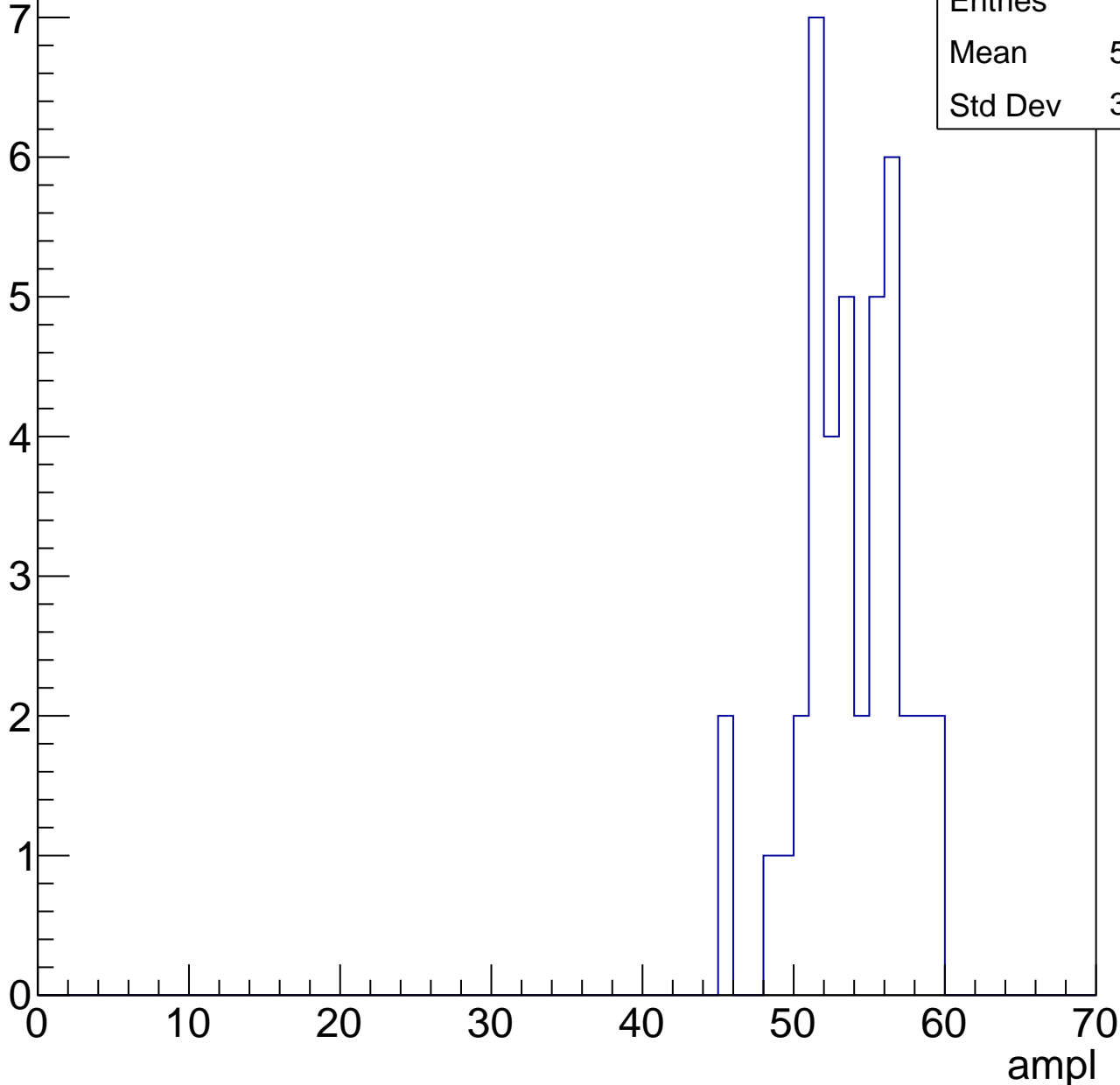


B1L100S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	53.27
Std Dev	3.298

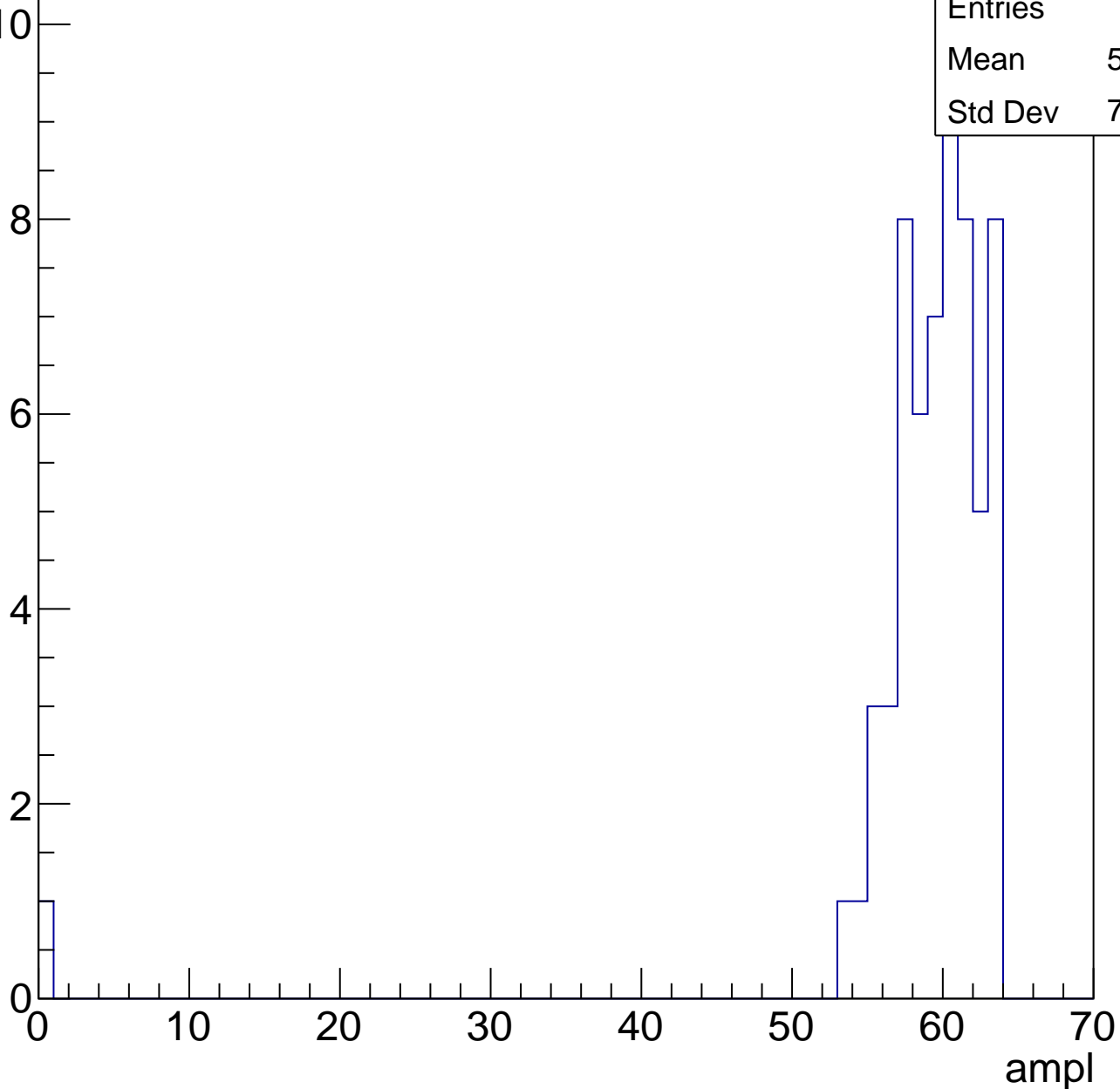


B1L100S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	58.34
Std Dev	7.938

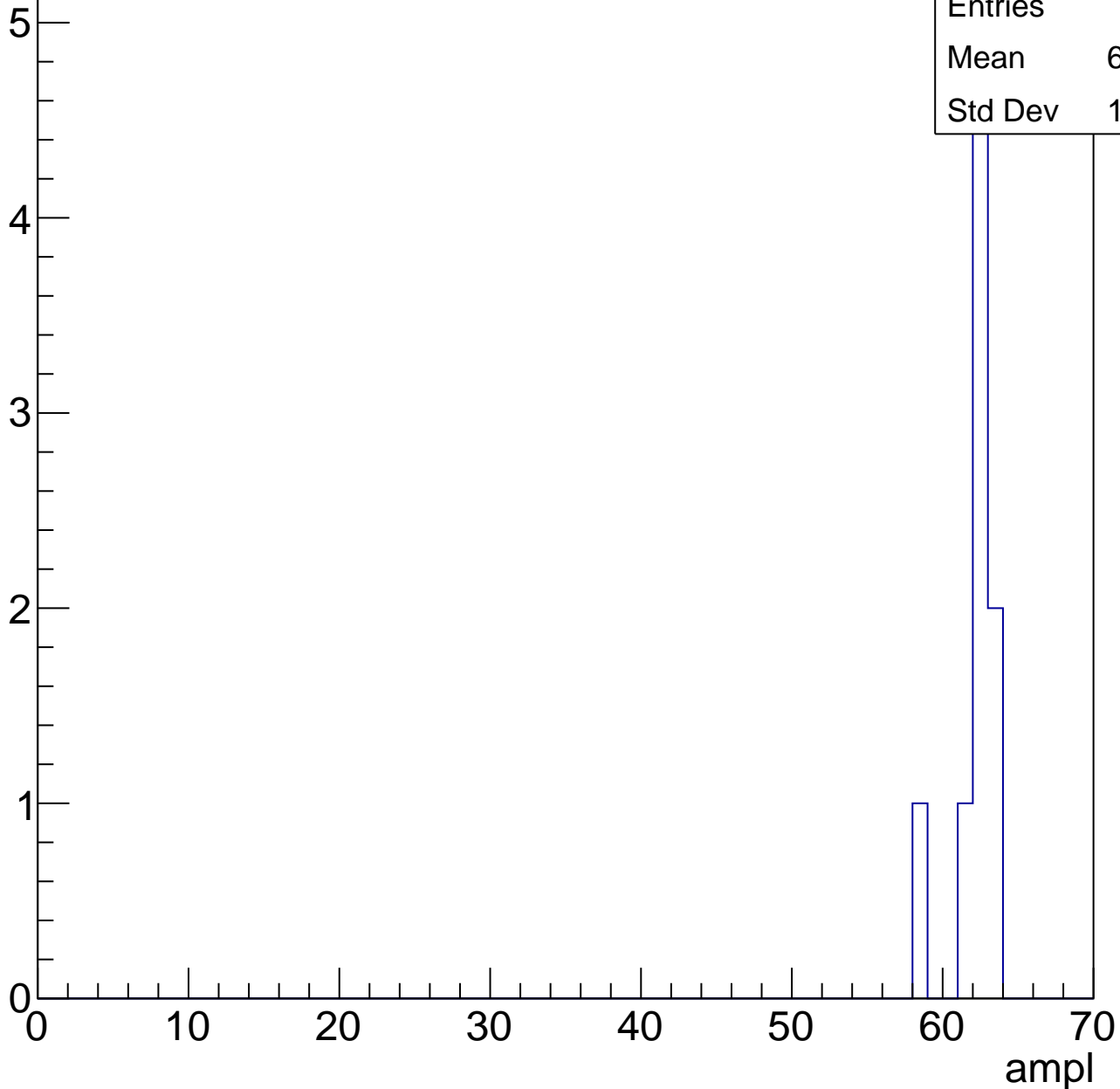


B1L100S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

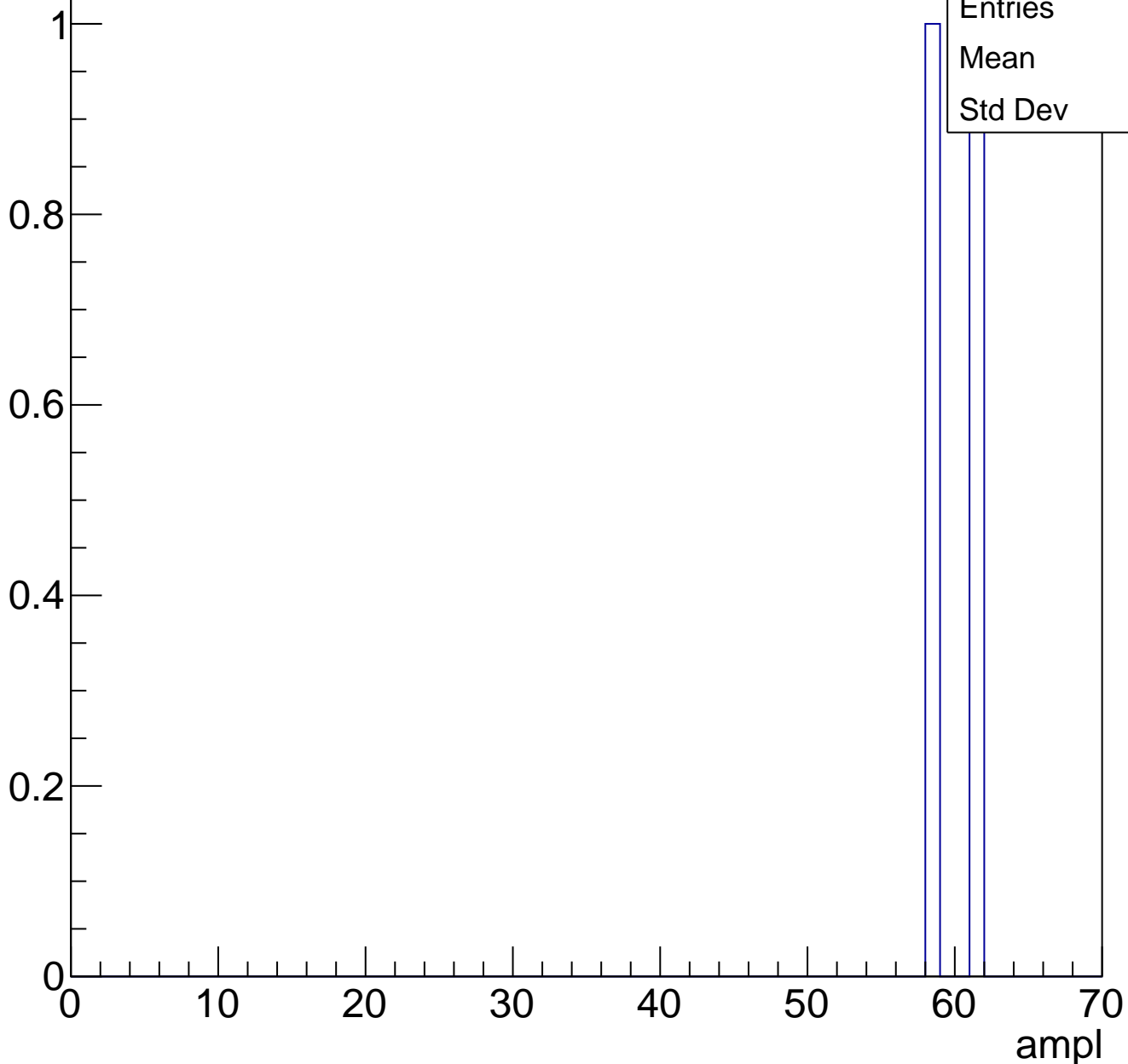
Entries	9
Mean	61.67
Std Dev	1.414



B1L100S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	59.5
Std Dev	1.5

B1L100S, U14-ch115, adc0

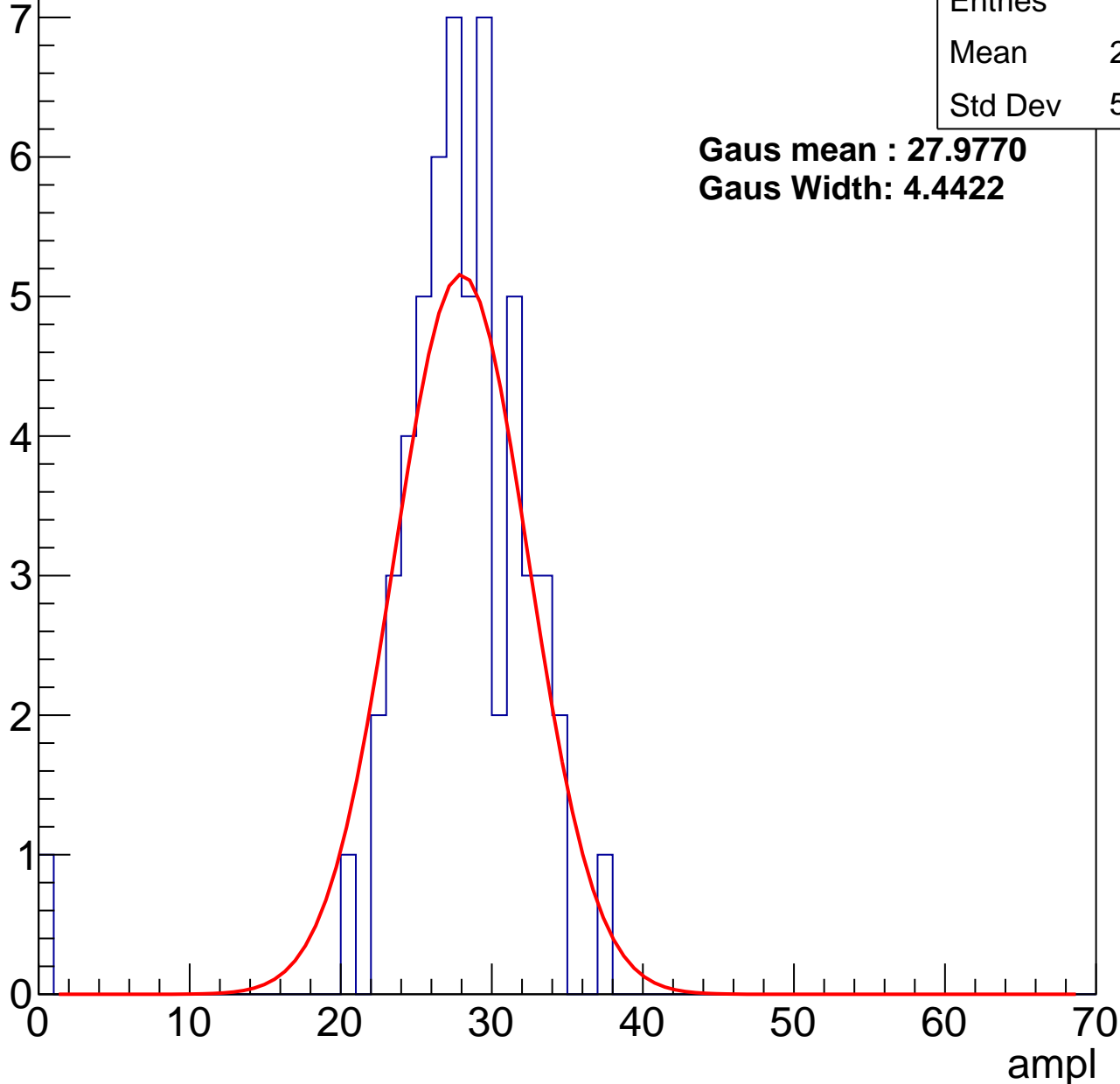
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	27.32
Std Dev	5.034

Gaus mean : 27.9770

Gaus Width: 4.4422



B1L100S, U14-ch115, adc1

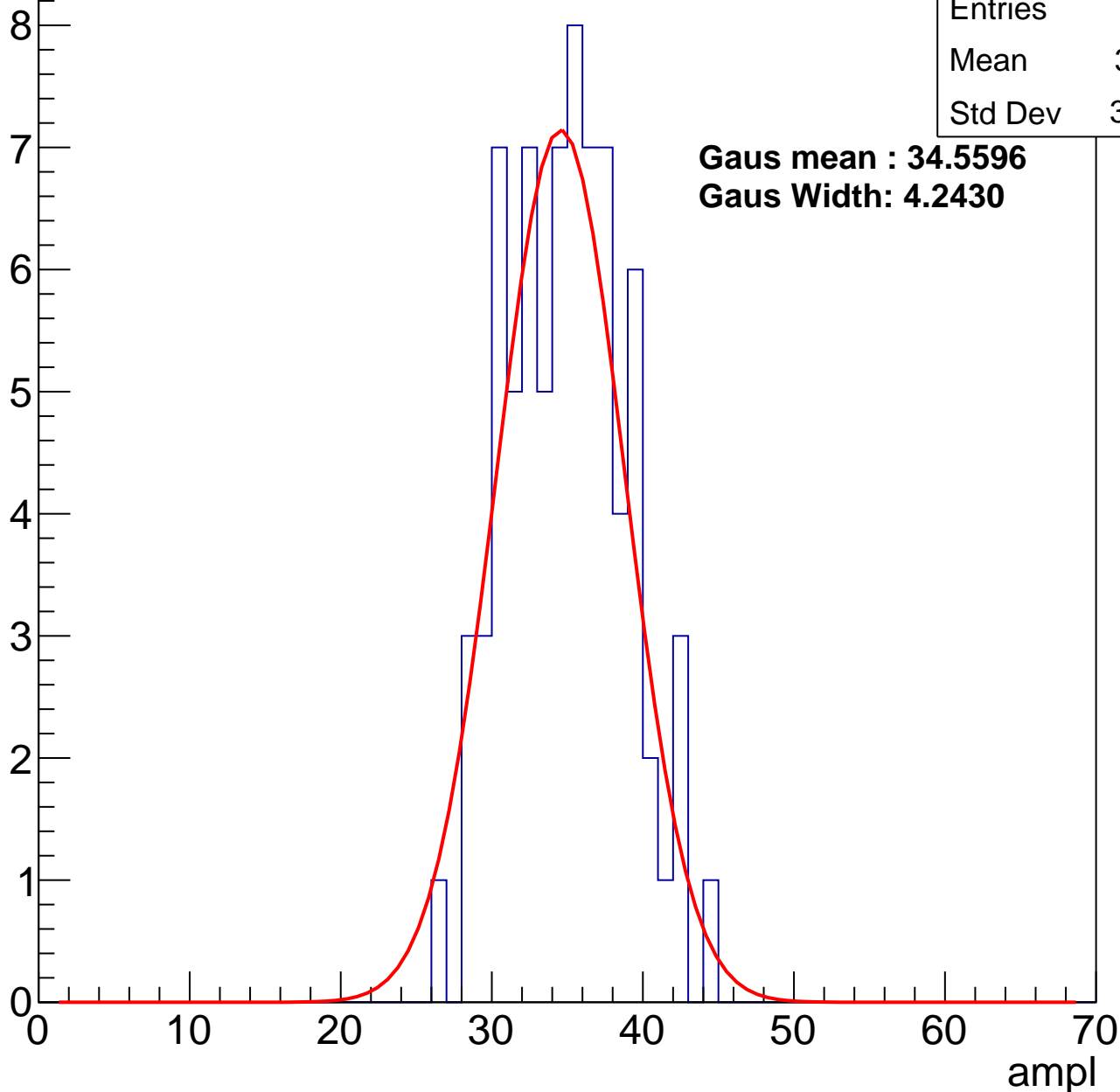
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	34.51
Std Dev	3.853

Gaus mean : 34.5596

Gaus Width: 4.2430



B1L100S, U14-ch115, adc2

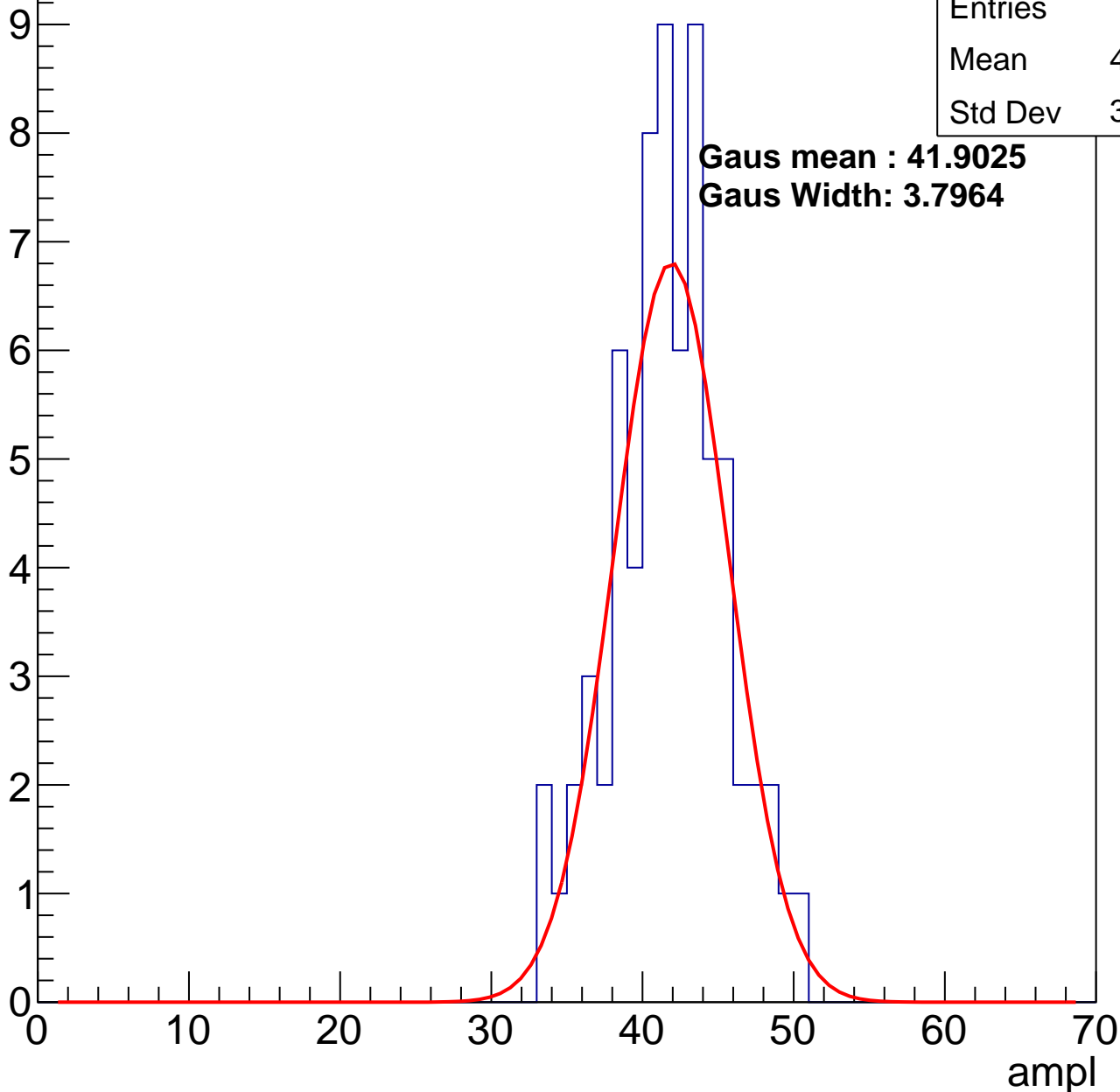
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	41.29
Std Dev	3.704

Gaus mean : 41.9025

Gaus Width: 3.7964

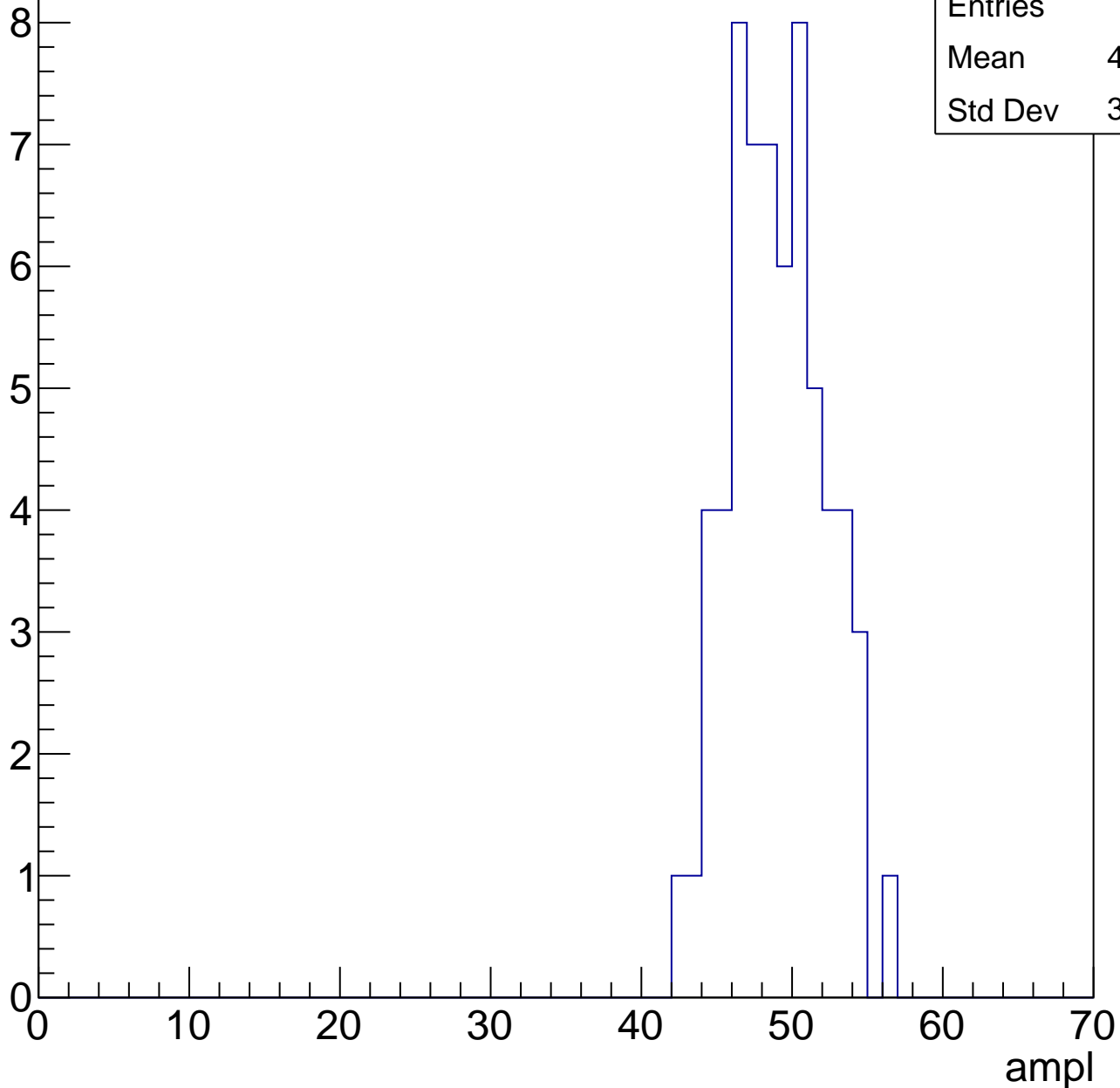


B1L100S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.59
Std Dev	3.079

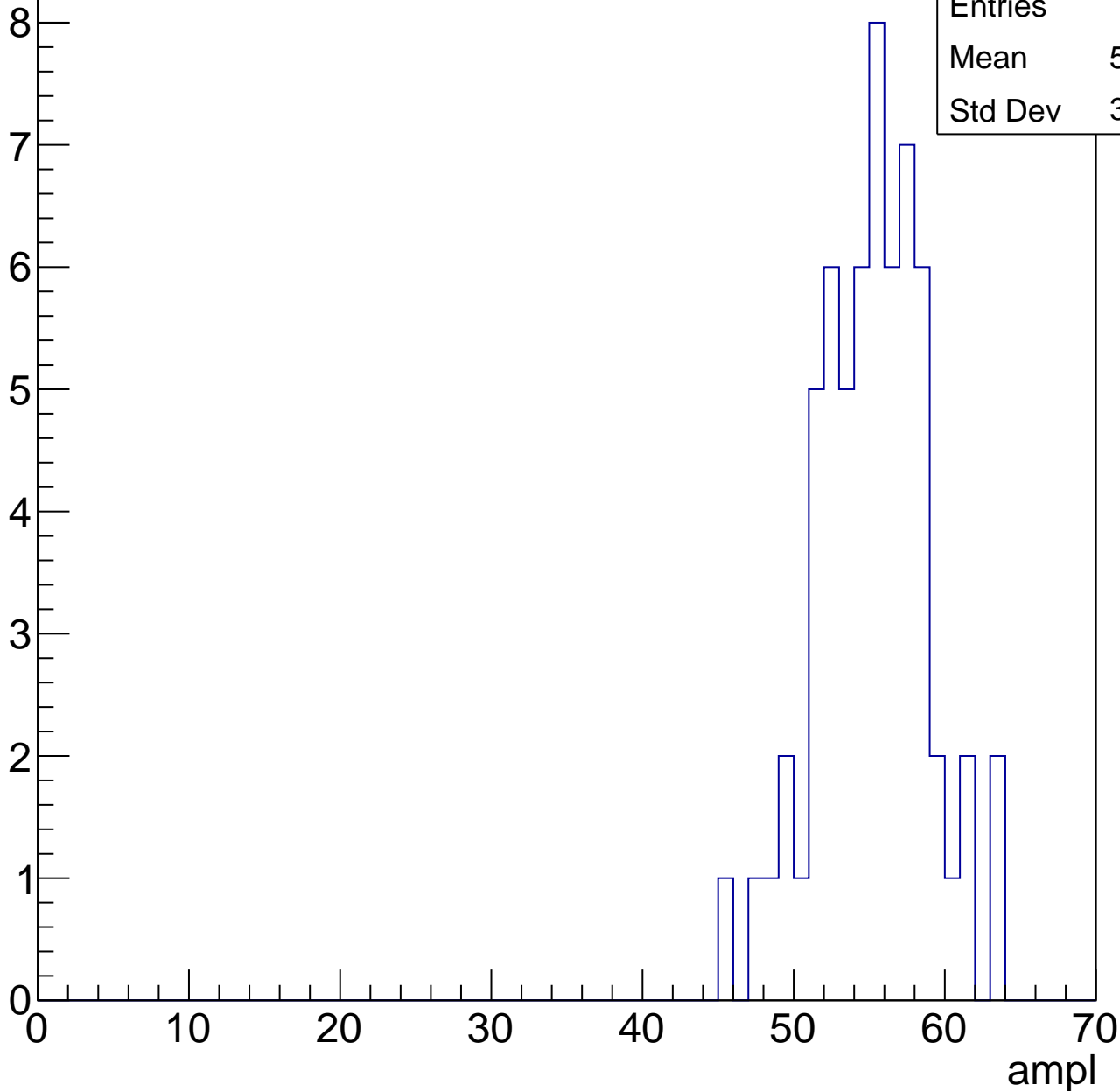


B1L100S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

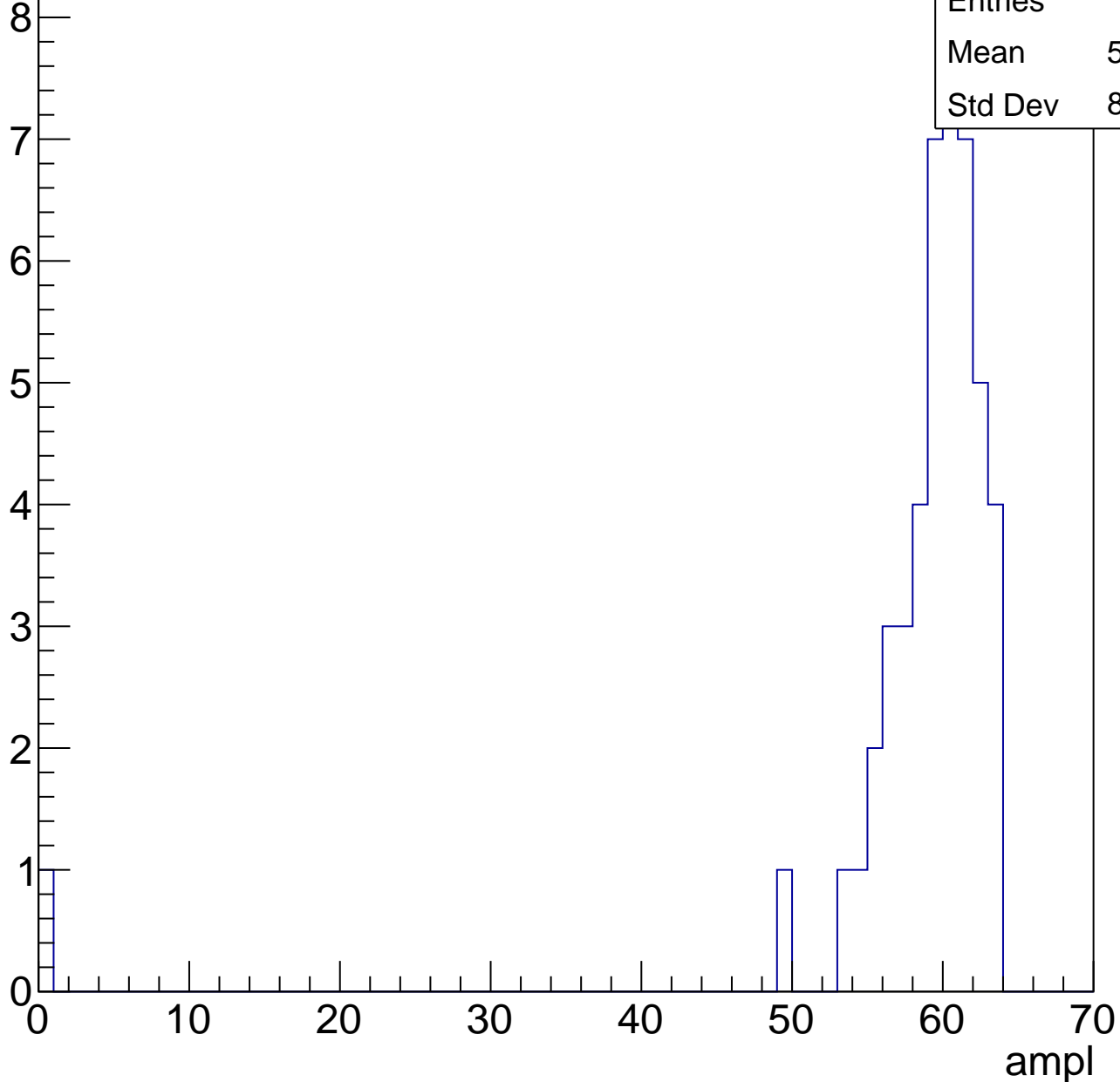
Entries	62
Mean	54.73
Std Dev	3.615



B1L100S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

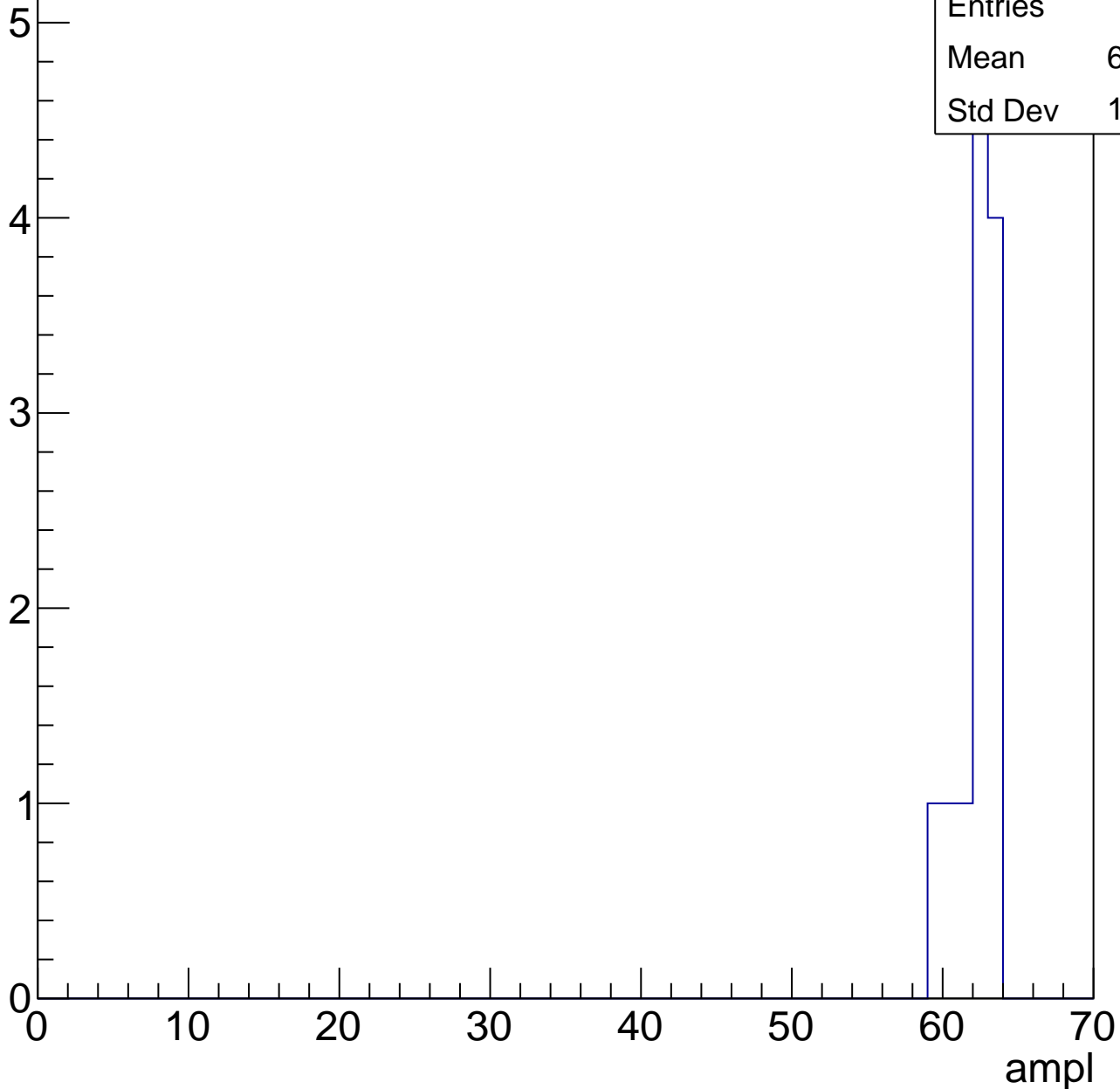


B1L100S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	61.83
Std Dev	1.213



B1L100S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch116, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	30.25
Std Dev	5.293

Gaus mean : 31.3343

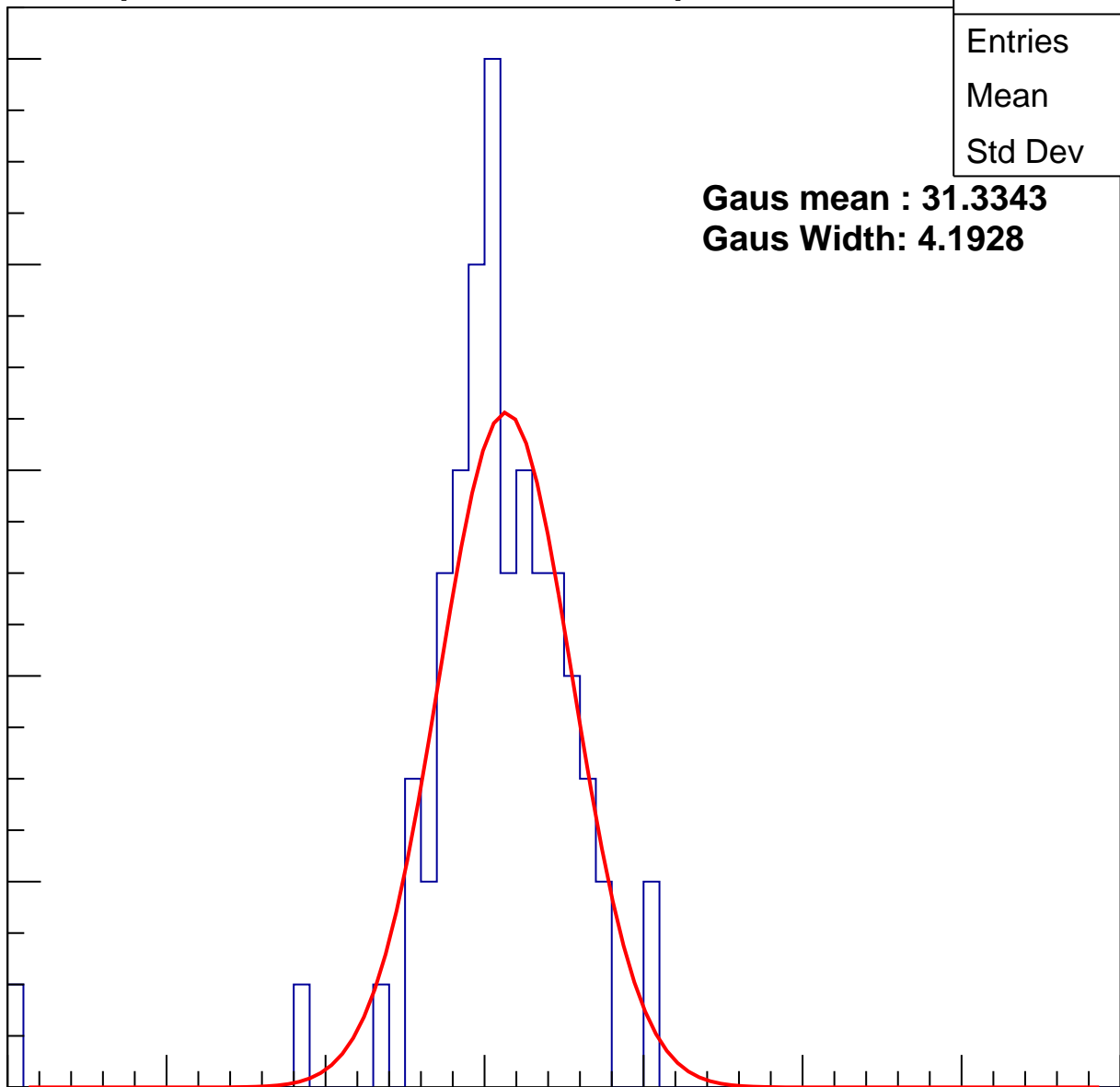
Gaus Width: 4.1928

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch116, adc1

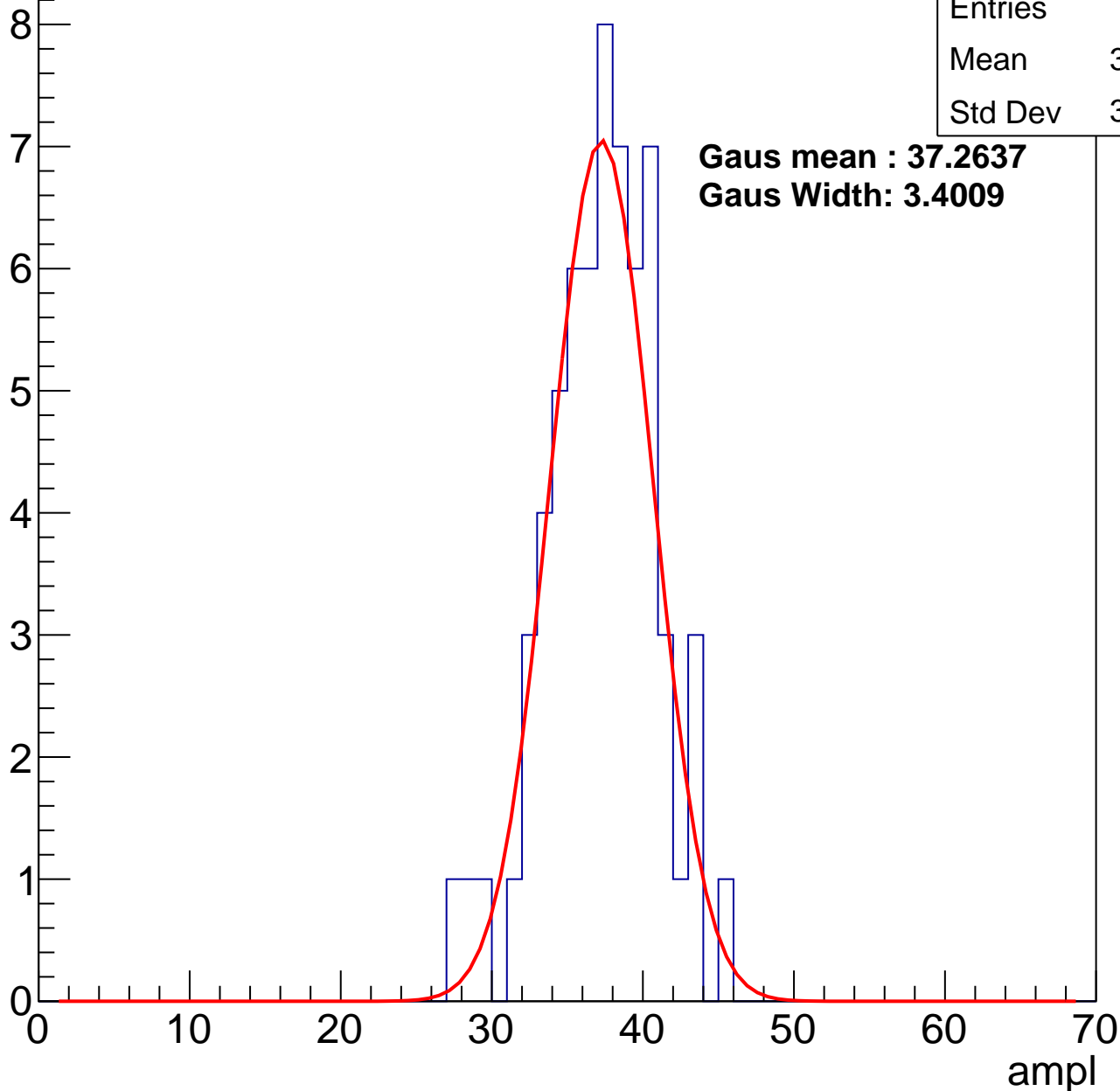
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.78
Std Dev	3.603

Gaus mean : 37.2637

Gaus Width: 3.4009



B1L100S, U14-ch116, adc2

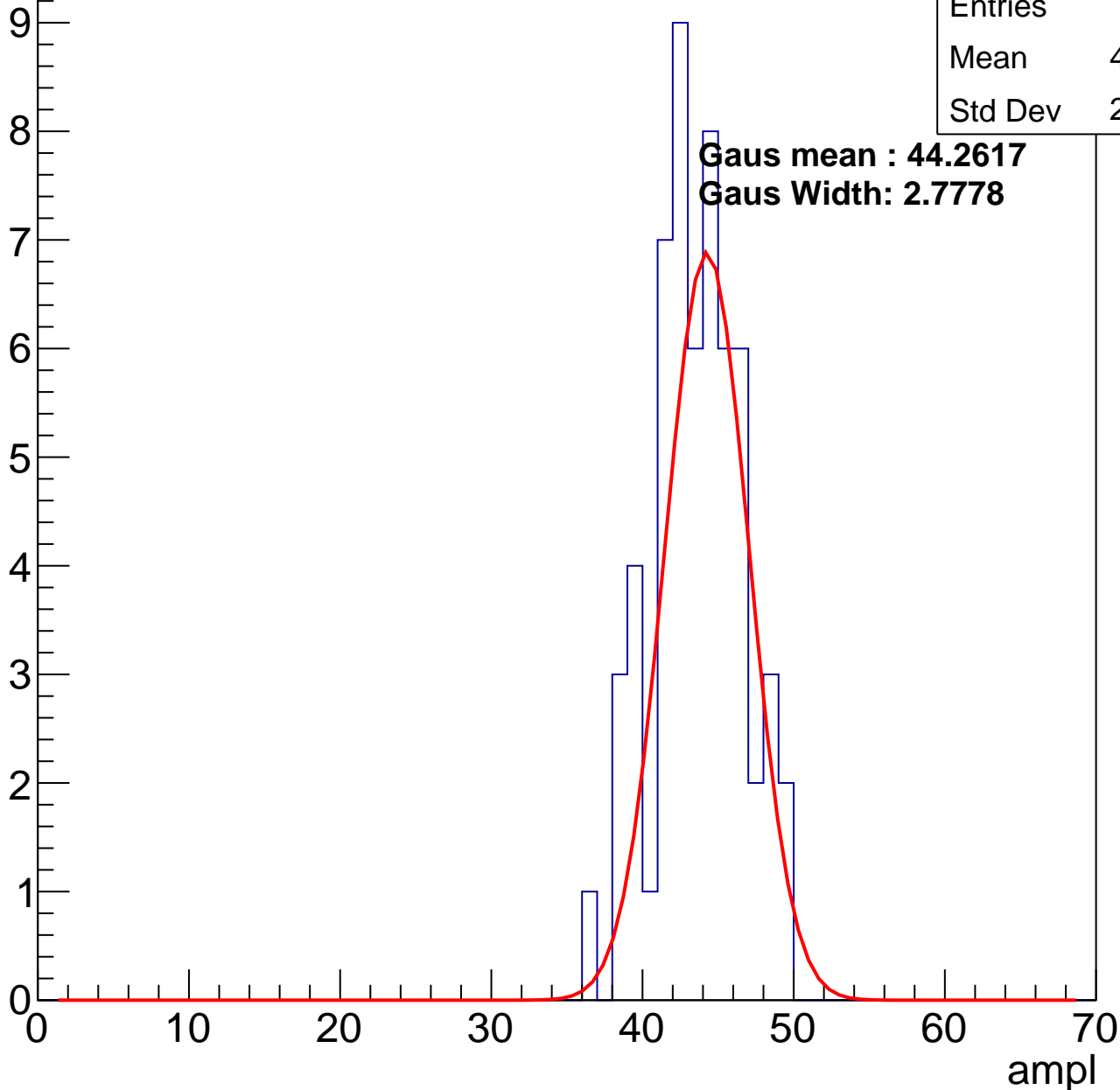
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.16
Std Dev	2.929

Gaus mean : 44.2617

Gaus Width: 2.7778

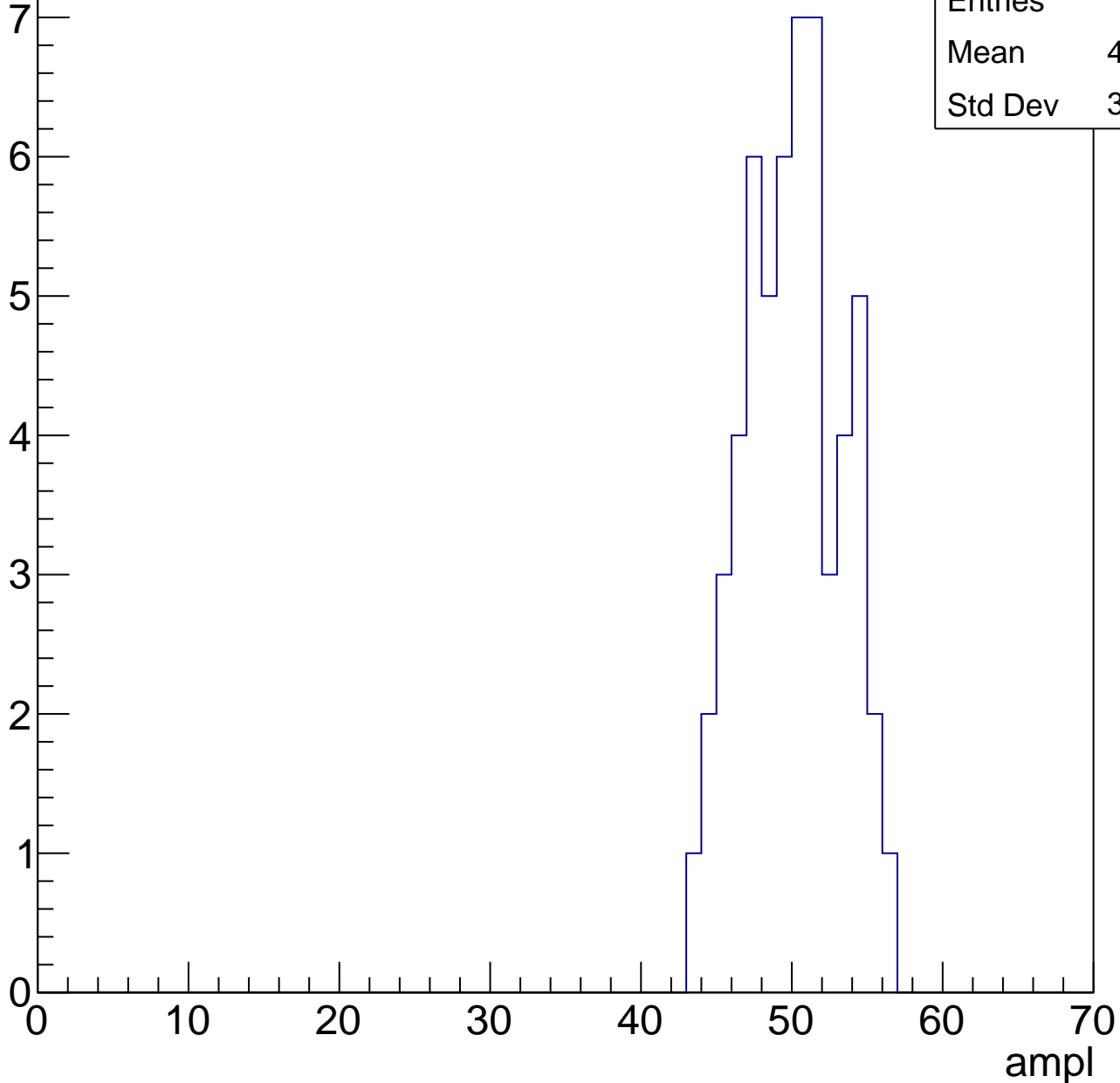


B1L100S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	49.59
Std Dev	3.138

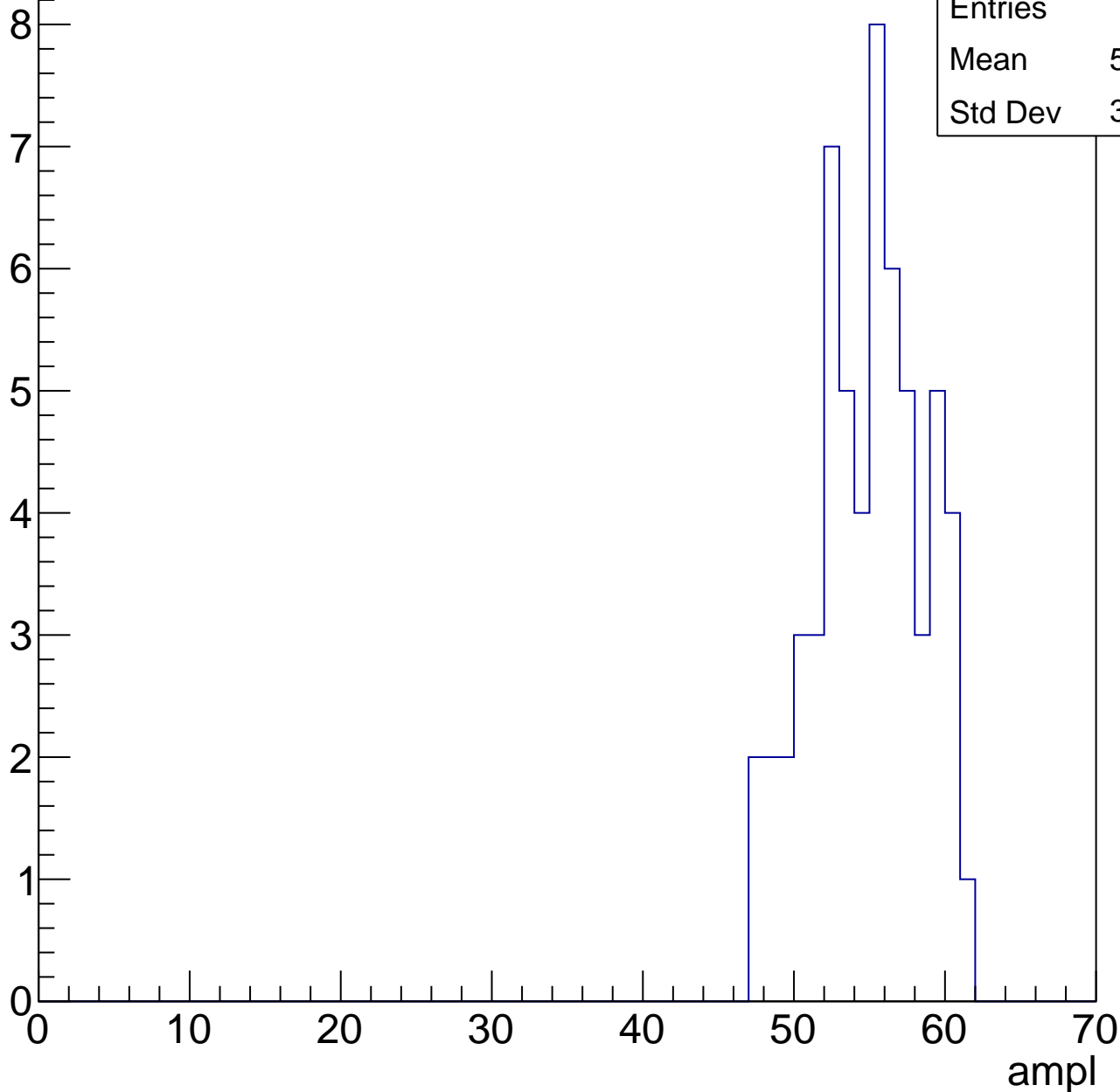


B1L100S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	54.45
Std Dev	3.556

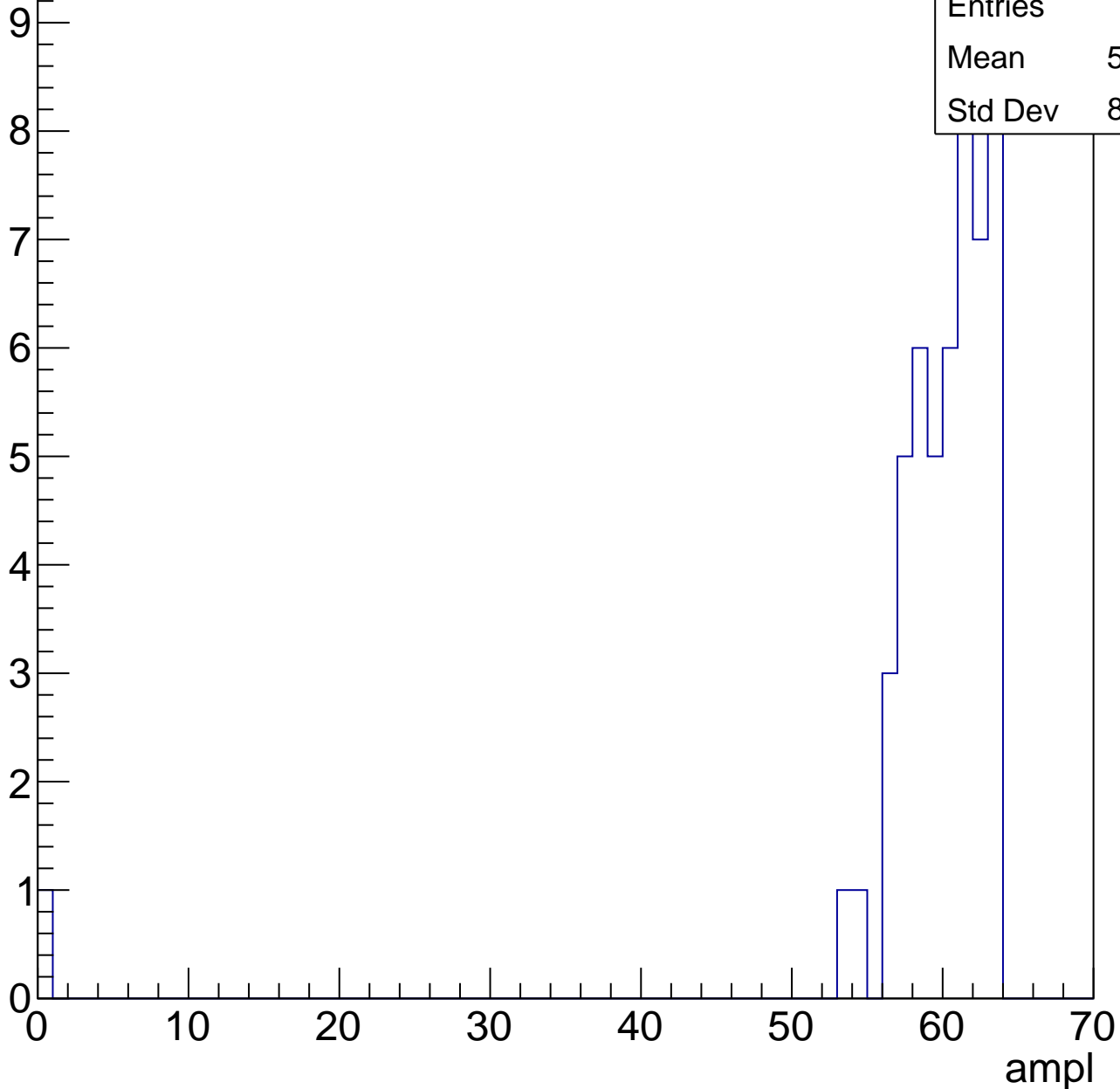


B1L100S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

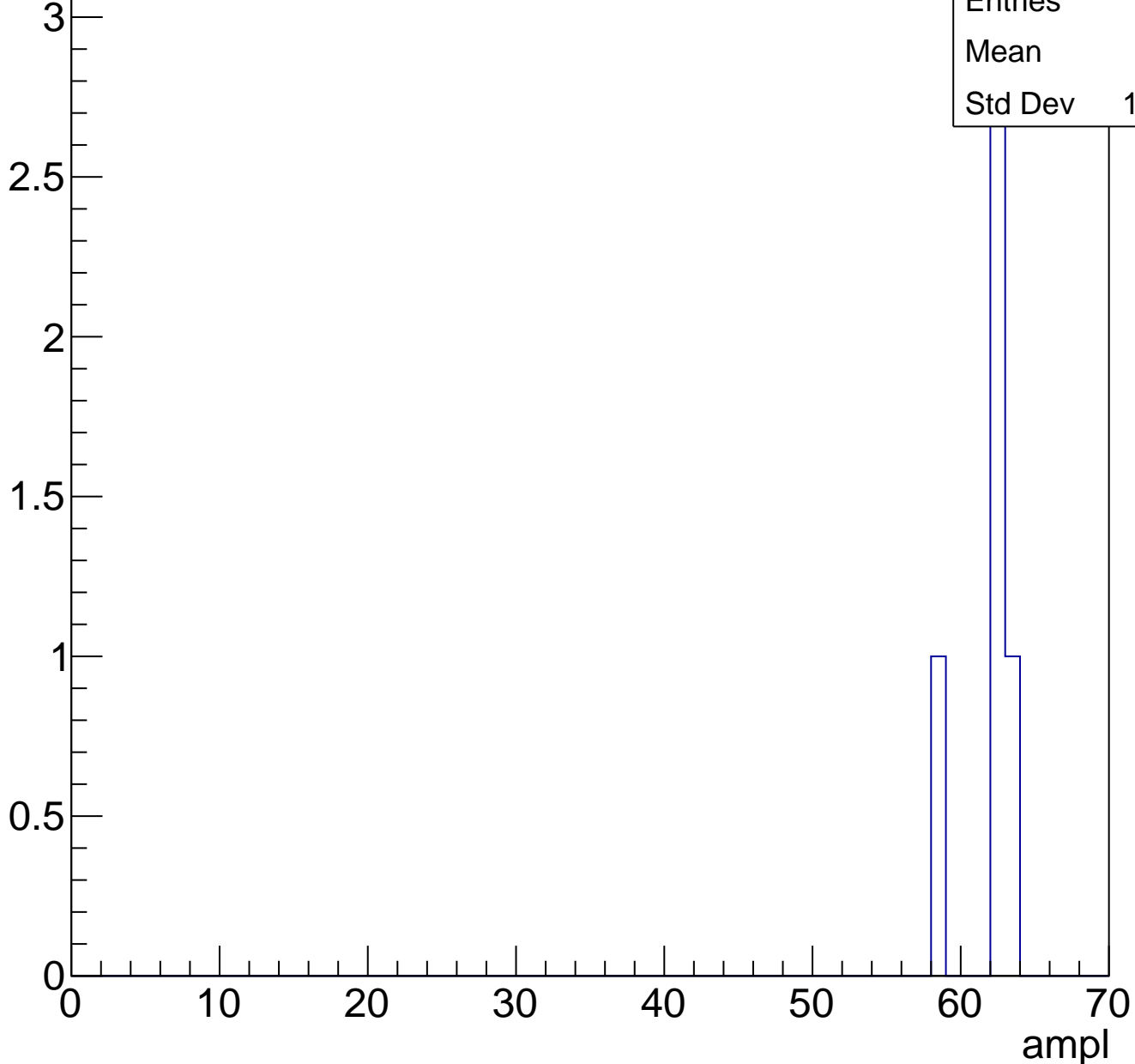
Entries	52
Mean	58.65
Std Dev	8.573



B1L100S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch117, adc0

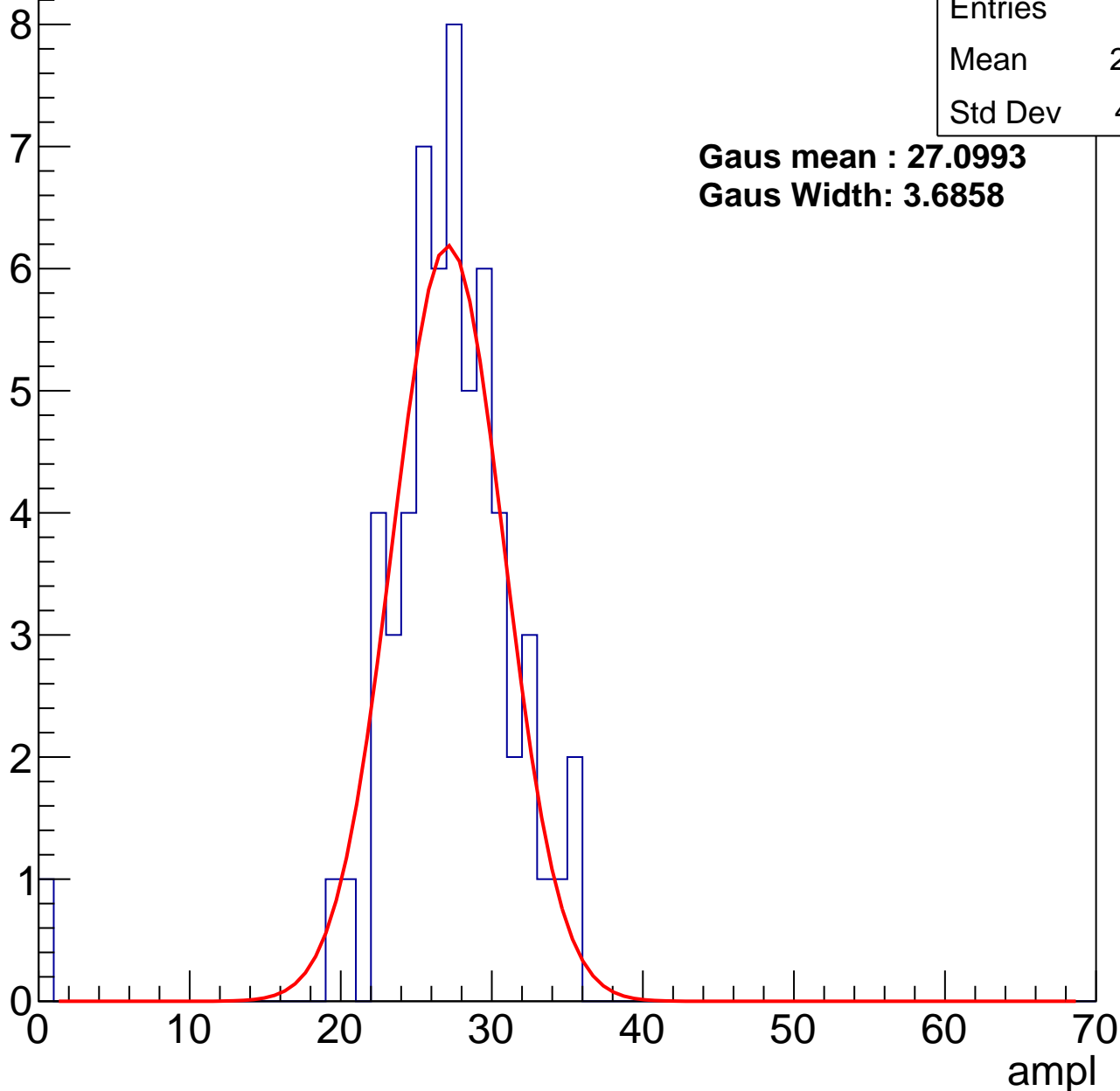
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	26.58
Std Dev	4.931

Gaus mean : 27.0993

Gaus Width: 3.6858



B1L100S, U14-ch117, adc1

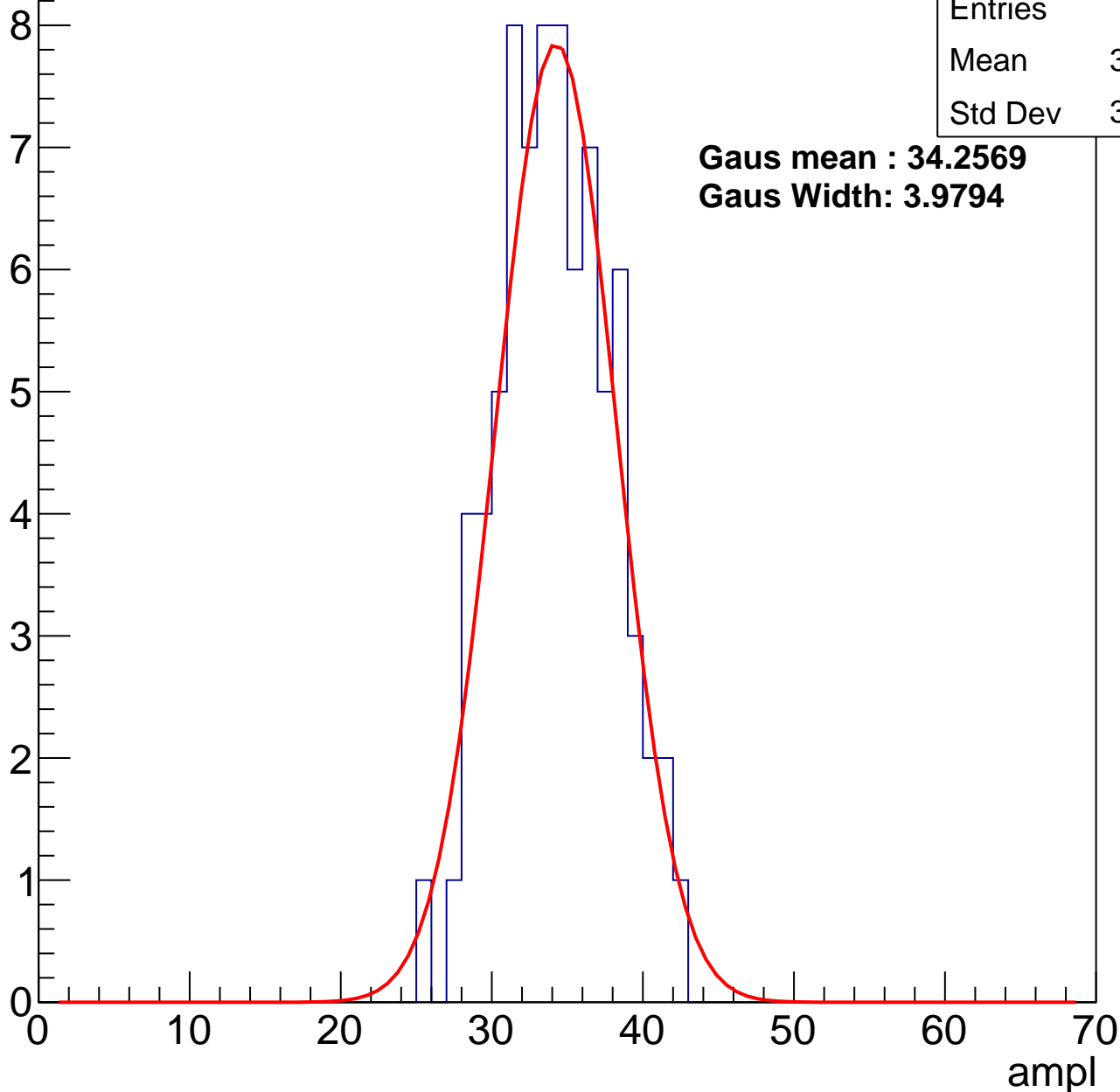
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	33.77
Std Dev	3.655

Gaus mean : 34.2569

Gaus Width: 3.9794



B1L100S, U14-ch117, adc2

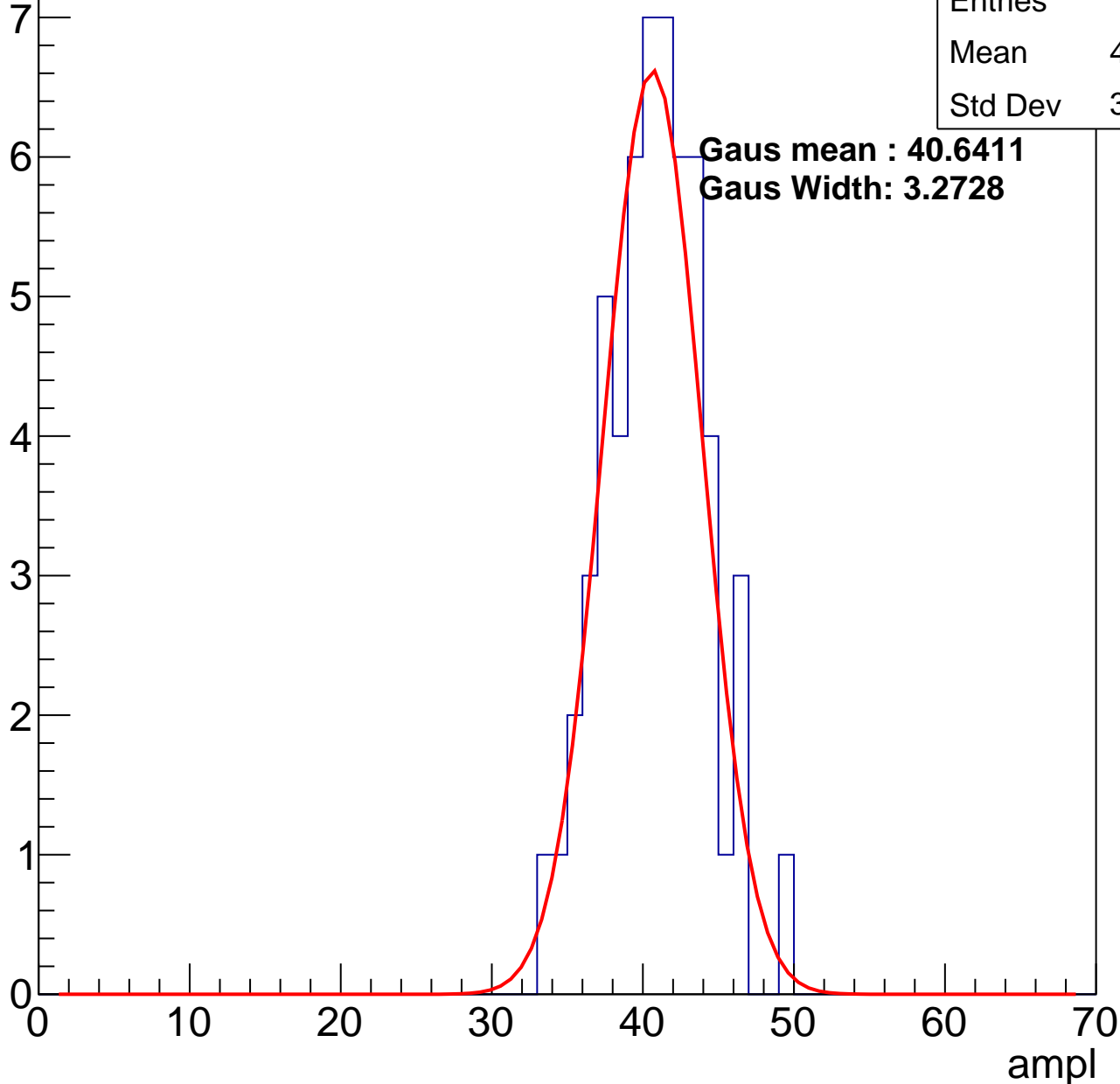
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	40.37
Std Dev	3.259

Gaus mean : 40.6411

Gaus Width: 3.2728

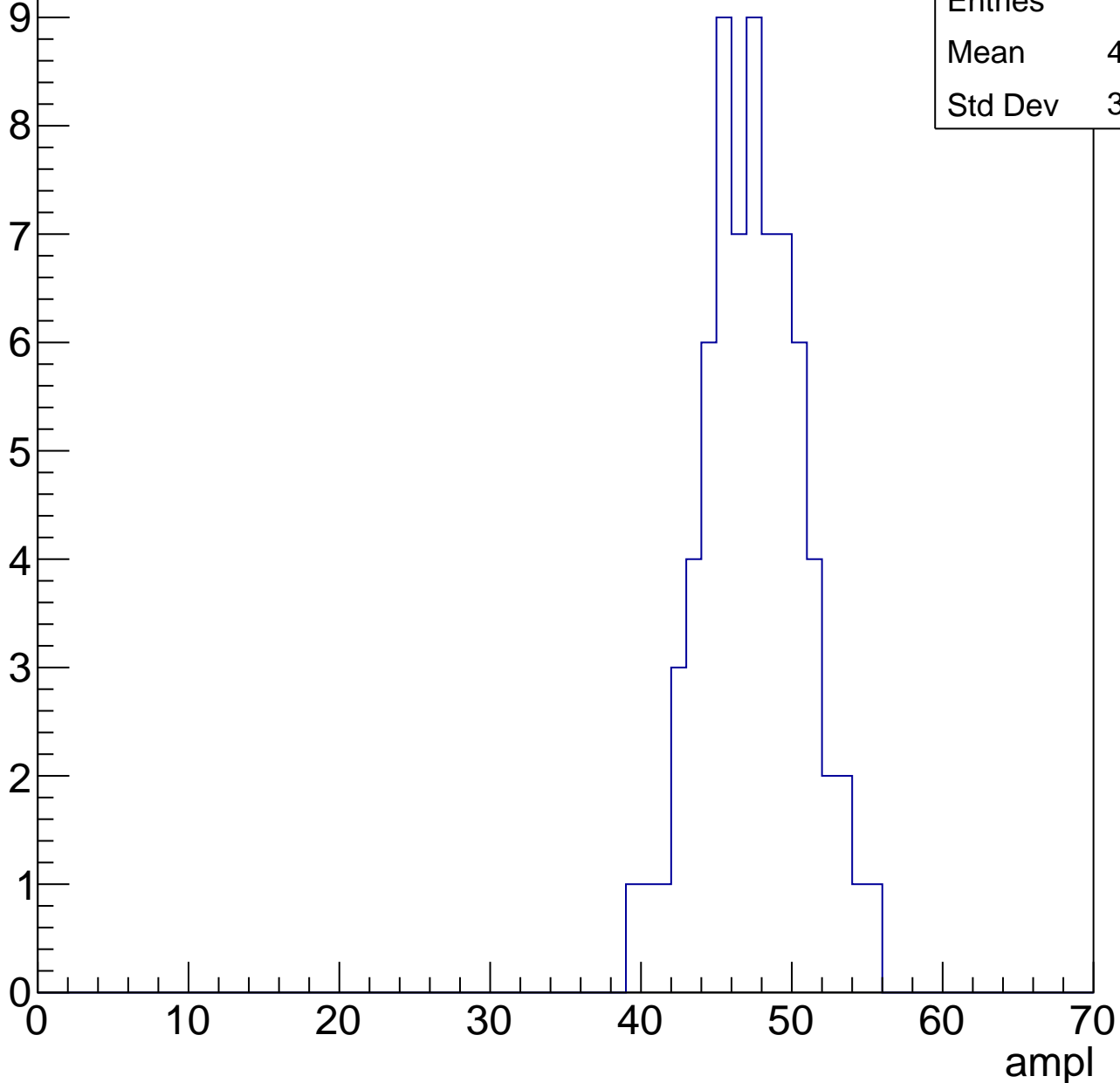


B1L100S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	46.96
Std Dev	3.299

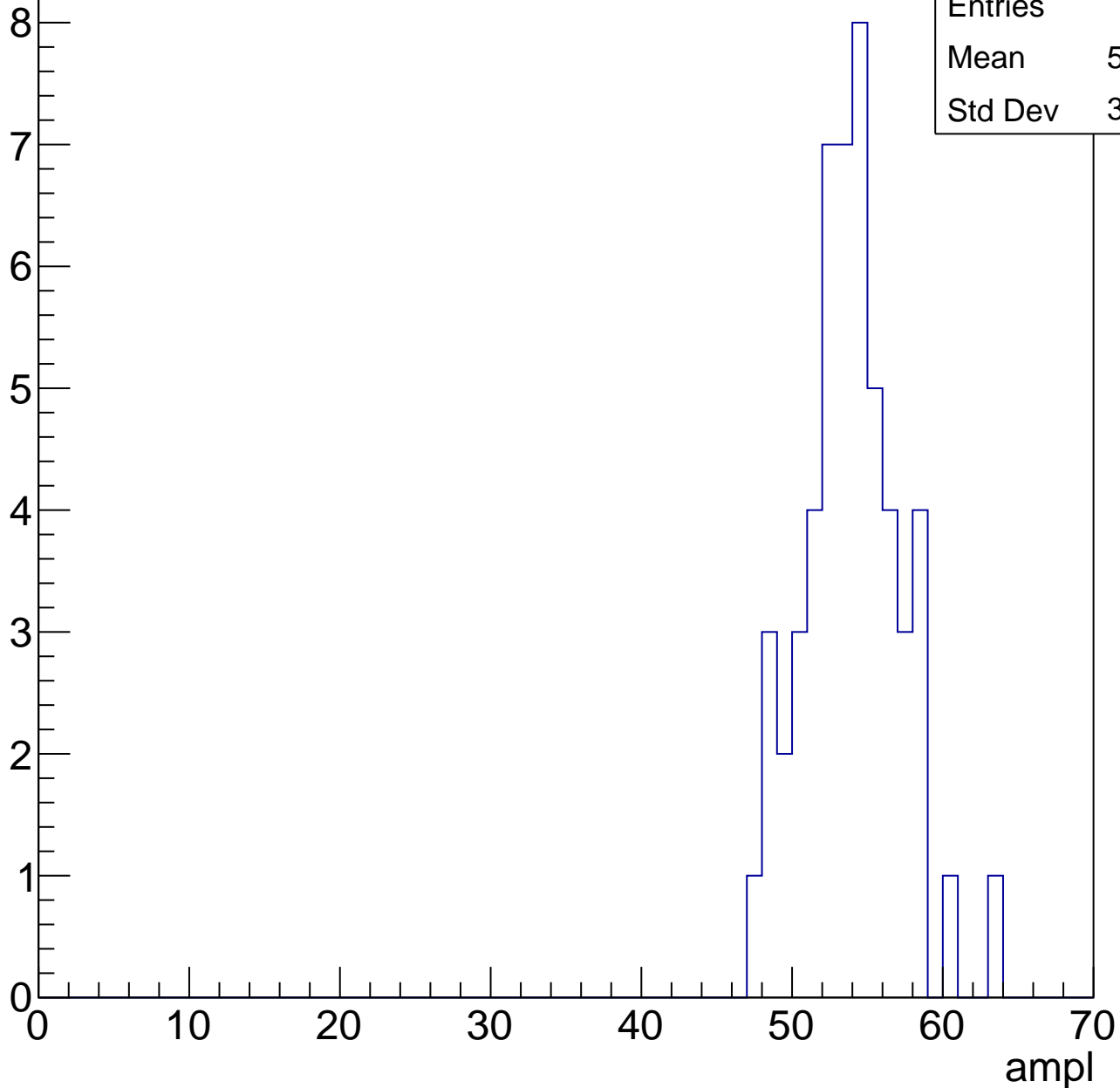


B1L100S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

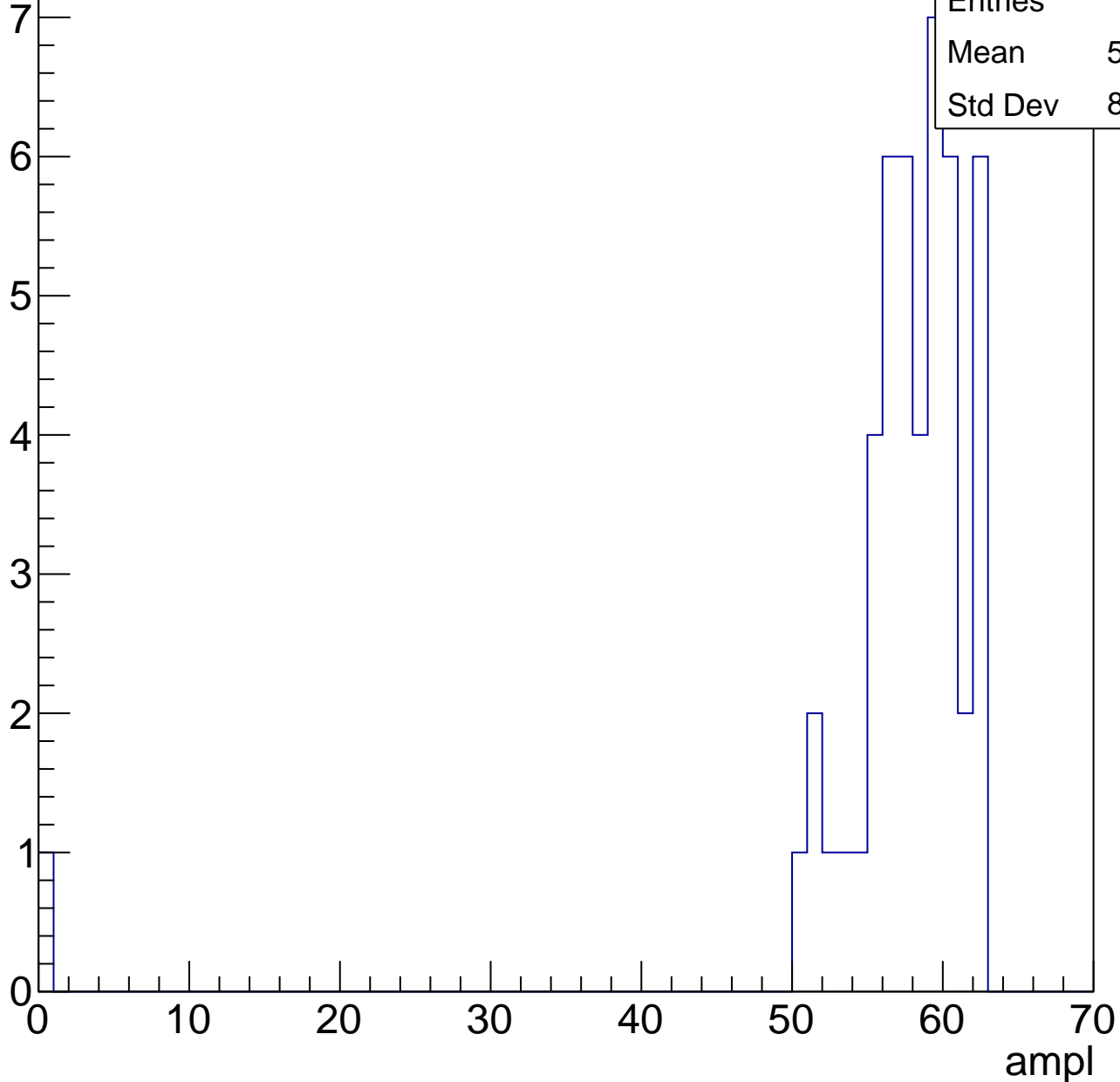
Entries	53
Mean	53.49
Std Dev	3.196



B1L100S, U14-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

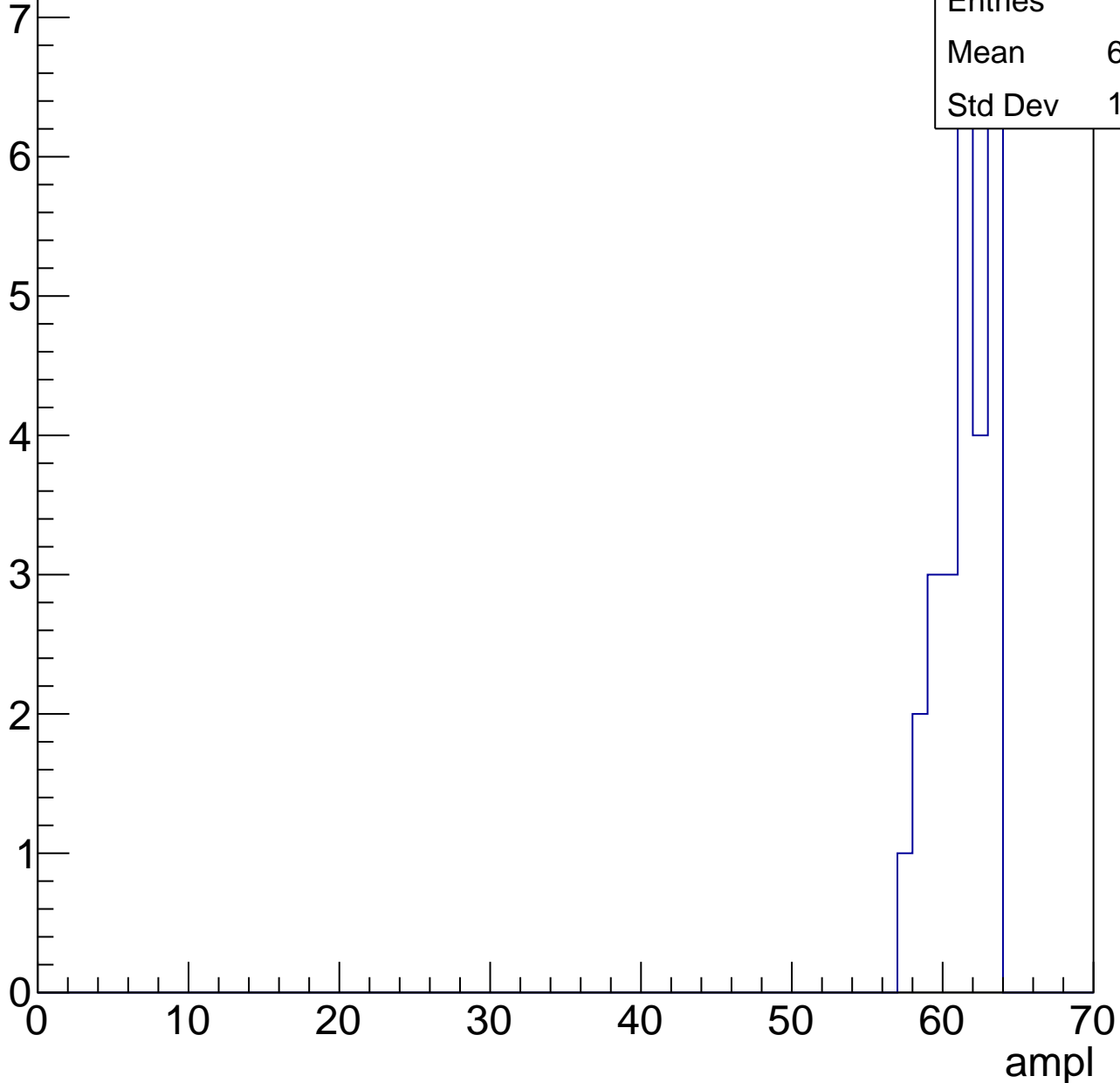
Entry



B1L100S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

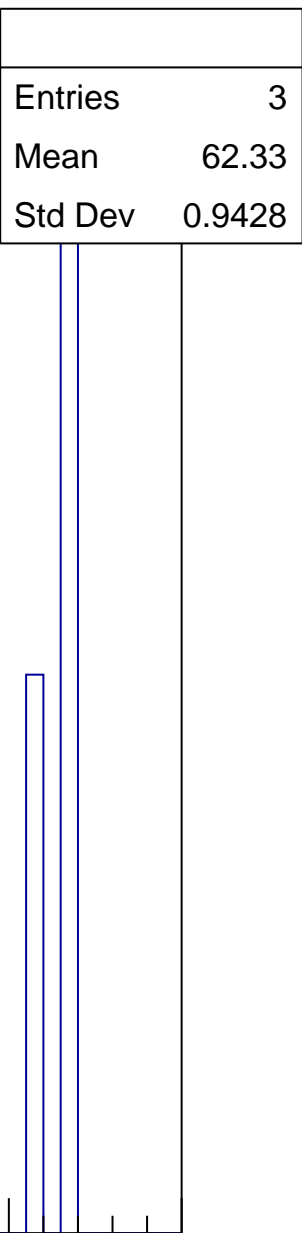
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U14-ch118, adc0

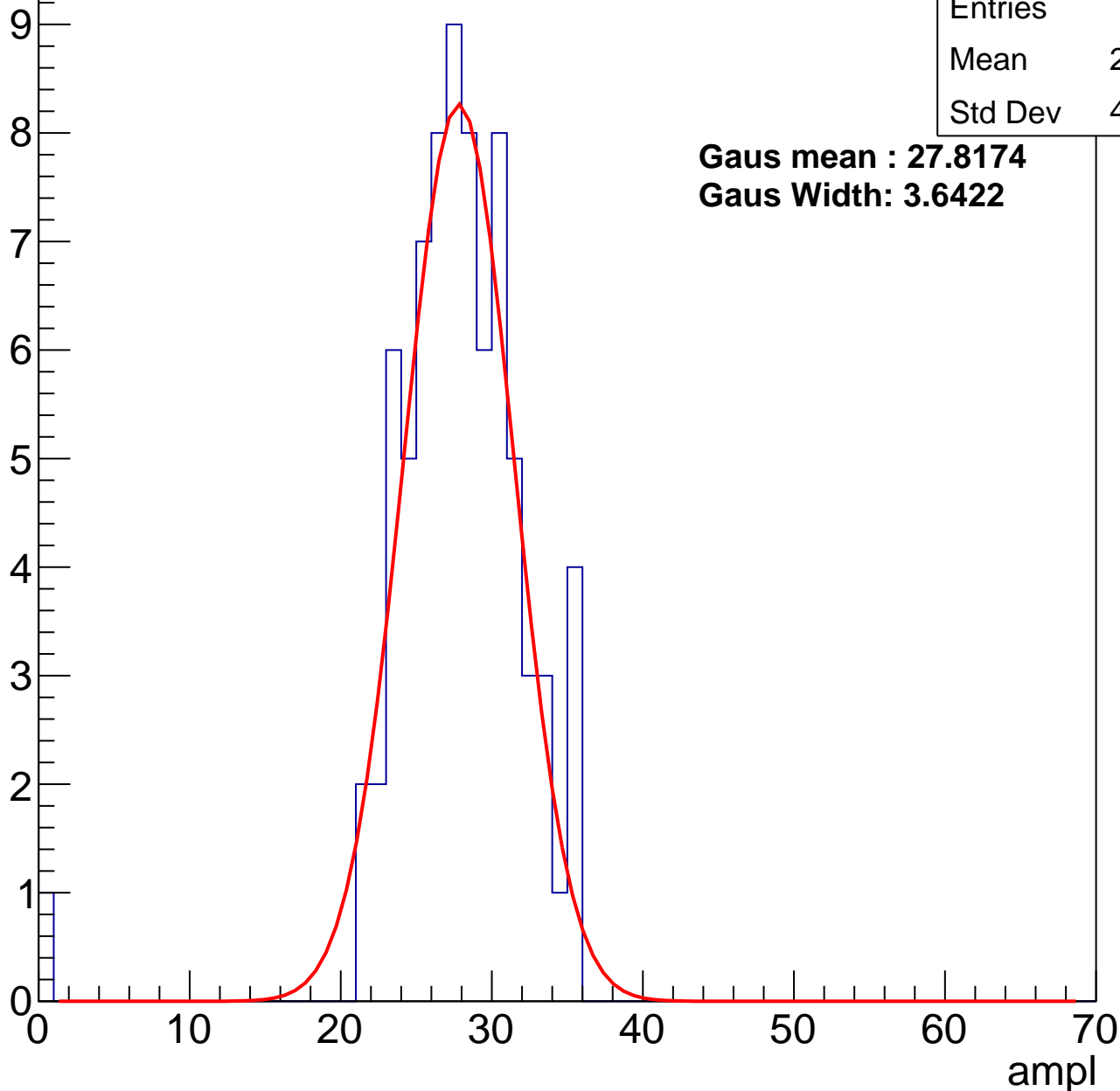
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	27.33
Std Dev	4.657

Gaus mean : 27.8174

Gaus Width: 3.6422



B1L100S, U14-ch118, adc1

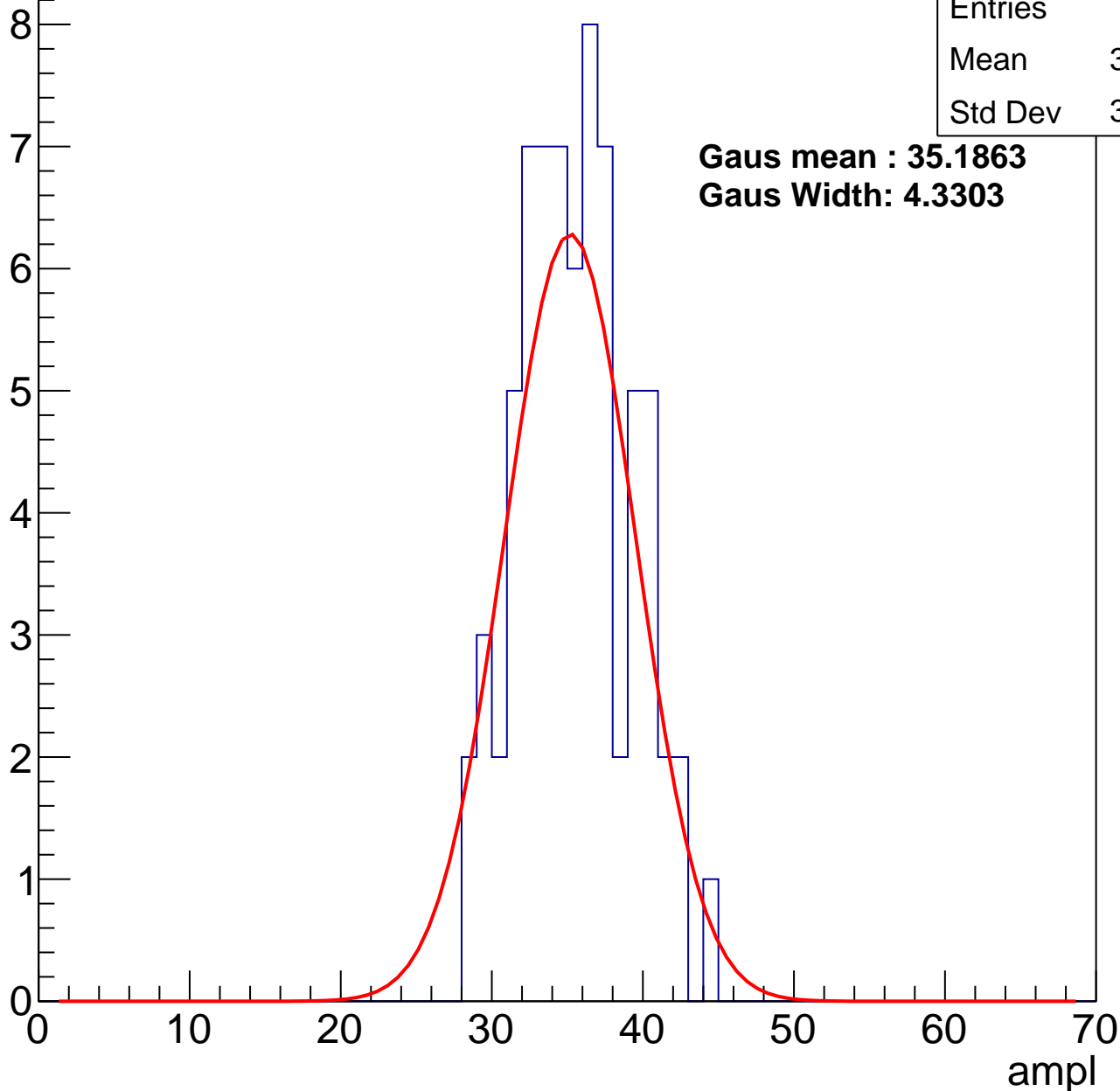
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.06
Std Dev	3.646

Gaus mean : 35.1863

Gaus Width: 4.3303



B1L100S, U14-ch118, adc2

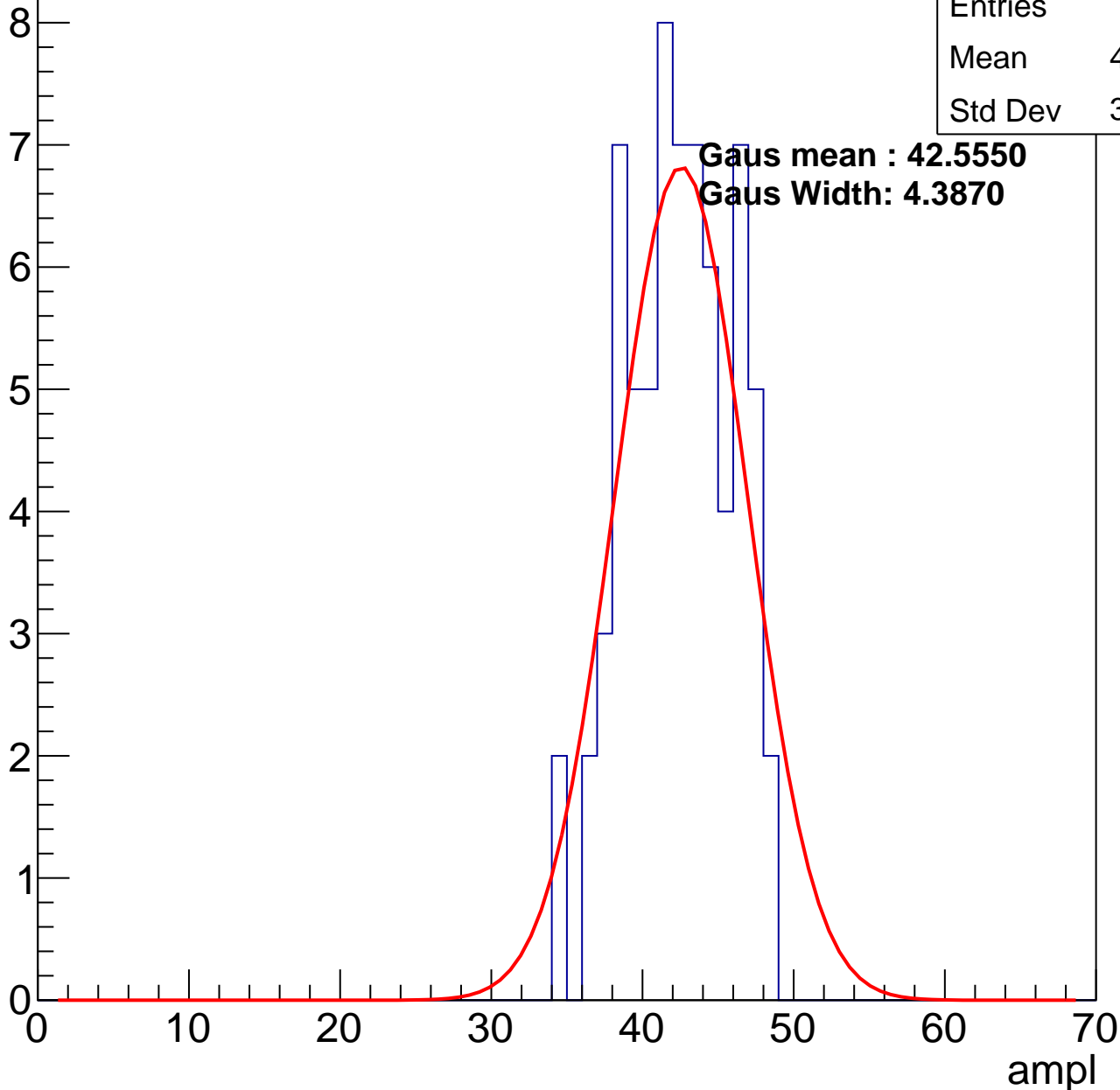
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	41.89
Std Dev	3.454

Gaus mean : 42.5550

Gaus Width: 4.3870

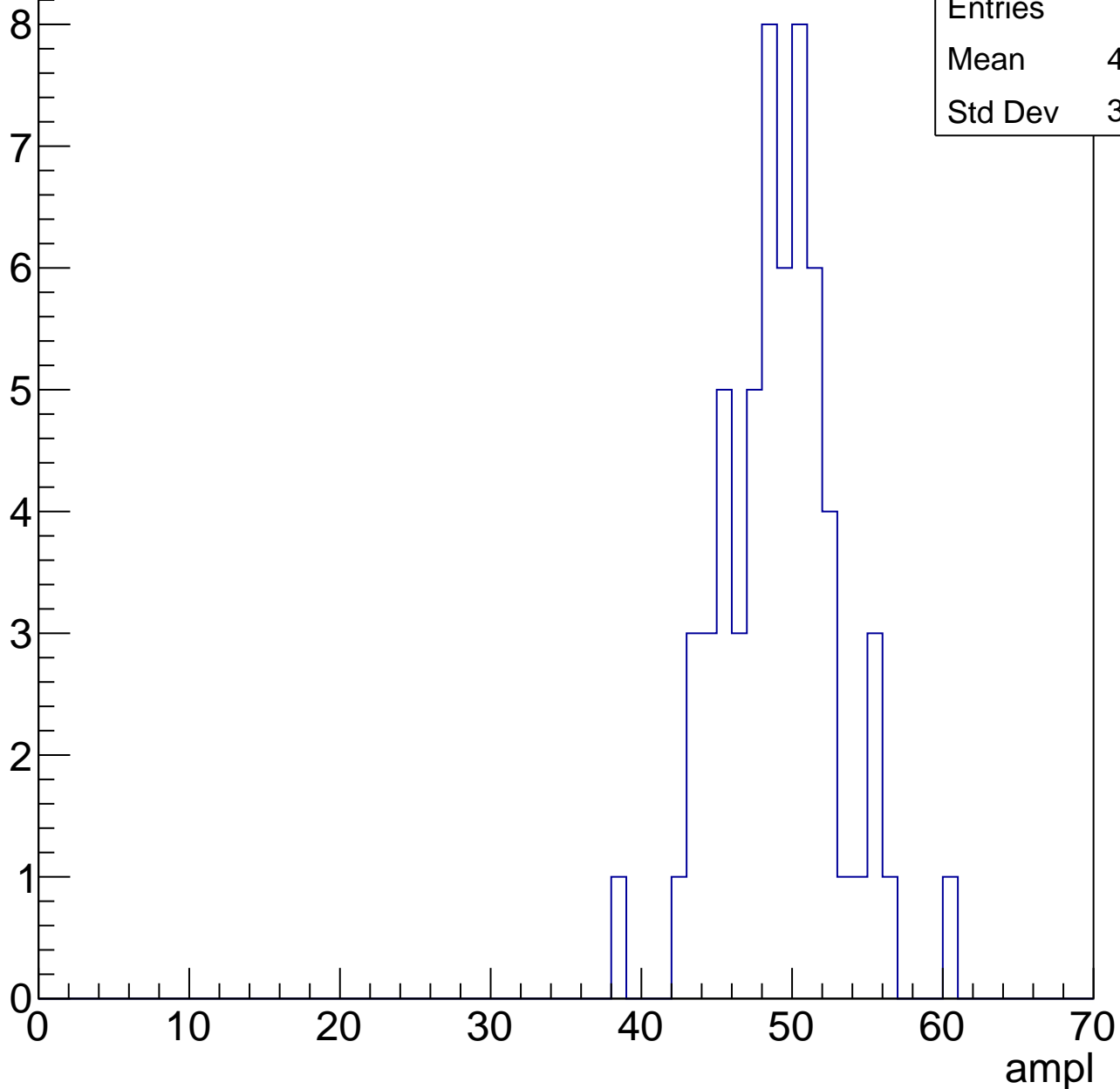


B1L100S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	48.65
Std Dev	3.803

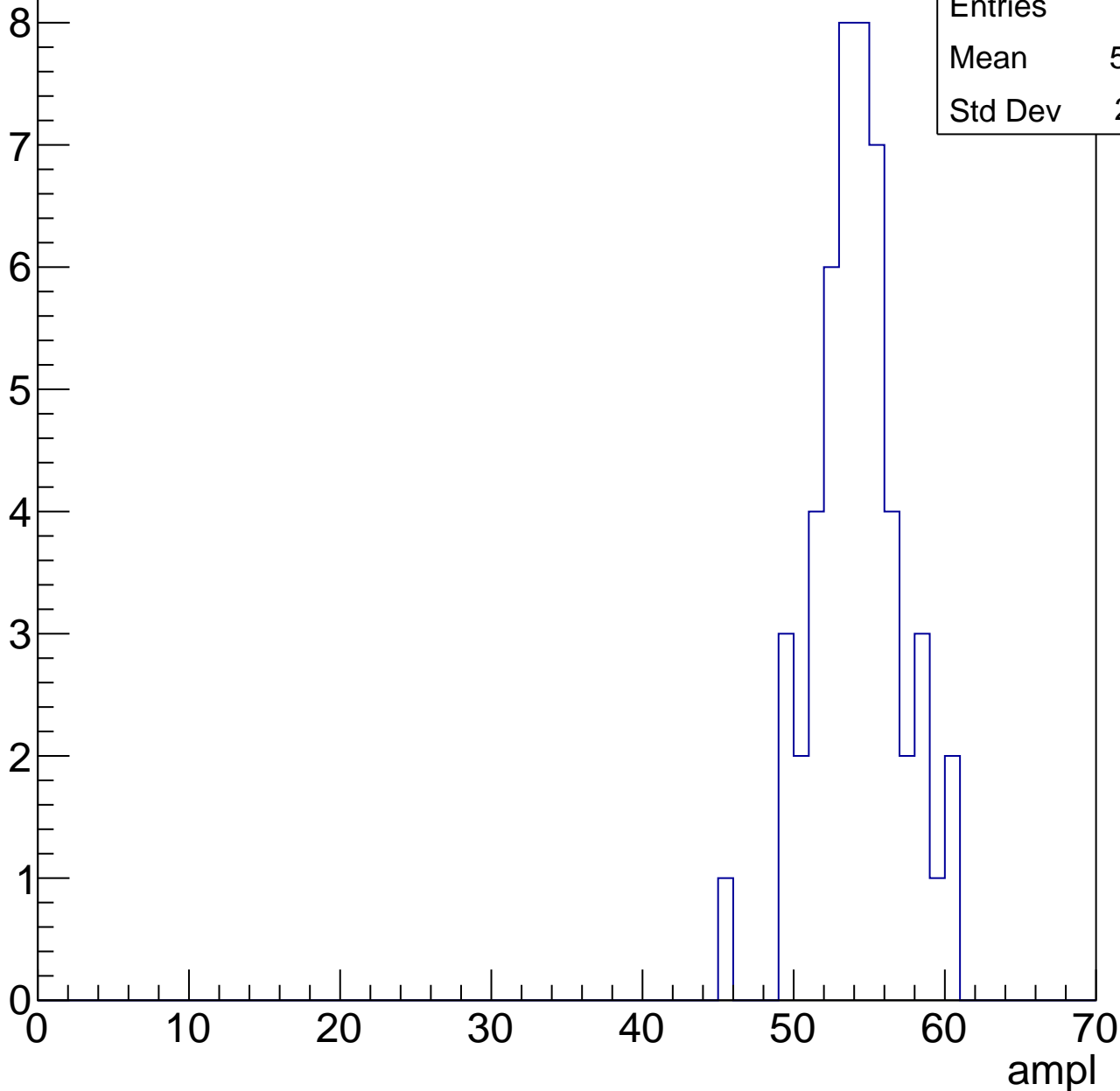


B1L100S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

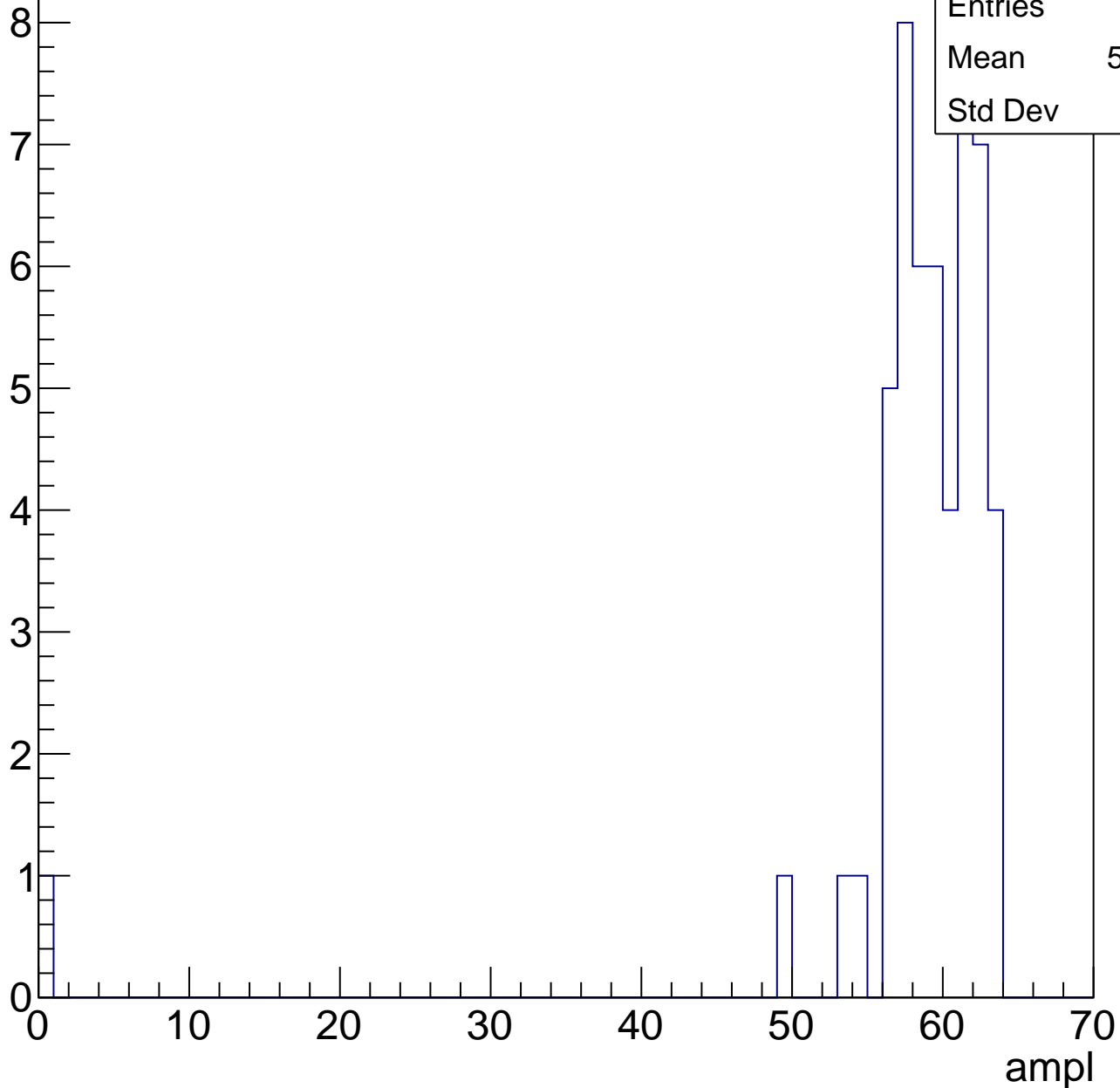
Entries	51
Mean	53.73
Std Dev	2.931



B1L100S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	16
Mean	60.94
Std Dev	1.638

ampl

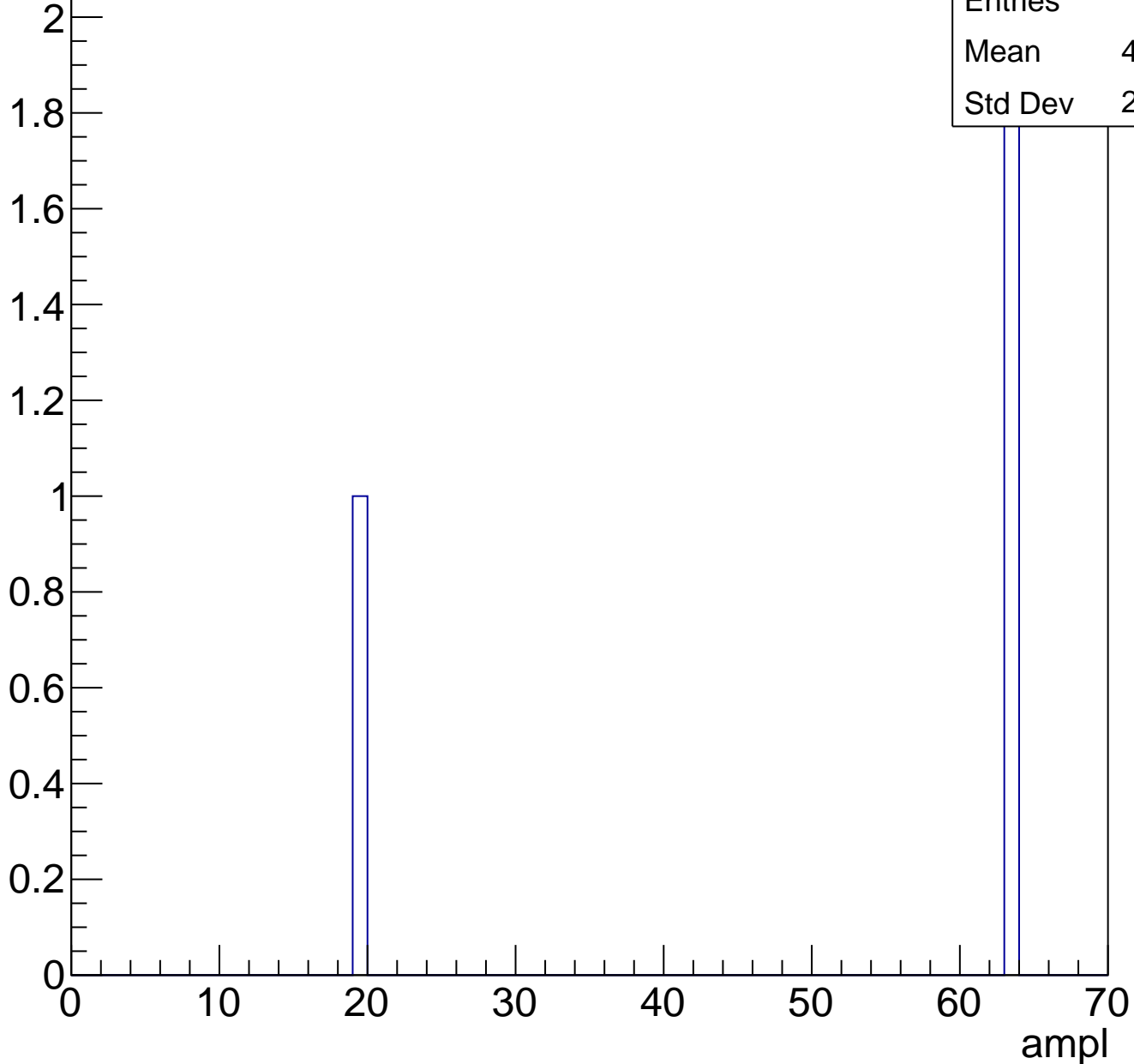
0 10 20 30 40 50 60 70

0 0.5 1 1.5 2 2.5 3 3.5 4

B1L100S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	48.33
Std Dev	20.74

B1L100S, U14-ch119, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	28.66
Std Dev	4.737

Gaus mean : 29.4720

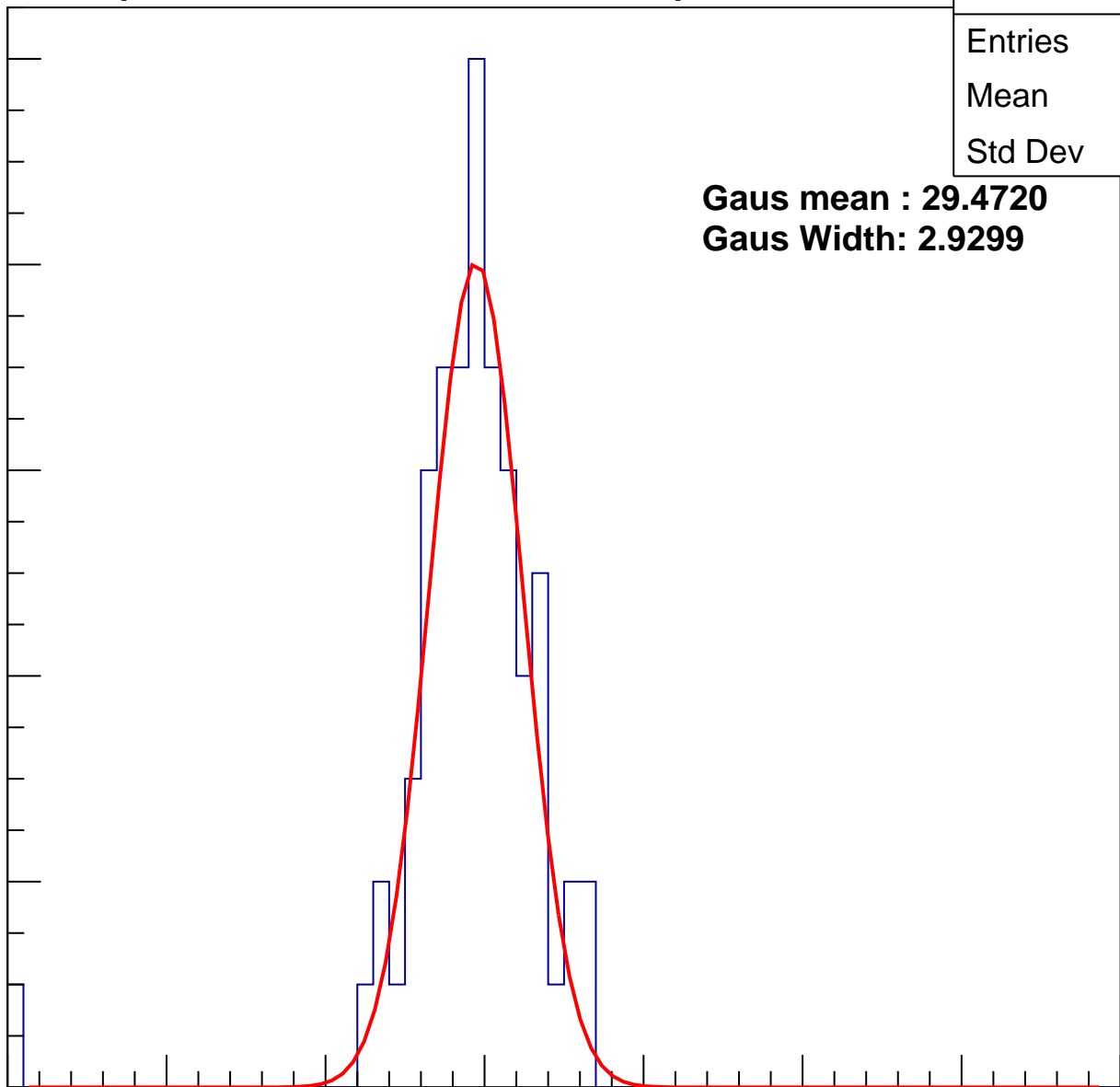
Gaus Width: 2.9299

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch119, adc1

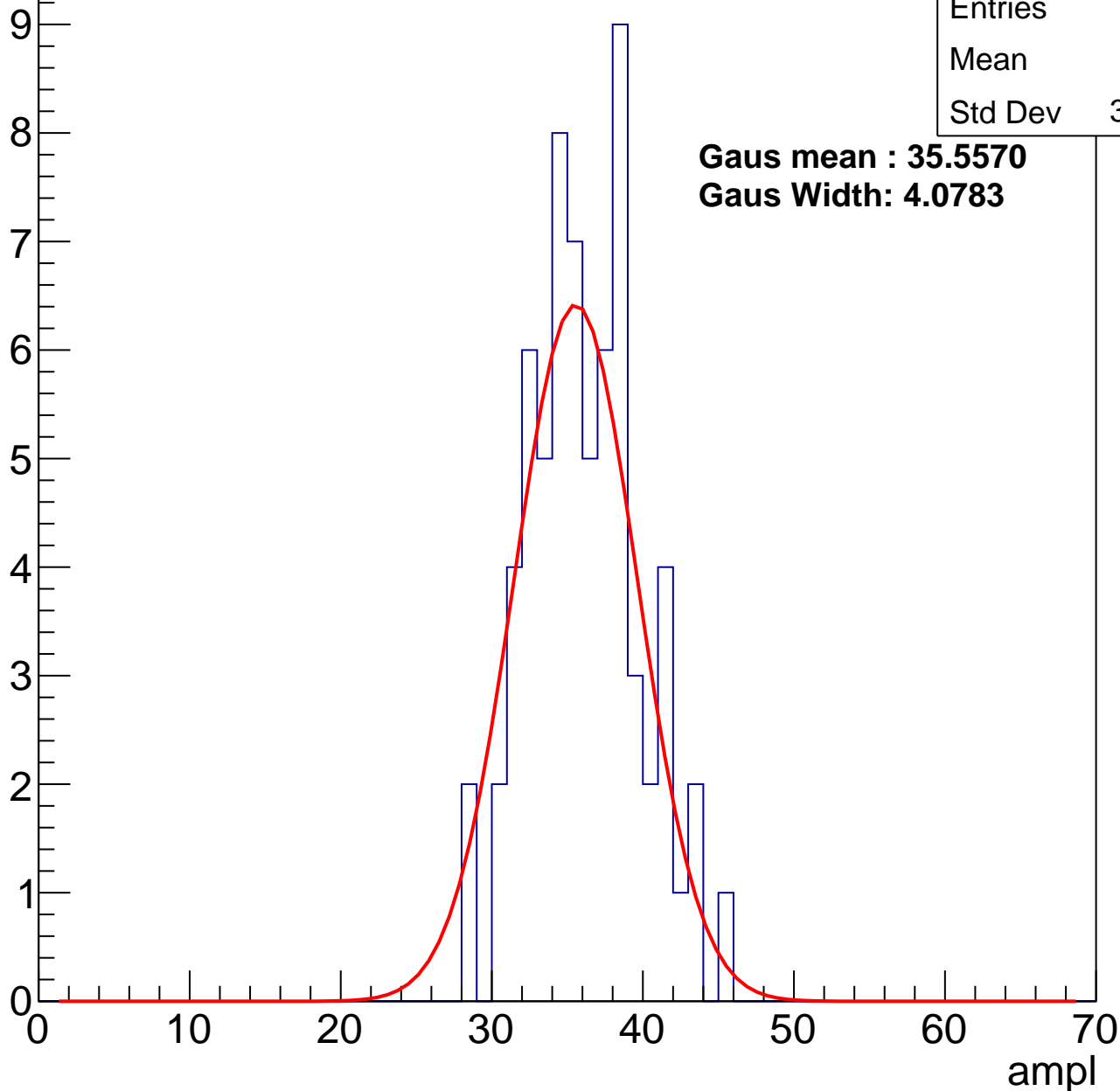
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.7
Std Dev	3.645

Gaus mean : 35.5570

Gaus Width: 4.0783



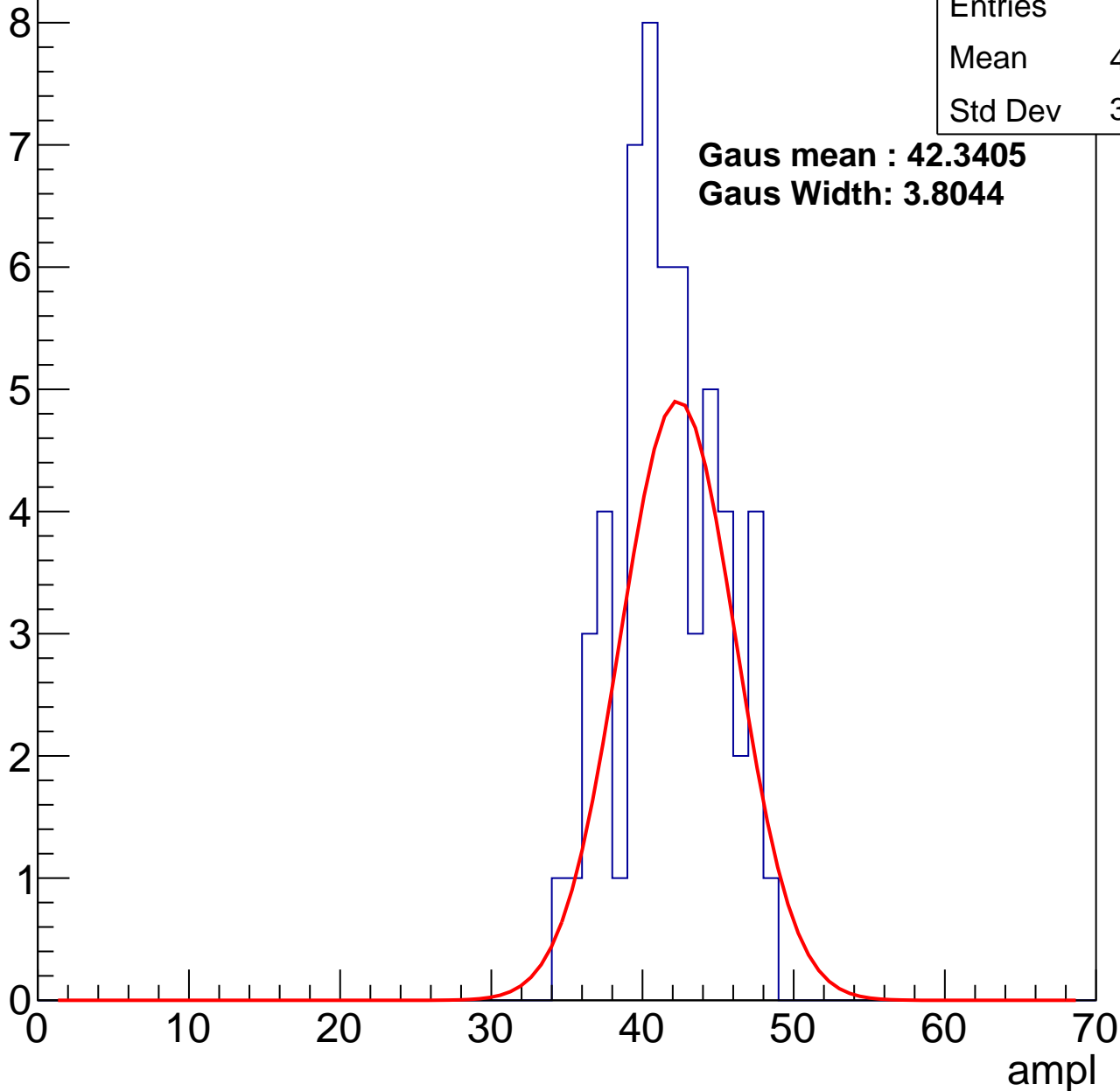
B1L100S, U14-ch119, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	41.27
Std Dev	3.367

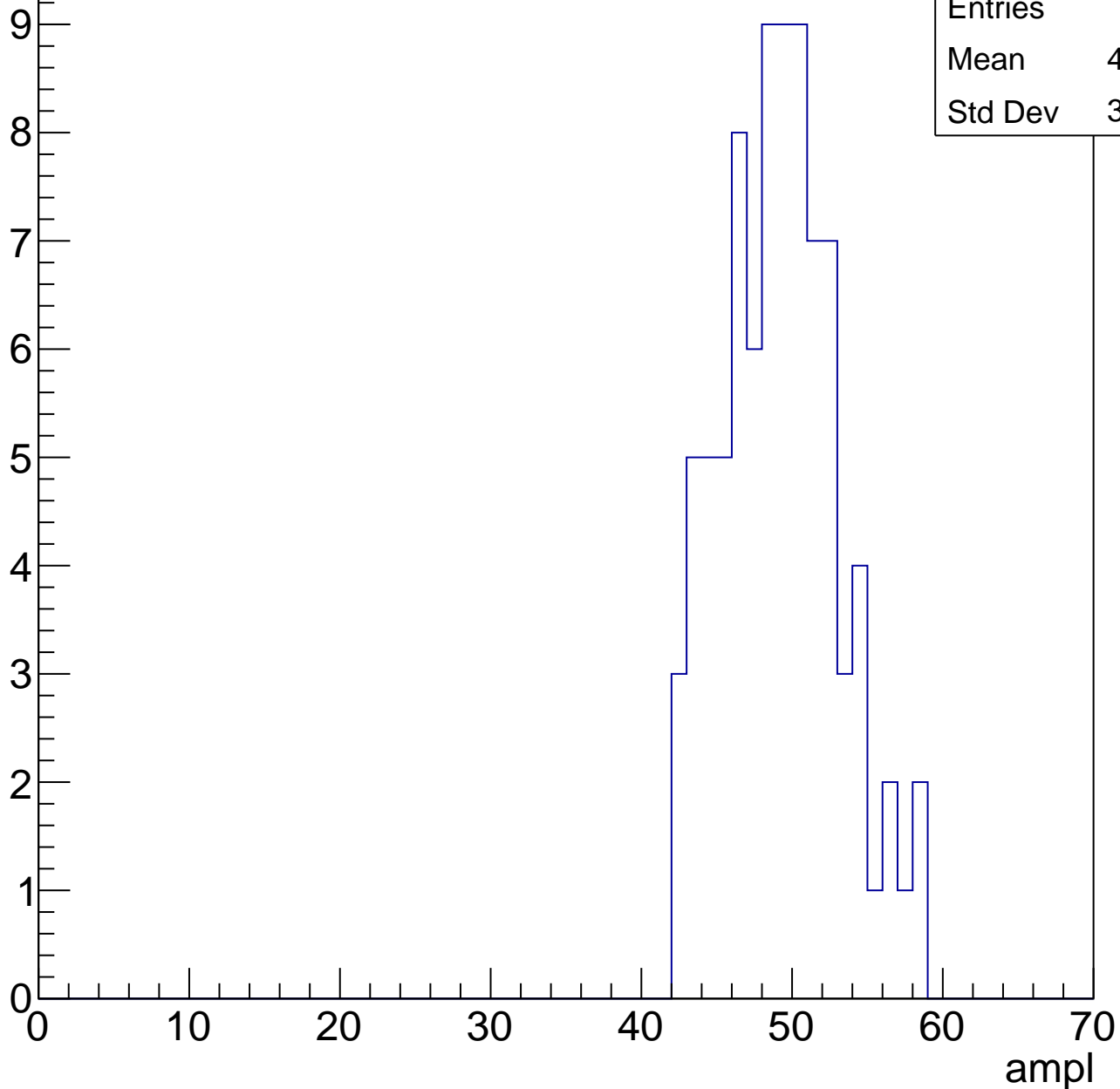
Gaus mean : 42.3405
Gaus Width: 3.8044



B1L100S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

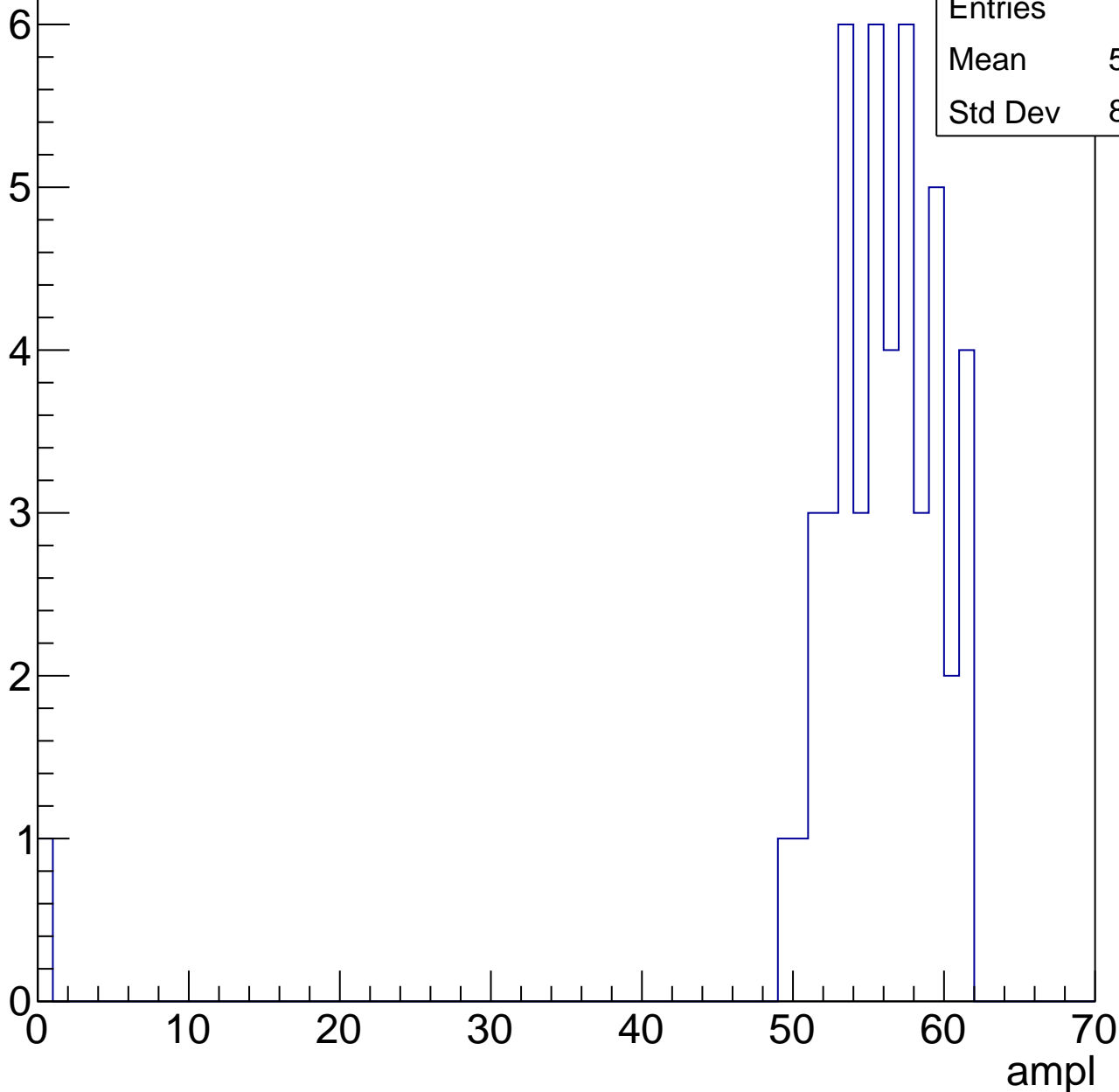


B1L100S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	54.52
Std Dev	8.546

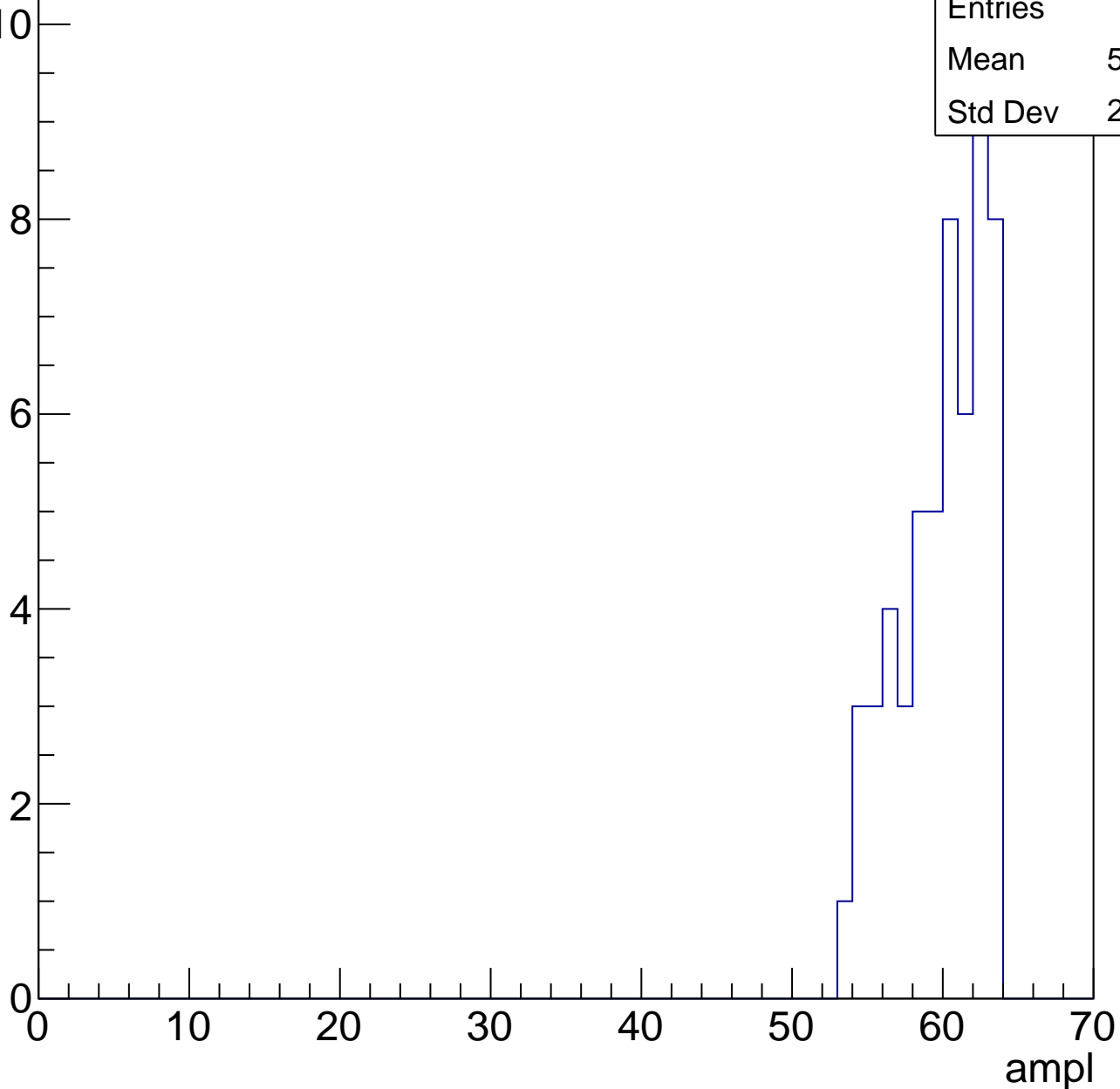


B1L100S, U14-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	59.46
Std Dev	2.835



B1L100S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U14-ch120, adc0

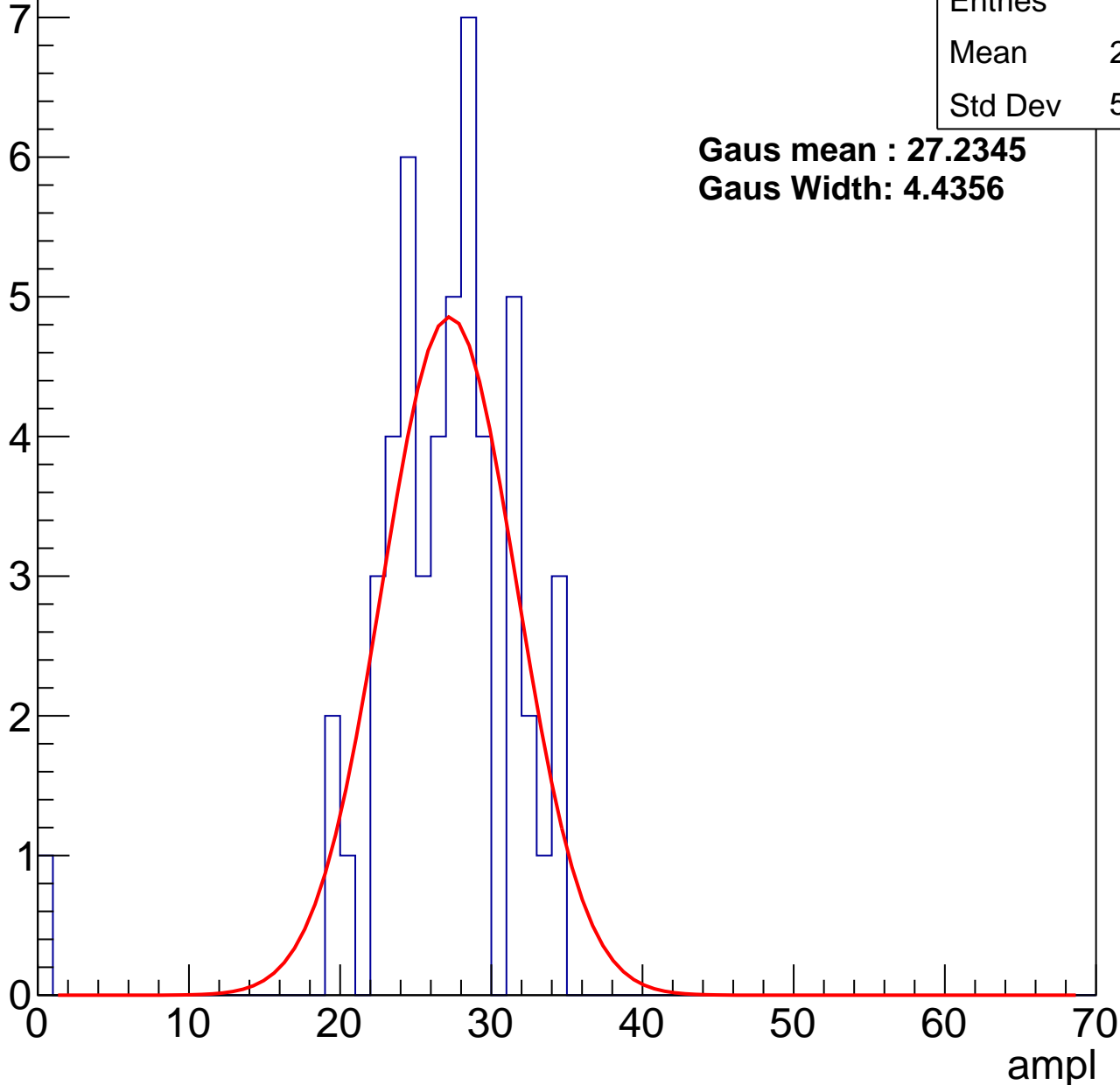
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	26.27
Std Dev	5.284

Gaus mean : 27.2345

Gaus Width: 4.4356



B1L100S, U14-ch120, adc1

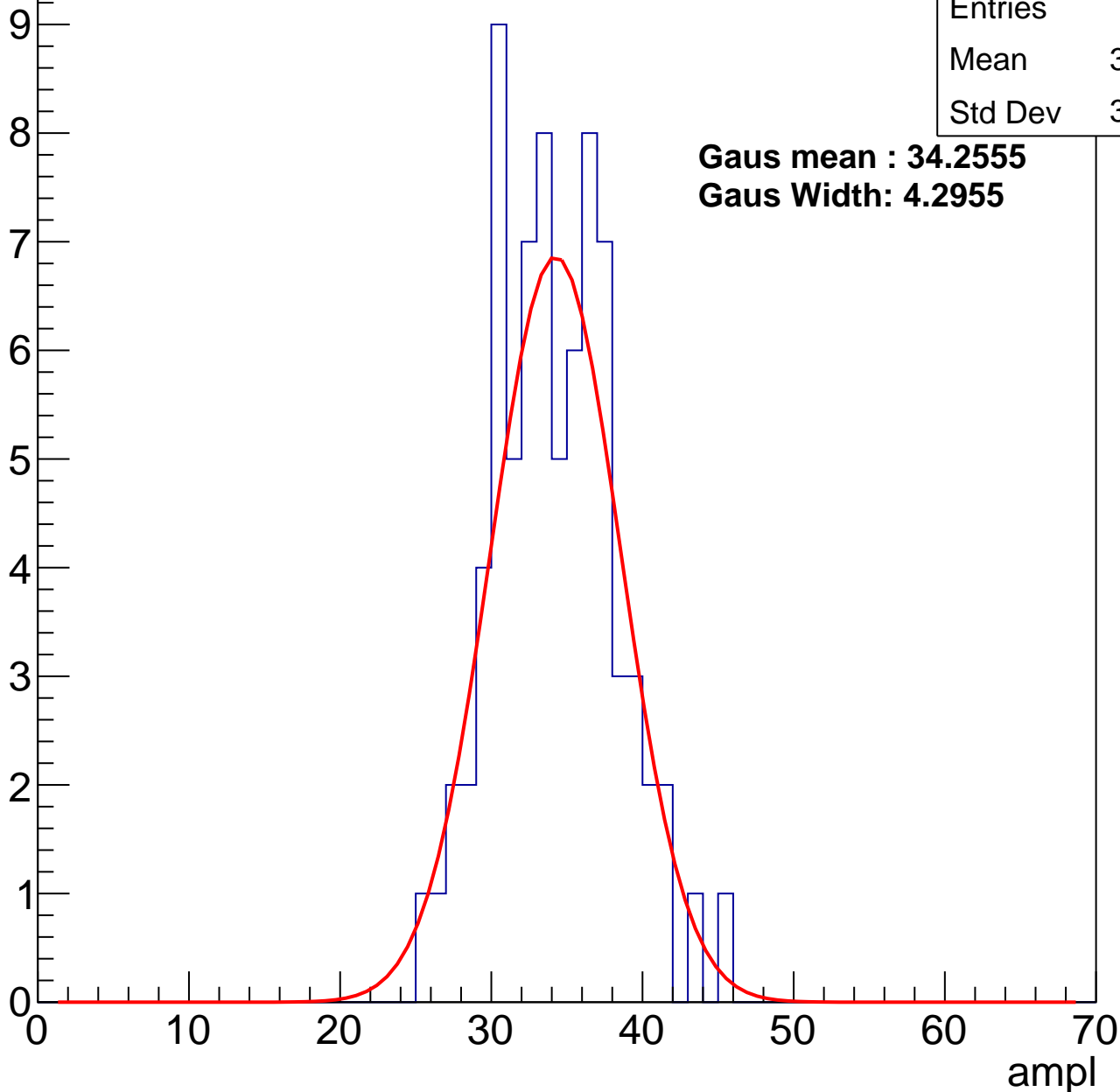
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	33.74
Std Dev	3.998

Gaus mean : 34.2555

Gaus Width: 4.2955



B1L100S, U14-ch120, adc2

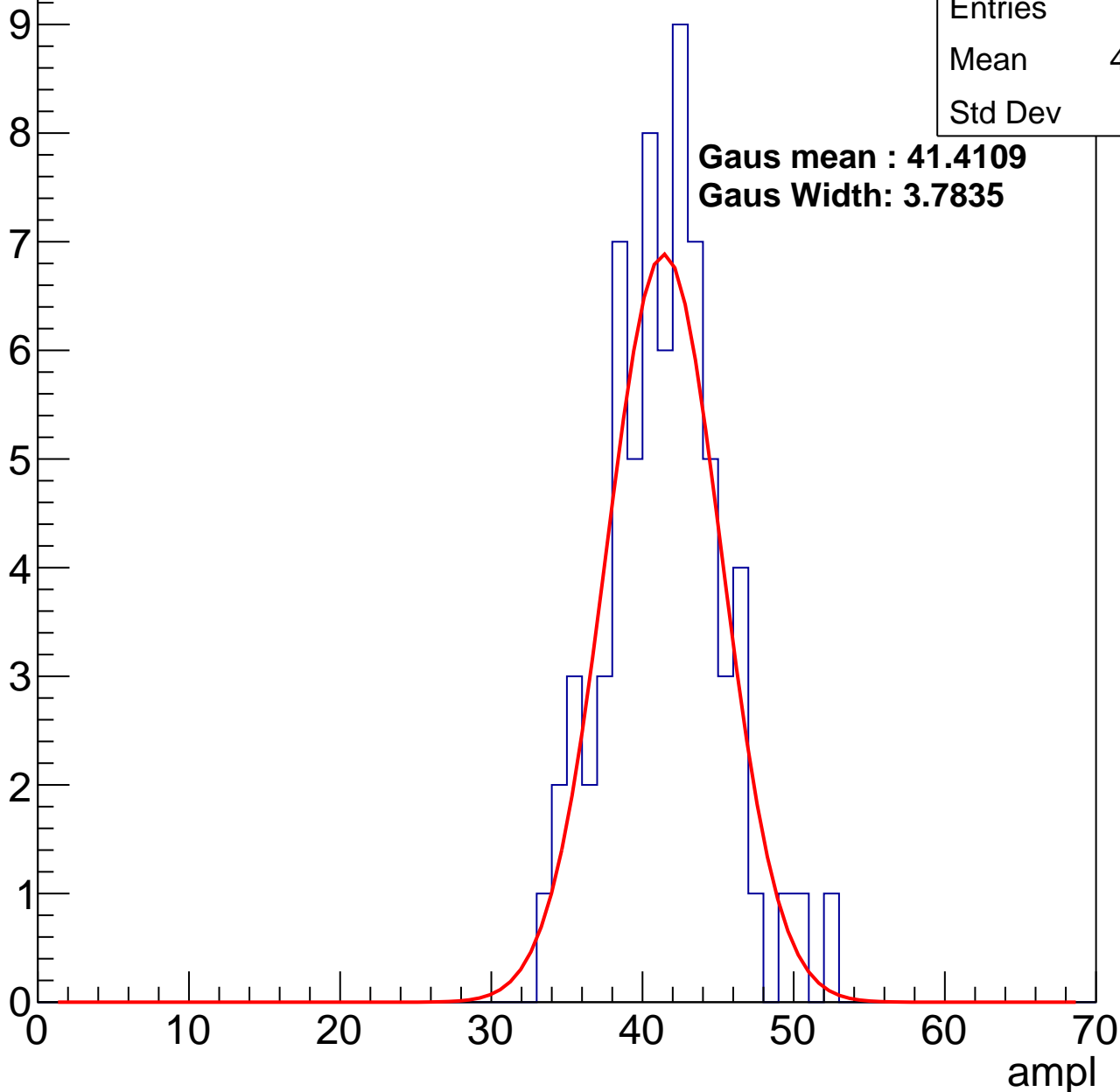
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.04
Std Dev	3.79

Gaus mean : 41.4109

Gaus Width: 3.7835

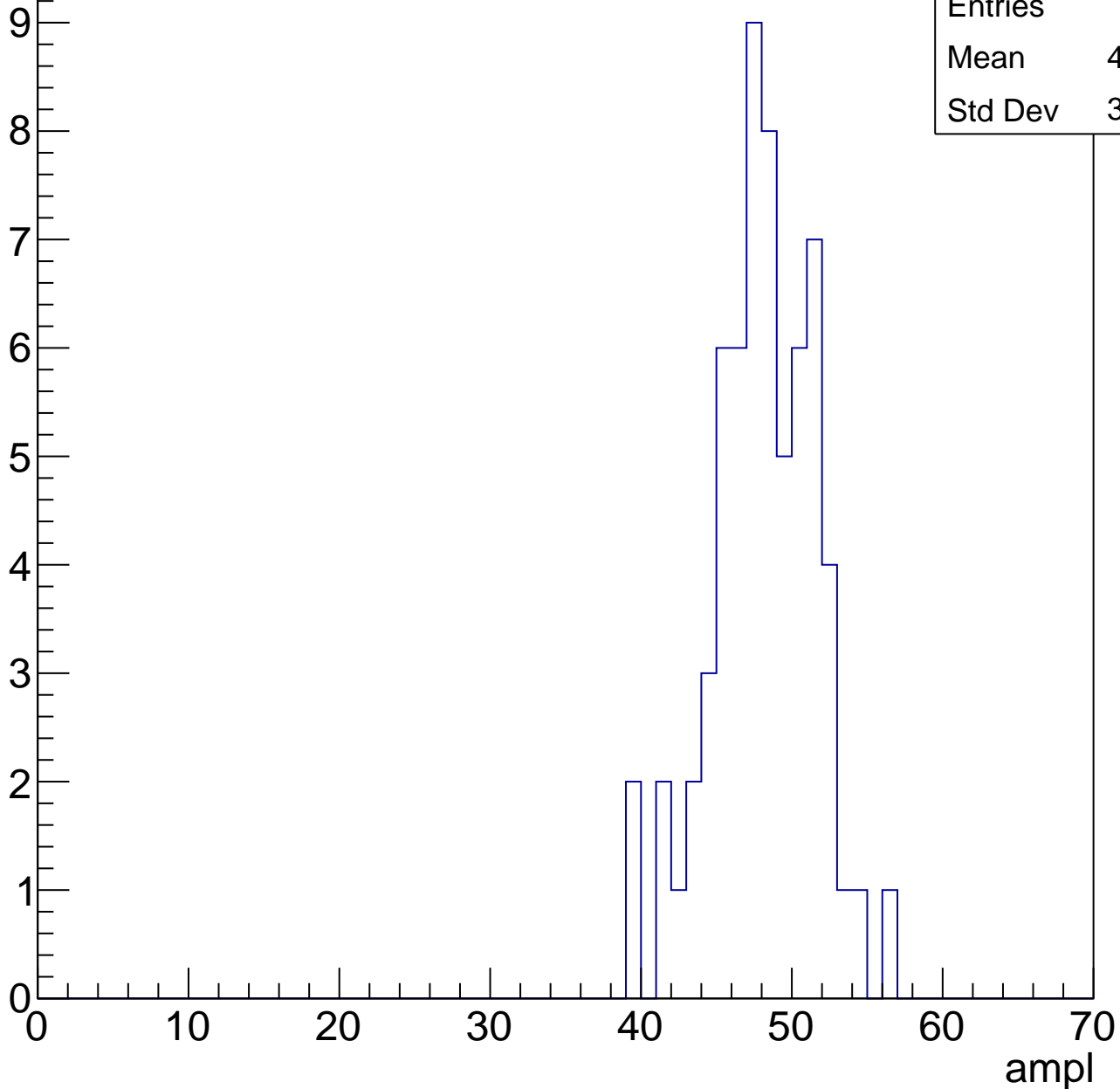


B1L100S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	47.59
Std Dev	3.427

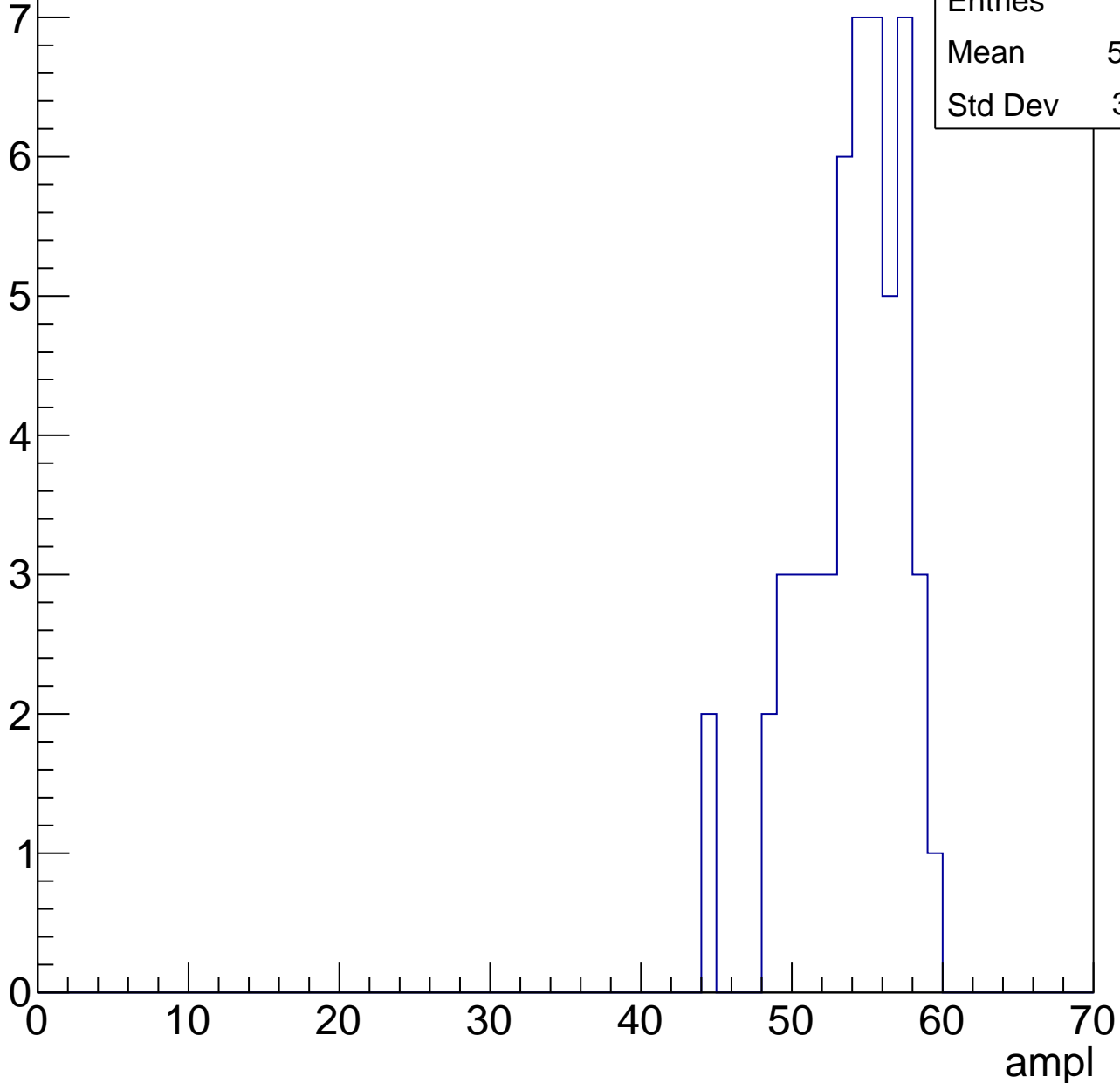


B1L100S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

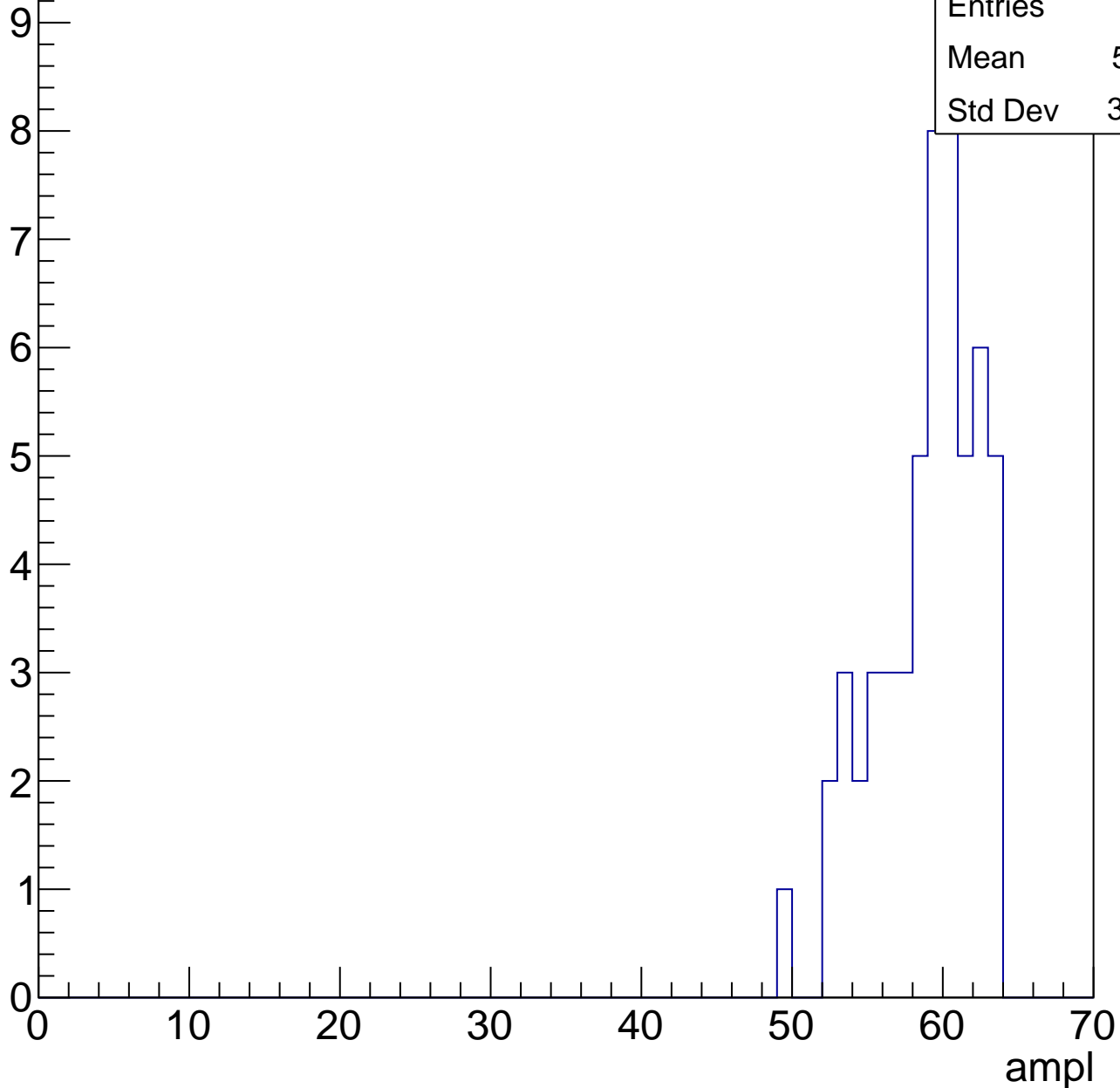
Entries	52
Mean	53.52
Std Dev	3.371



B1L100S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



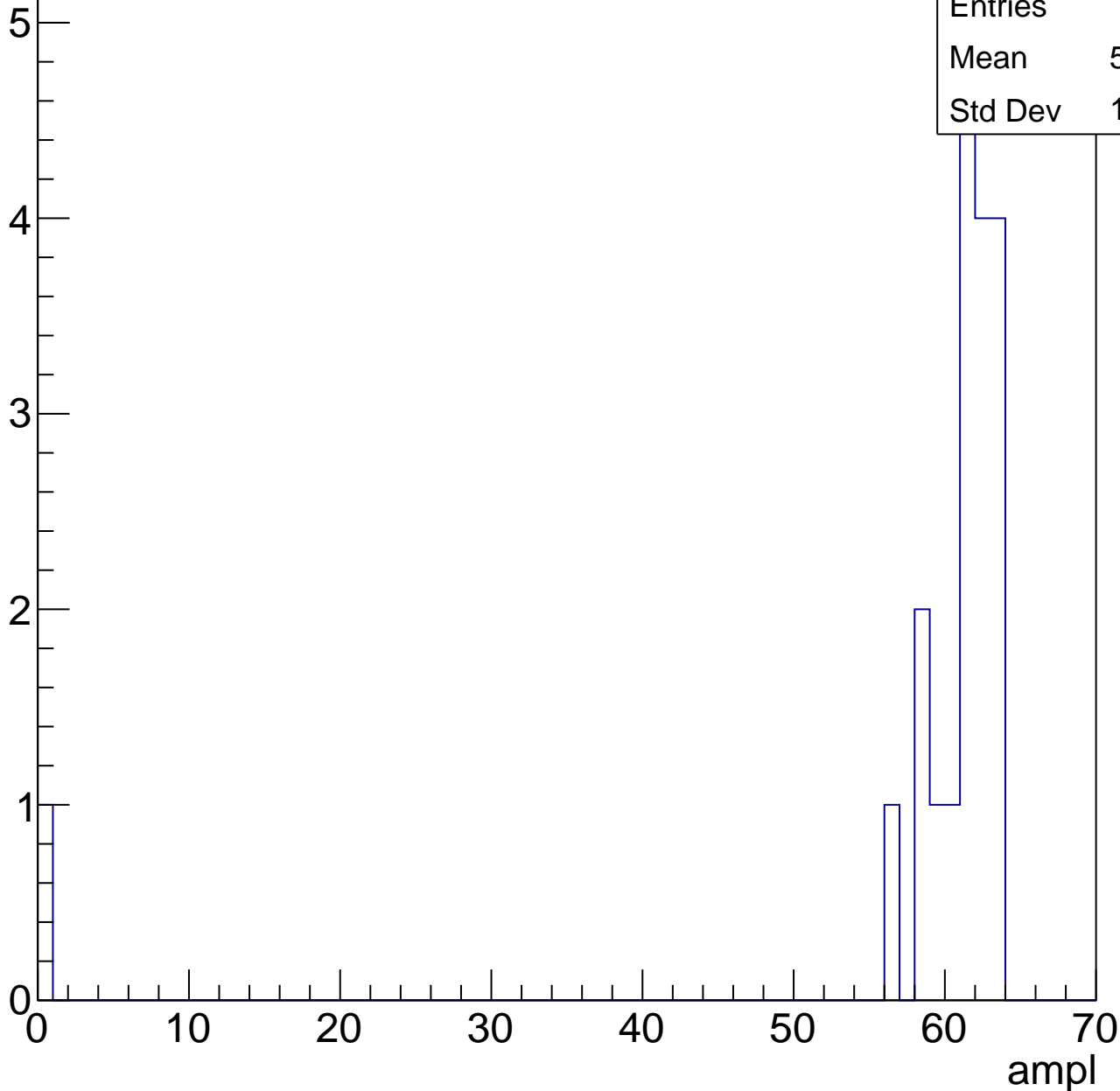
Entries	55
Mean	58.51
Std Dev	3.297

B1L100S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

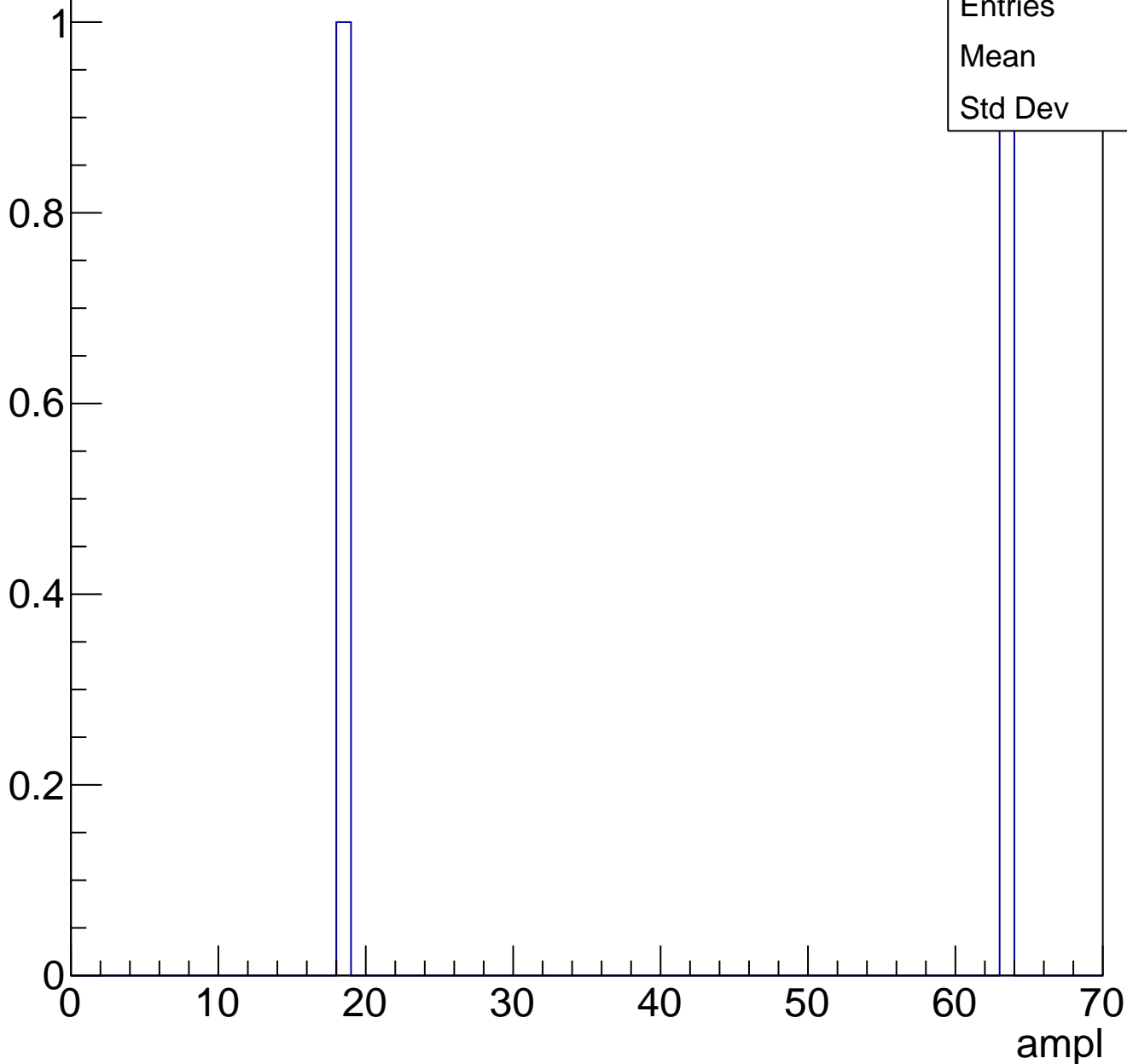
Entries	19
Mean	57.68
Std Dev	13.73



B1L100S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch121, adc0

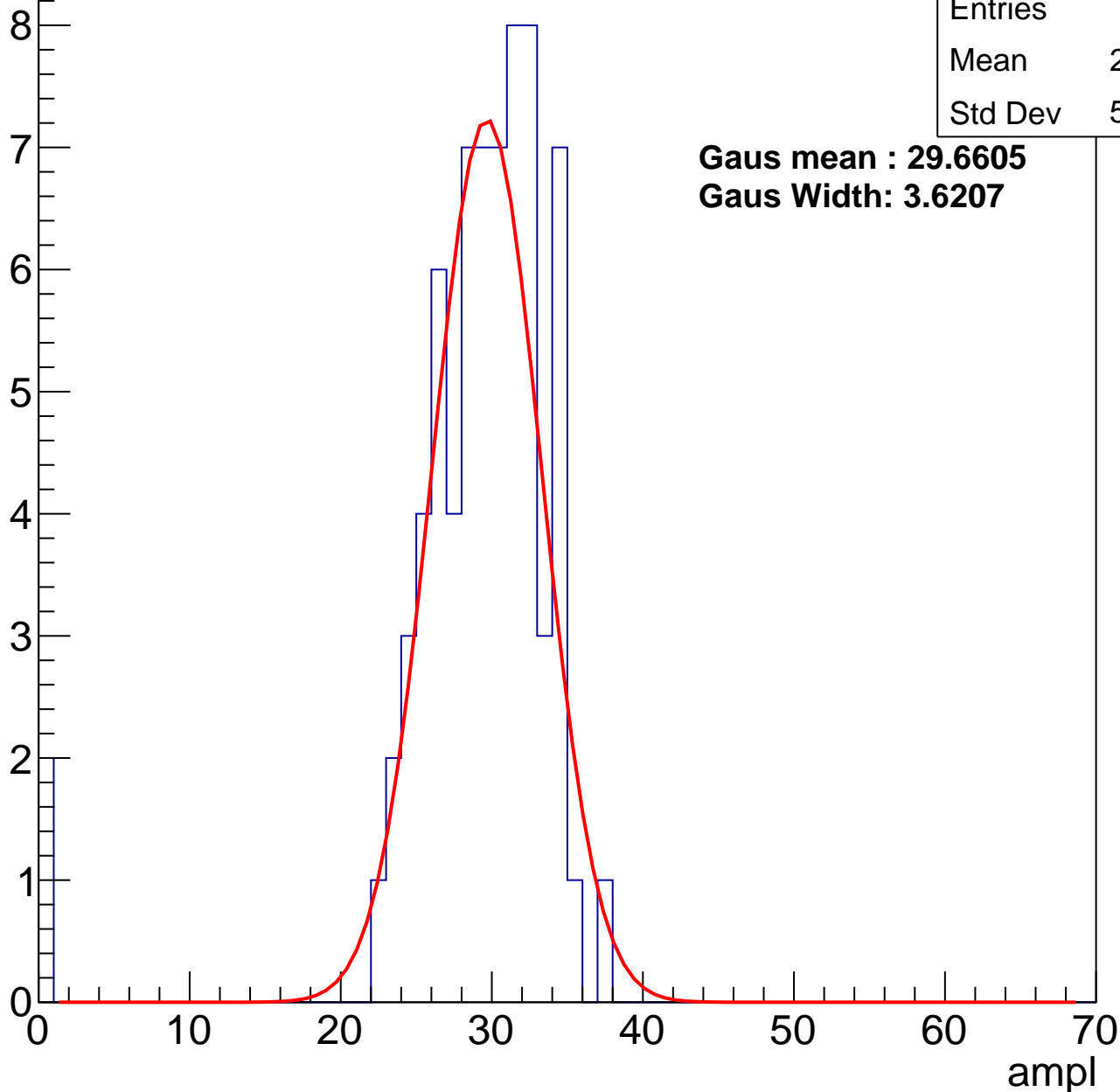
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	28.54
Std Dev	5.859

Gaus mean : 29.6605

Gaus Width: 3.6207



B1L100S, U14-ch121, adc1

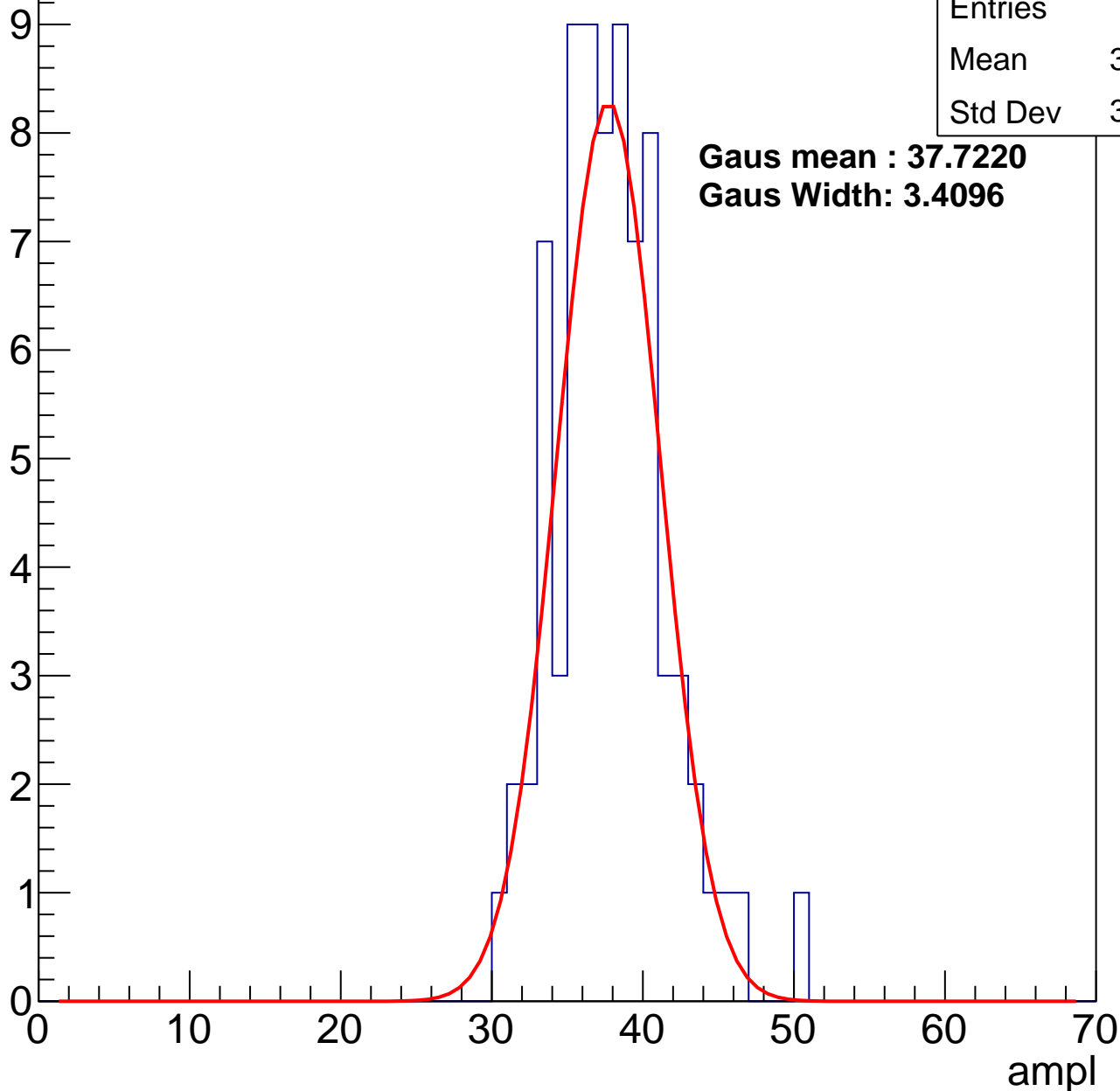
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	37.39
Std Dev	3.622

Gaus mean : 37.7220

Gaus Width: 3.4096



B1L100S, U14-ch121, adc2

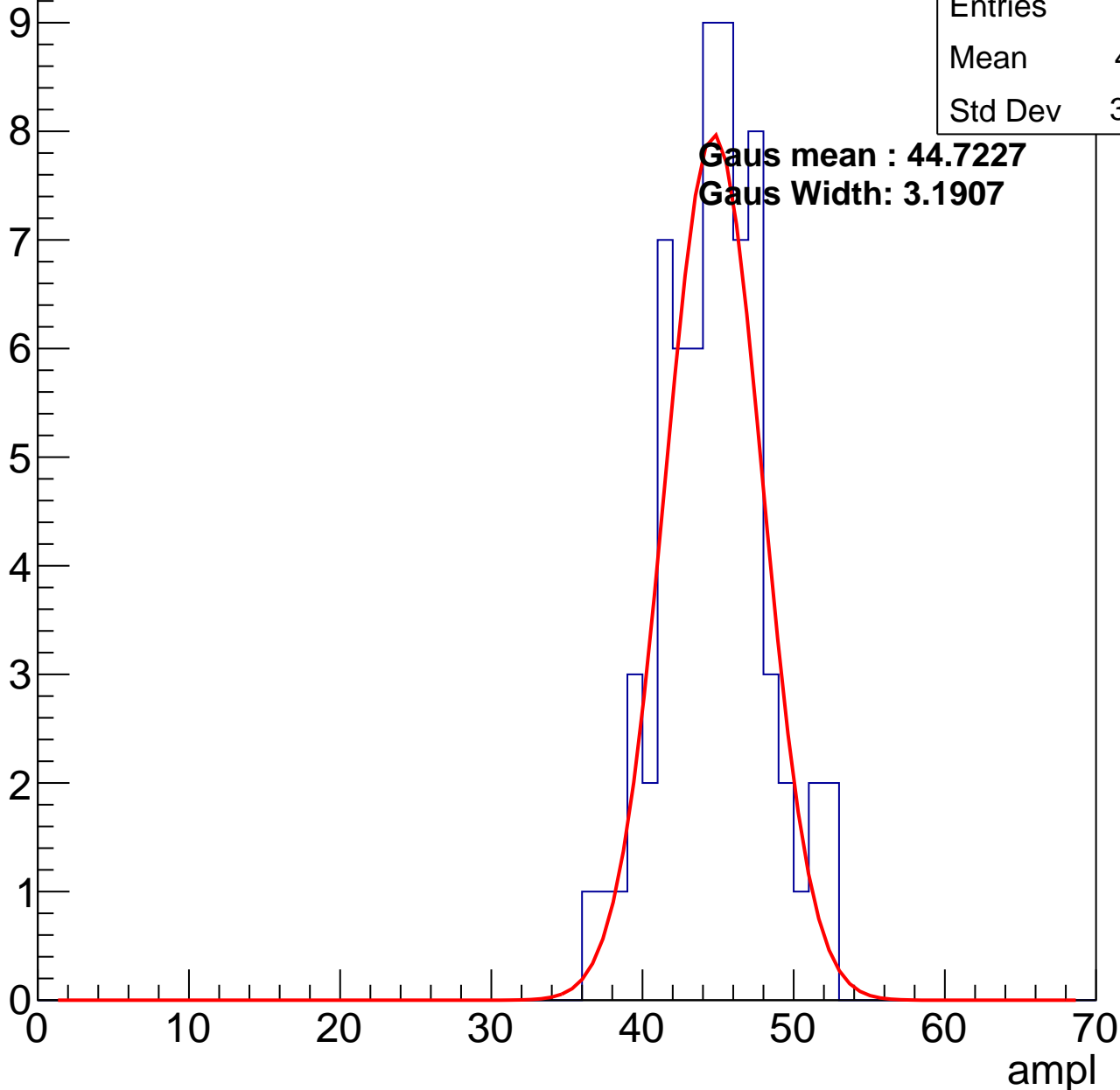
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.31
Std Dev	3.404

Gaus mean : 44.7227

Gaus Width: 3.1907

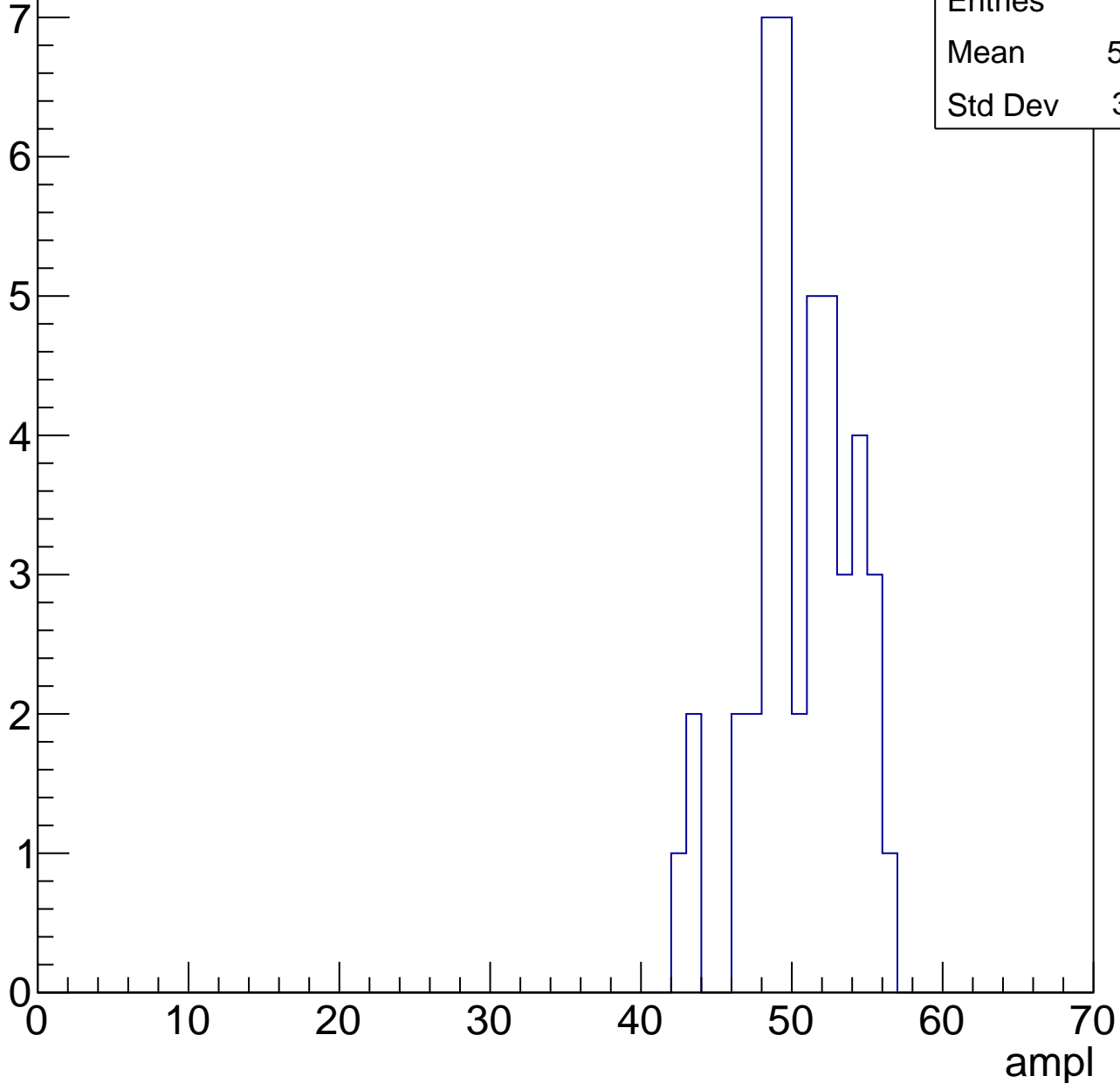


B1L100S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	50.09
Std Dev	3.281

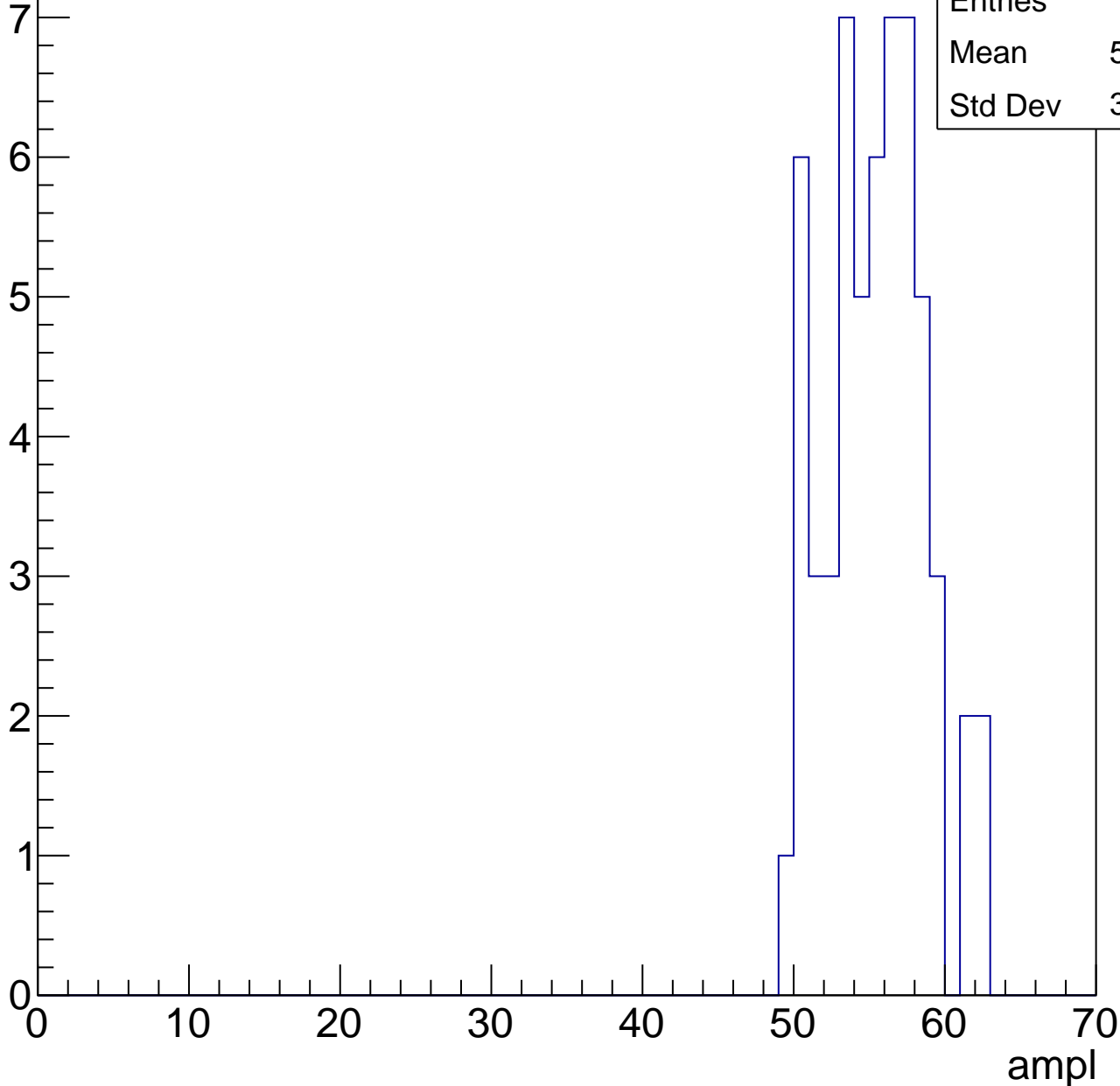


B1L100S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

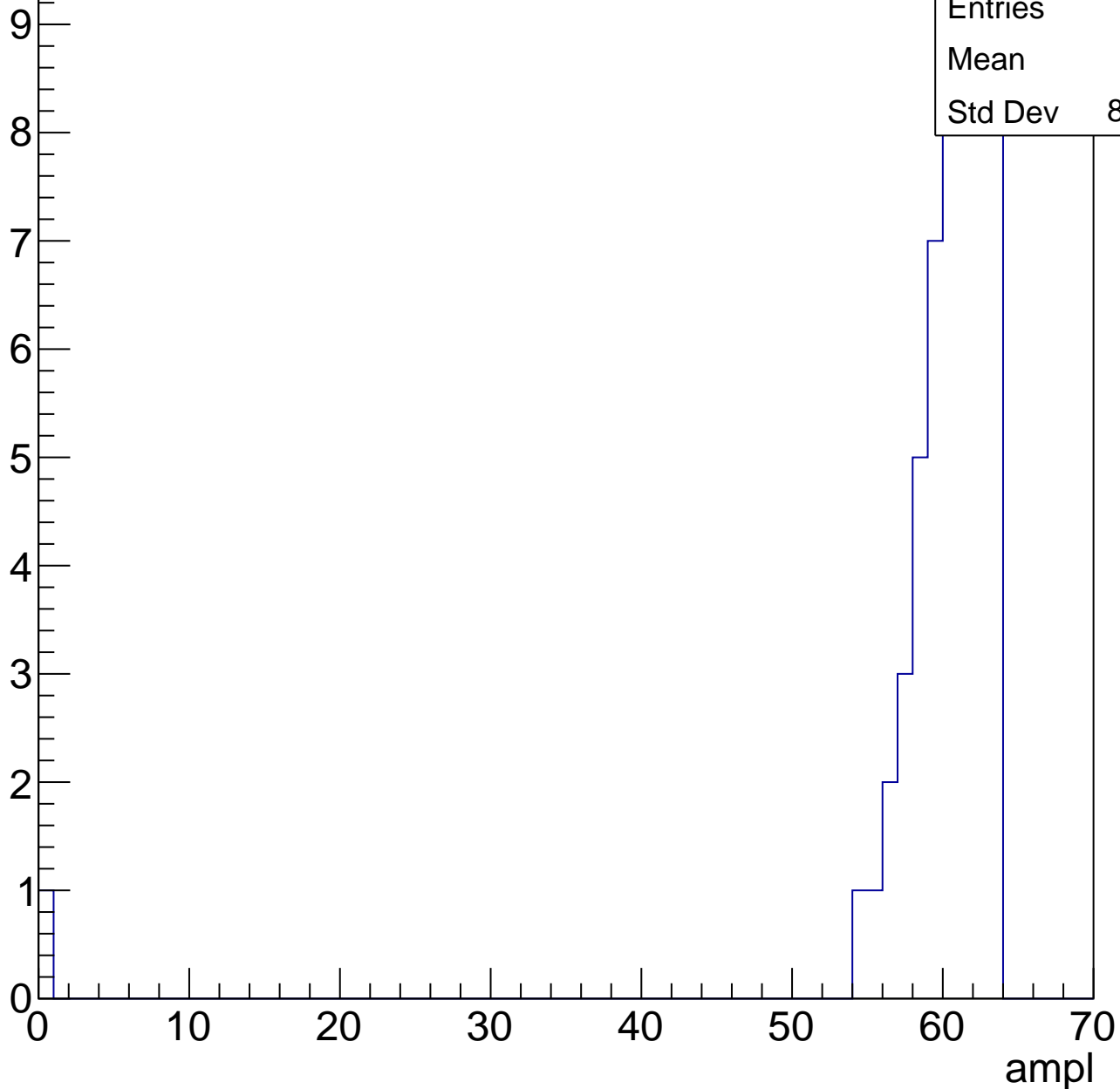
Entries	57
Mean	54.96
Std Dev	3.223



B1L100S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch122, adc0

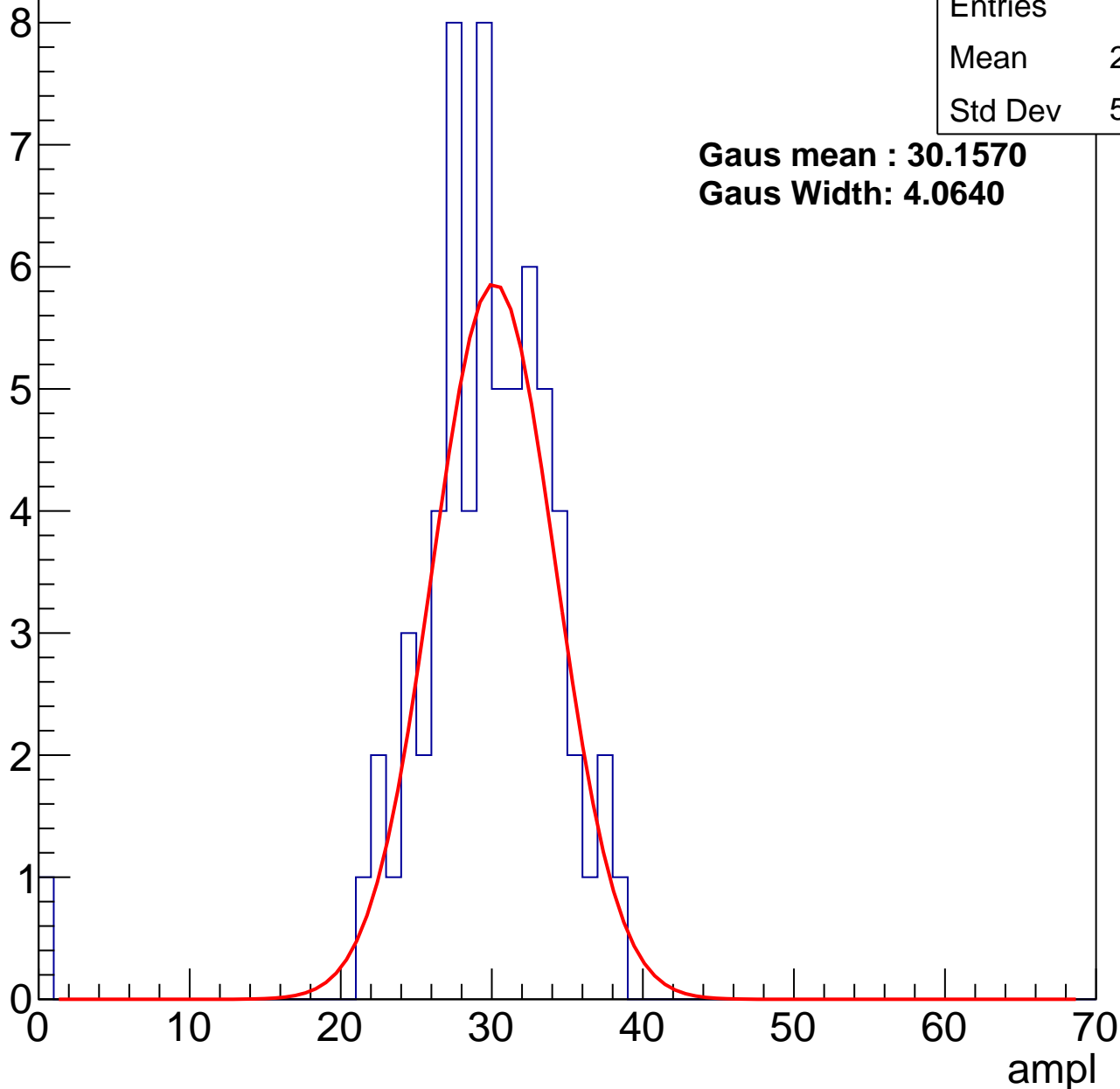
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.08
Std Dev	5.272

Gaus mean : 30.1570

Gaus Width: 4.0640



B1L100S, U14-ch122, adc1

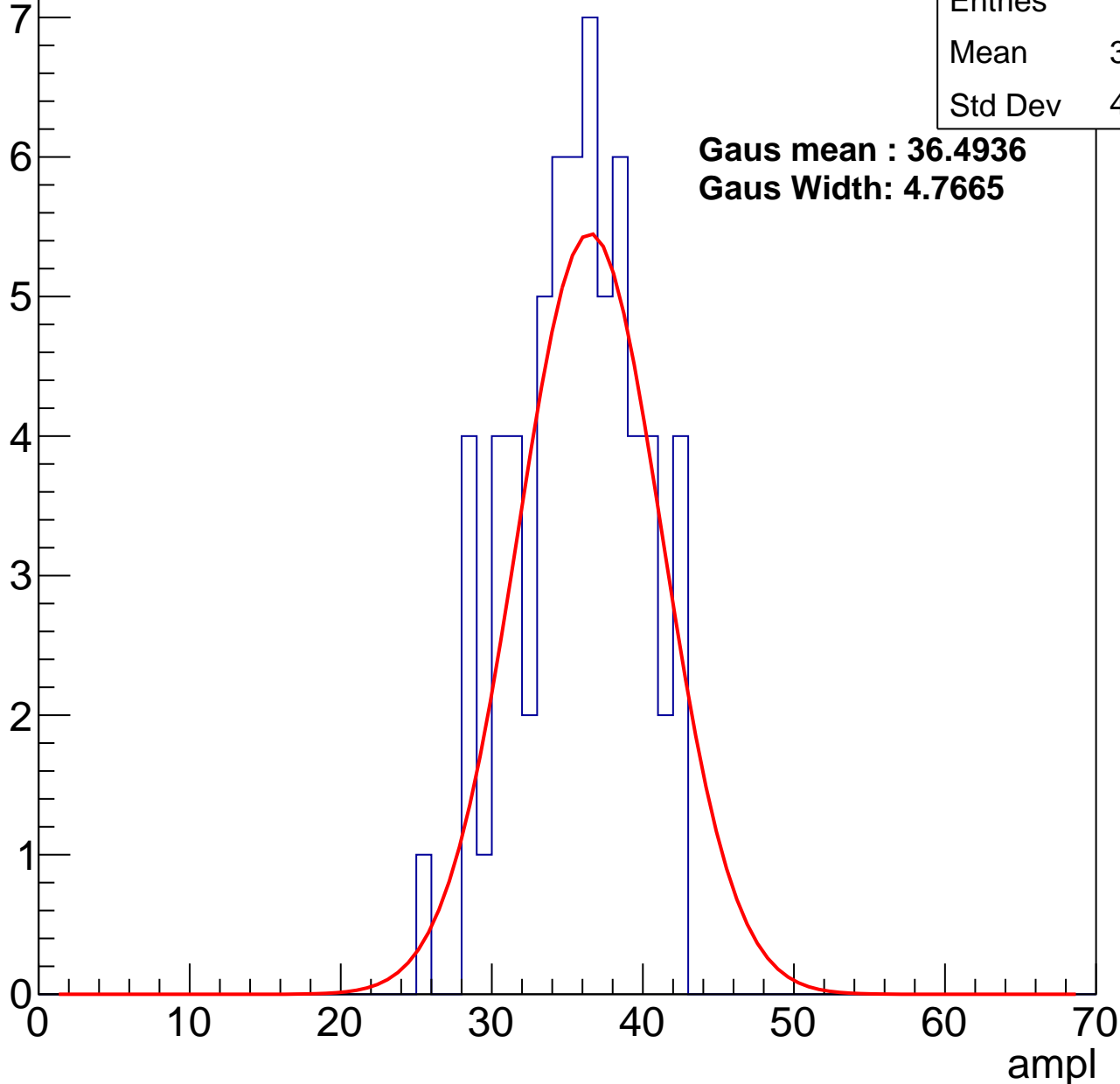
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.14
Std Dev	4.023

Gaus mean : 36.4936

Gaus Width: 4.7665



B1L100S, U14-ch122, adc2

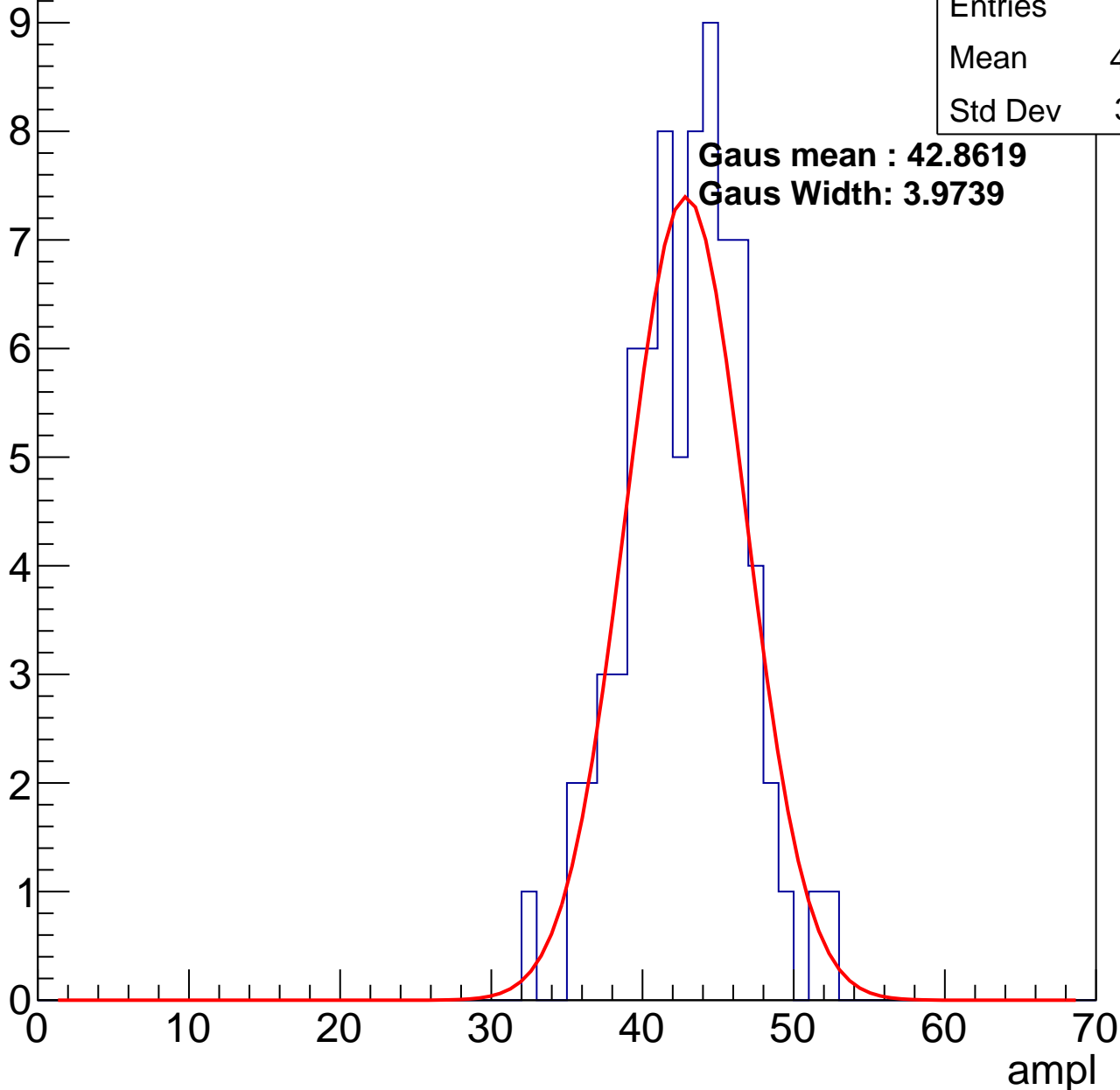
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.42
Std Dev	3.781

Gaus mean : 42.8619

Gaus Width: 3.9739

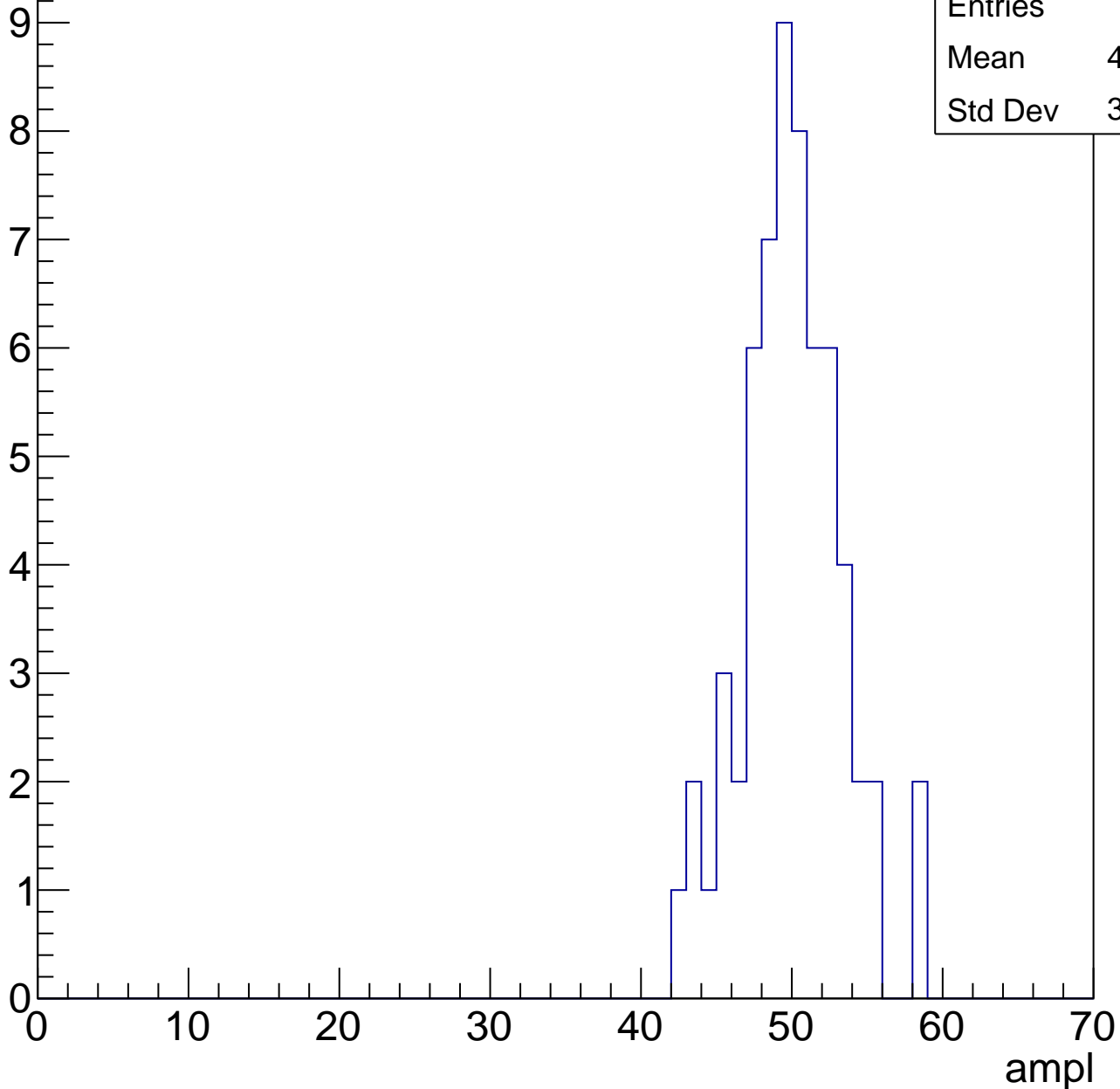


B1L100S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.54
Std Dev	3.287

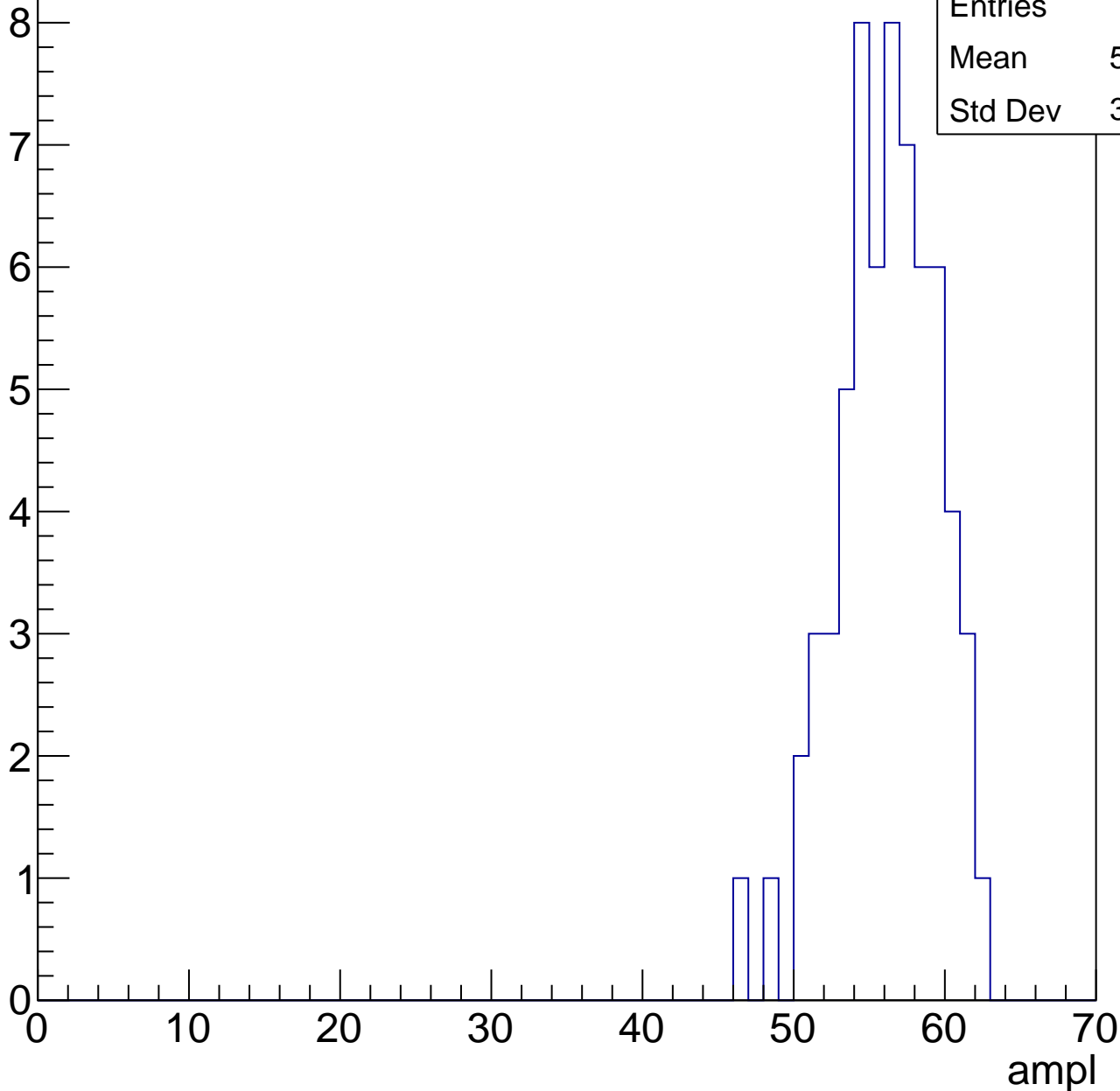


B1L100S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.69
Std Dev	3.297

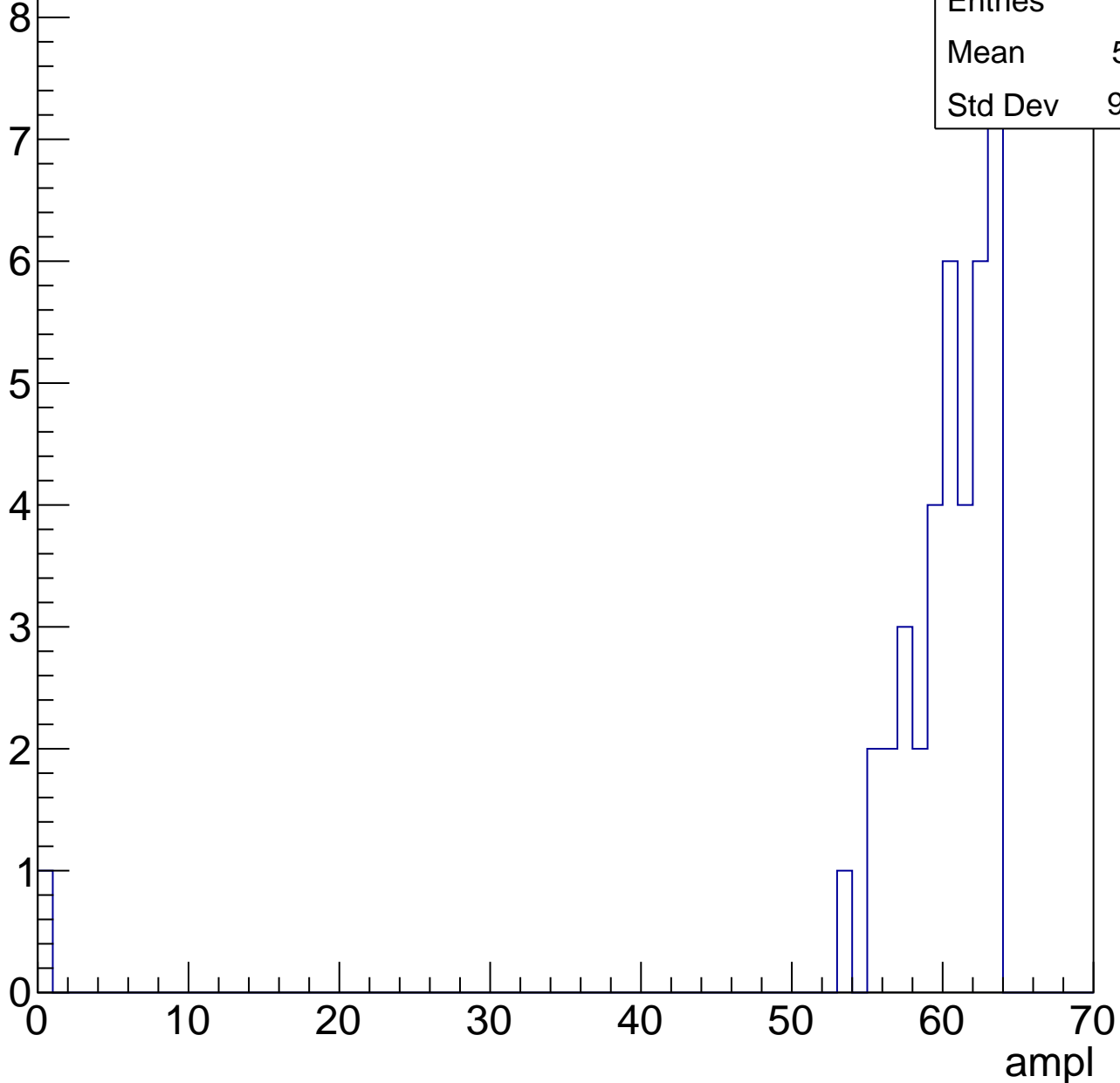


B1L100S, U14-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

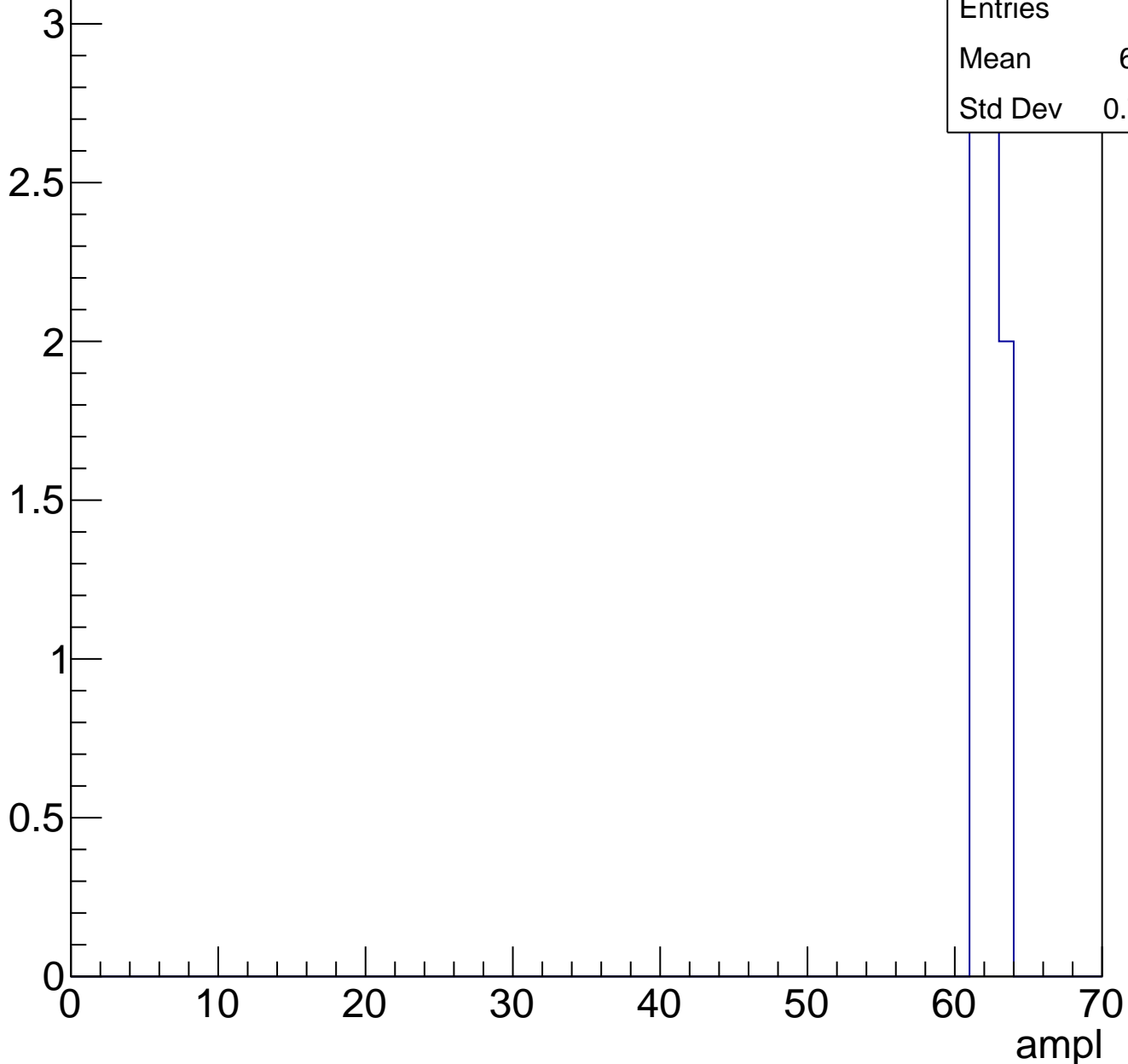
Entries	39
Mean	58.41
Std Dev	9.834



B1L100S, U14-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

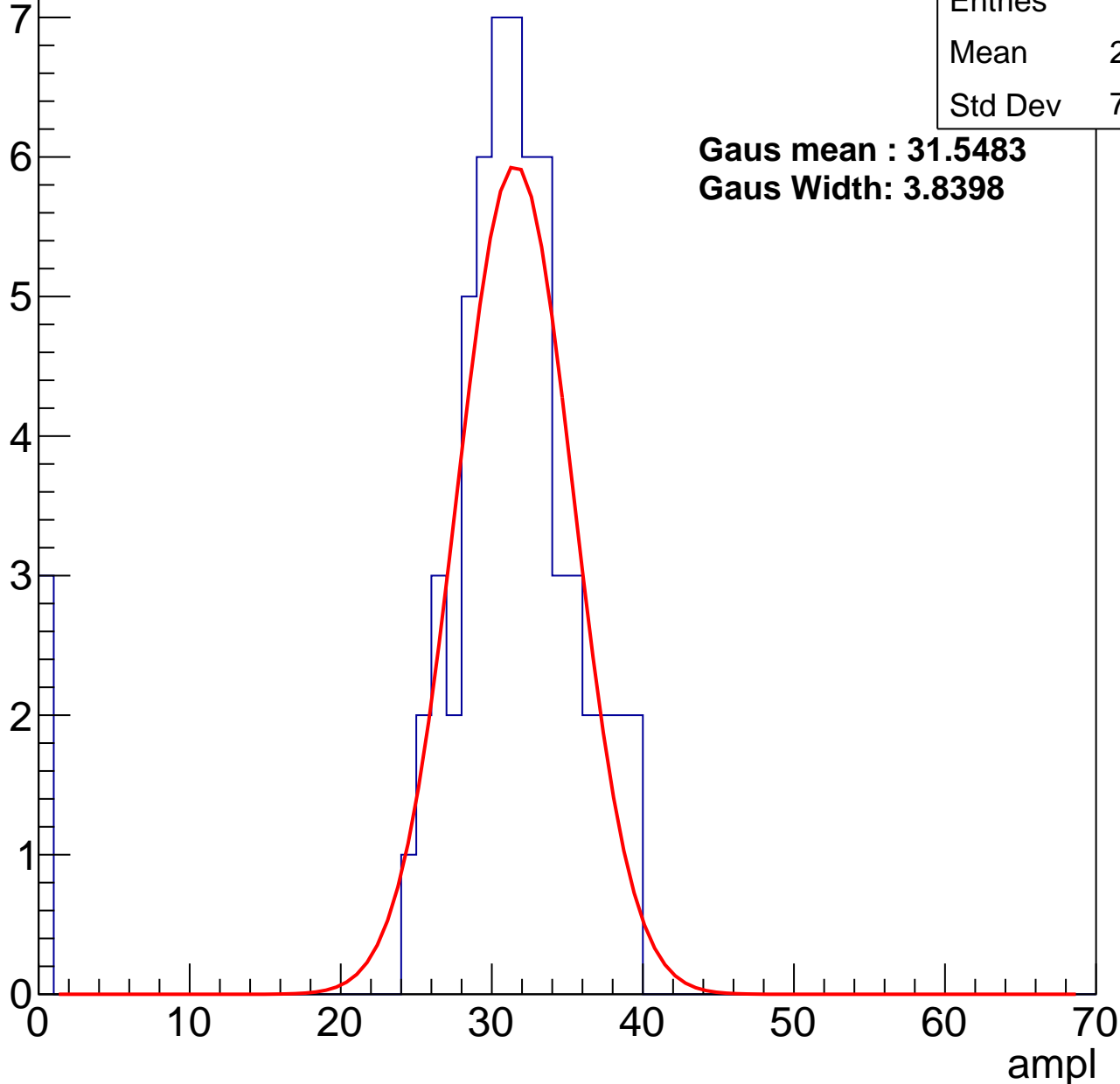
B1L100S, U14-ch123, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	29.74
Std Dev	7.558

Gaus mean : 31.5483
Gaus Width: 3.8398



B1L100S, U14-ch123, adc1

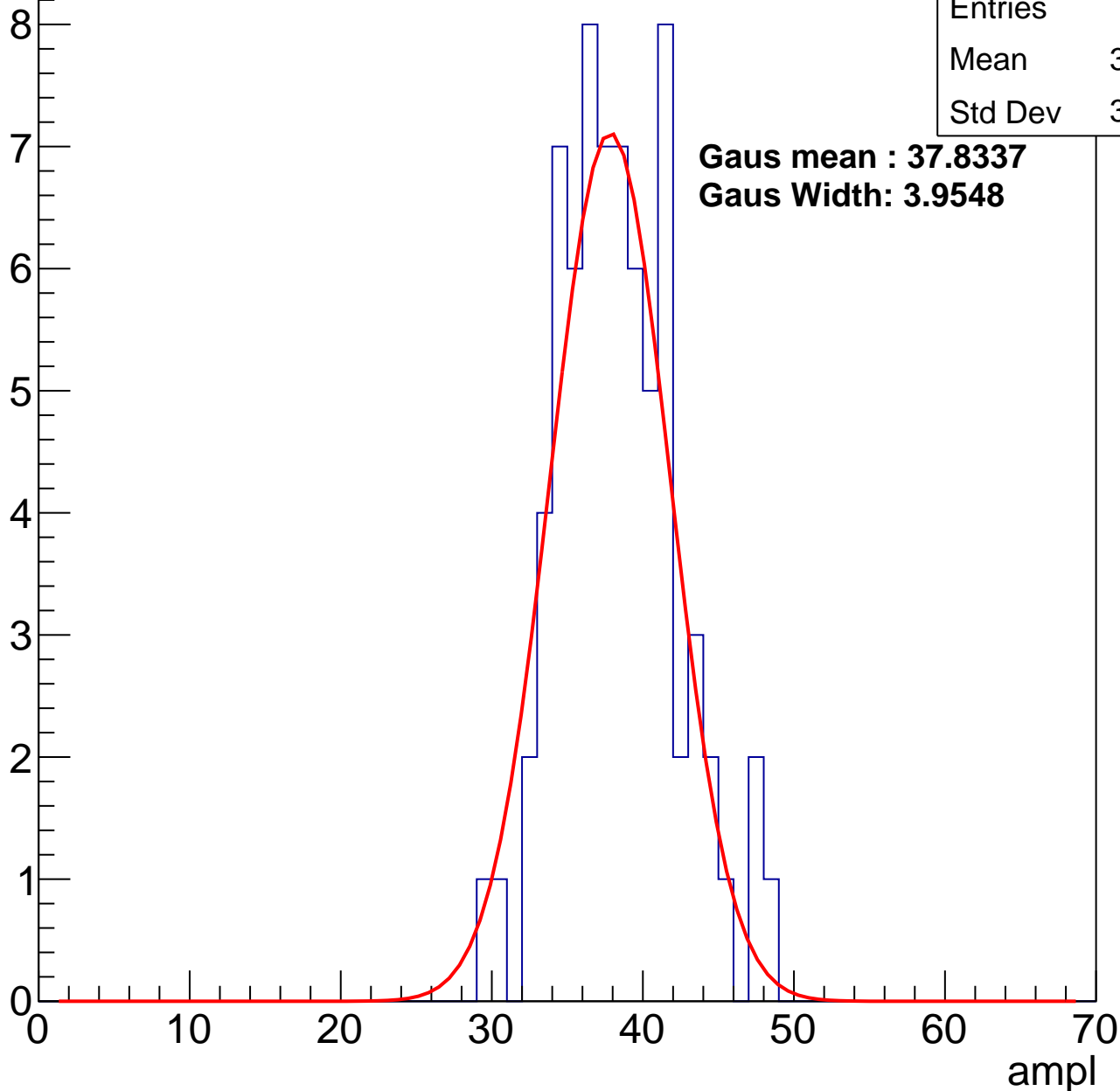
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.89
Std Dev	3.898

Gaus mean : 37.8337

Gaus Width: 3.9548



B1L100S, U14-ch123, adc2

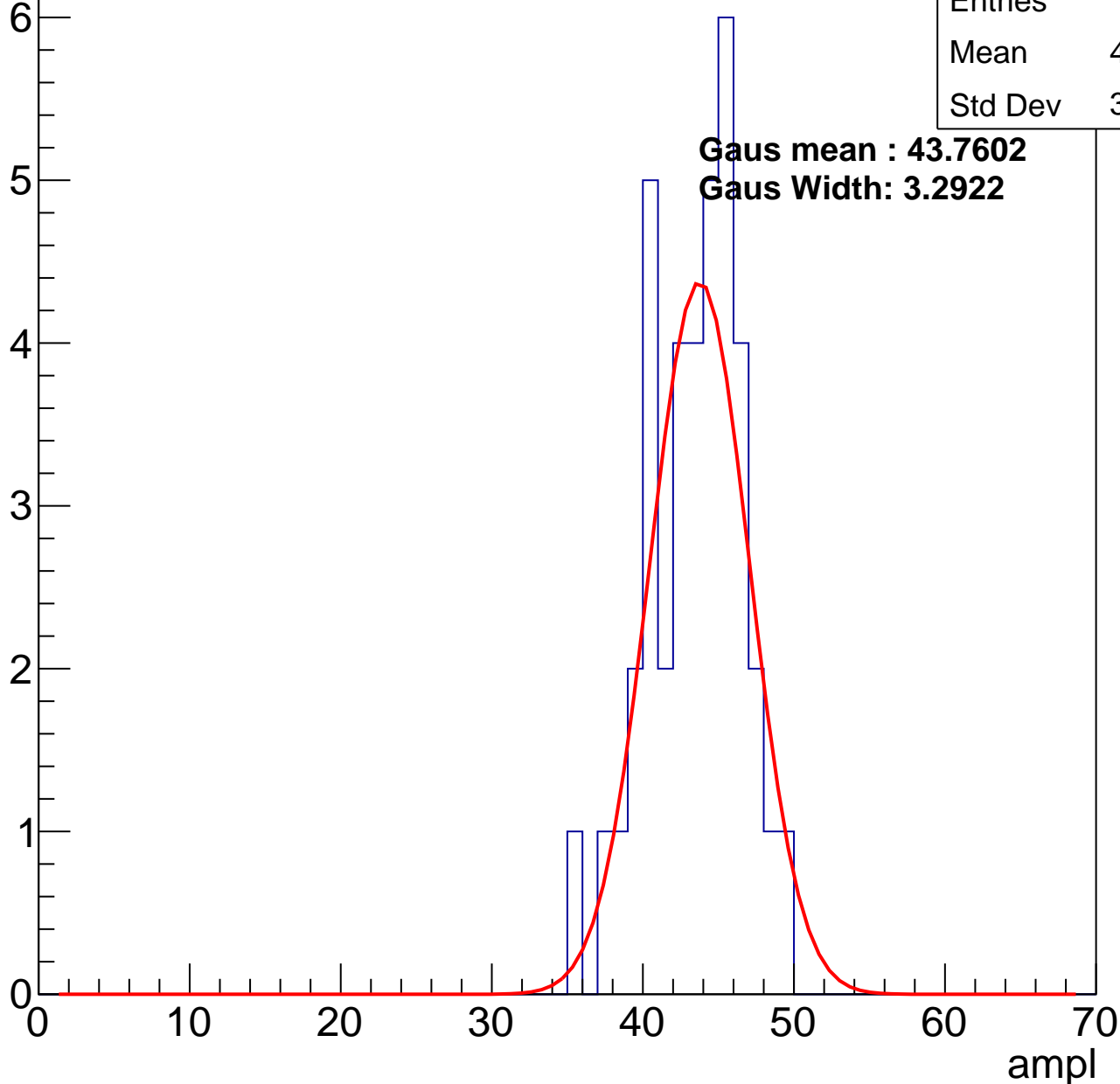
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	42.95
Std Dev	3.088

Gaus mean : 43.7602

Gaus Width: 3.2922

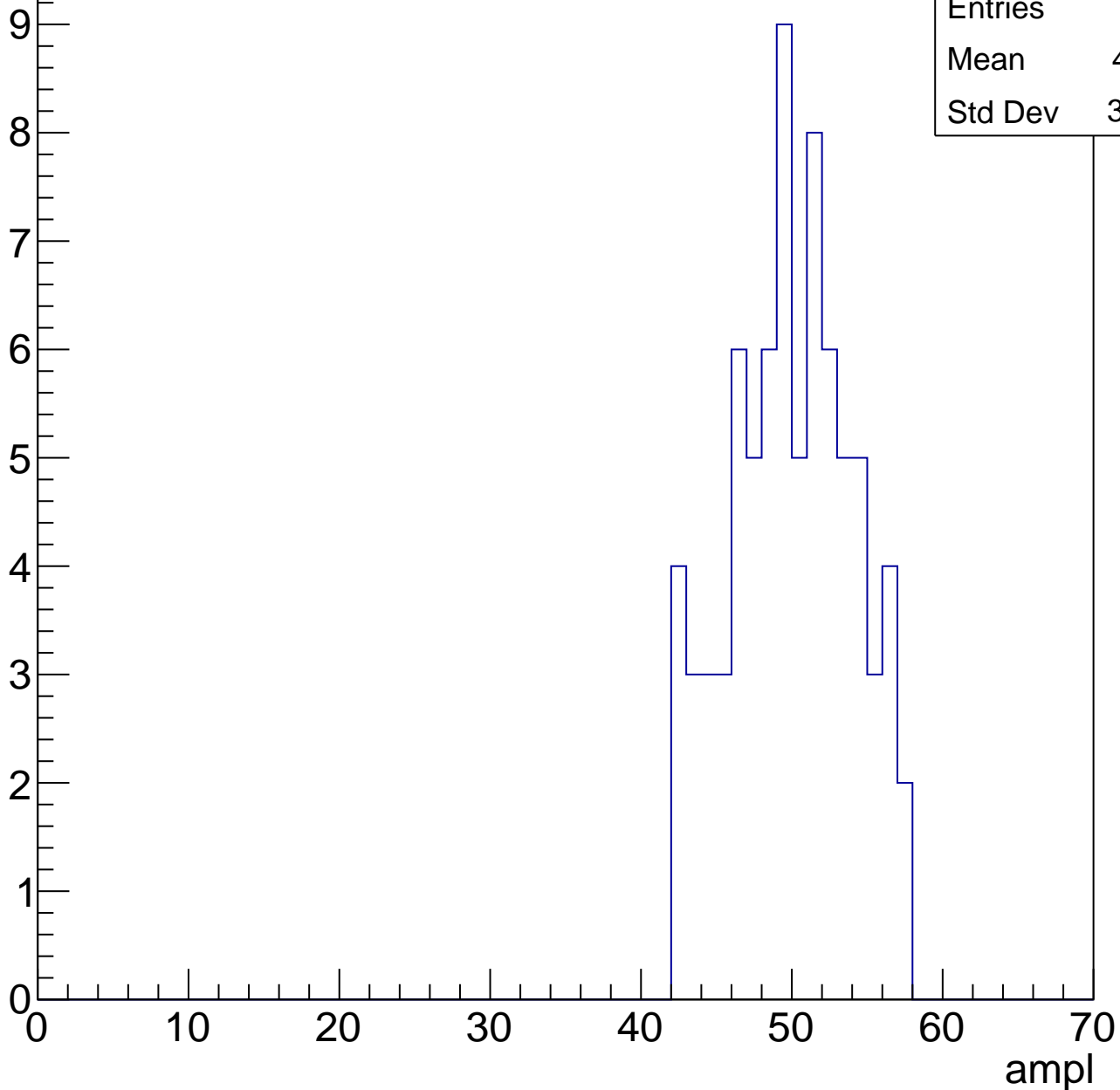


B1L100S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

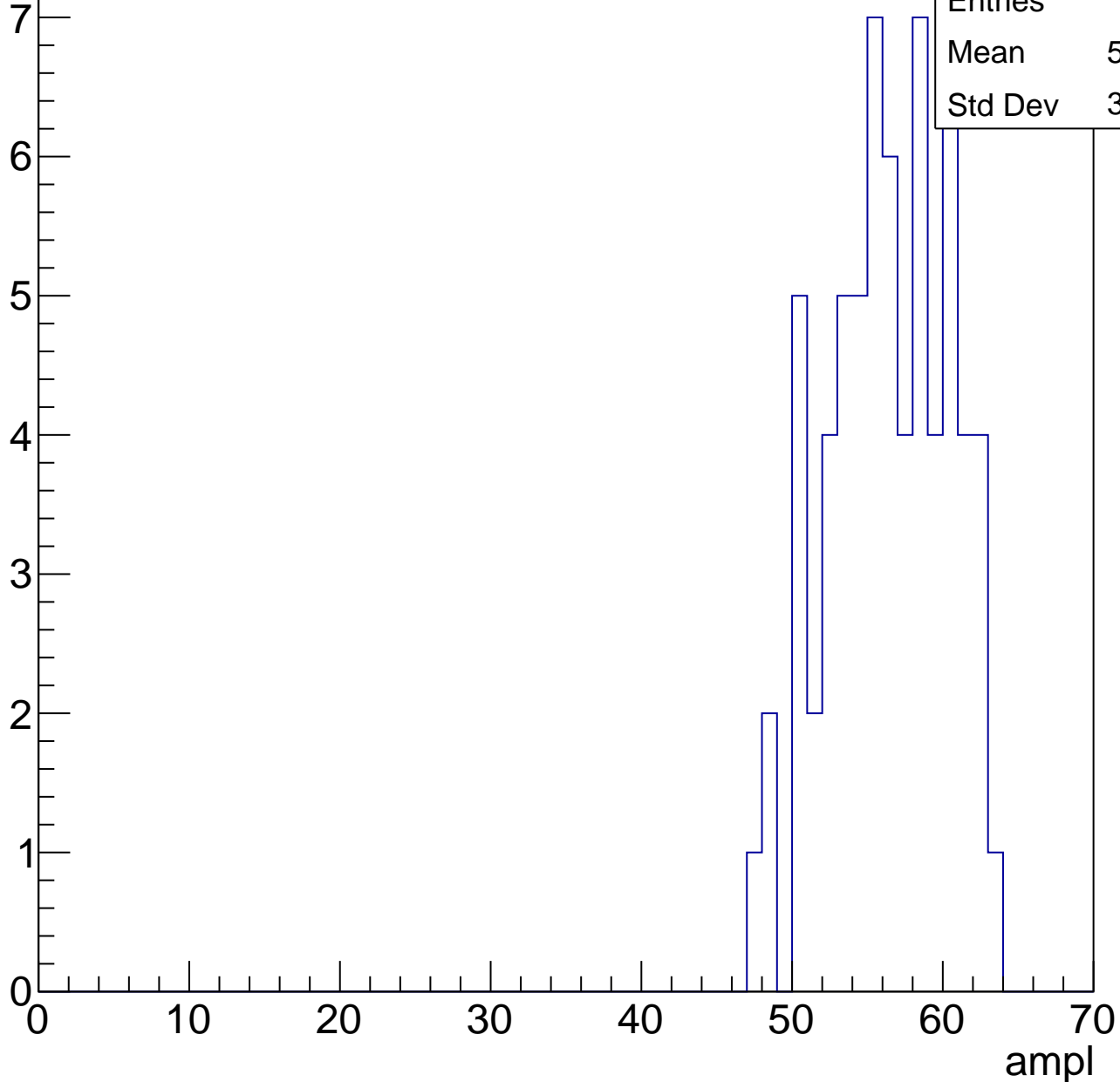
Entries	77
Mean	49.51
Std Dev	3.973



B1L100S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

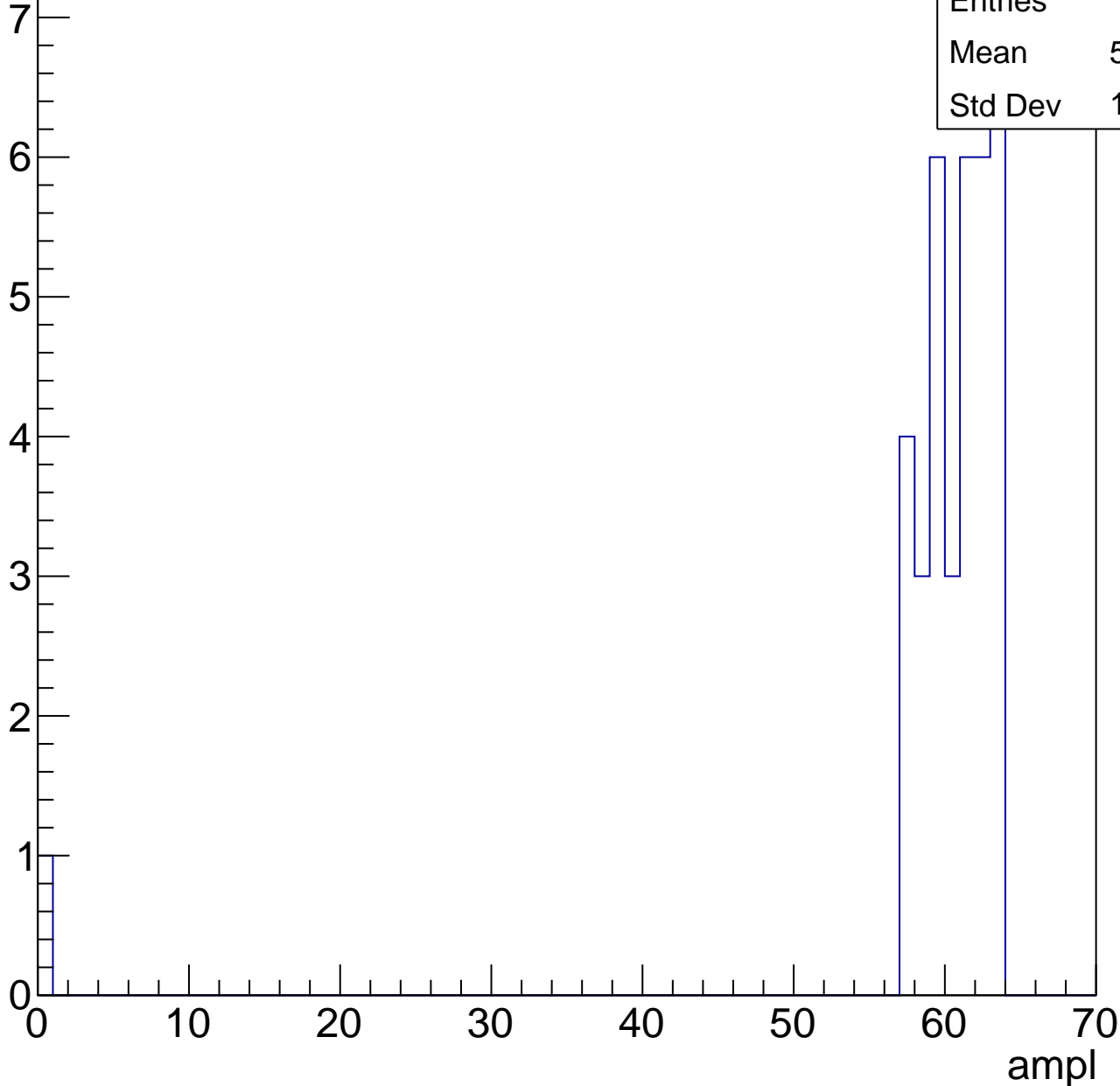


B1L100S, U14-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.75
Std Dev	10.13



B1L100S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

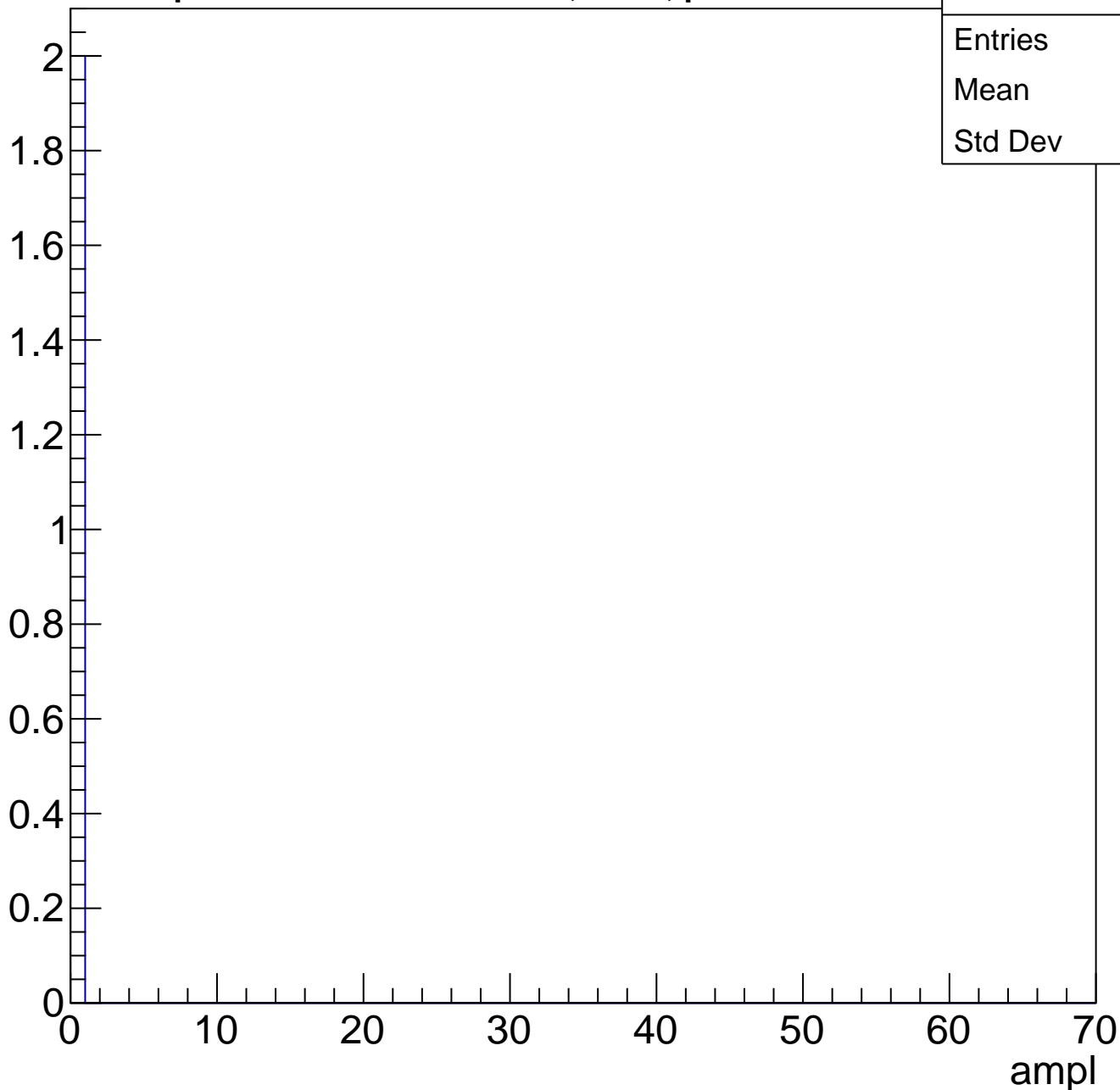
Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries

2

Mean

0

Std Dev

0

B1L100S, U14-ch124, adc0

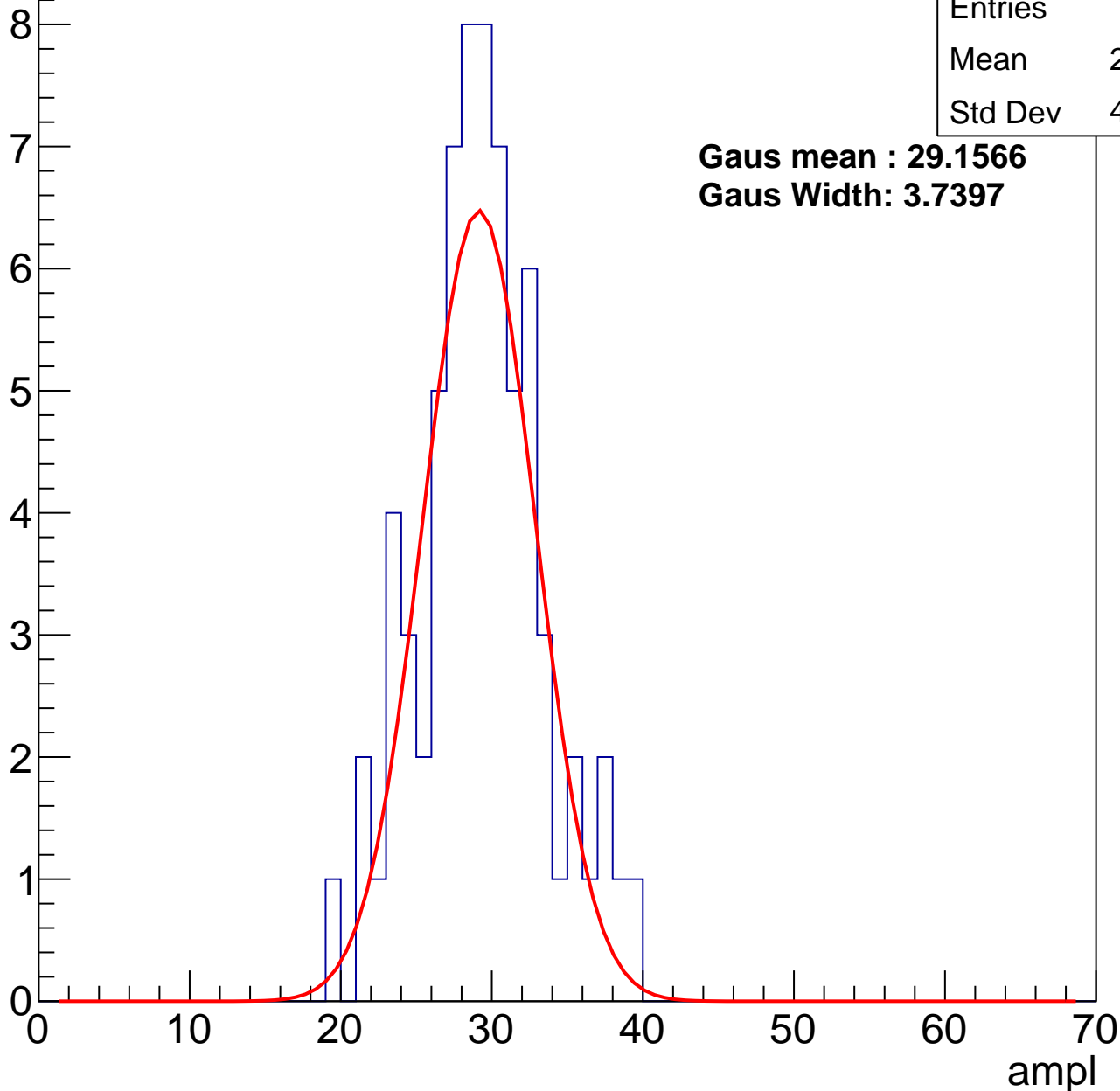
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.84
Std Dev	4.125

Gaus mean : 29.1566

Gaus Width: 3.7397



B1L100S, U14-ch124, adc1

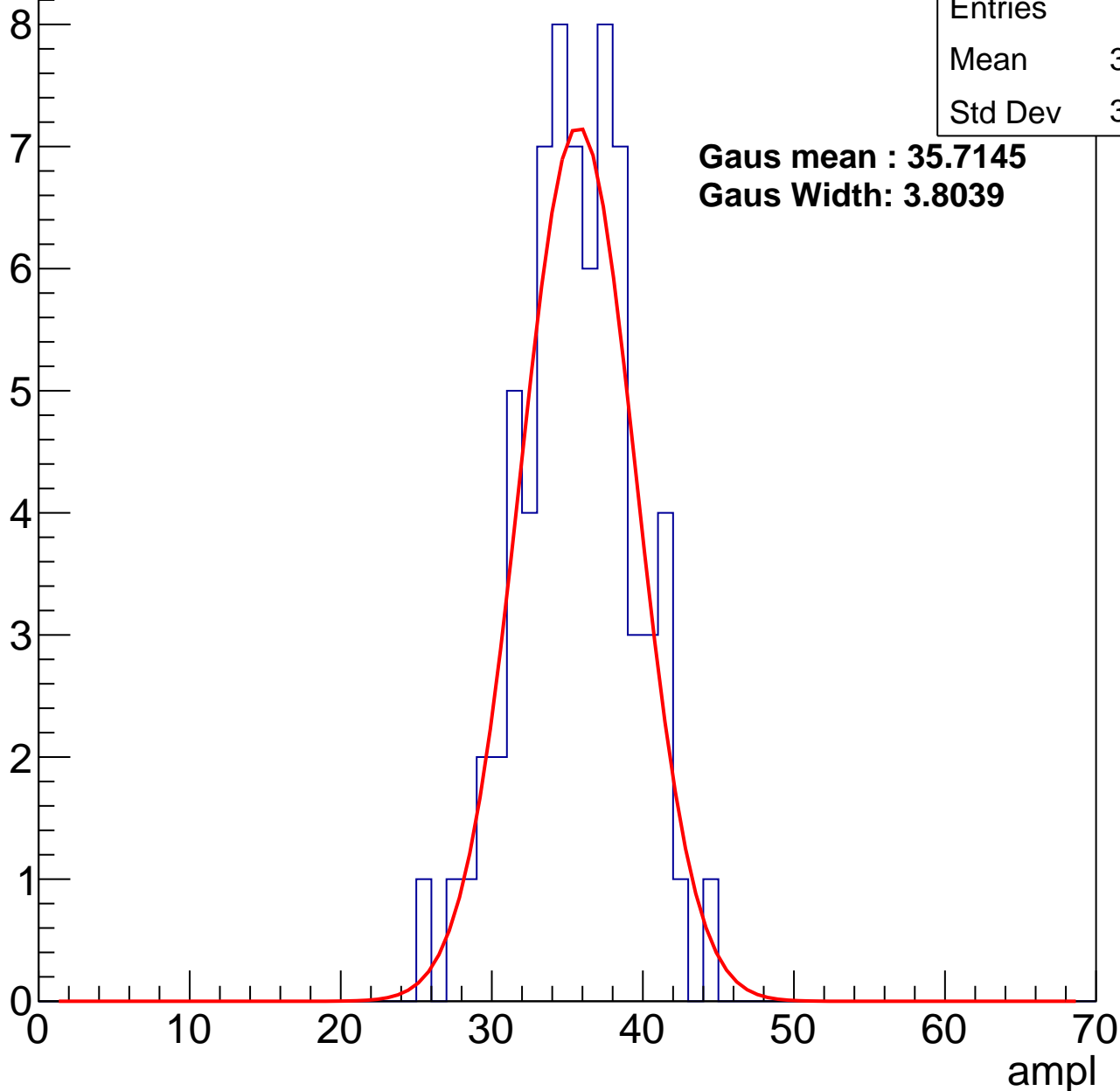
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.13
Std Dev	3.723

Gaus mean : 35.7145

Gaus Width: 3.8039



B1L100S, U14-ch124, adc2

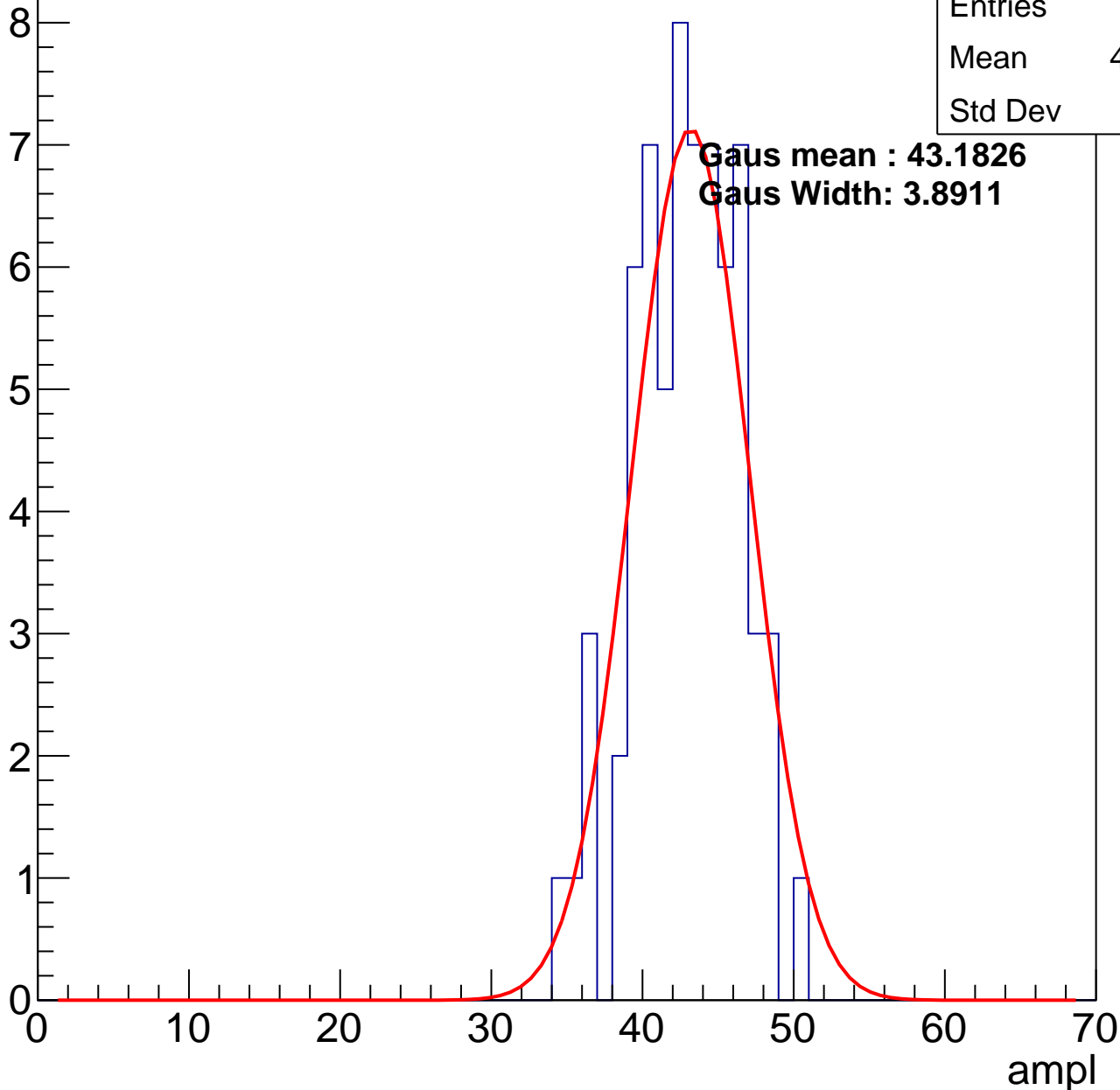
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.45
Std Dev	3.4

Gaus mean : 43.1826

Gaus Width: 3.8911

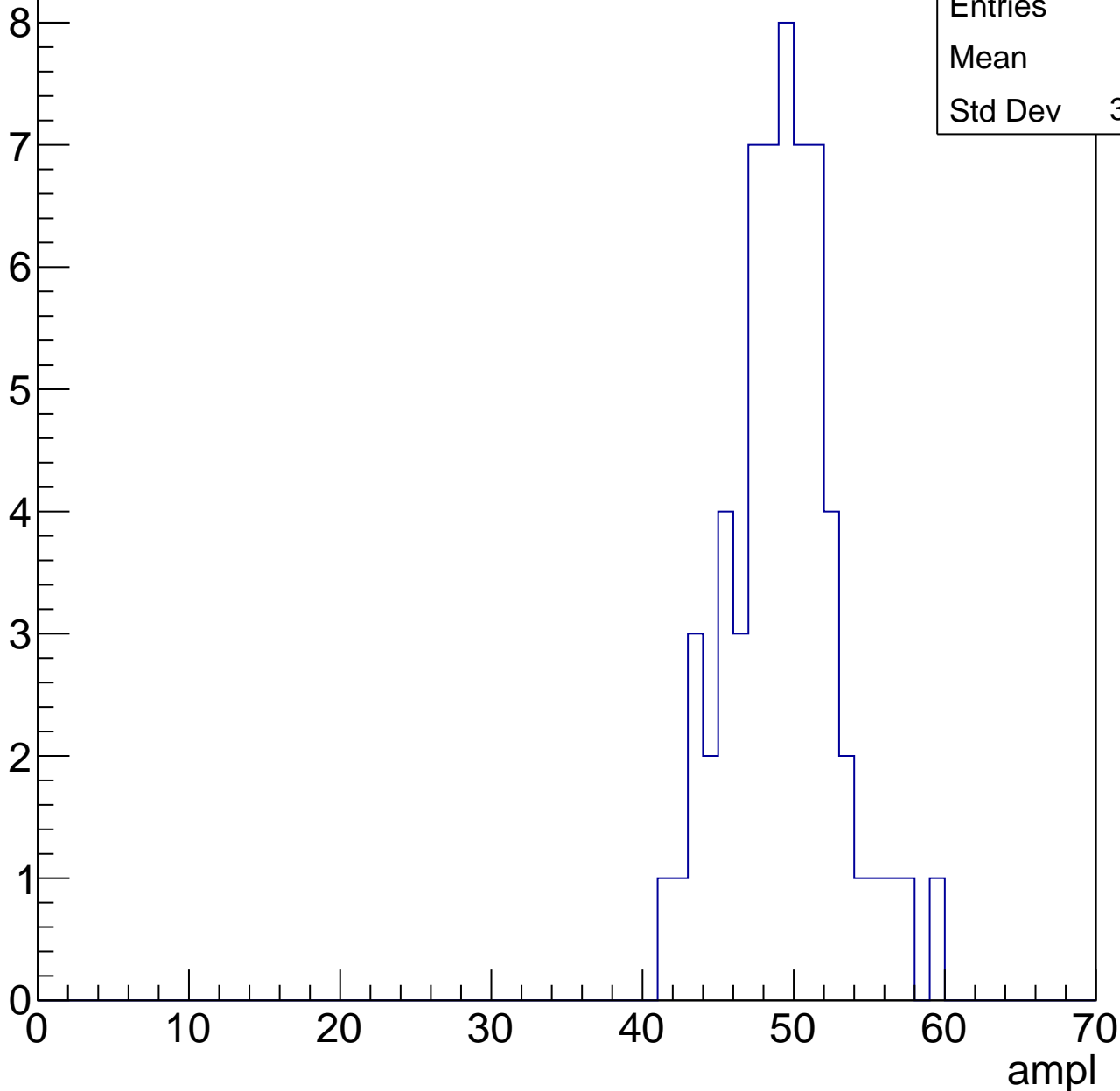


B1L100S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48.8
Std Dev	3.557

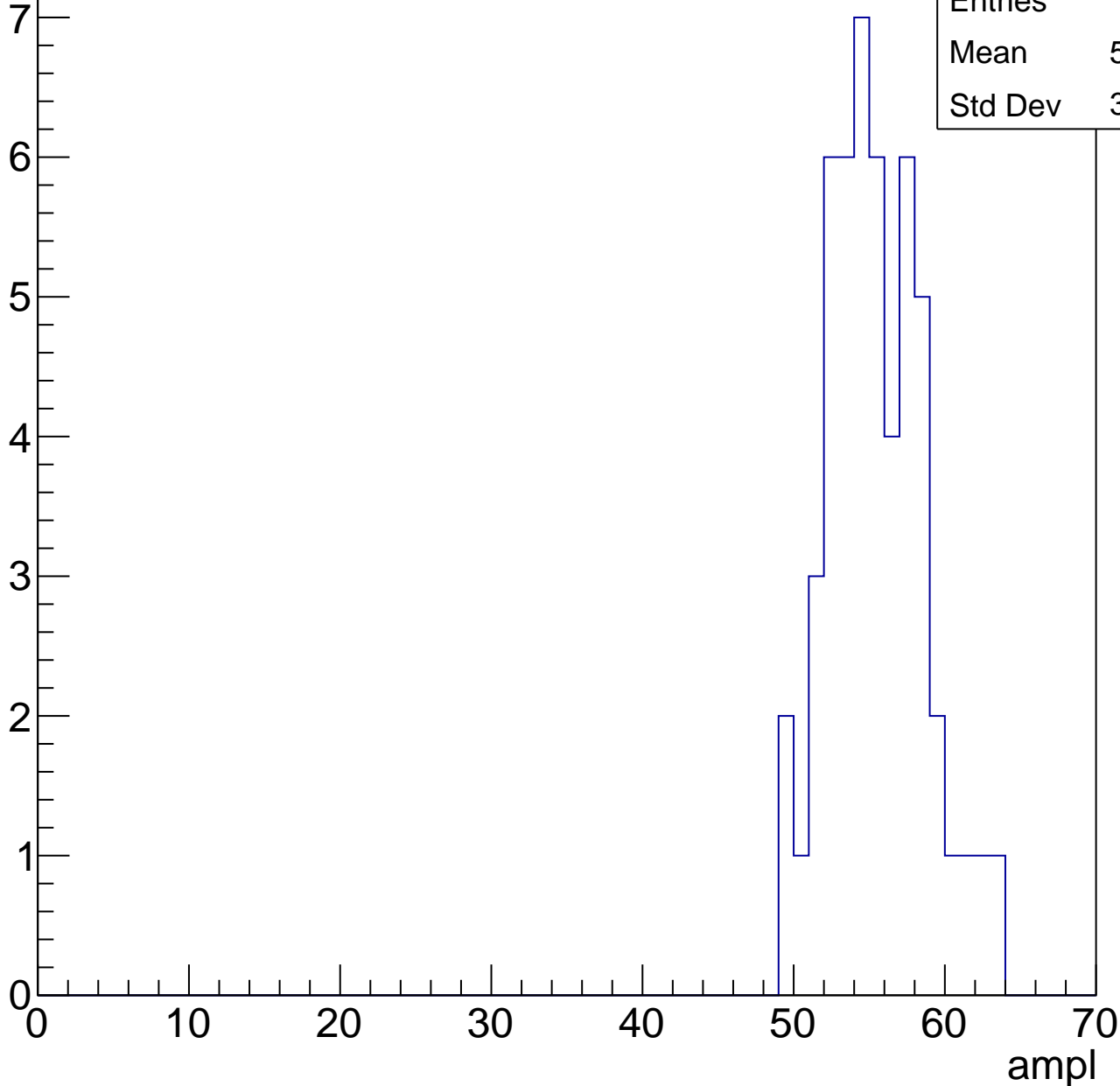


B1L100S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	54.98
Std Dev	3.129

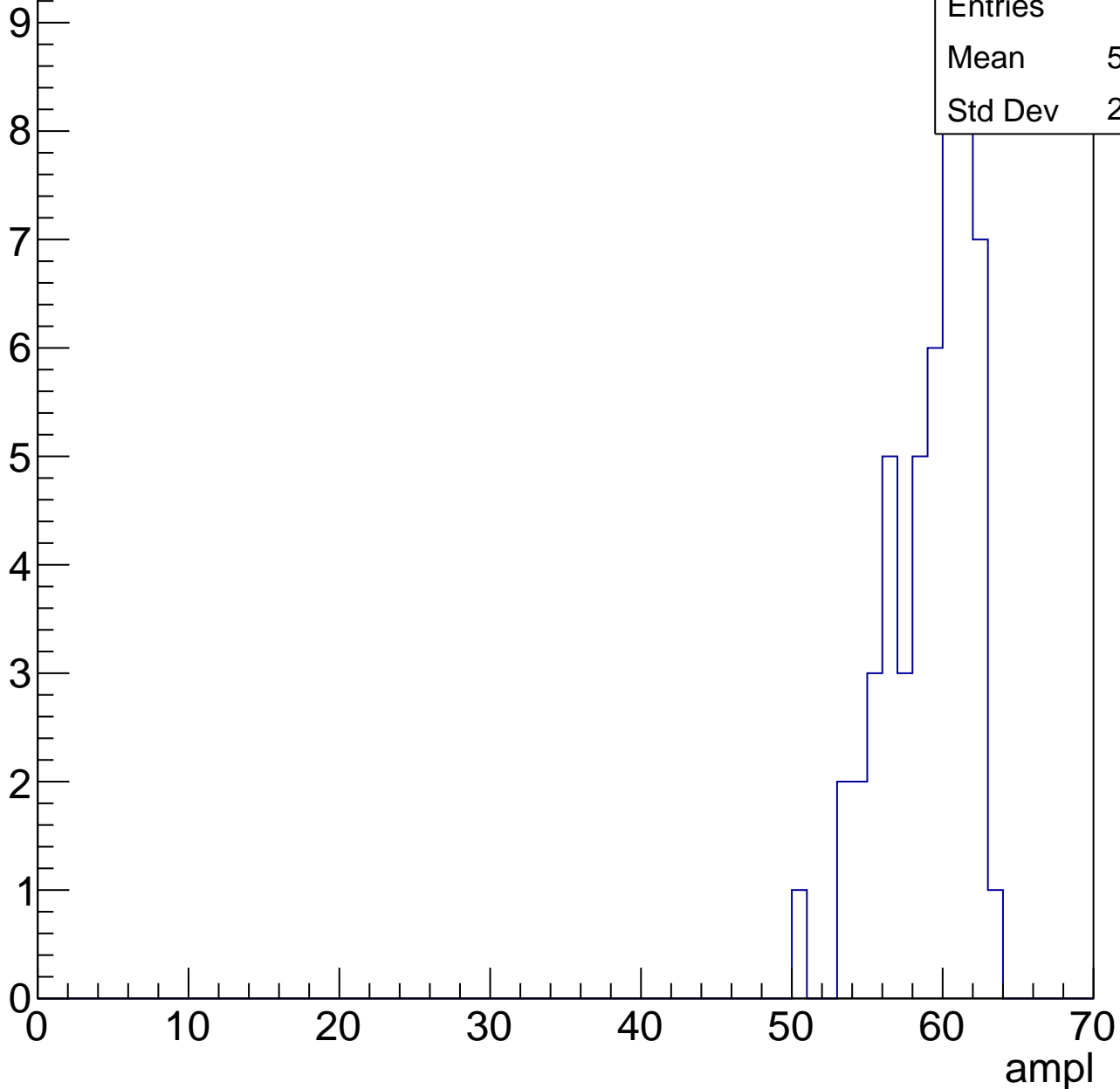


B1L100S, U14-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.63
Std Dev	2.862

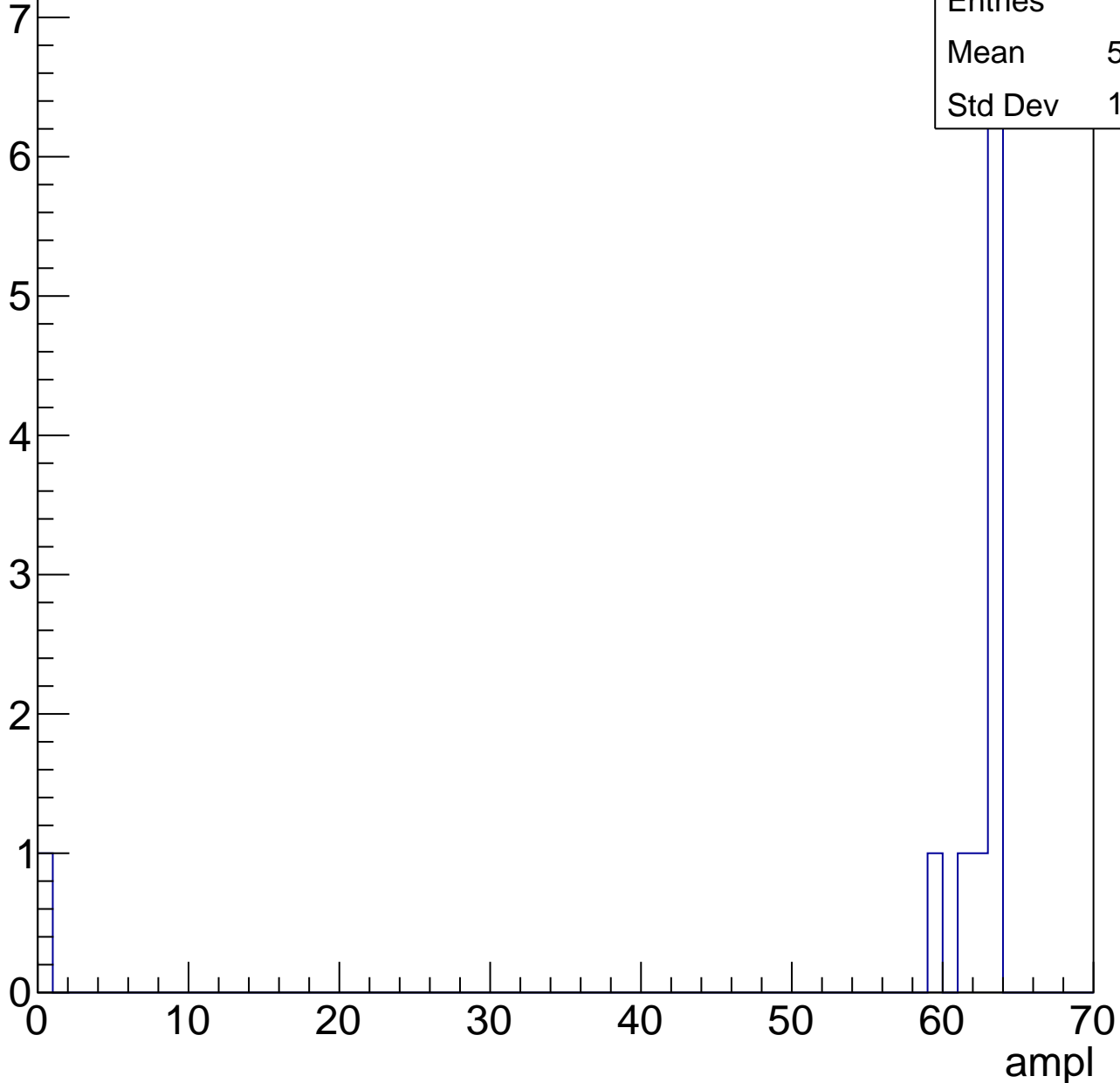


B1L100S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	56.64
Std Dev	17.95



B1L100S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch125, adc0

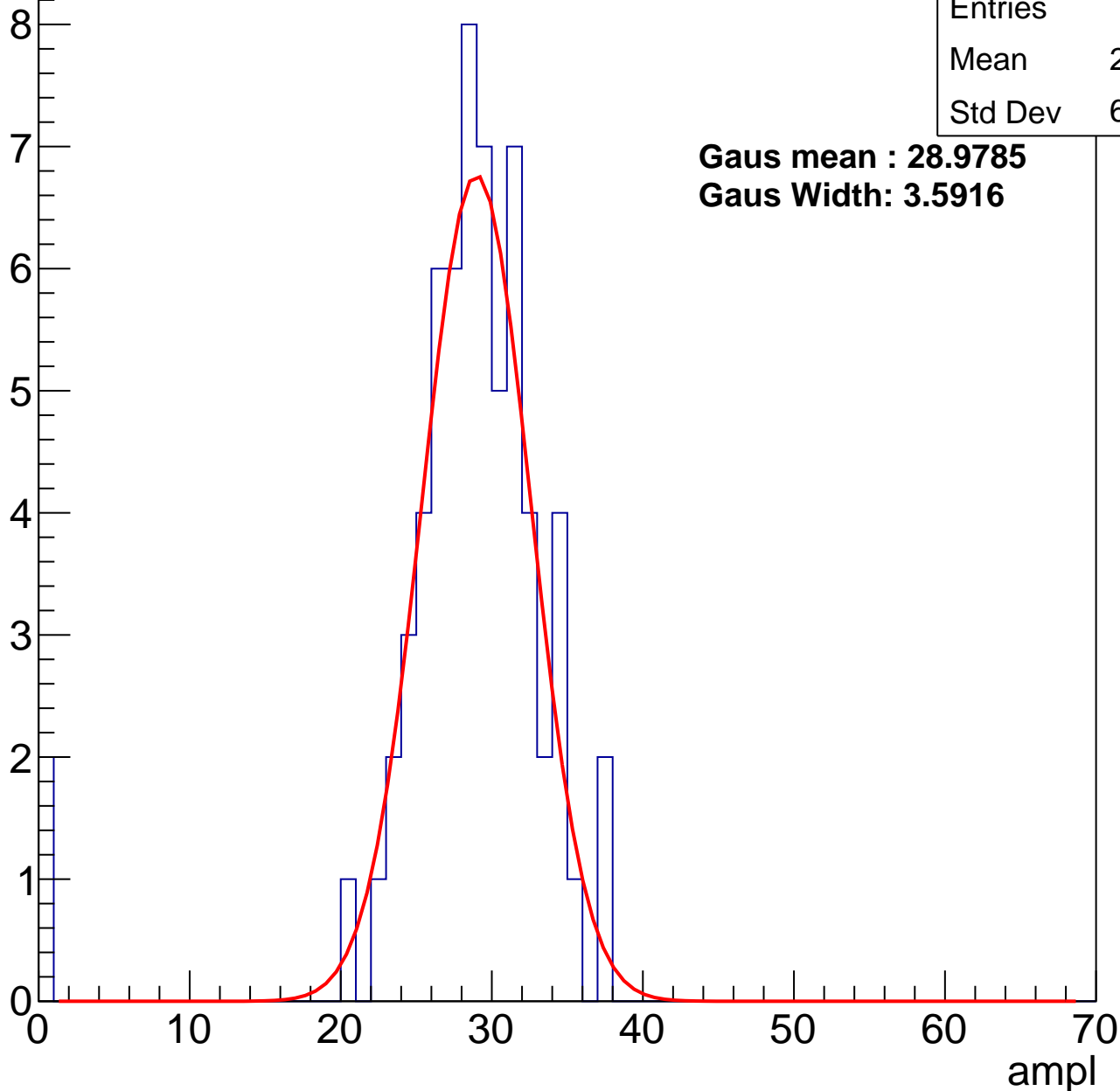
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.86
Std Dev	6.063

Gaus mean : 28.9785

Gaus Width: 3.5916



B1L100S, U14-ch125, adc1

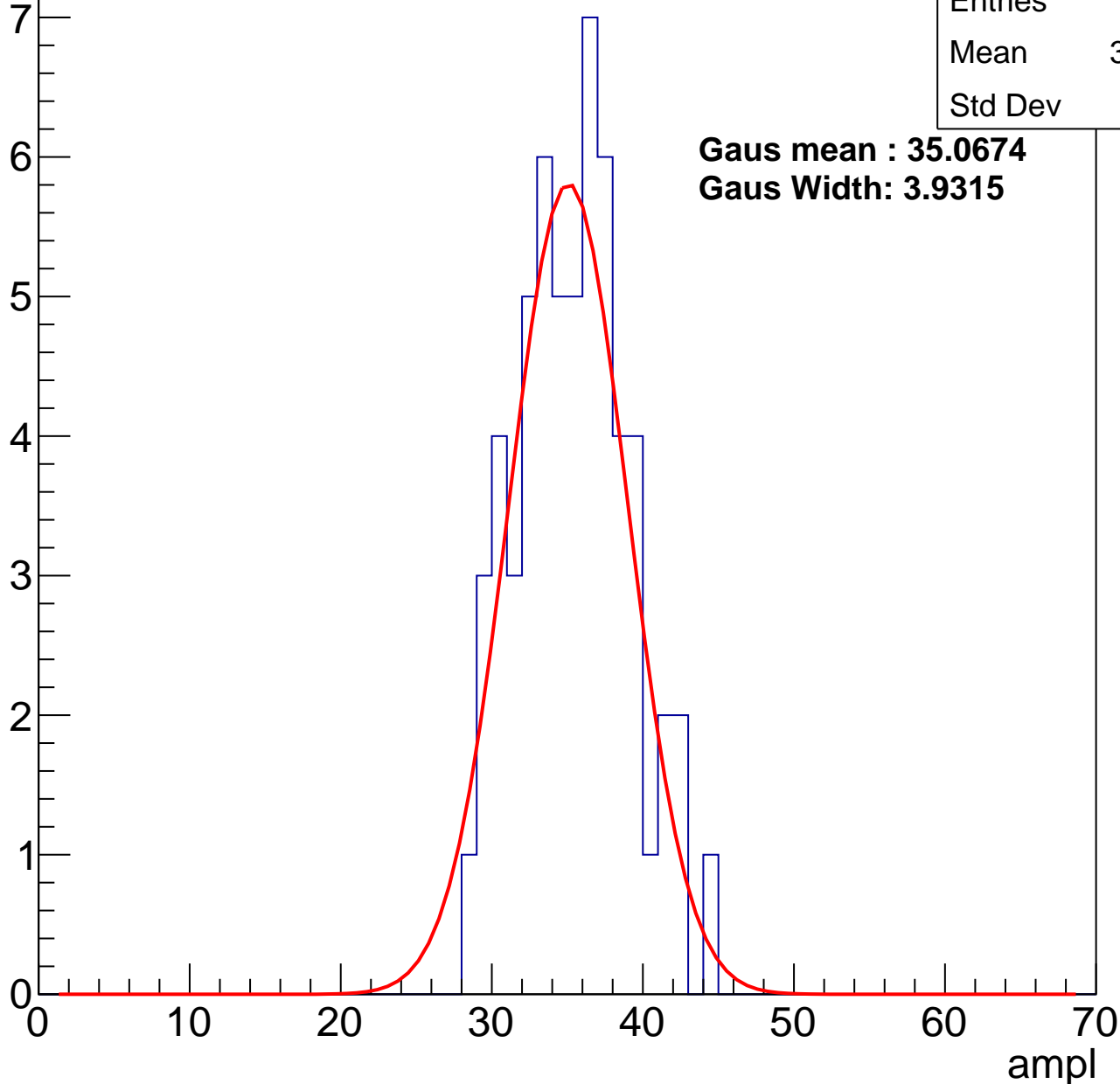
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.97
Std Dev	3.65

Gaus mean : 35.0674

Gaus Width: 3.9315



B1L100S, U14-ch125, adc2

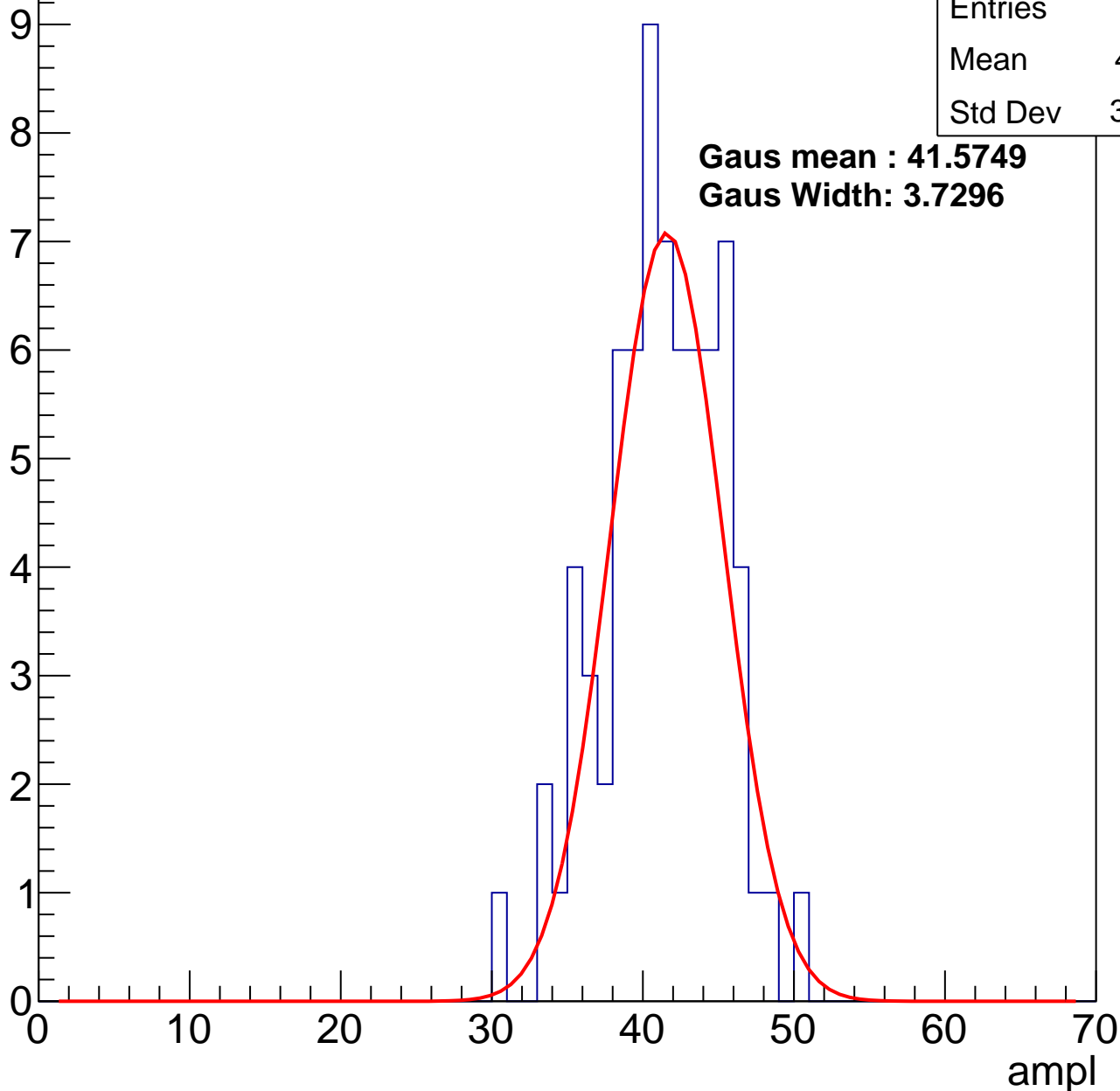
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	40.81
Std Dev	3.874

Gaus mean : 41.5749

Gaus Width: 3.7296

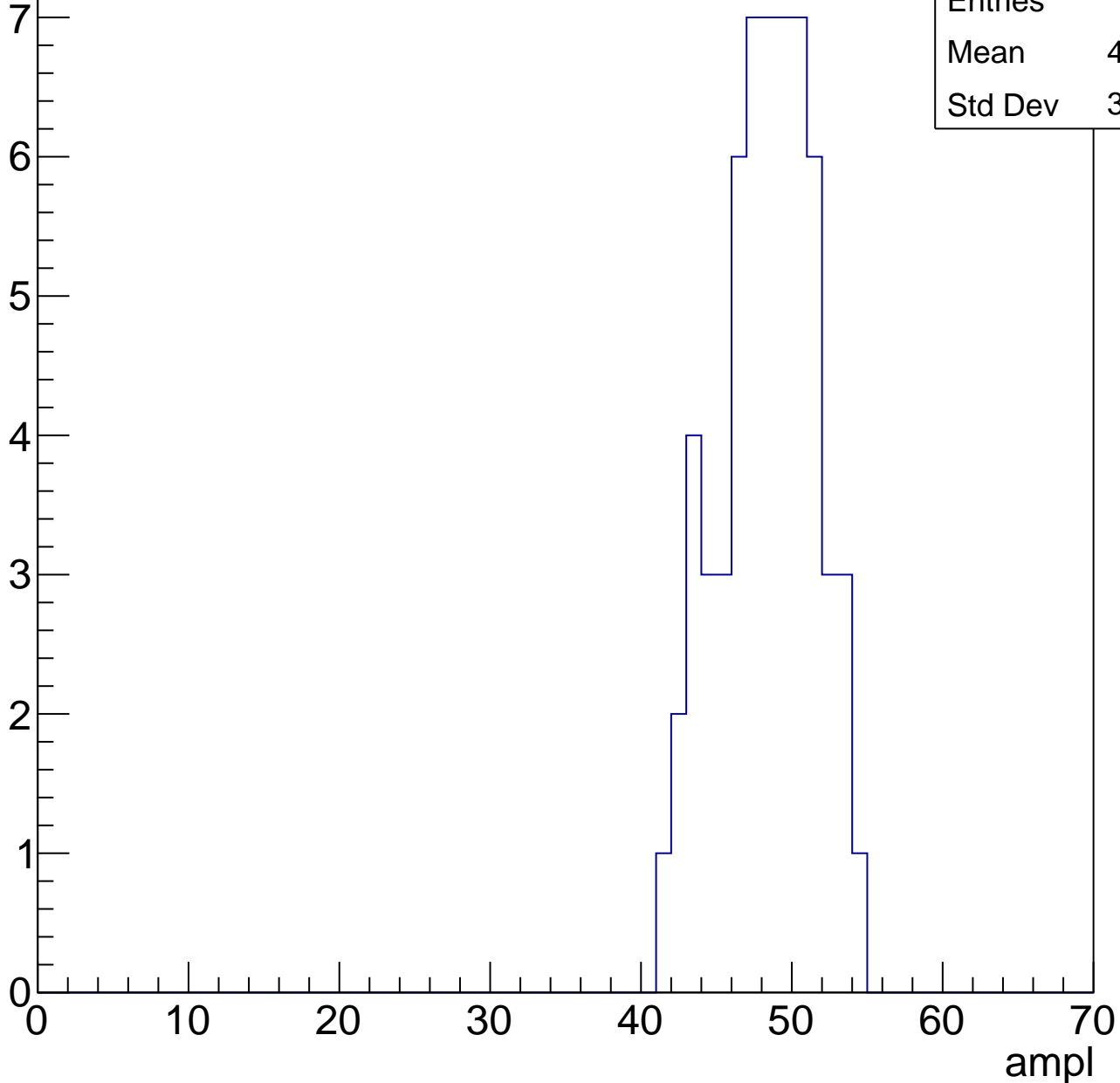


B1L100S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	47.88
Std Dev	3.093

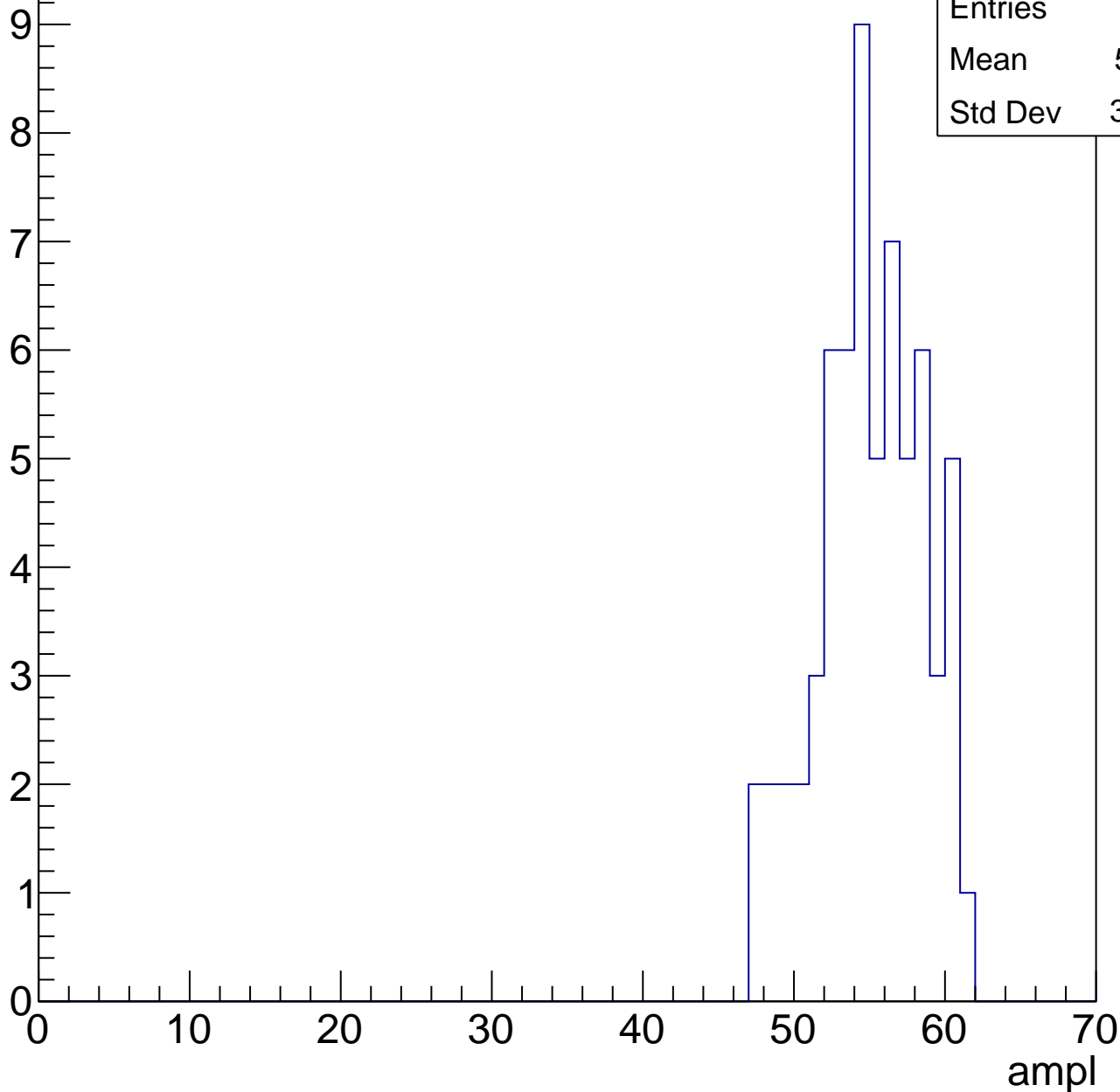


B1L100S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	54.61
Std Dev	3.453

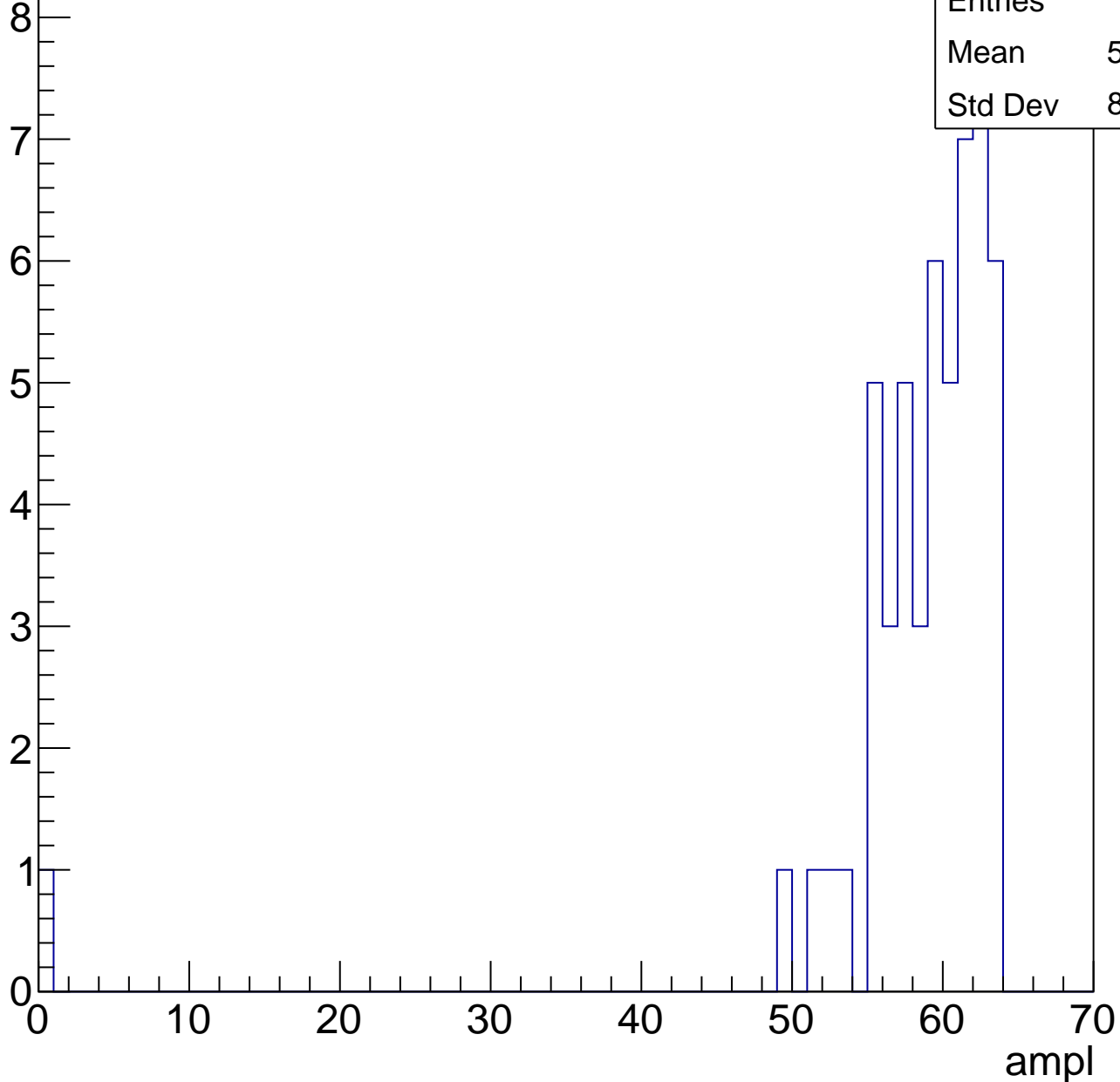


B1L100S, U14-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

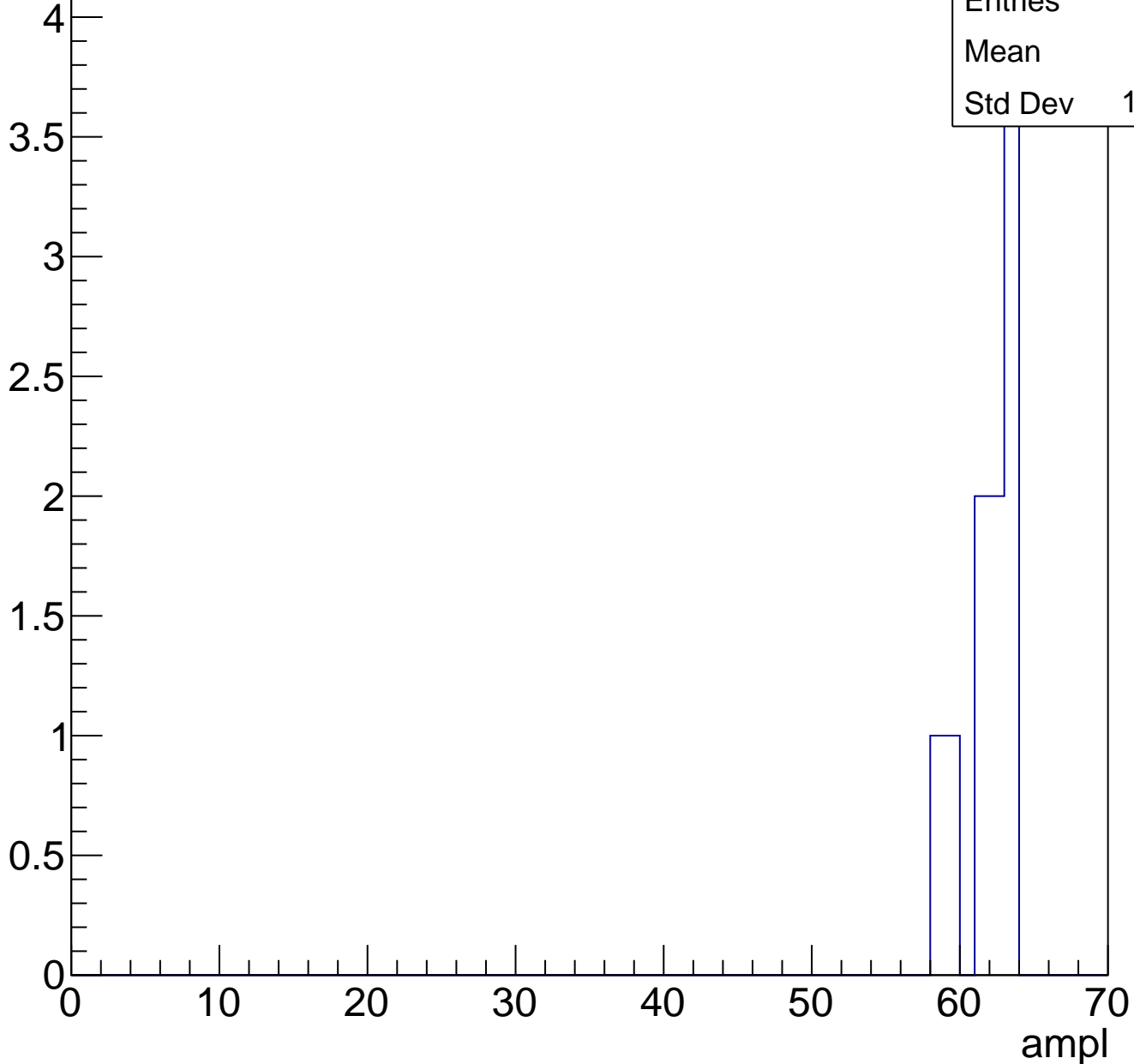
Entries	53
Mean	57.77
Std Dev	8.667



B1L100S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch126, adc0

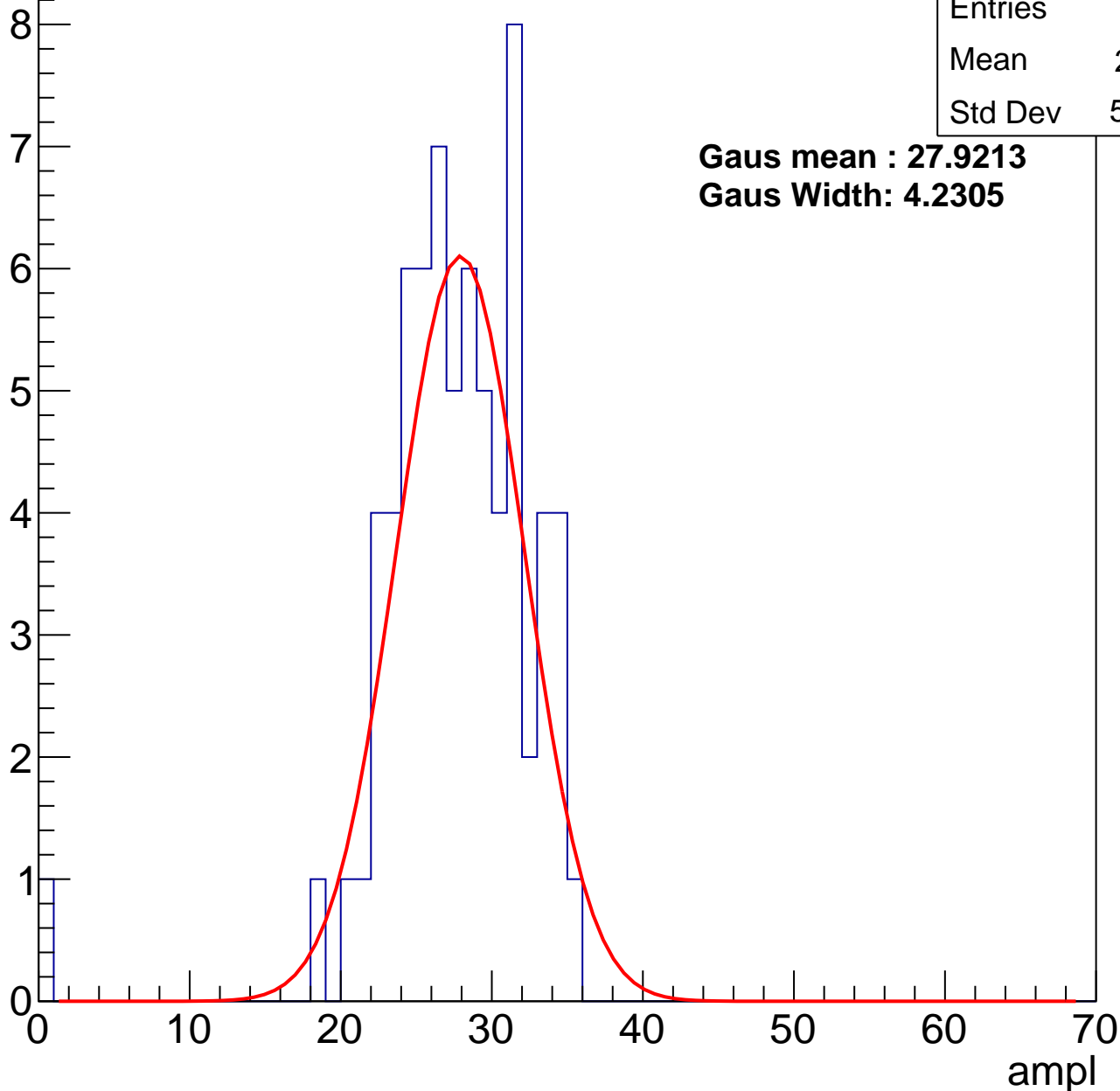
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	27.11
Std Dev	5.053

Gaus mean : 27.9213

Gaus Width: 4.2305



B1L100S, U14-ch126, adc1

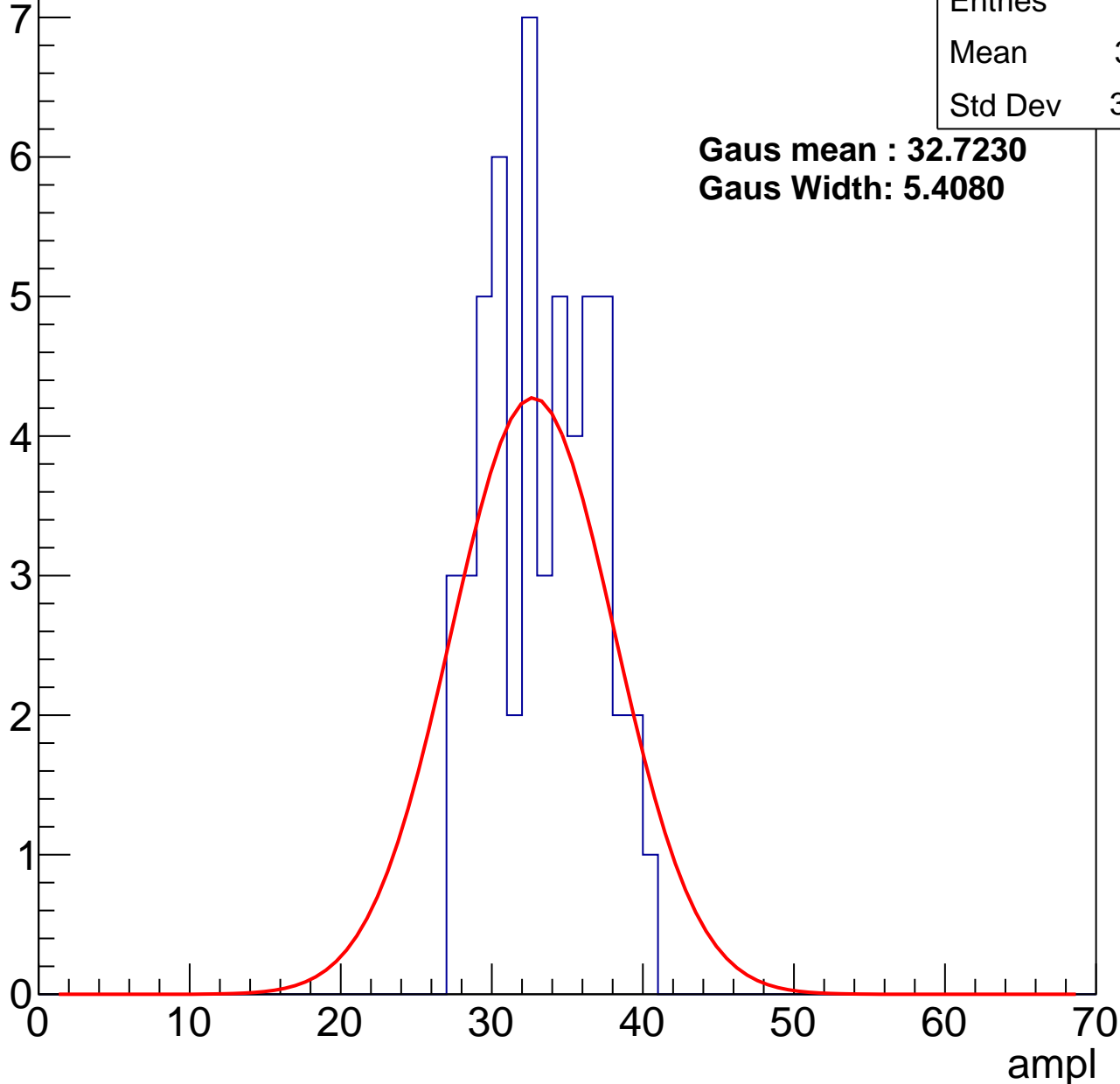
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	32.91
Std Dev	3.498

Gaus mean : 32.7230

Gaus Width: 5.4080



B1L100S, U14-ch126, adc2

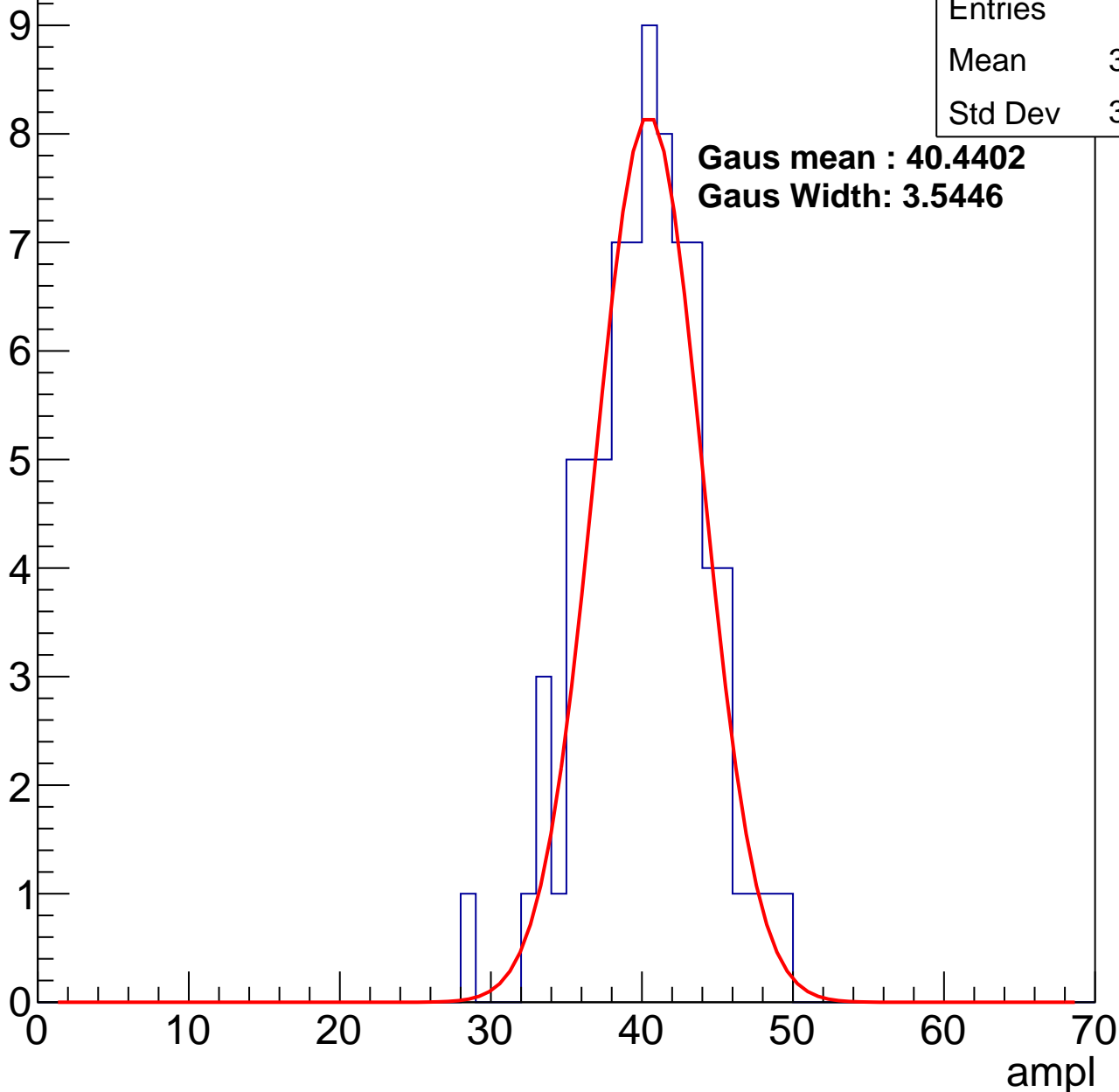
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	39.76
Std Dev	3.864

Gaus mean : 40.4402

Gaus Width: 3.5446

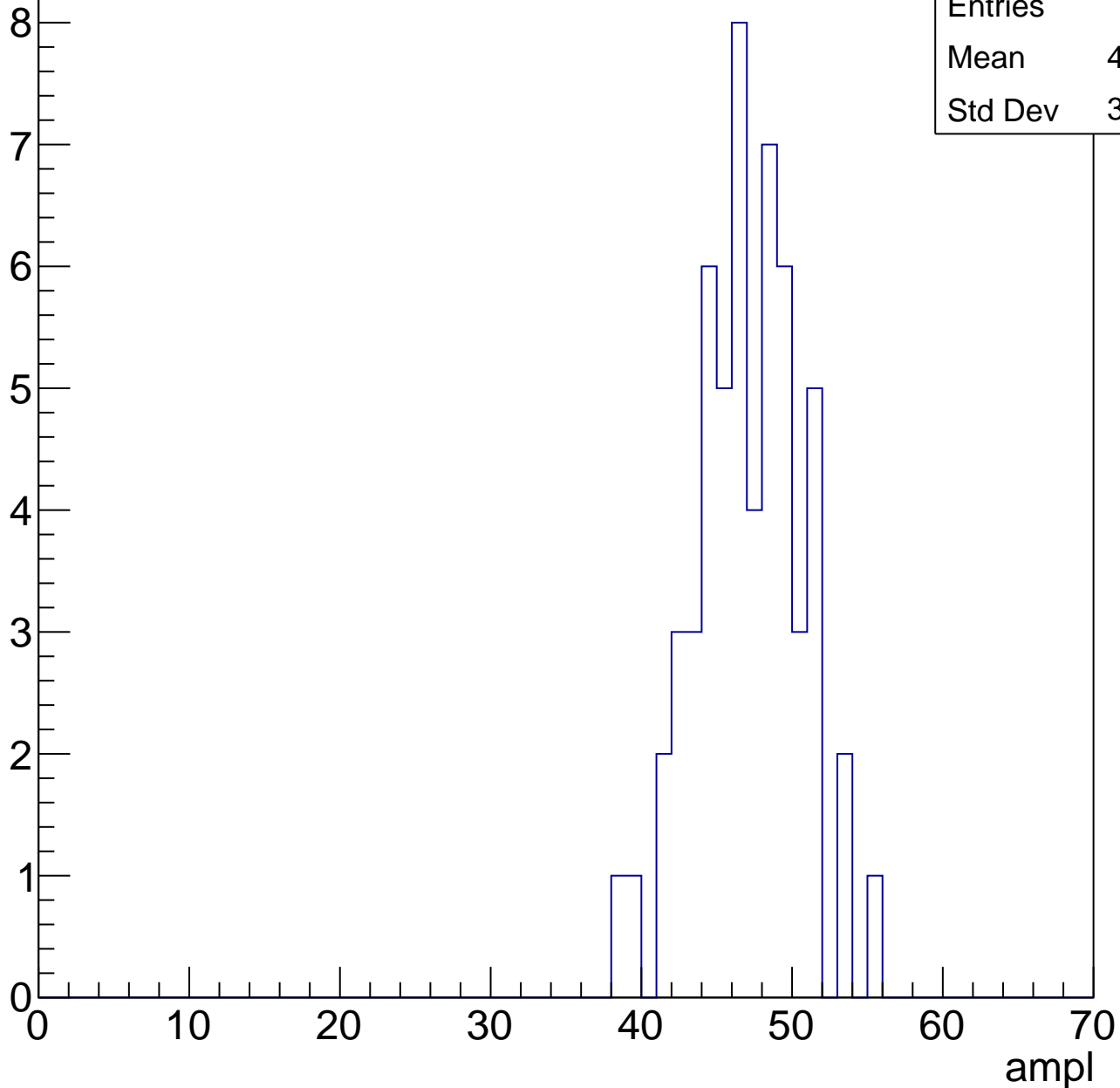


B1L100S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	46.58
Std Dev	3.469

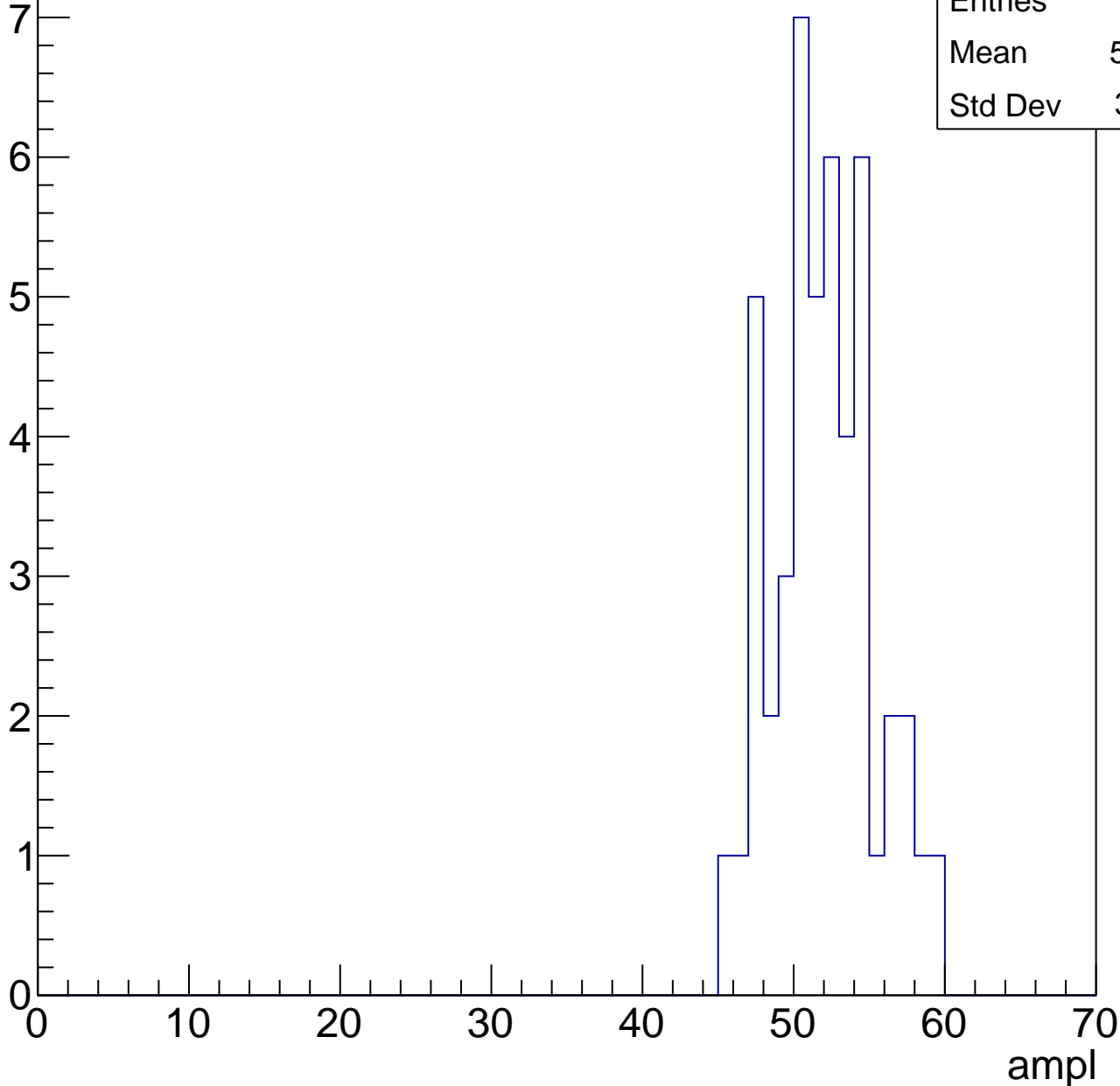


B1L100S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	51.49
Std Dev	3.241

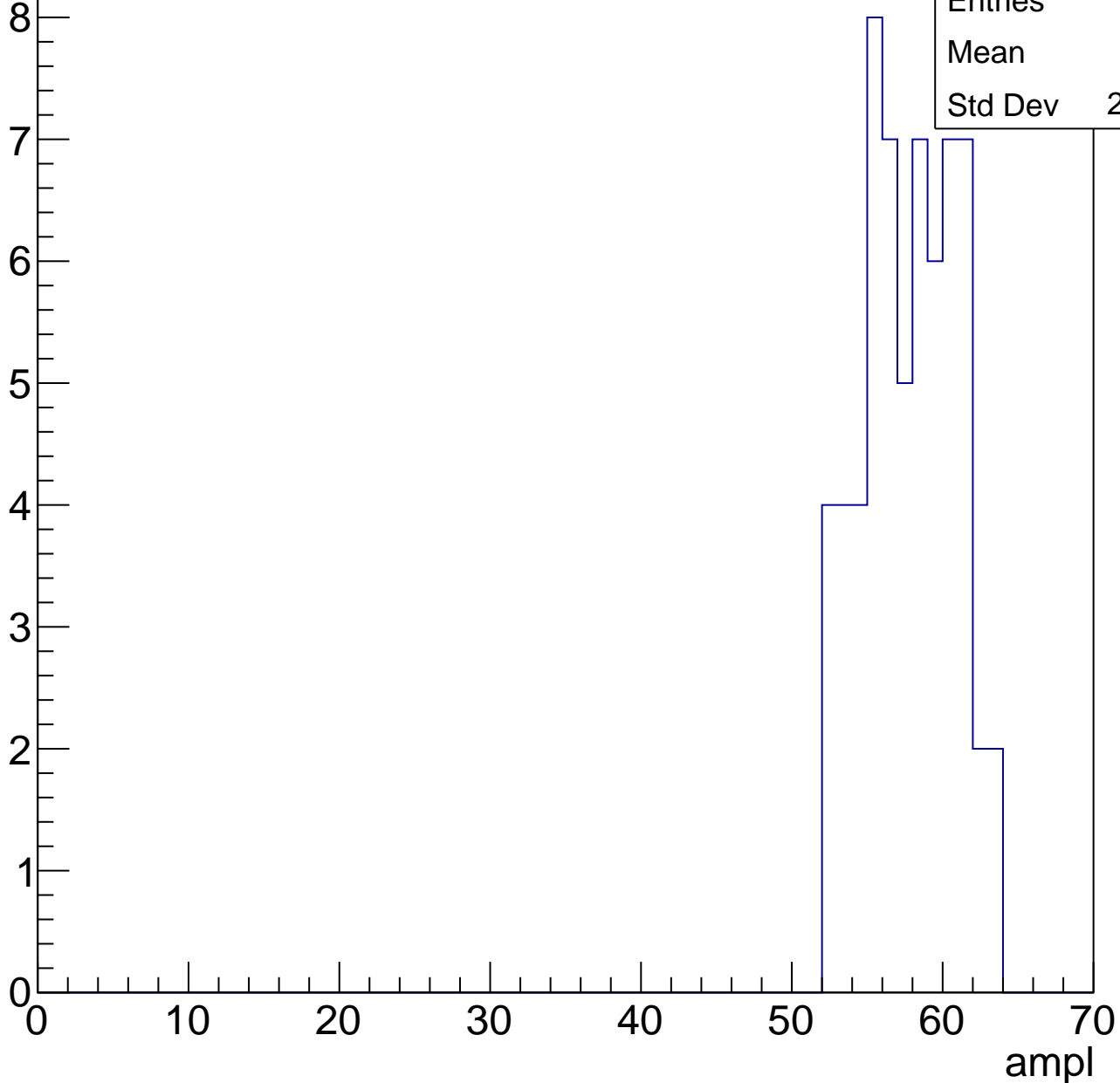


B1L100S, U14-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	57.3
Std Dev	2.985

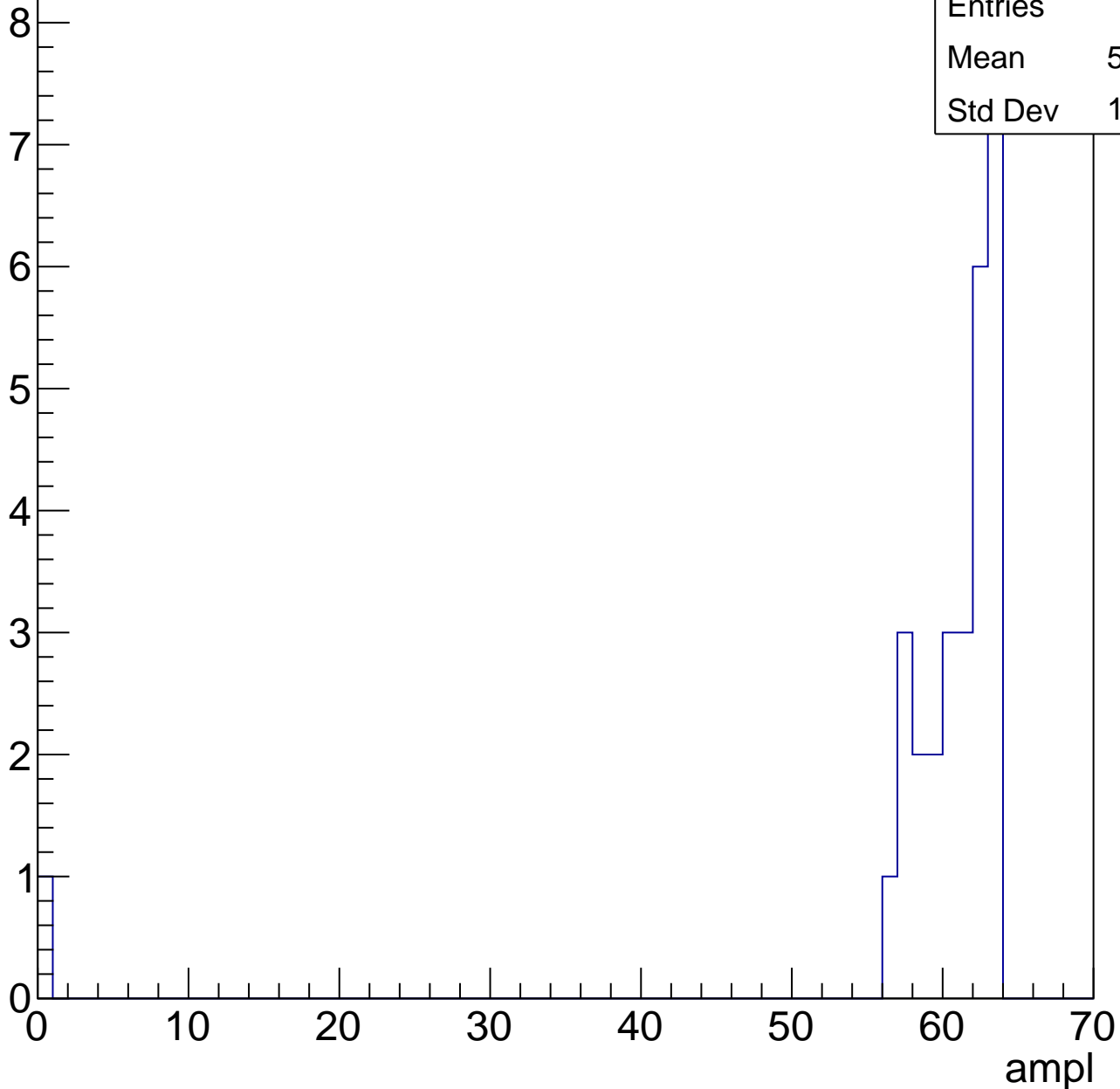


B1L100S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

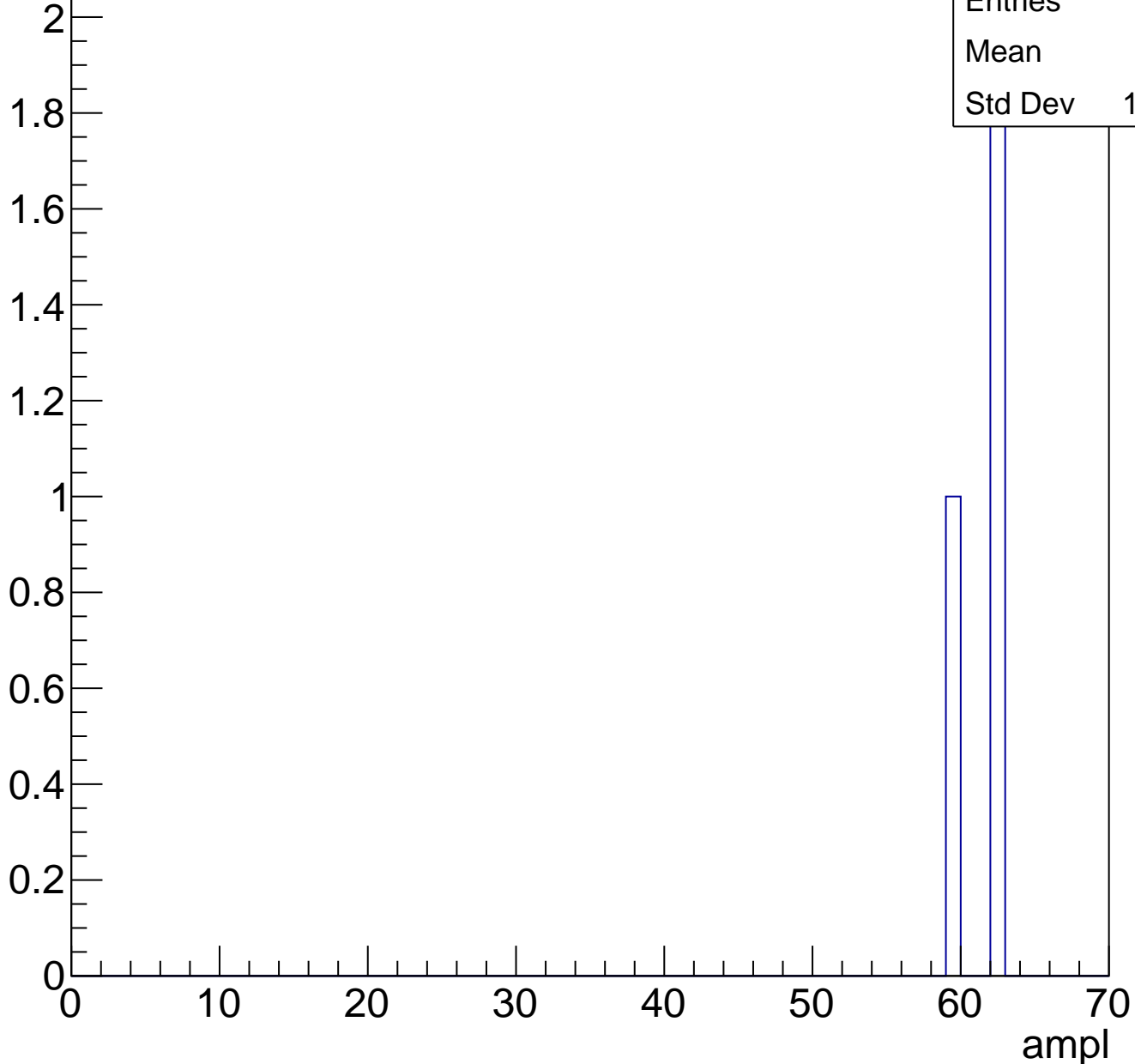
Entries	29
Mean	58.62
Std Dev	11.29



B1L100S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch127, adc0

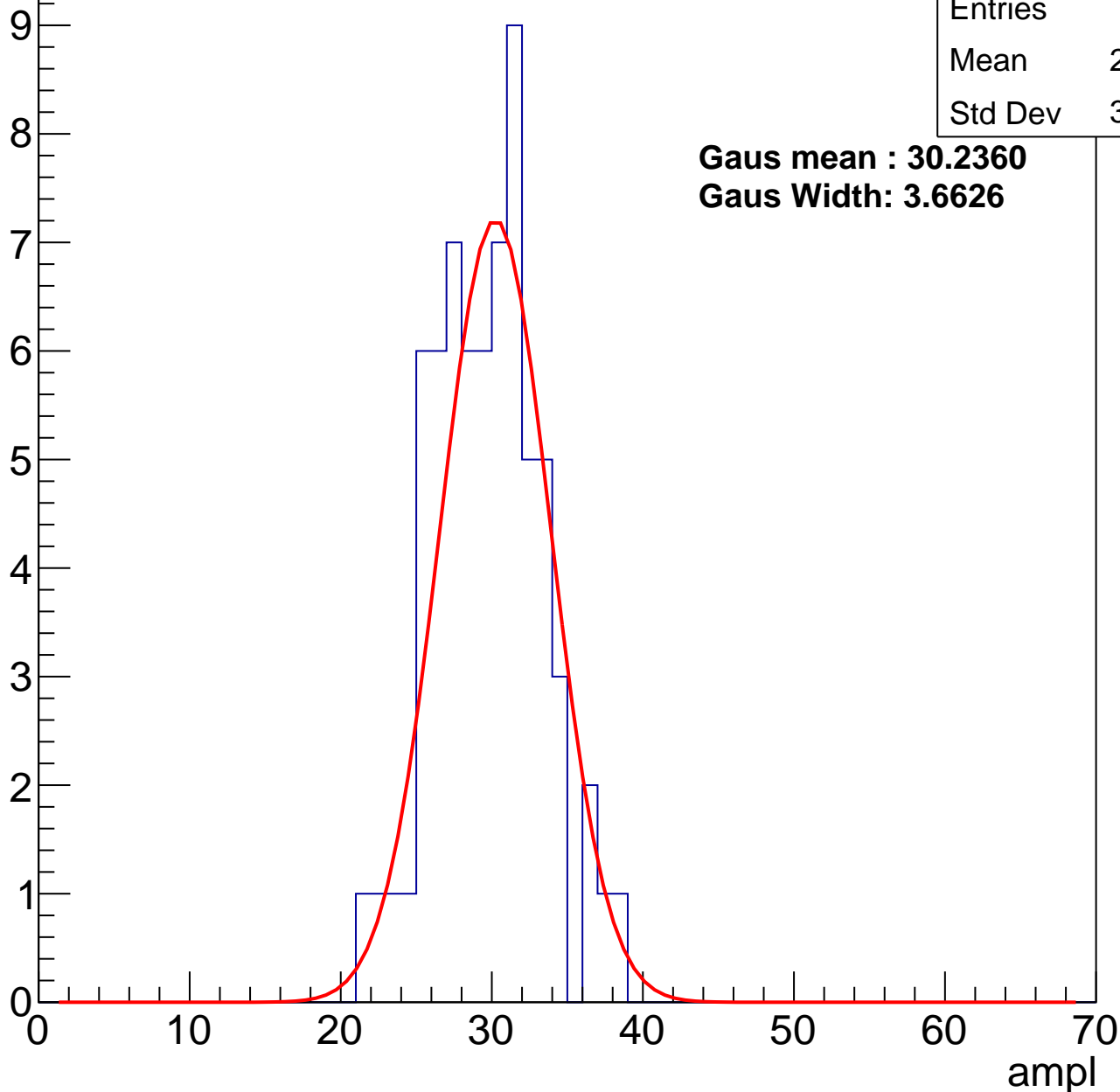
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.26
Std Dev	3.513

Gaus mean : 30.2360

Gaus Width: 3.6626



B1L100S, U14-ch127, adc1

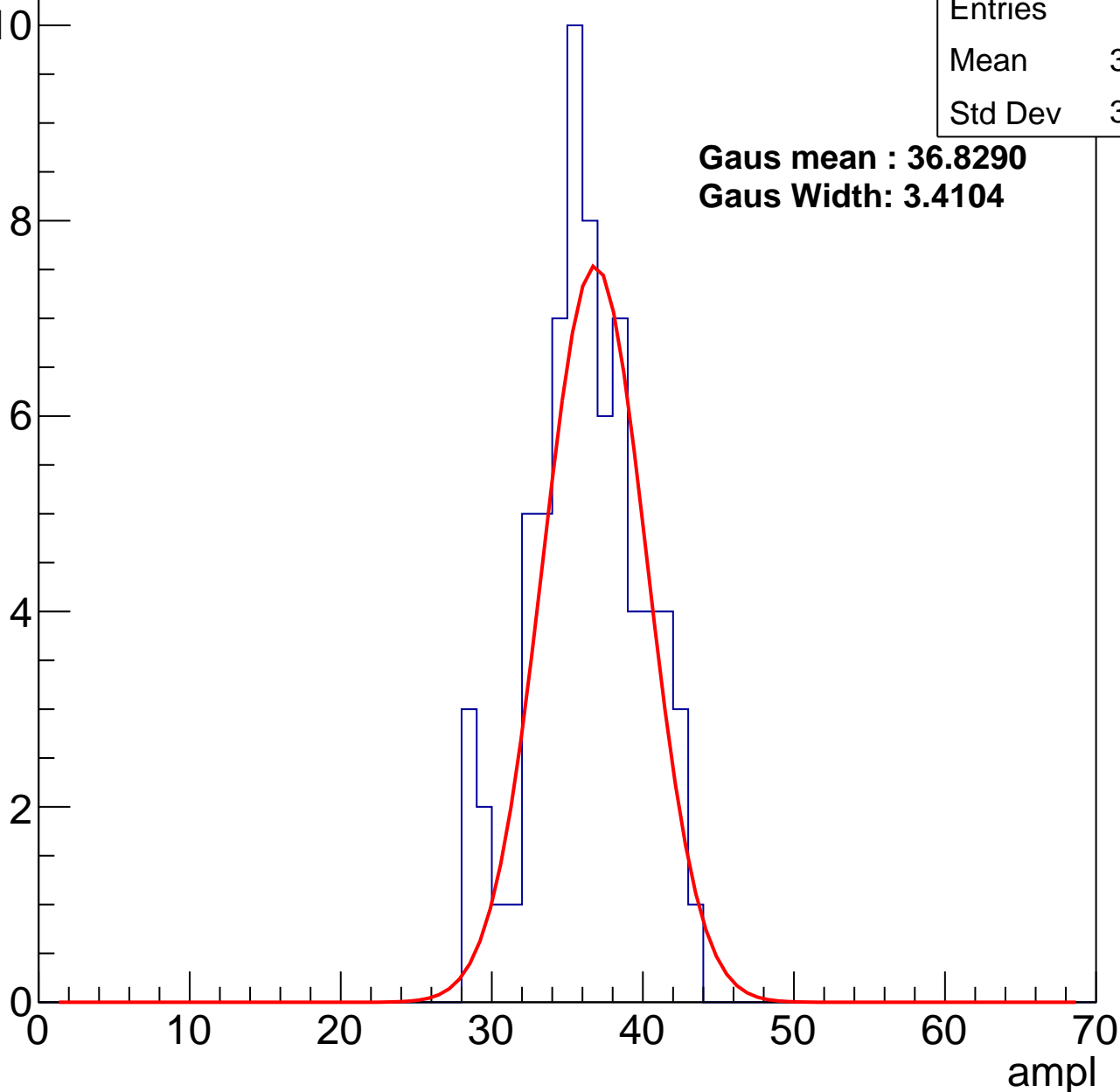
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.79
Std Dev	3.564

Gaus mean : 36.8290

Gaus Width: 3.4104



B1L100S, U14-ch127, adc2

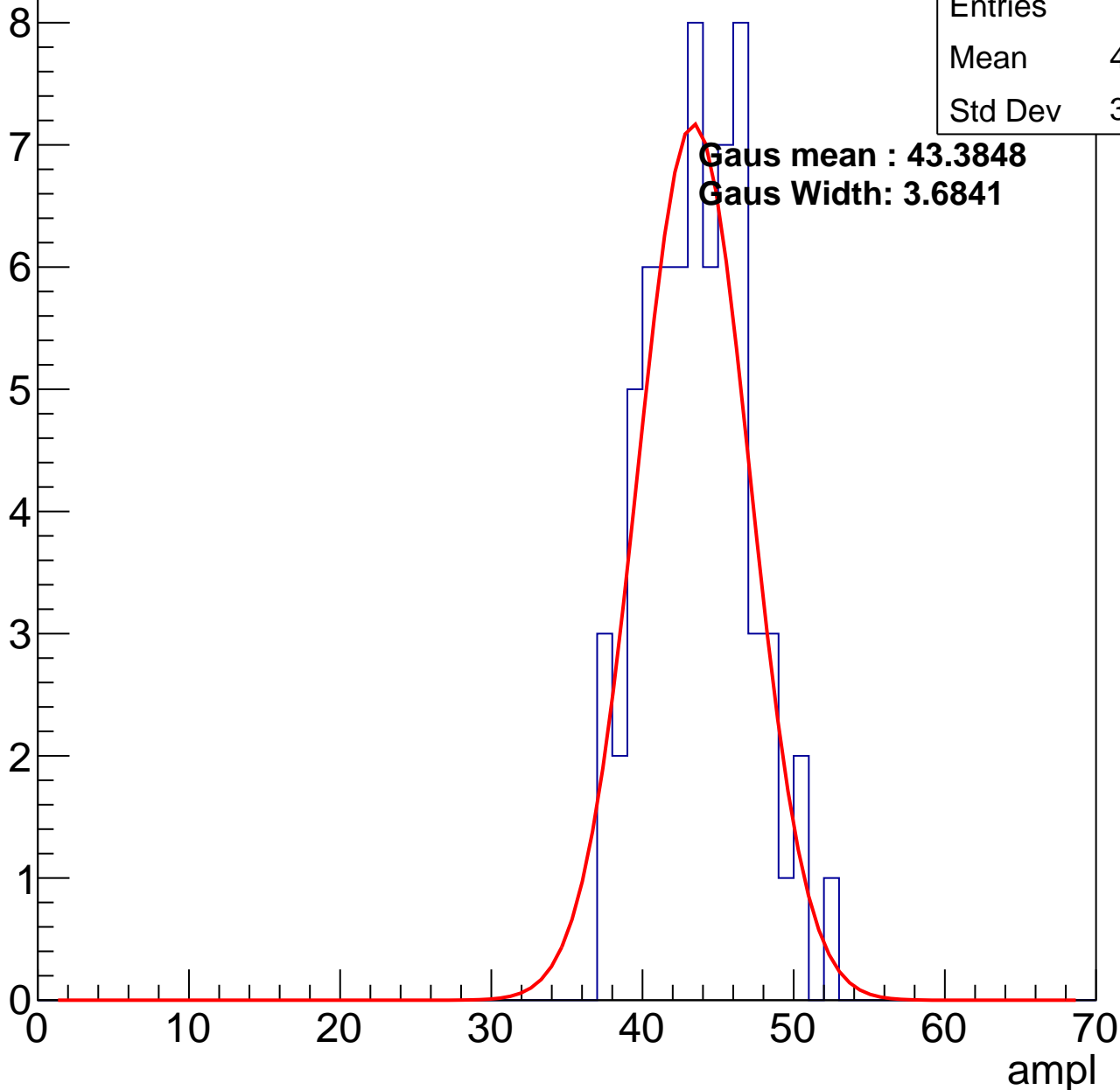
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.24
Std Dev	3.373

Gaus mean : 43.3848

Gaus Width: 3.6841

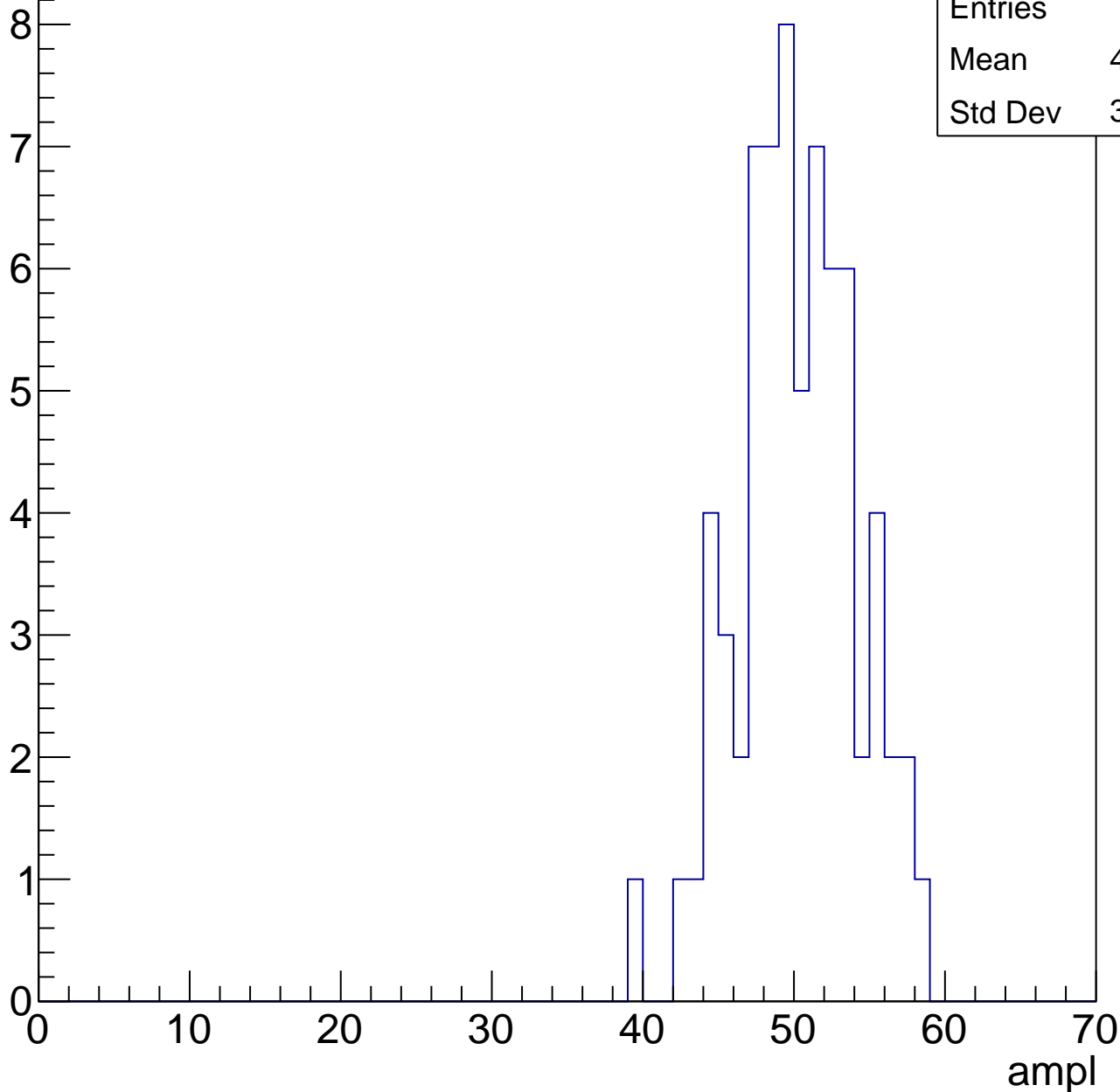


B1L100S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	49.75
Std Dev	3.865

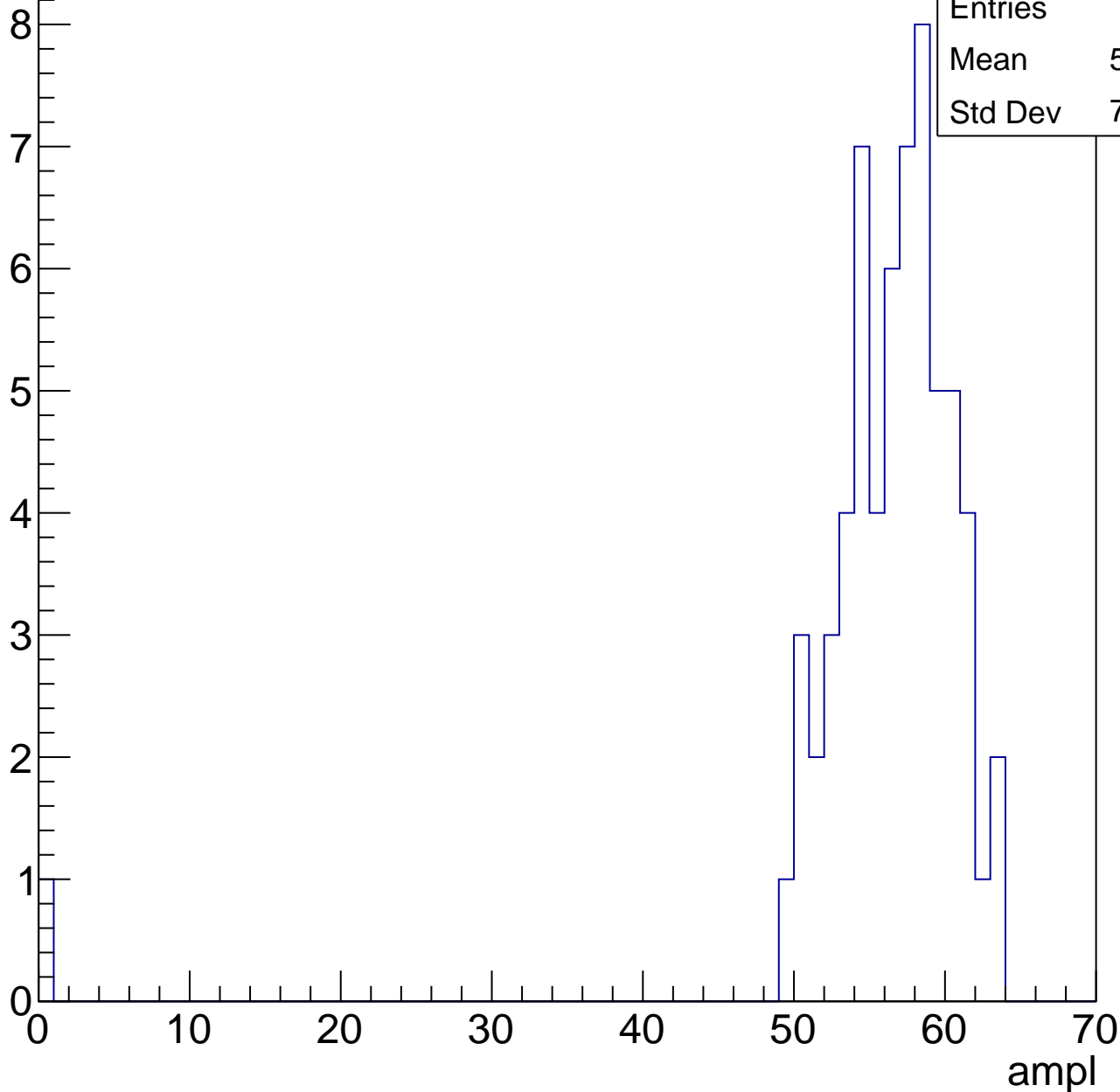


B1L100S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.44
Std Dev	7.805

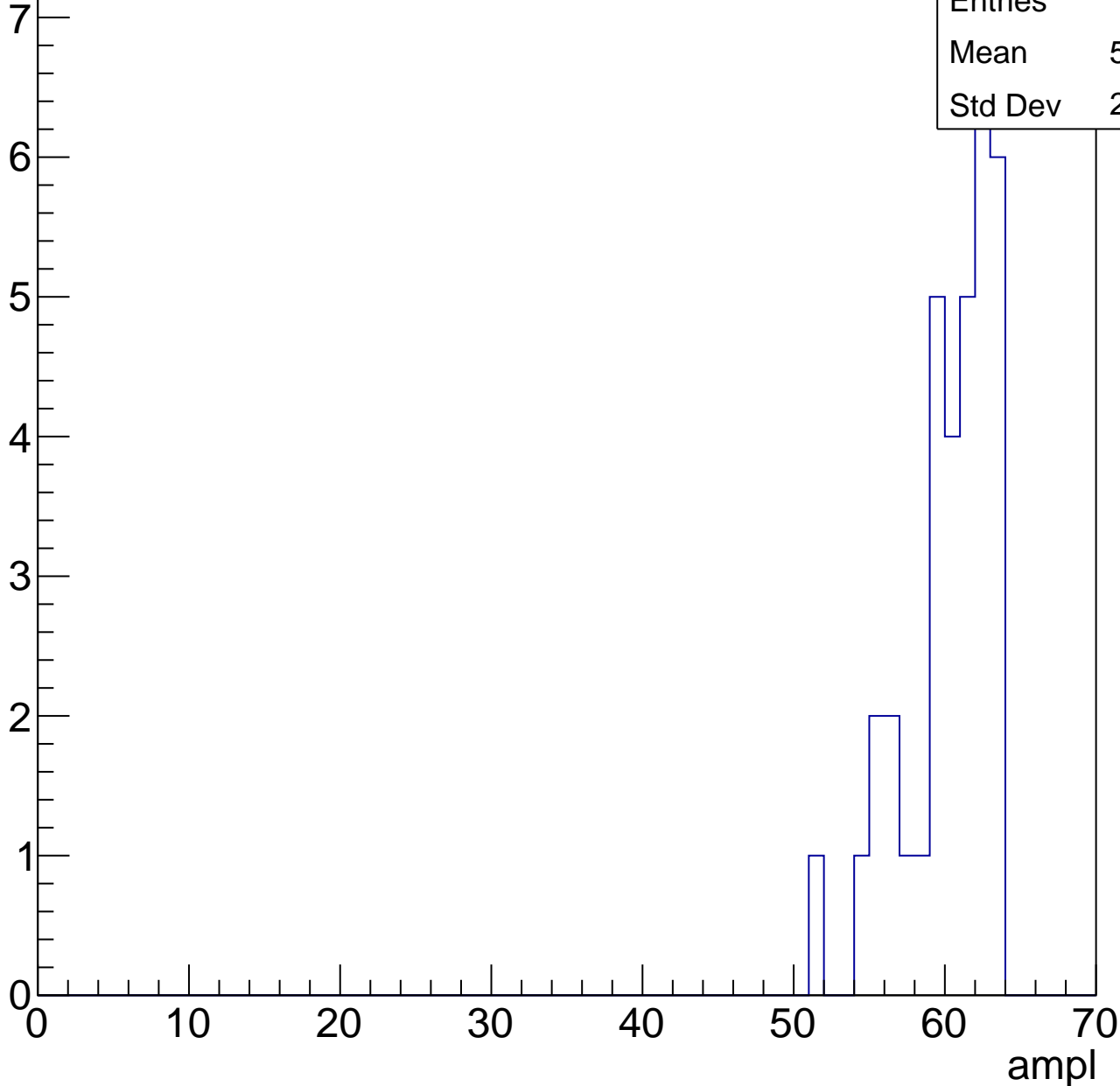


B1L100S, U14-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

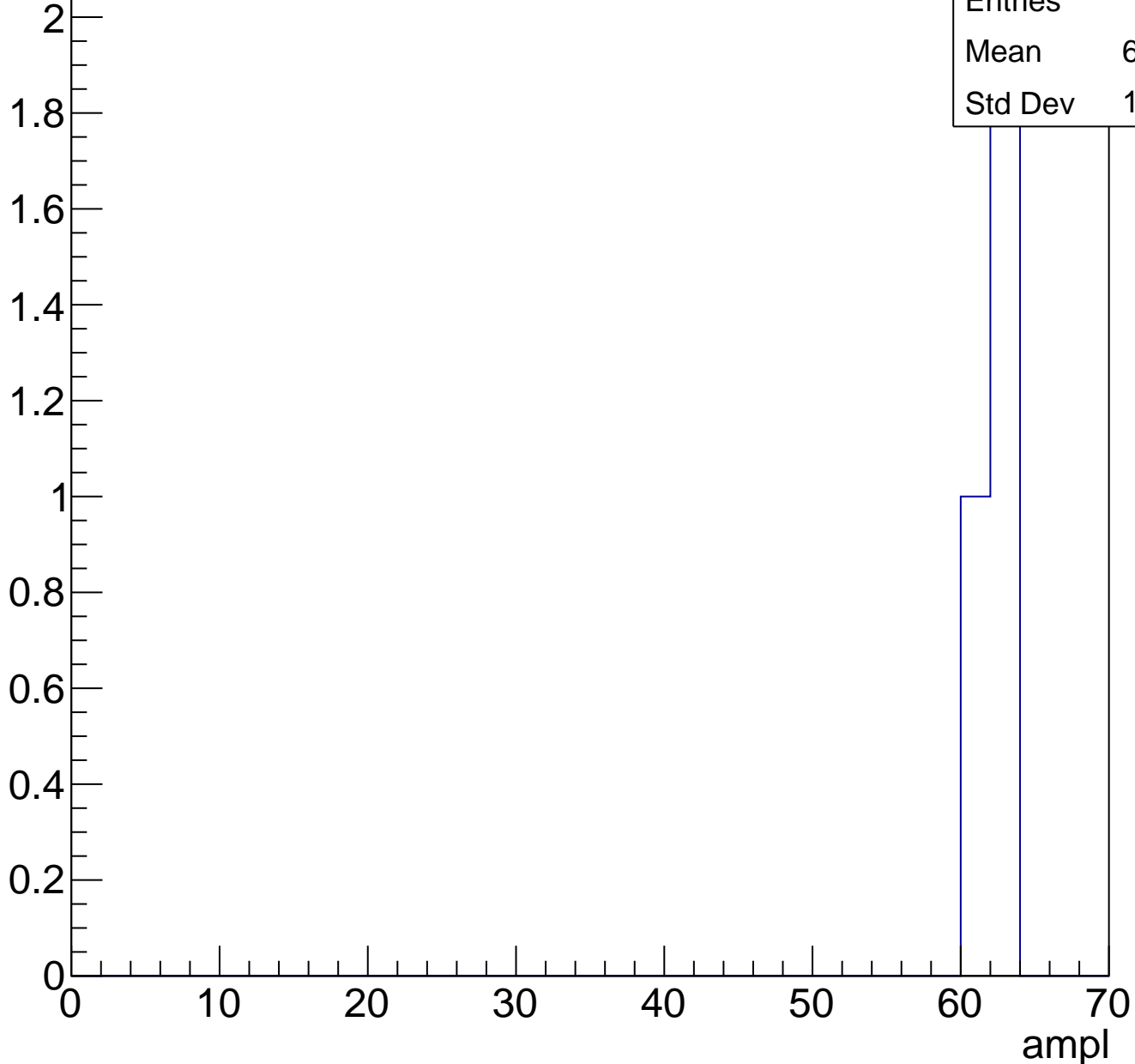
Entries	35
Mean	59.83
Std Dev	2.942



B1L100S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

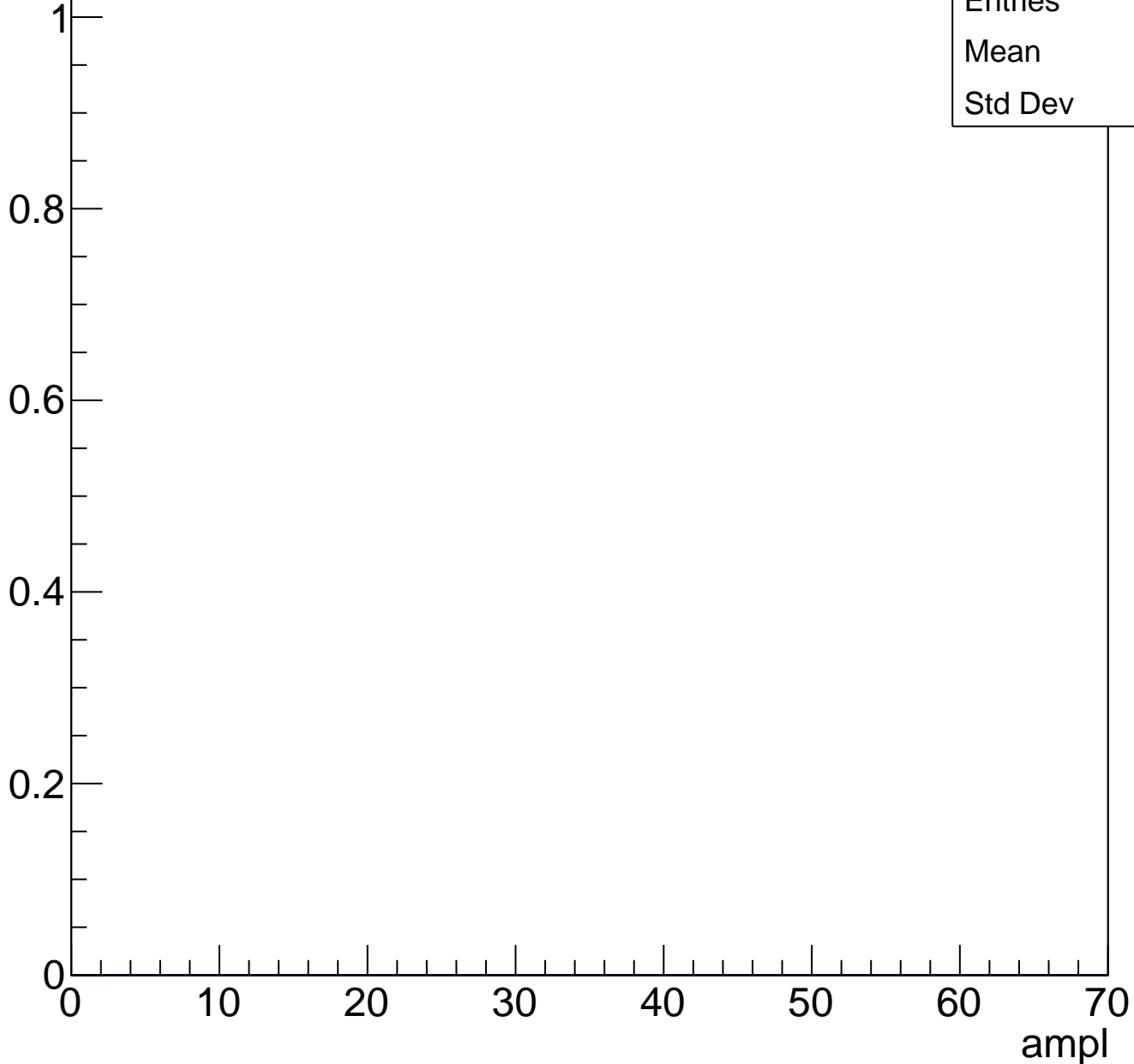


Entries	0
Mean	0
Std Dev	0

B1L100S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0