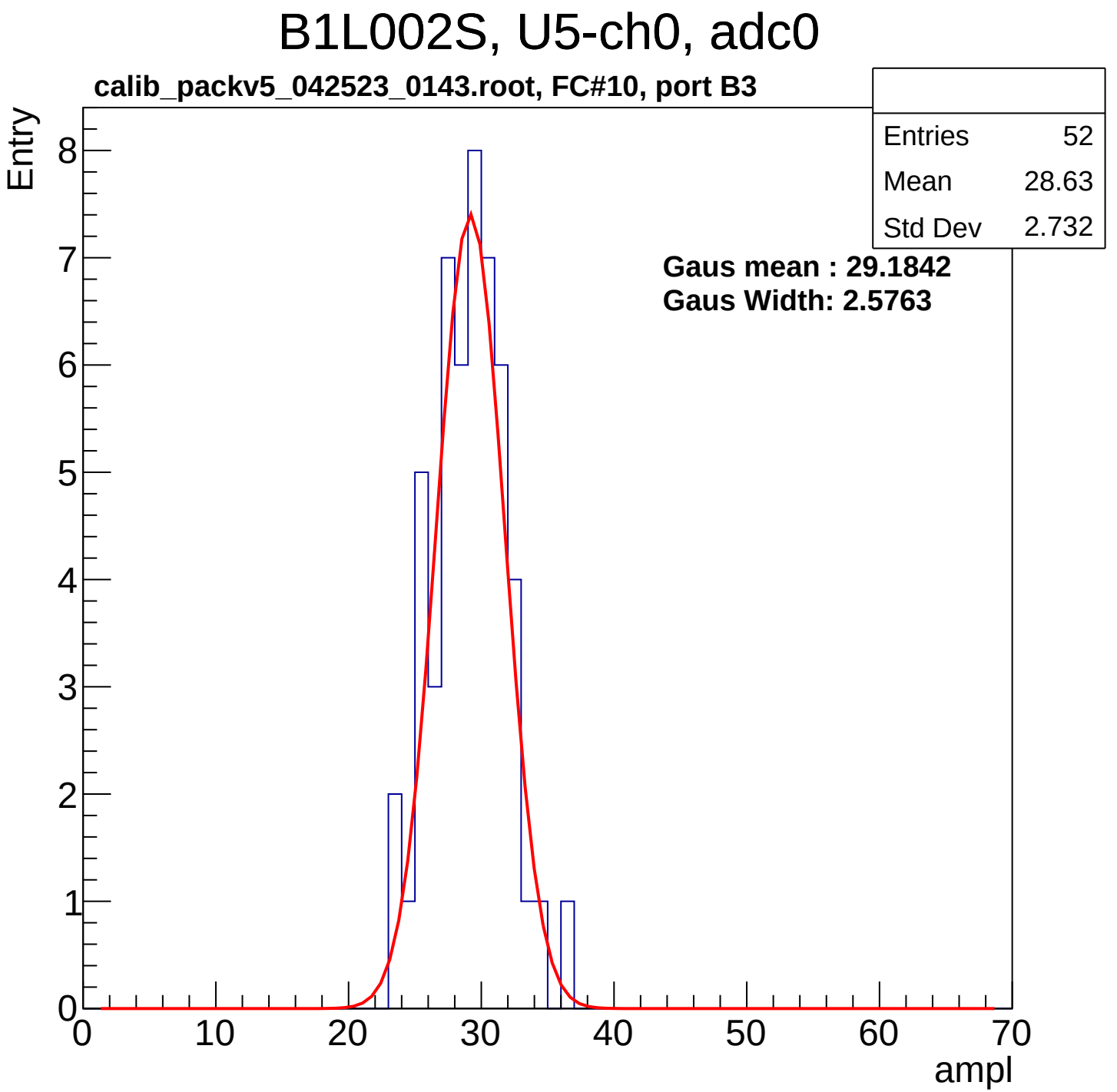
B1L002S, U5-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28.63
Std Dev 2.732
Gaus mean : 29.1842
Gaus Width: 2.5763
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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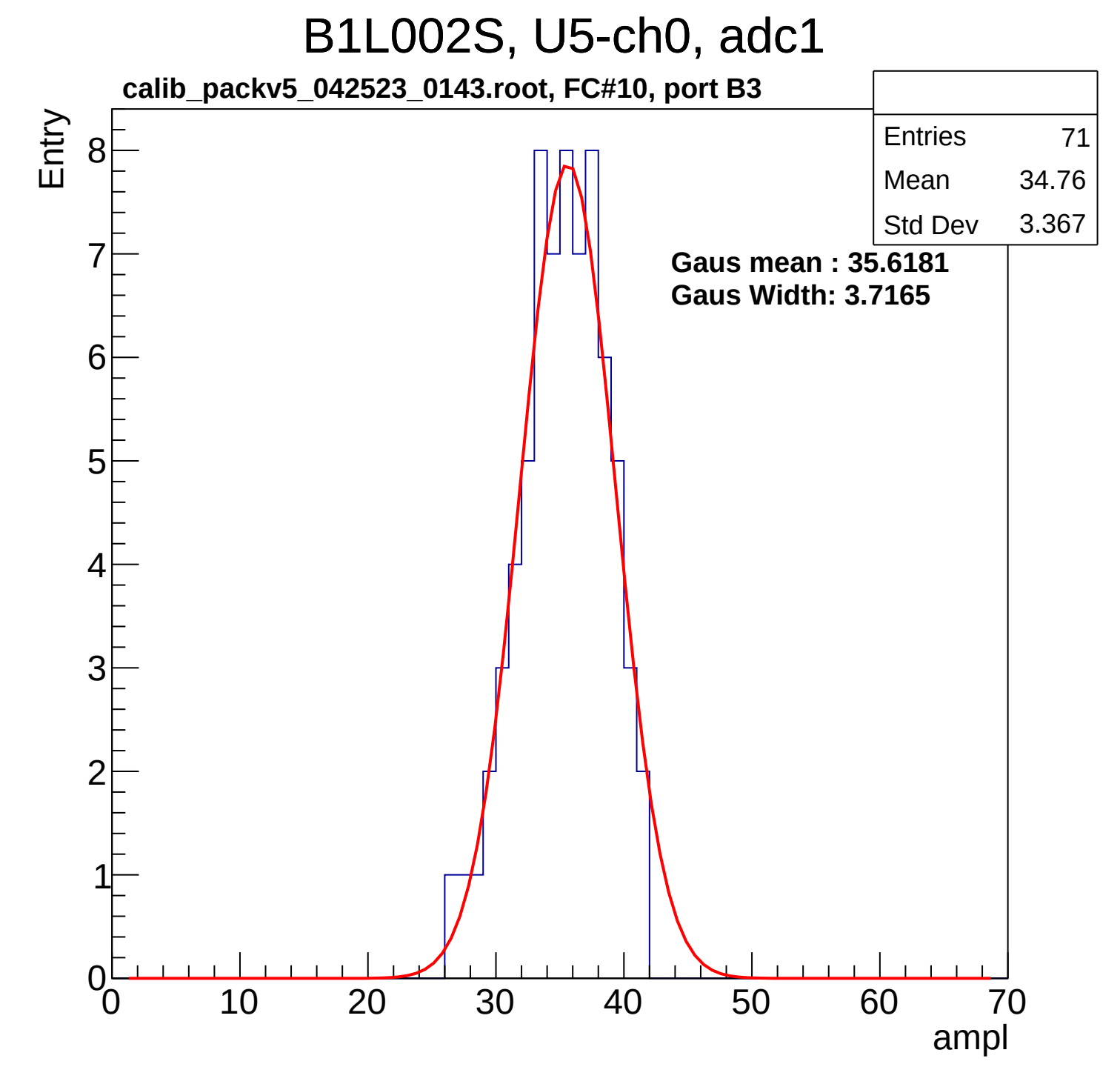
B1L002S, U5-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.76
Std Dev 3.367
Gaus mean : 35.6181
Gaus Width: 3.7165
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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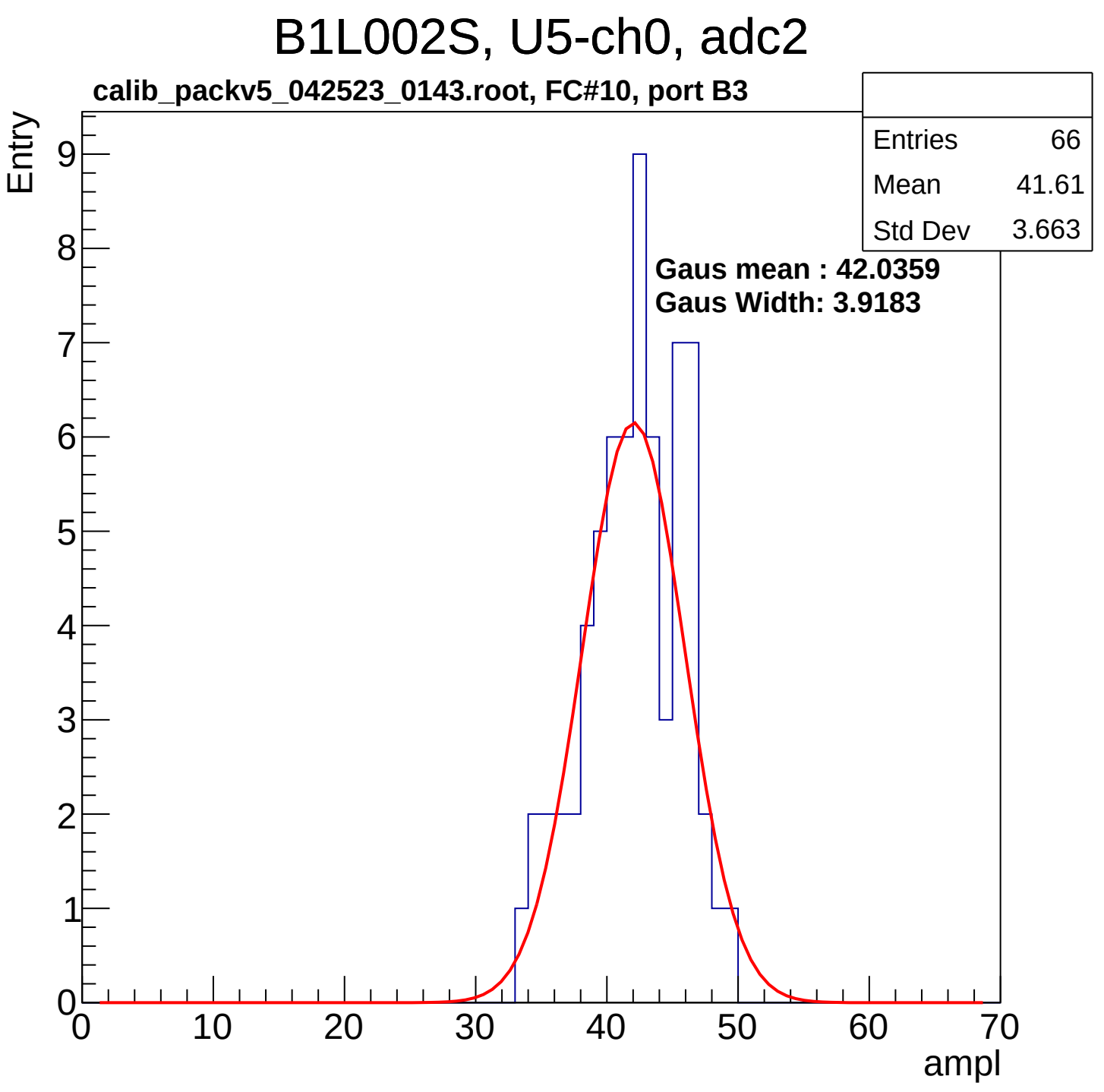
B1L002S, U5-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.61
Std Dev 3.663
Gaus mean : 42.0359
Gaus Width: 3.9183
Entry
ampl
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5
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9
0
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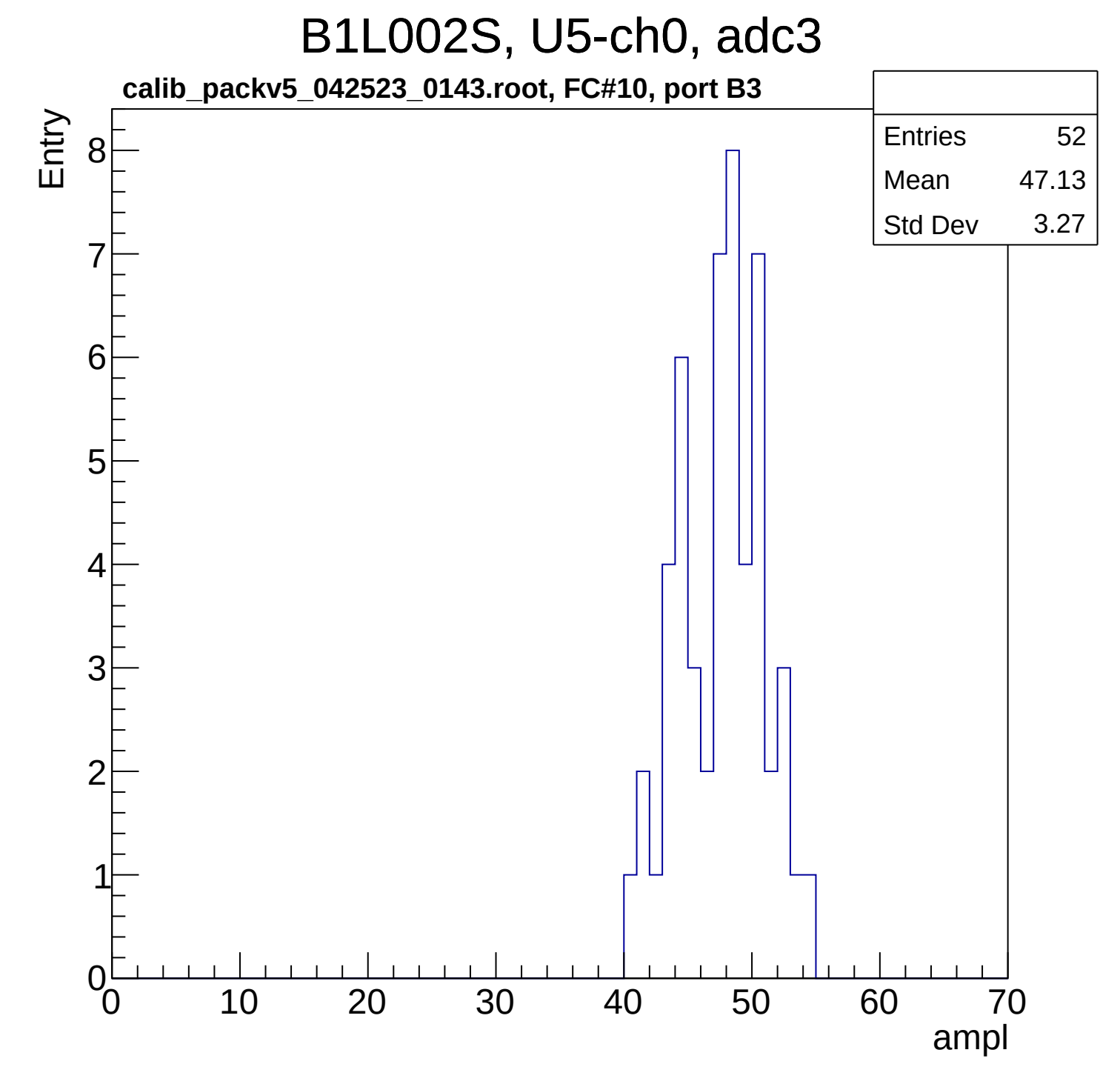
B1L002S, U5-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.13
Std Dev 3.27
Entry
ampl
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1
2
3
4
5
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7
8
0
10
20
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40
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60
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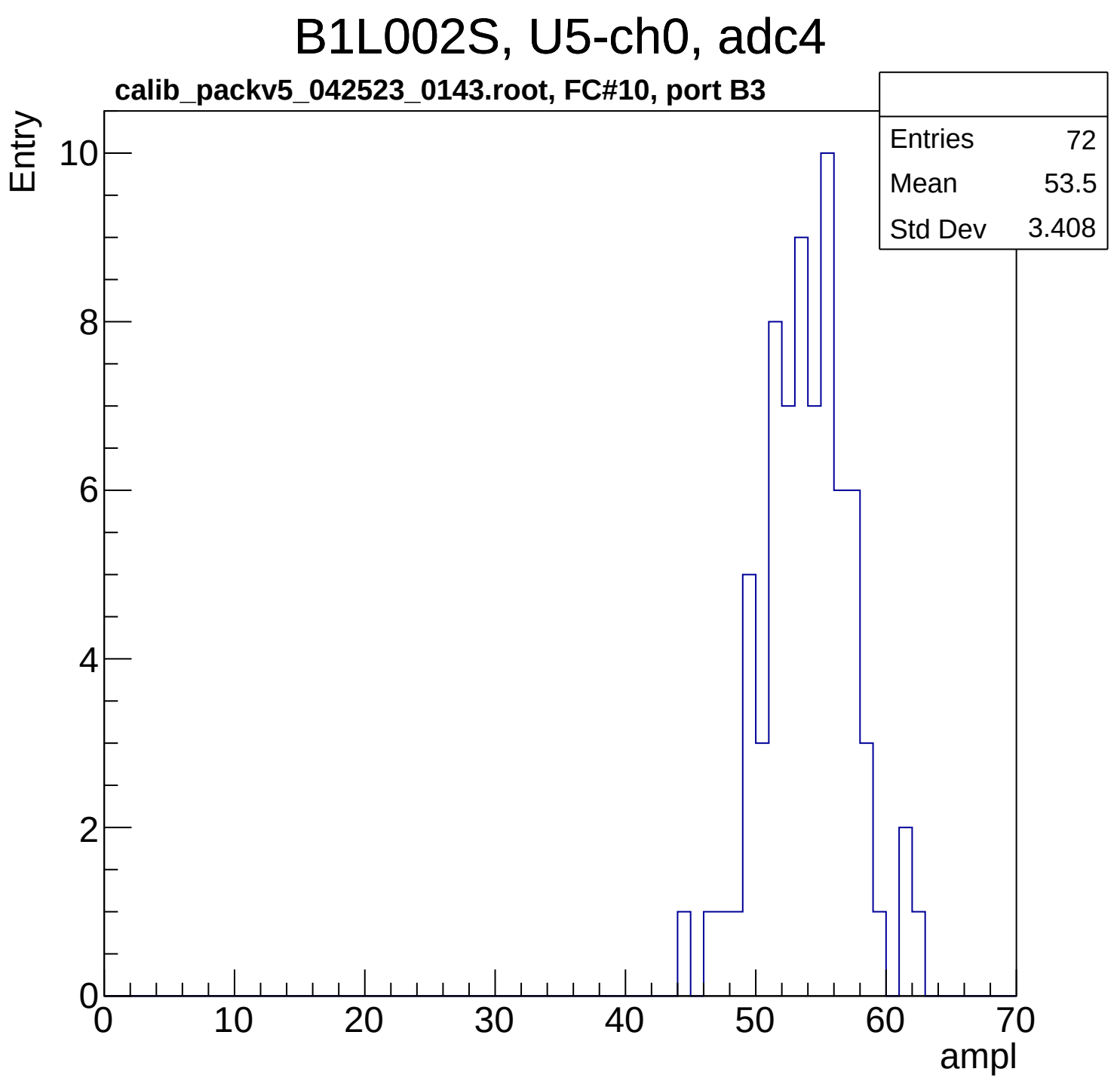
B1L002S, U5-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 53.5
Std Dev 3.408
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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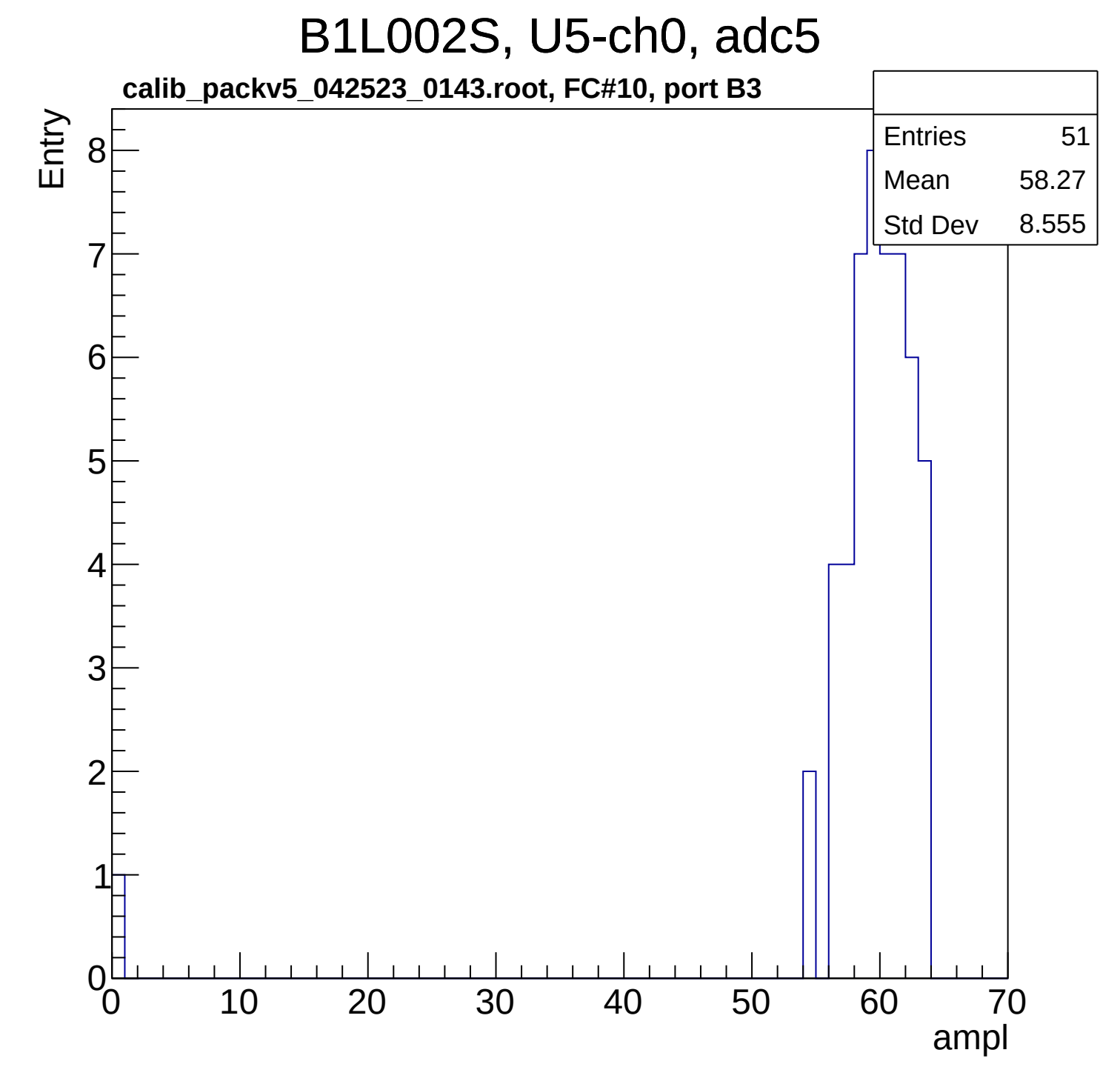
B1L002S, U5-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.27
Std Dev 8.555
Entry
ampl
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1
2
3
4
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6
7
8
0
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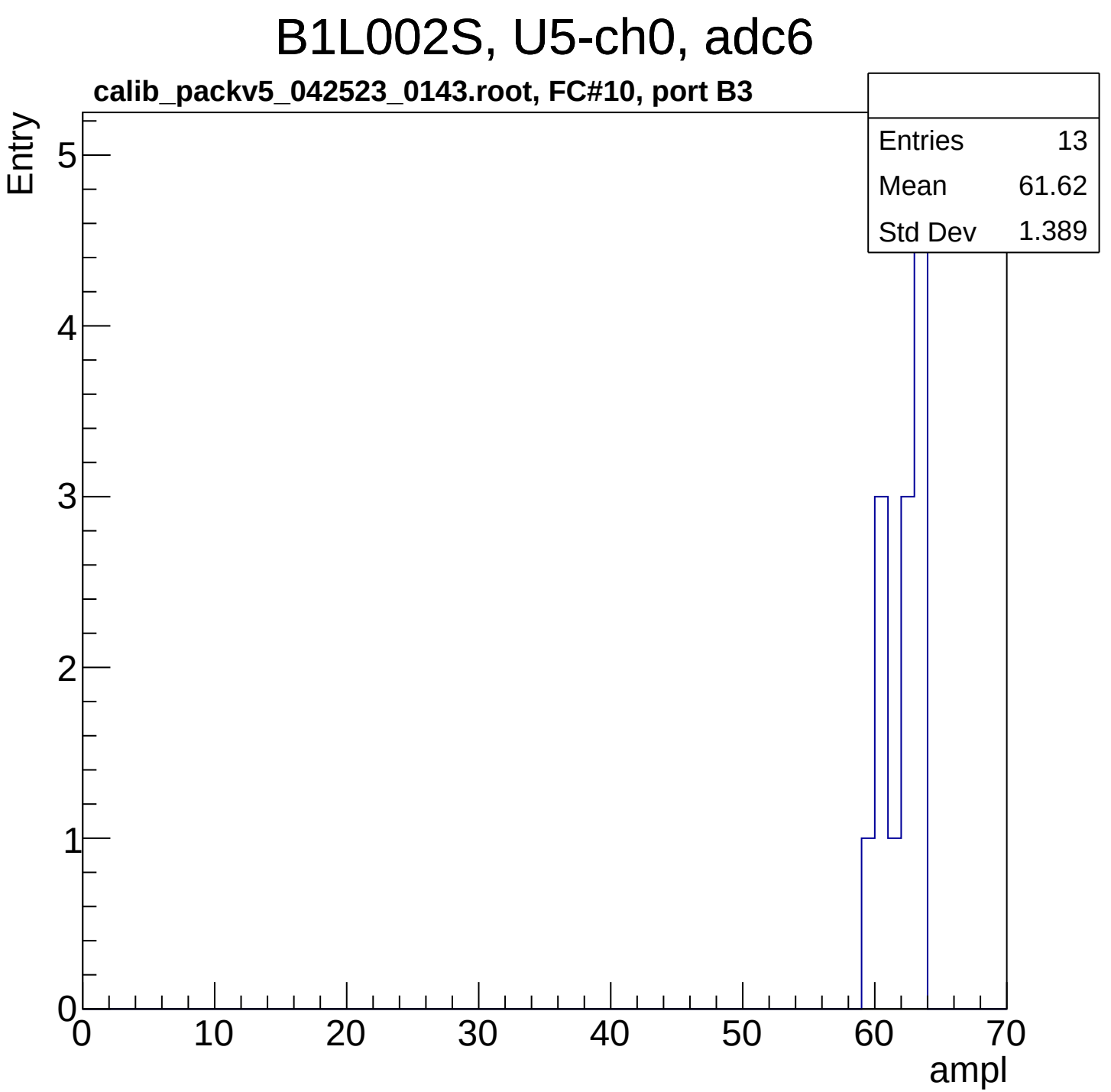
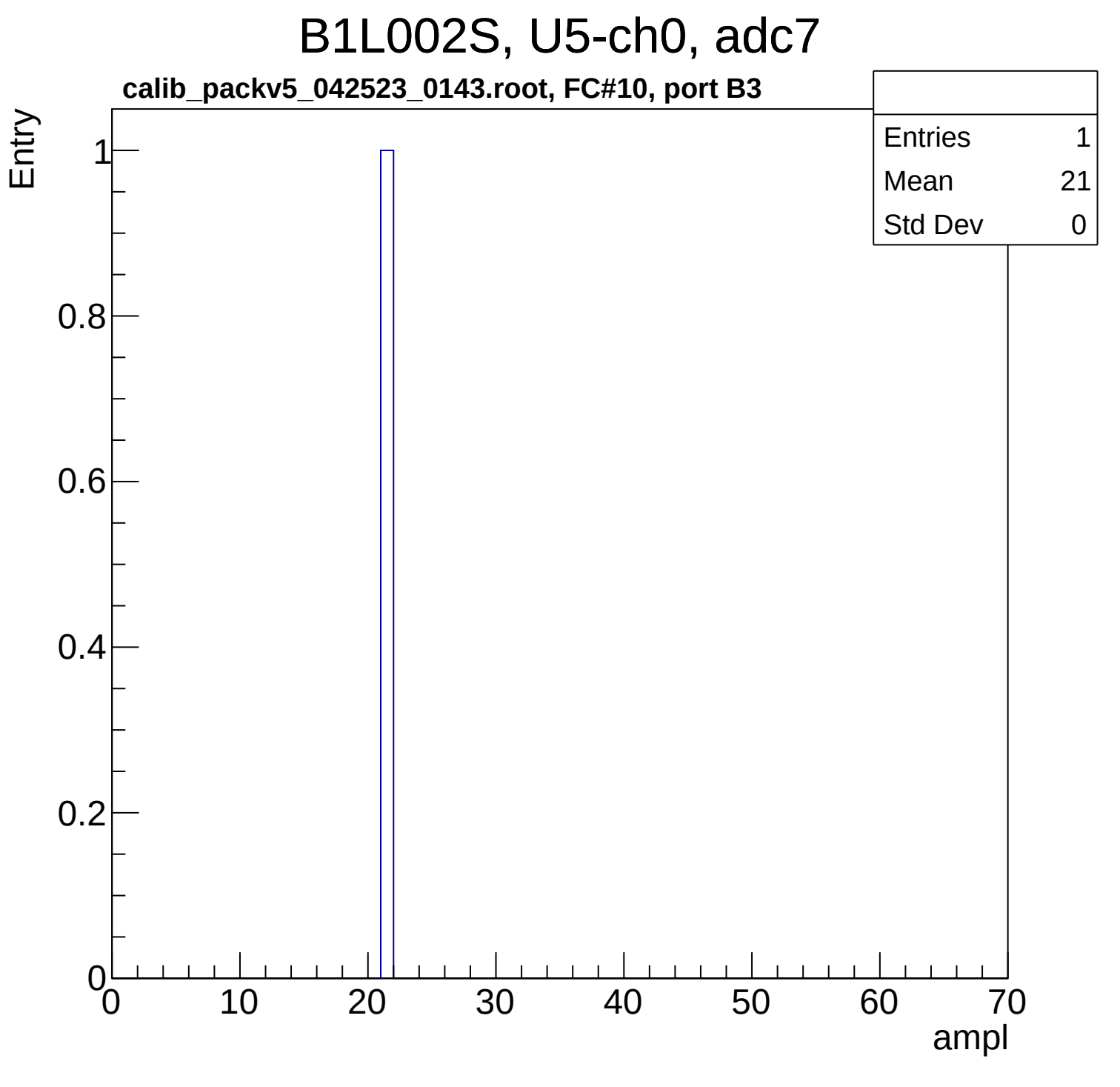
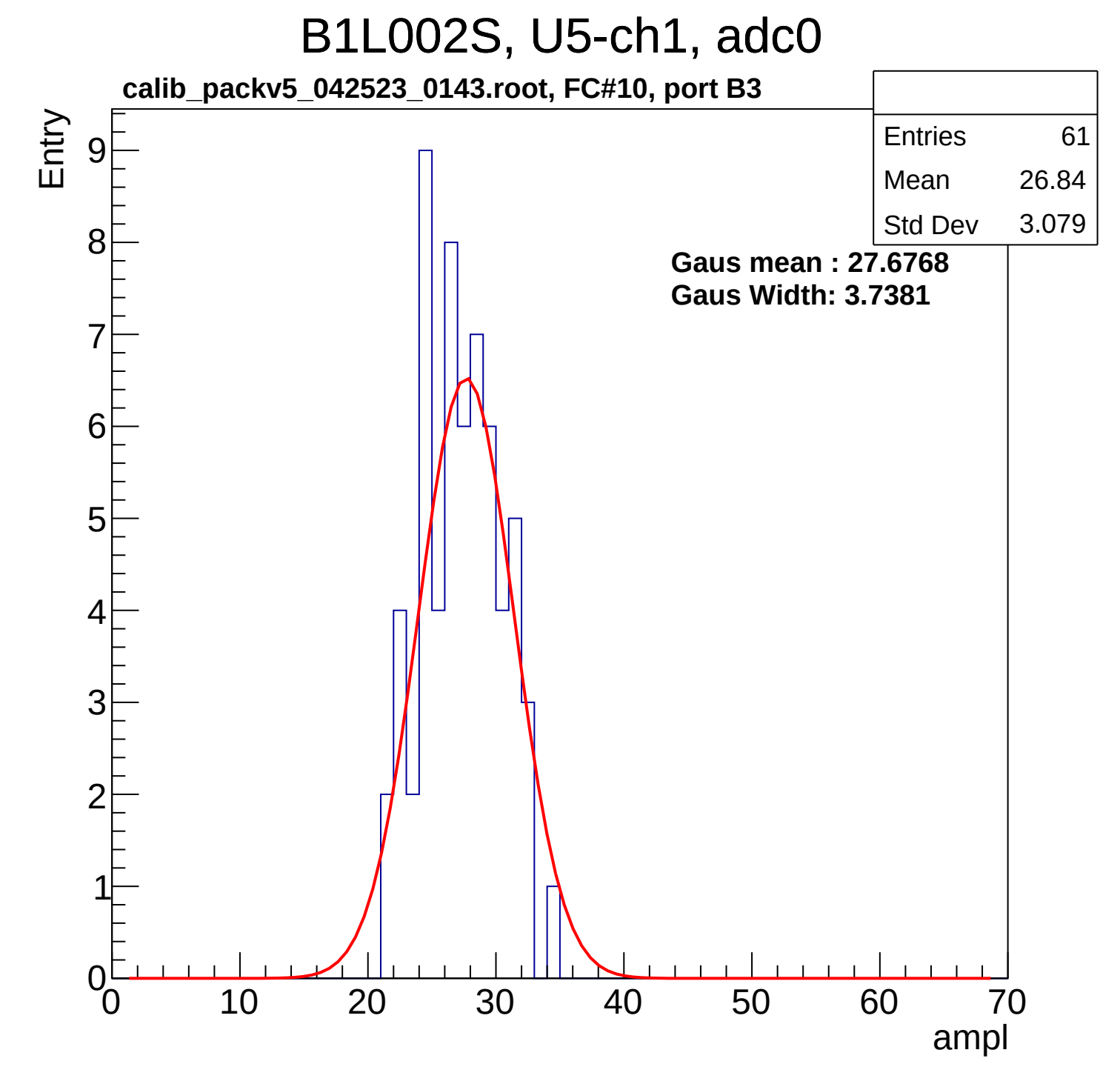

B1L002S, U5-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.62
Std Dev 1.389
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.84
Std Dev 3.079
Gaus mean : 27.6768
Gaus Width: 3.7381
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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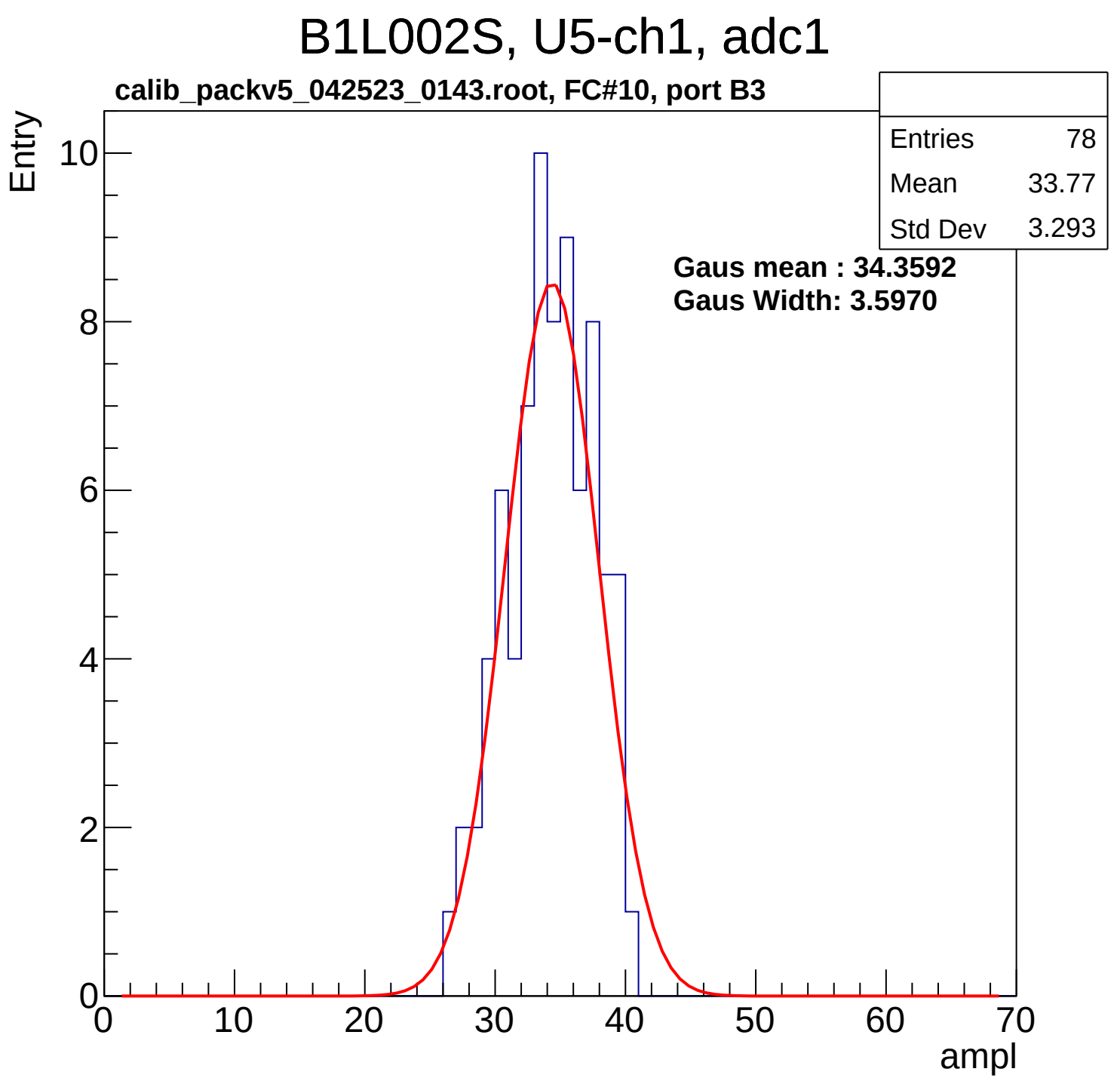
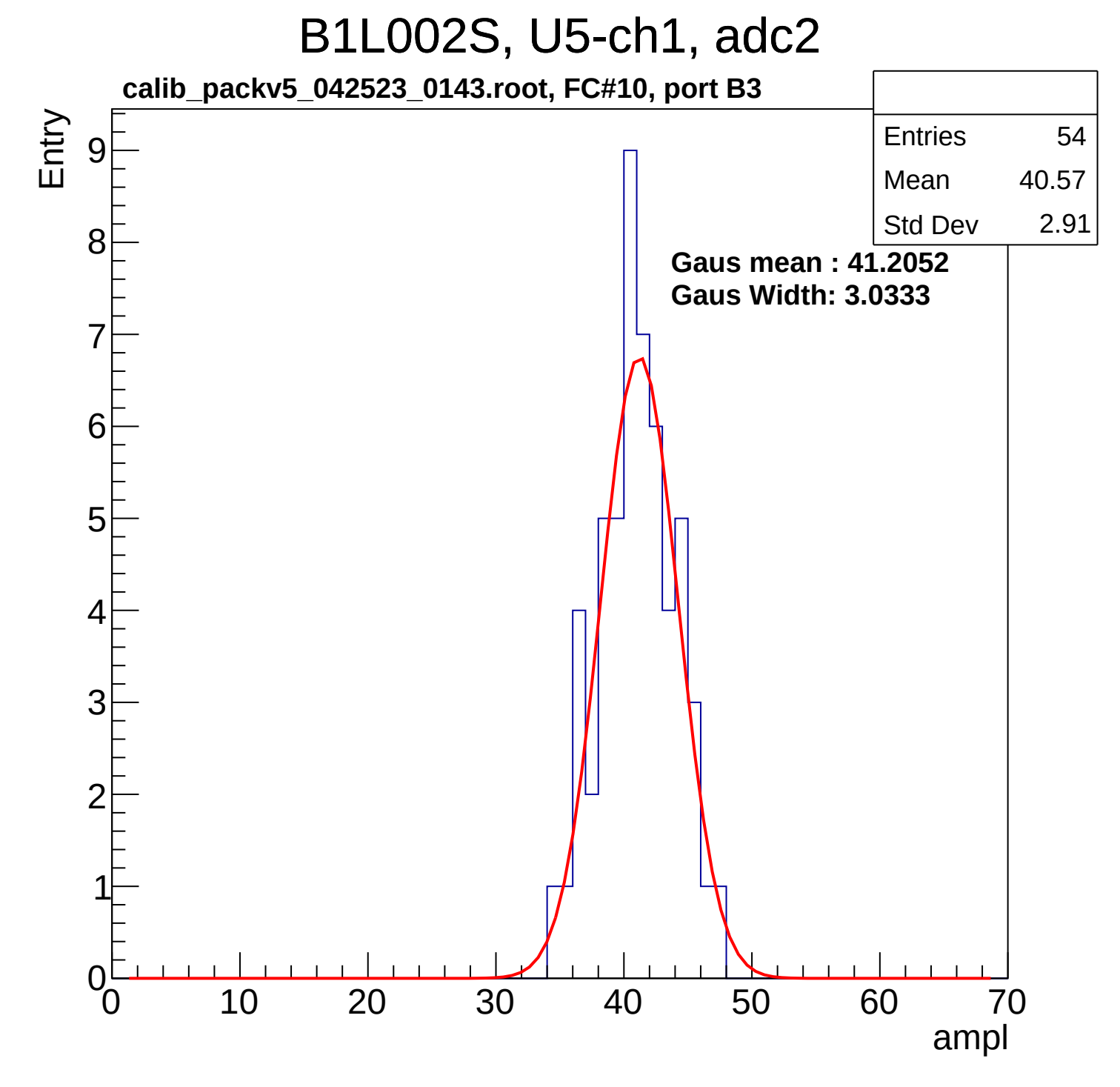

B1L002S, U5-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 33.77
Std Dev 3.293
Gaus mean : 34.3592
Gaus Width: 3.5970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.57
Std Dev 2.91
Gaus mean : 41.2052
Gaus Width: 3.0333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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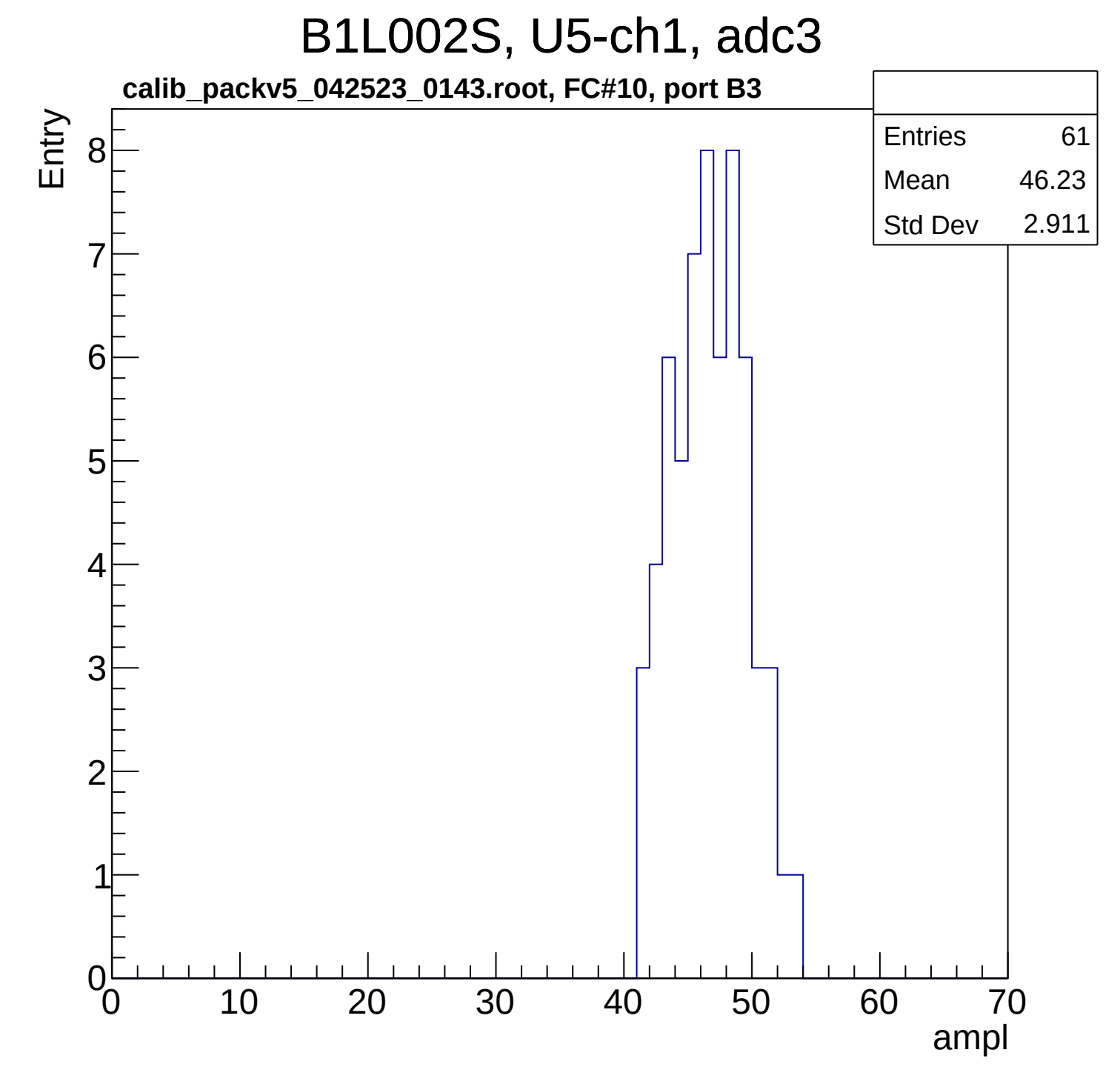
B1L002S, U5-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.23
Std Dev 2.911
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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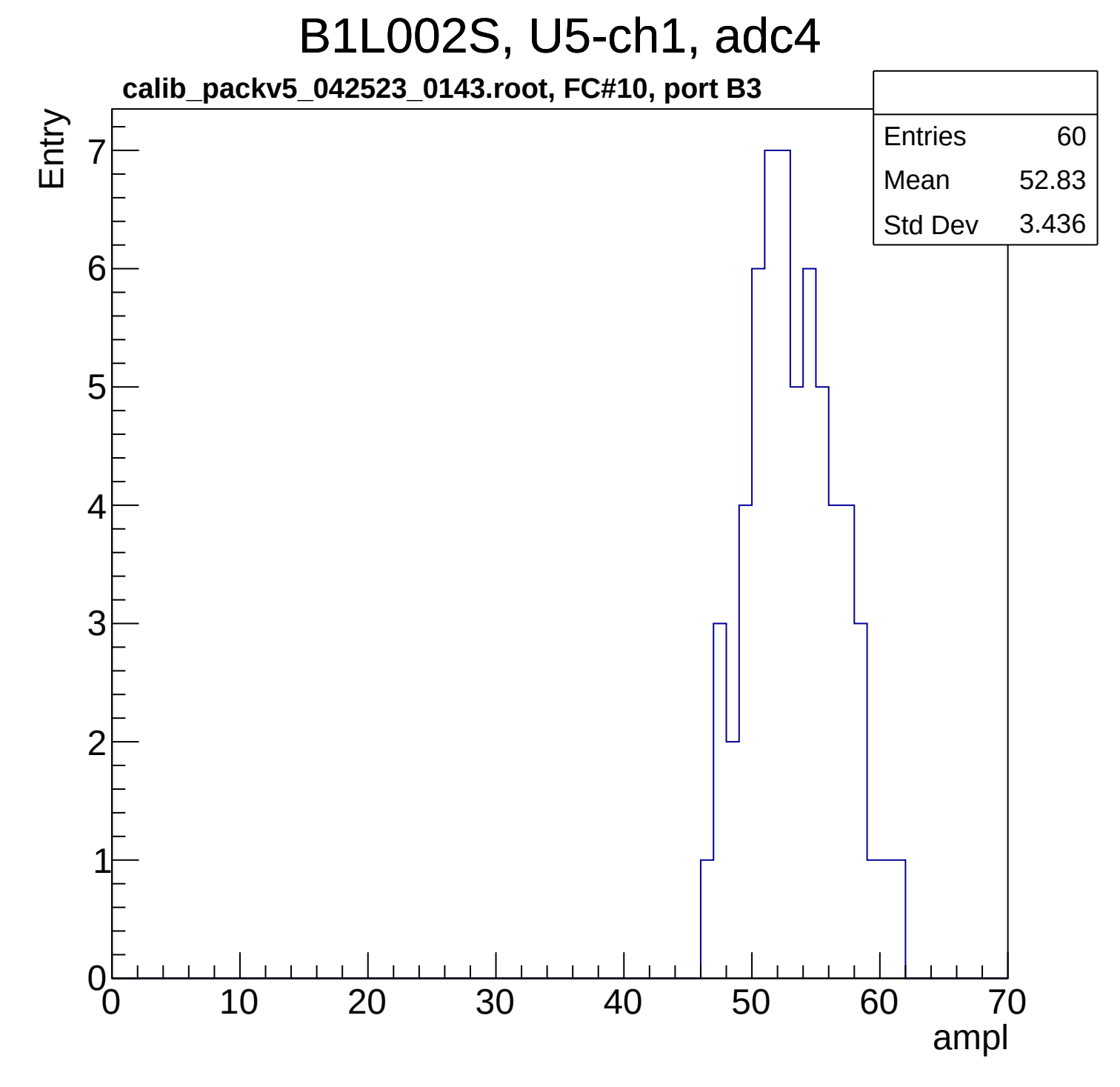
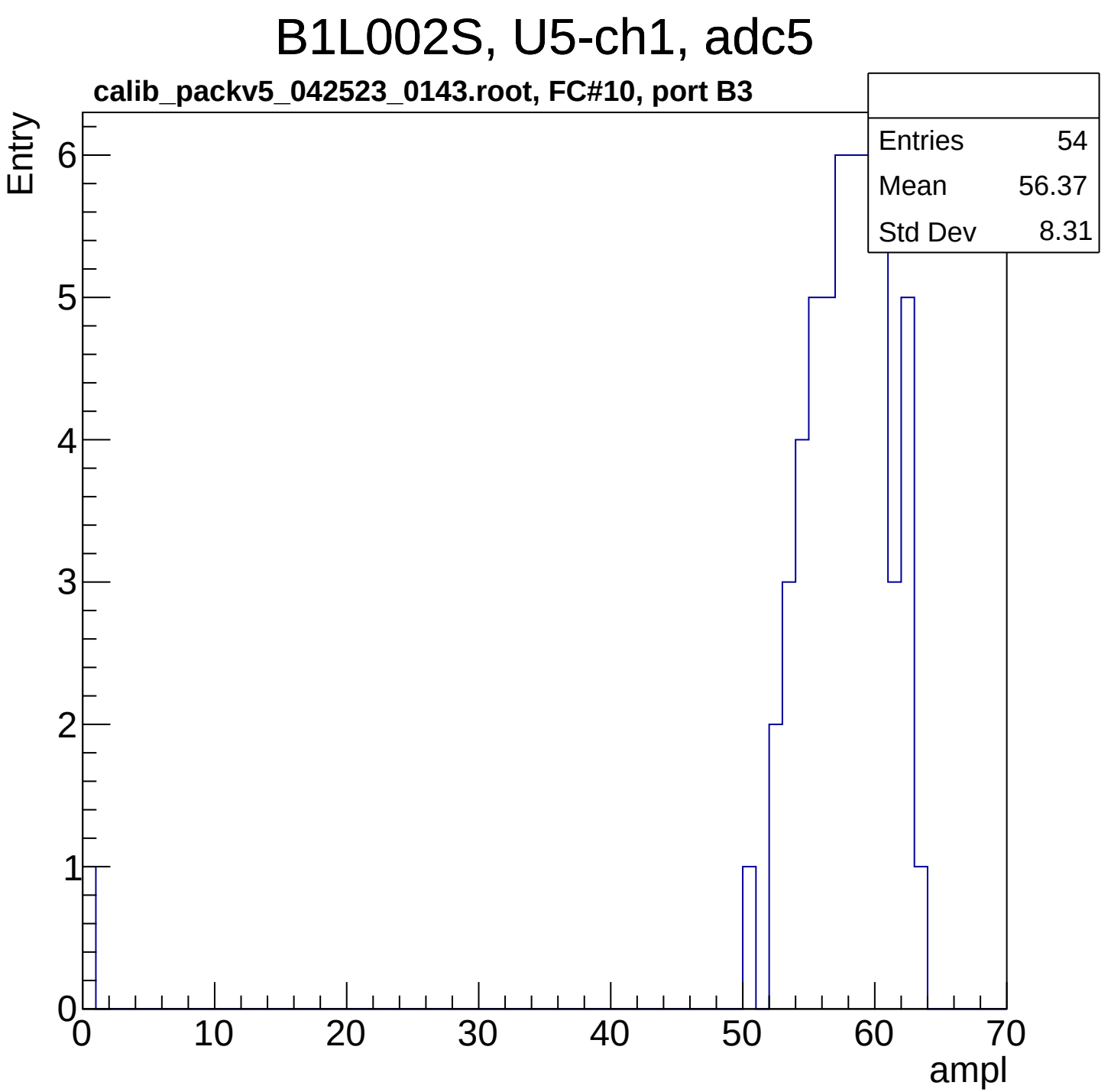
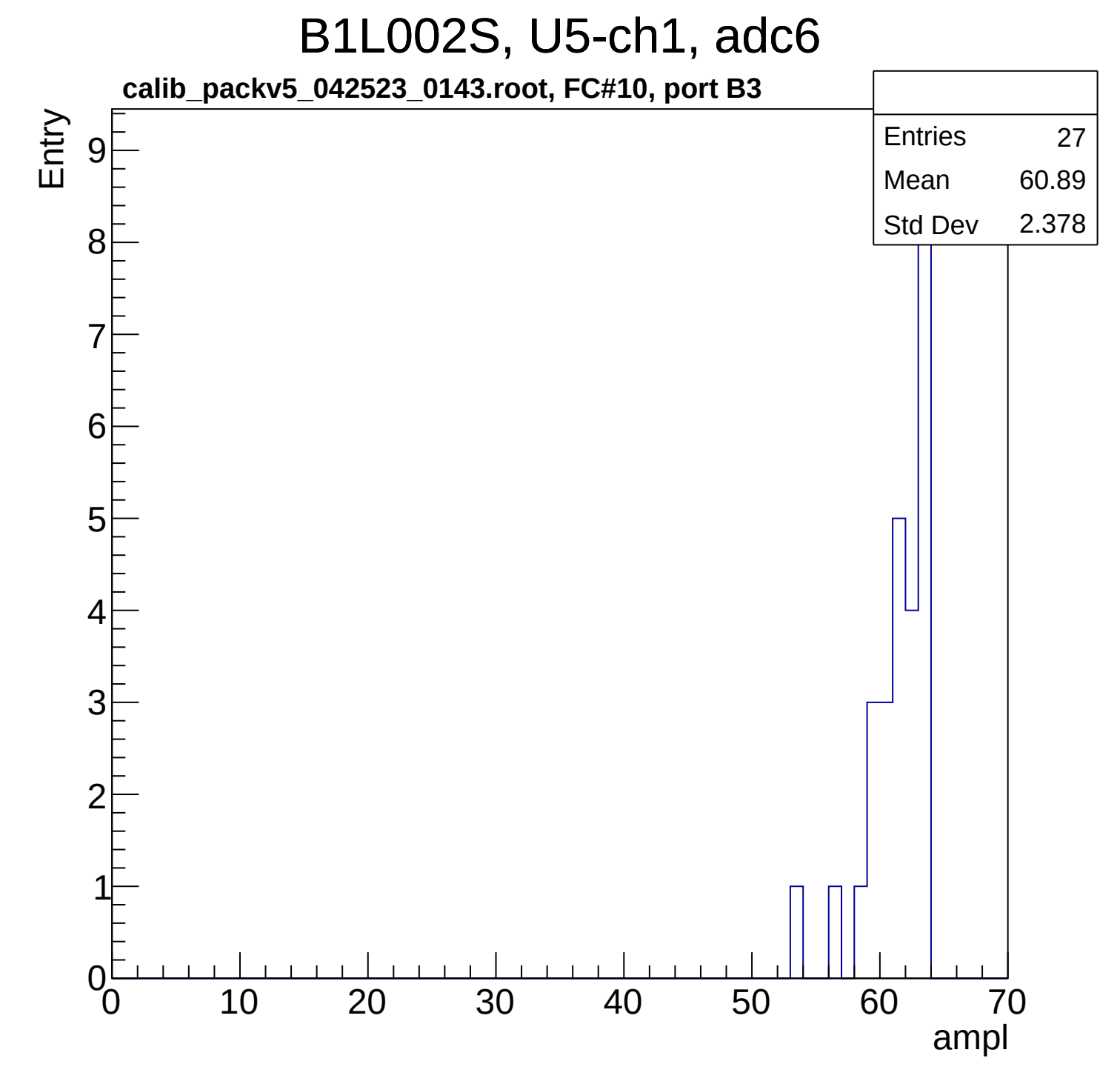

B1L002S, U5-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.83
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.37
Std Dev 8.31
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.89
Std Dev 2.378
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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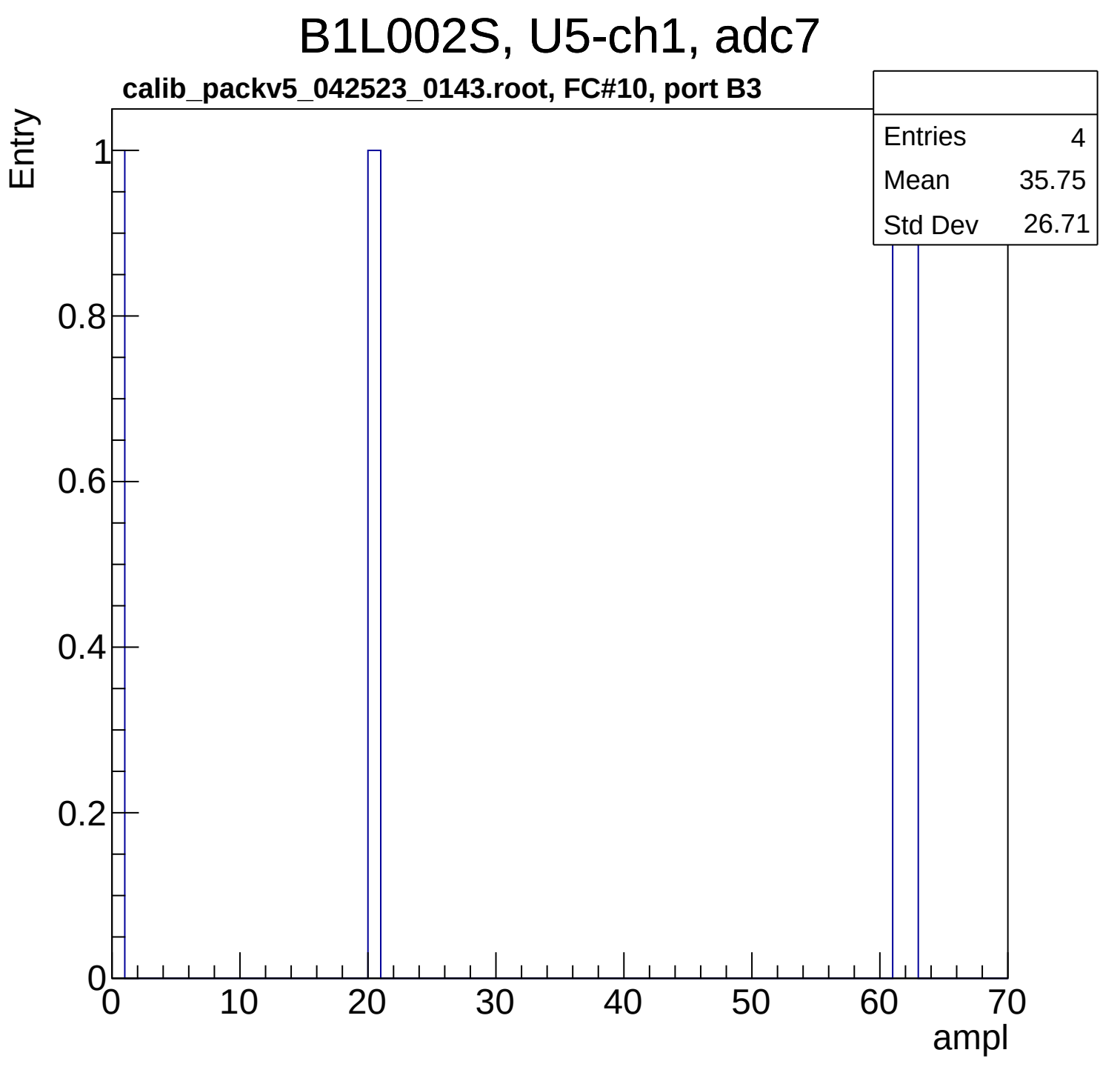
B1L002S, U5-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 35.75
Std Dev 26.71
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
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40
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60
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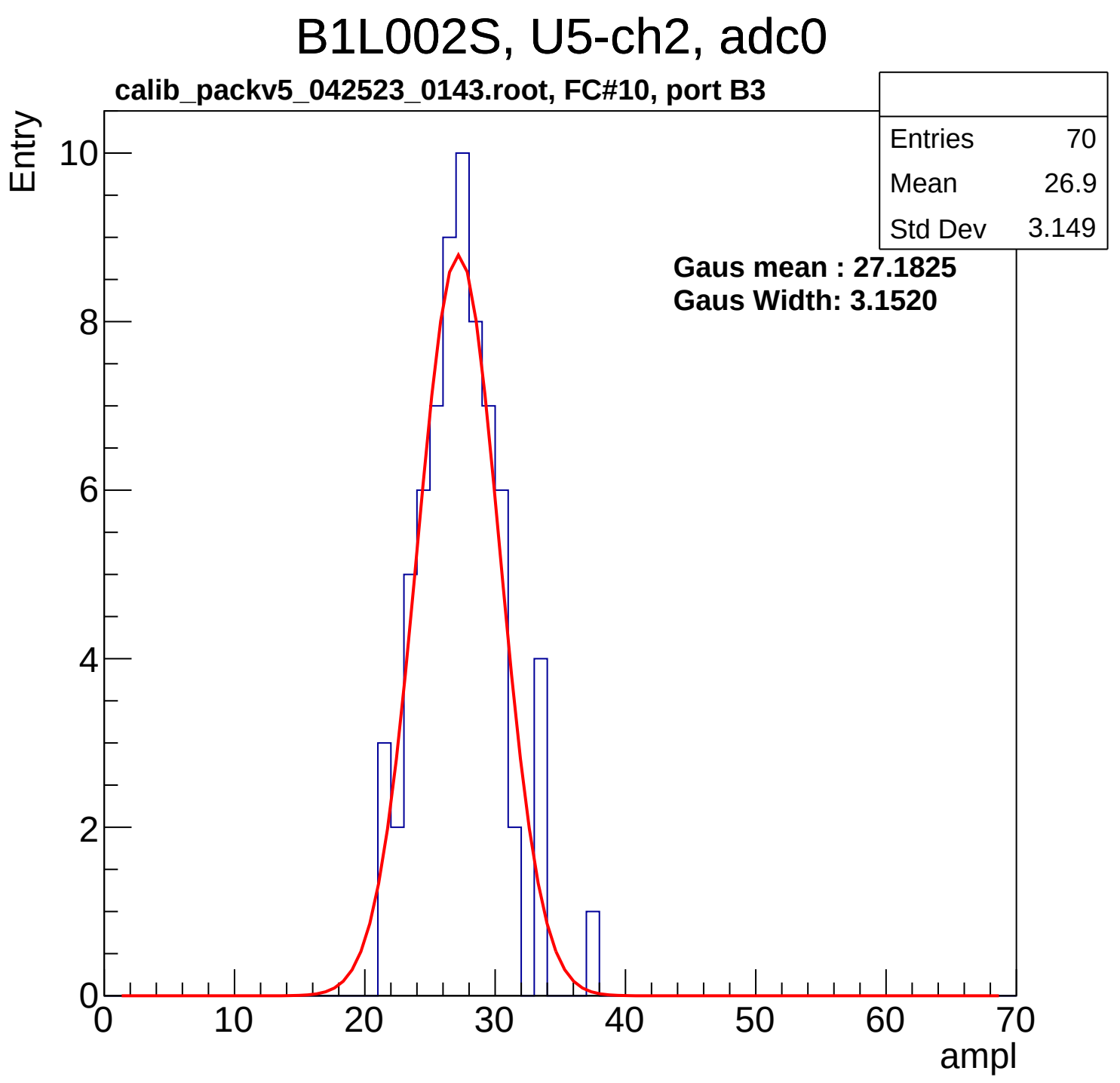
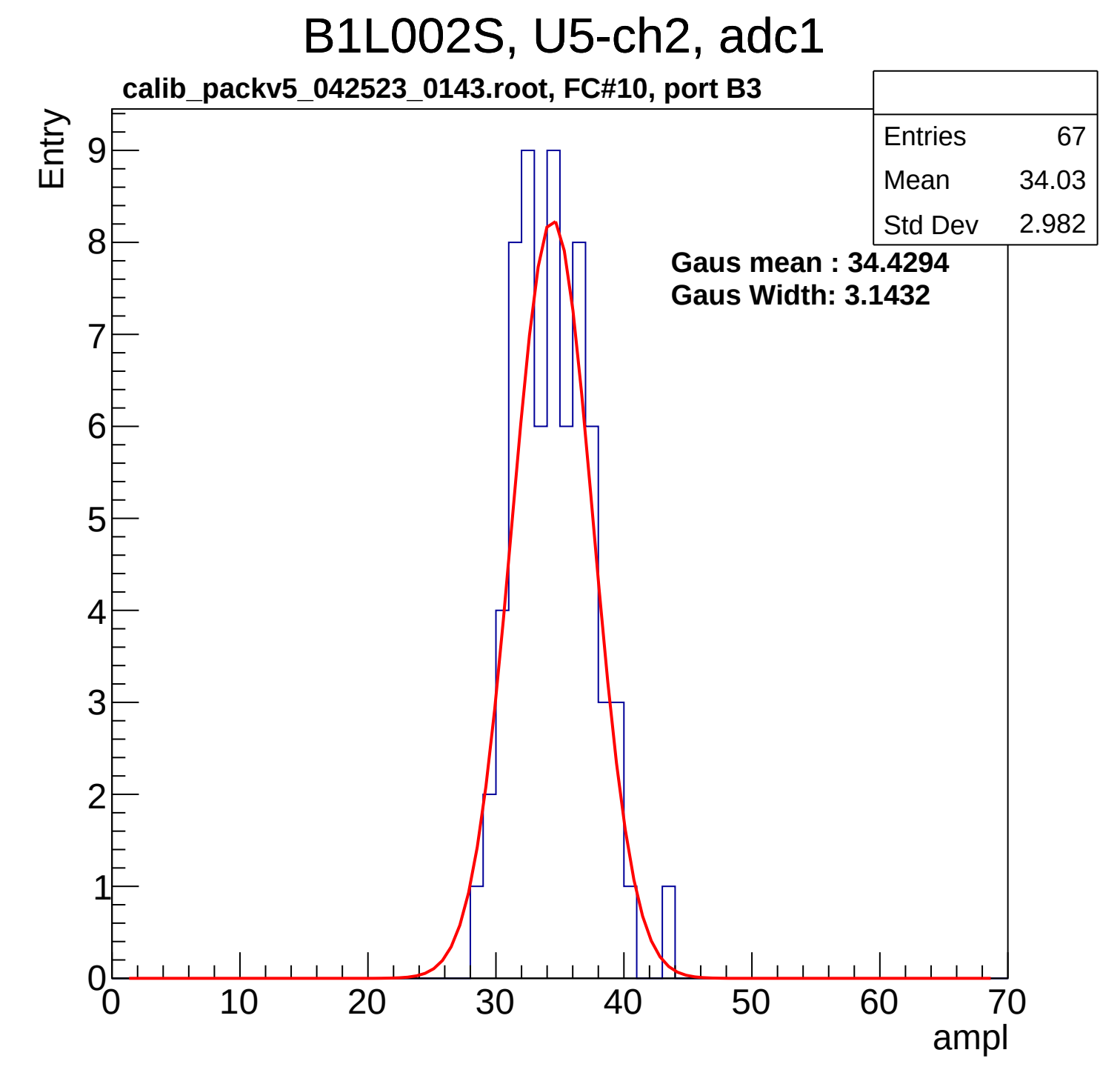

B1L002S, U5-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.9
Std Dev 3.149
Gaus mean : 27.1825
Gaus Width: 3.1520
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.03
Std Dev 2.982
Gaus mean : 34.4294
Gaus Width: 3.1432
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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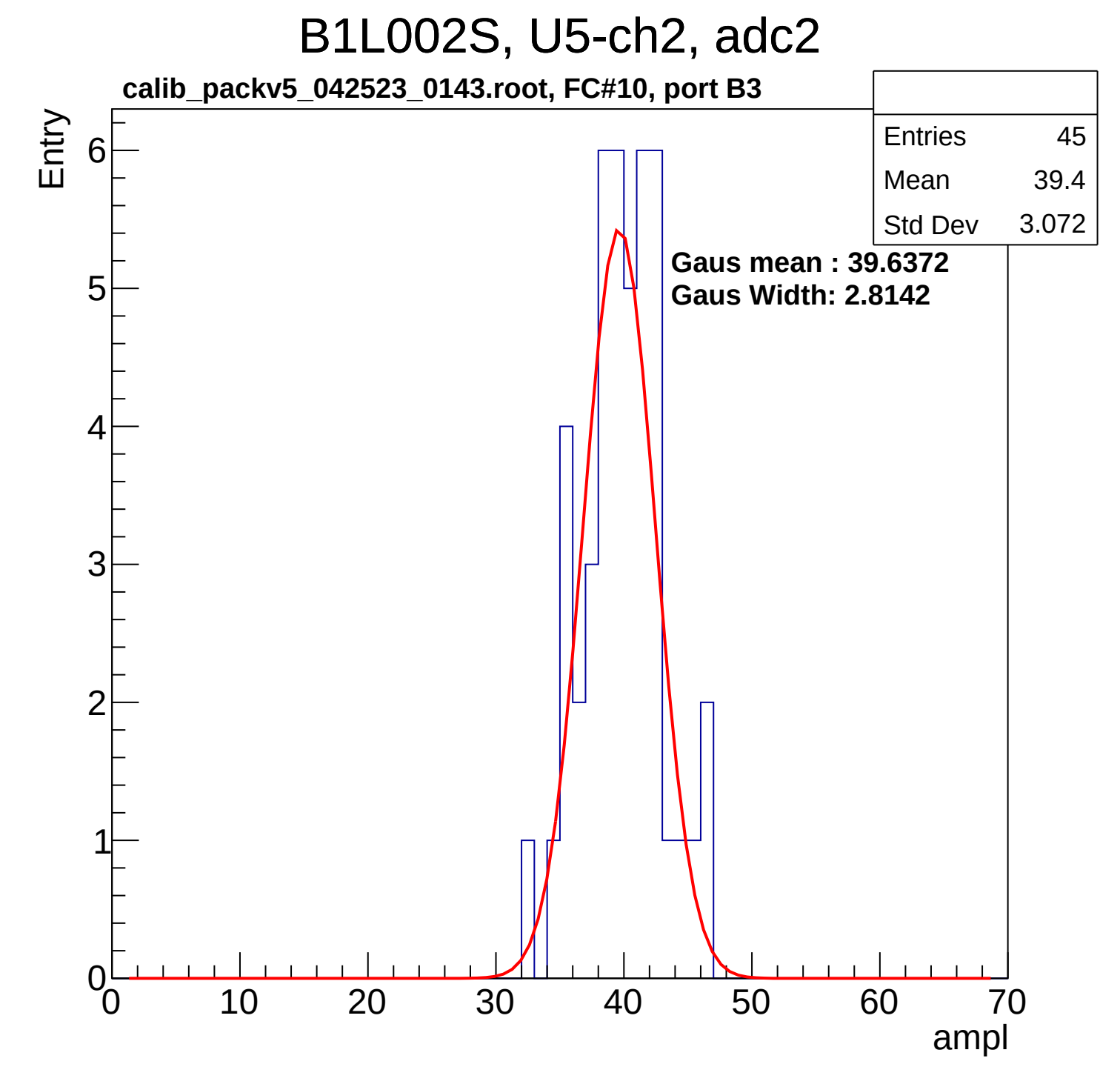
B1L002S, U5-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 39.4
Std Dev 3.072
Gaus mean : 39.6372
Gaus Width: 2.8142
Entry
ampl
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1
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3
4
5
6
0
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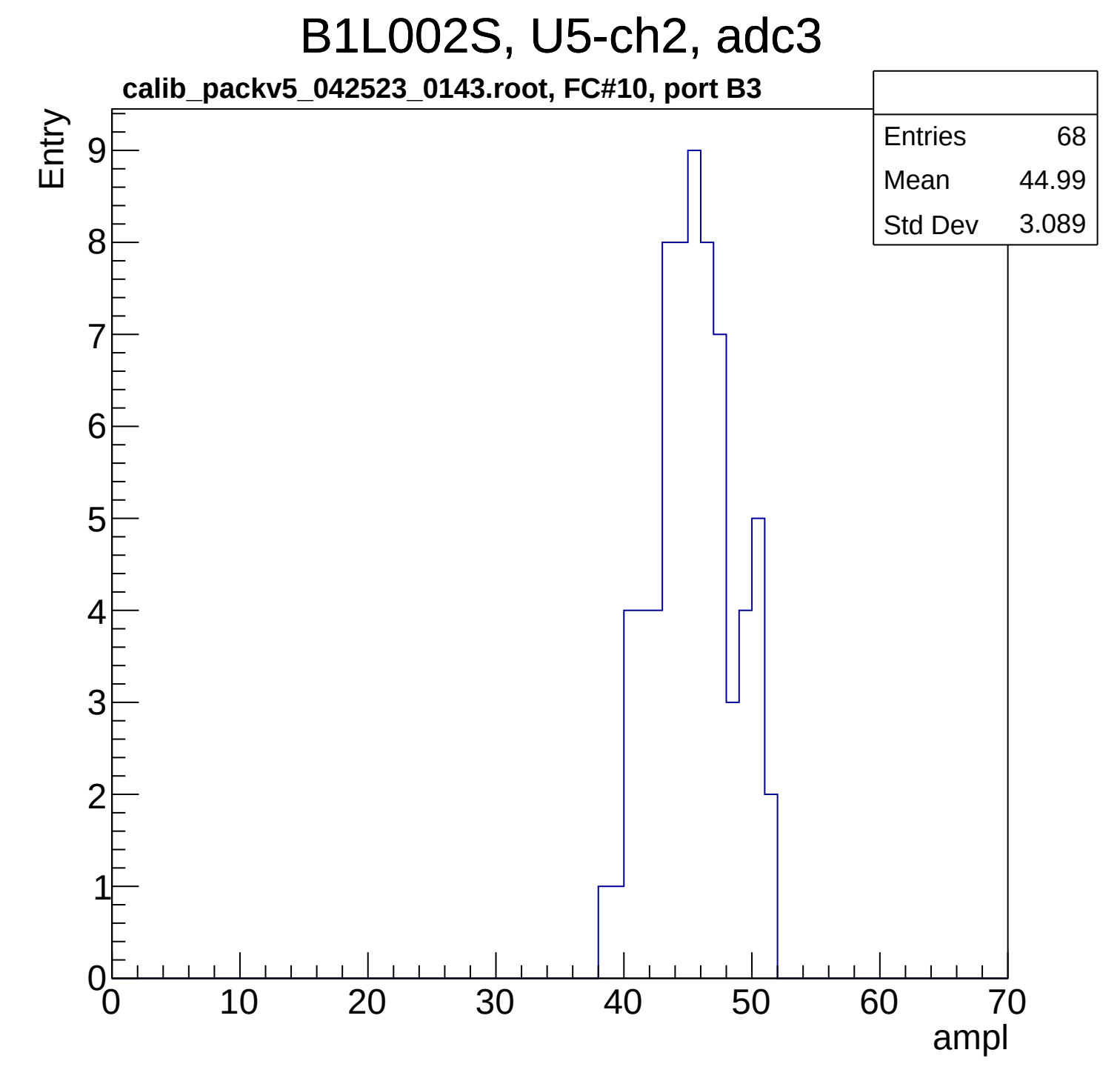
B1L002S, U5-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 44.99
Std Dev 3.089
Entry
ampl
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2
3
4
5
6
7
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0
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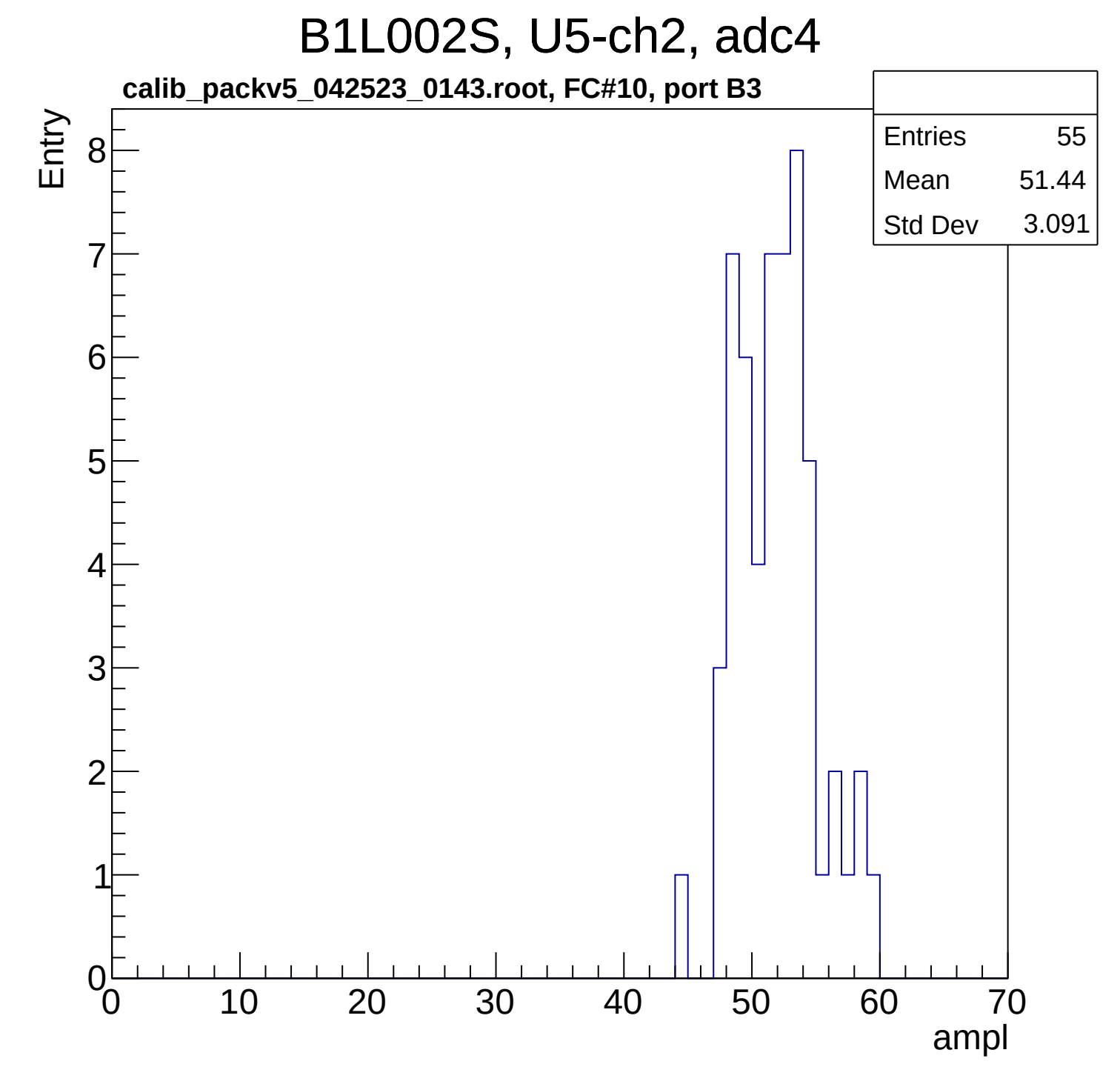
B1L002S, U5-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.44
Std Dev 3.091
Entry
ampl
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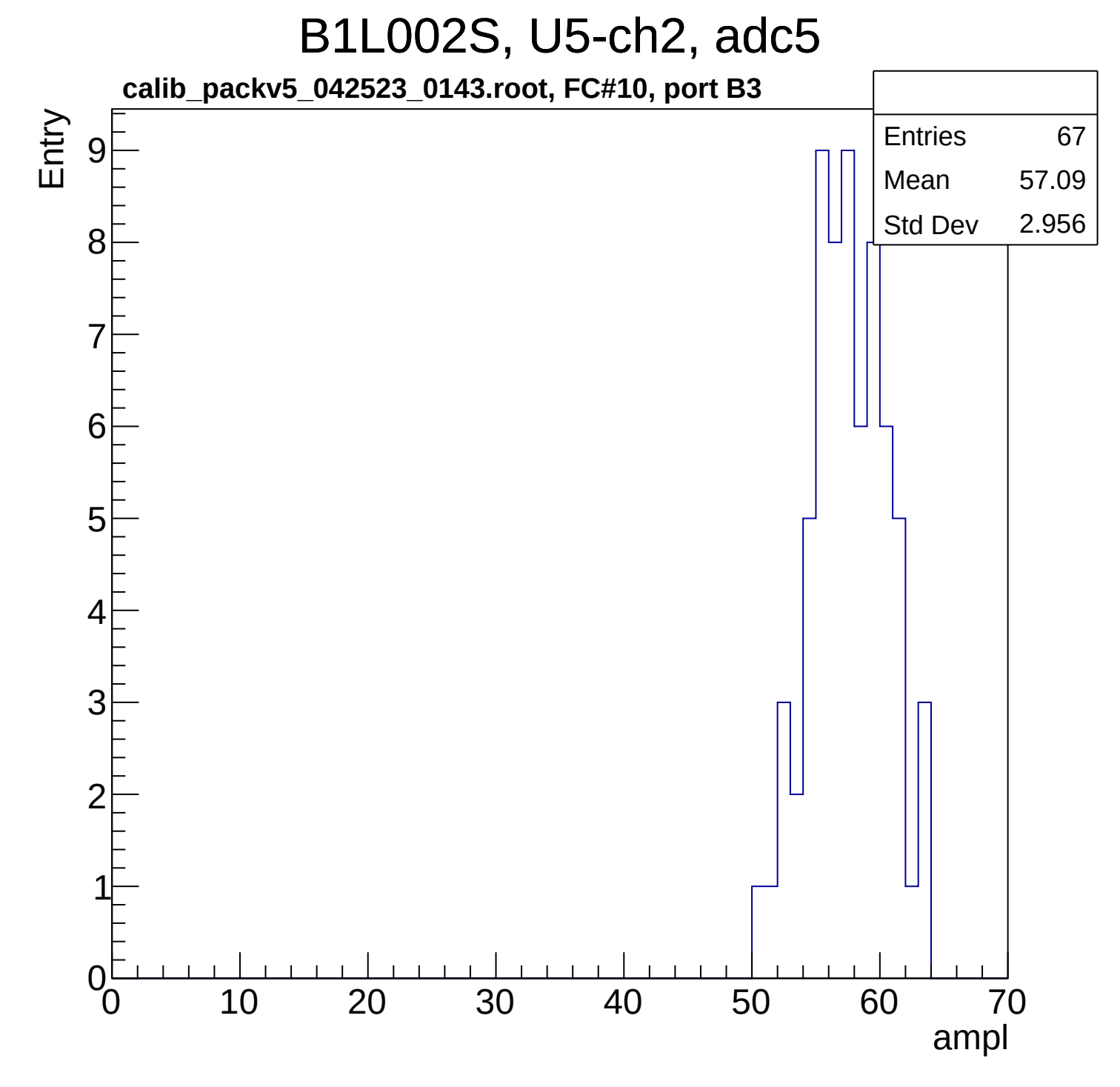
B1L002S, U5-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.09
Std Dev 2.956
Entry
ampl
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4
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6
7
8
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0
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60
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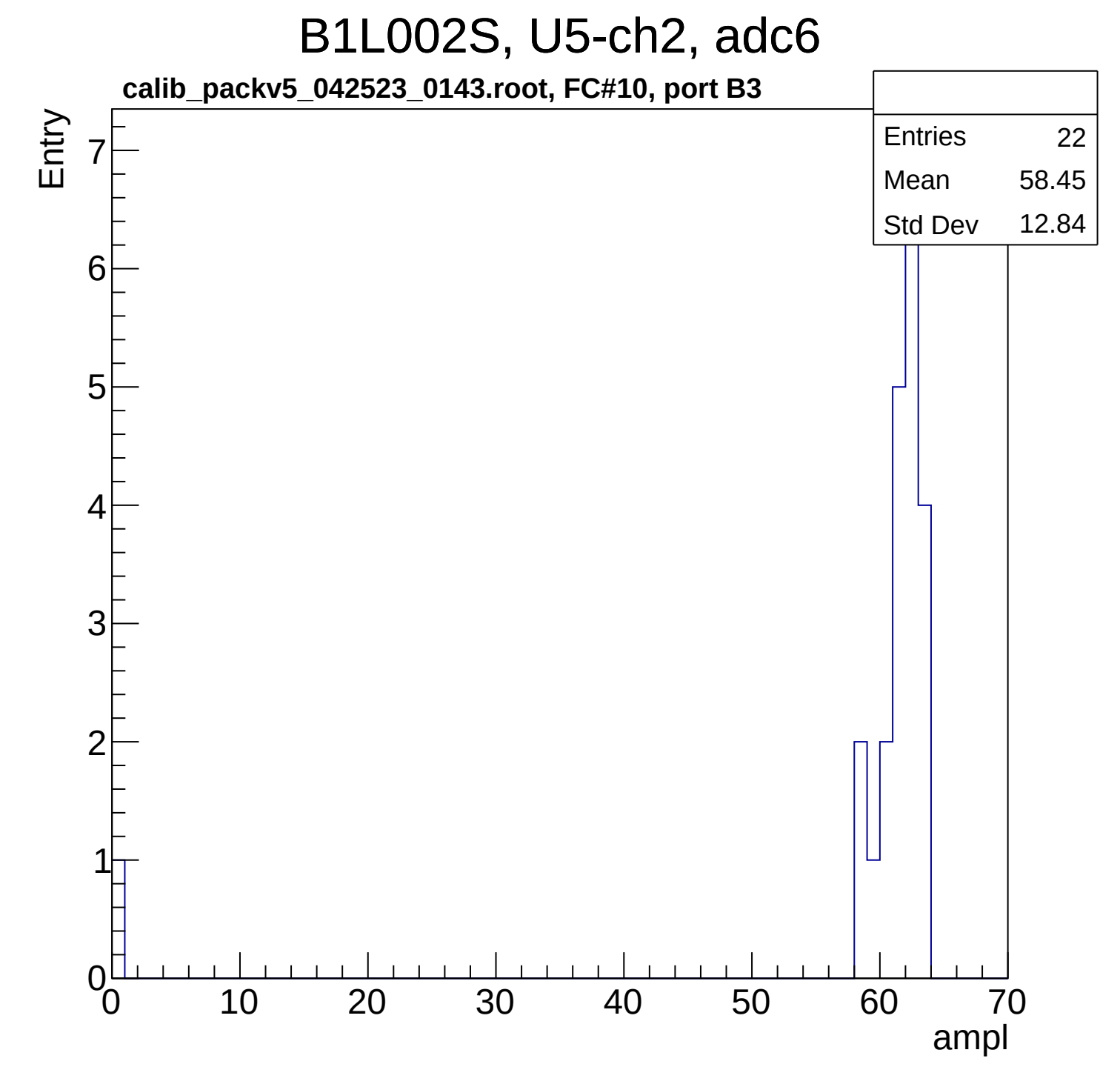
B1L002S, U5-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.45
Std Dev 12.84
Entry
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2
3
4
5
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7
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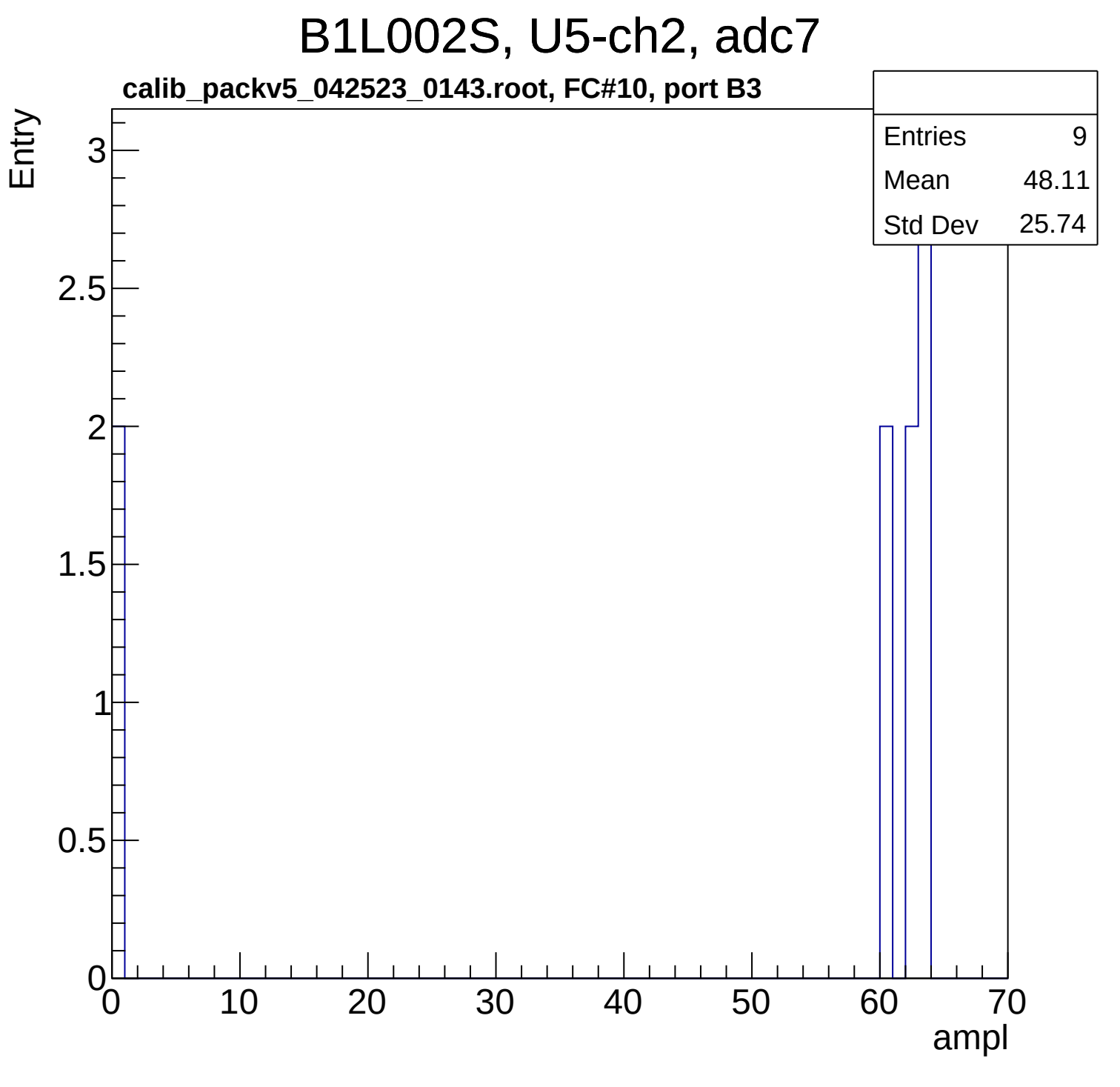
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 48.11
Std Dev 25.74
Entry
ampl
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1
1.5
2
2.5
3
0
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20
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40
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60
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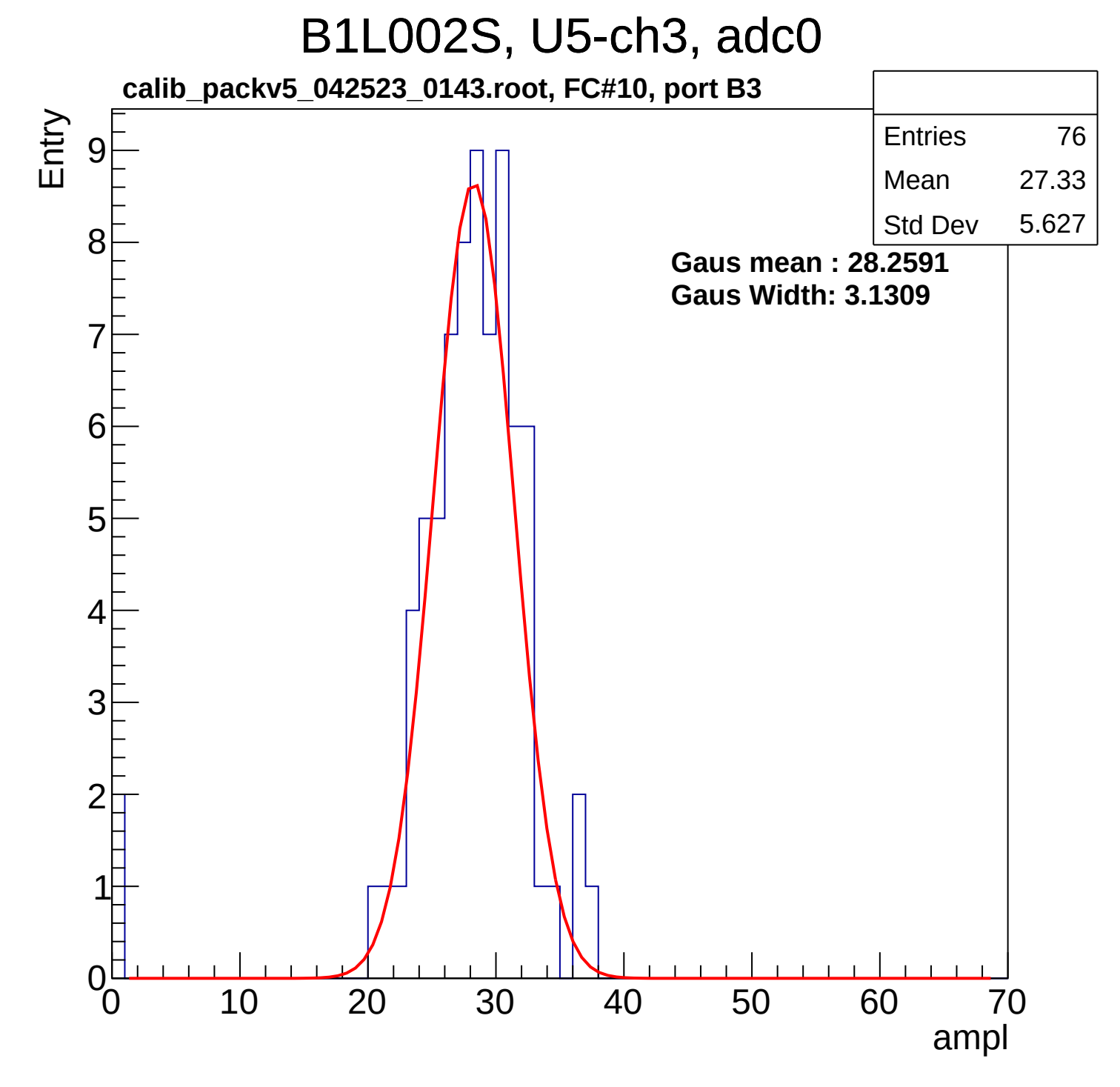
B1L002S, U5-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.33
Std Dev 5.627
Gaus mean : 28.2591
Gaus Width: 3.1309
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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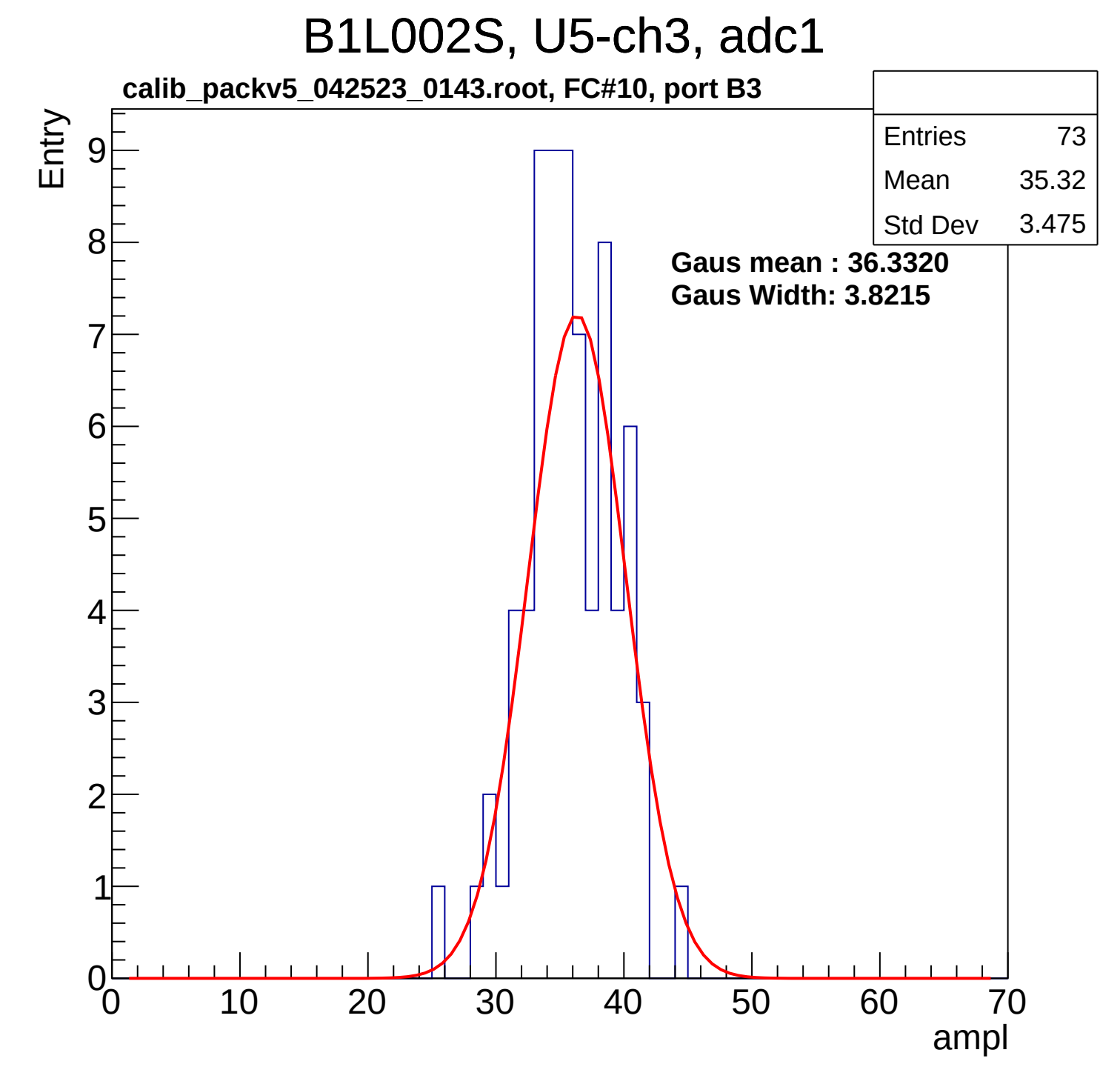
B1L002S, U5-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.32
Std Dev 3.475
Gaus mean : 36.3320
Gaus Width: 3.8215
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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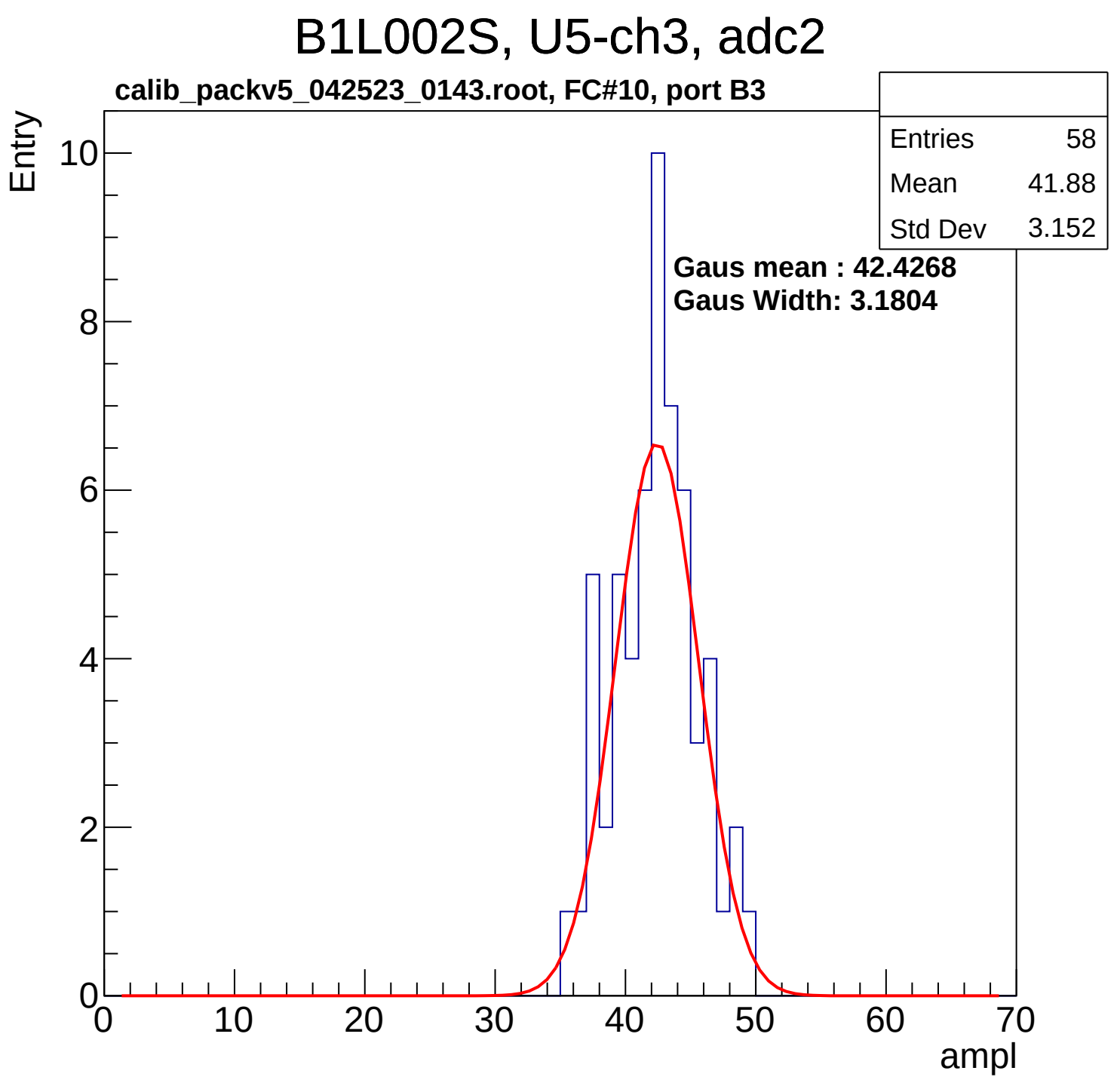
B1L002S, U5-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.88
Std Dev 3.152
Gaus mean : 42.4268
Gaus Width: 3.1804
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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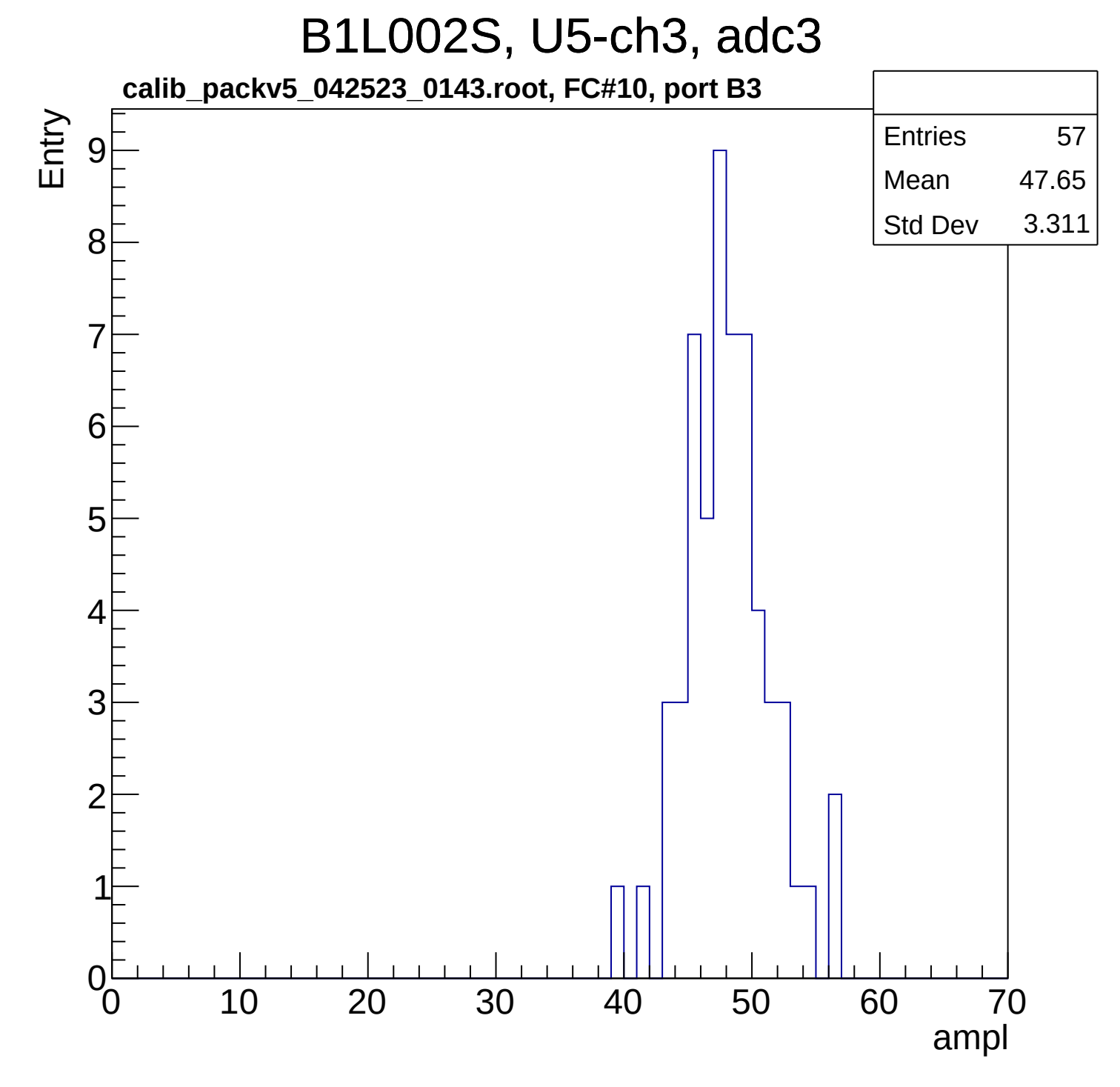
B1L002S, U5-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.65
Std Dev 3.311
Entry
ampl
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30
40
50
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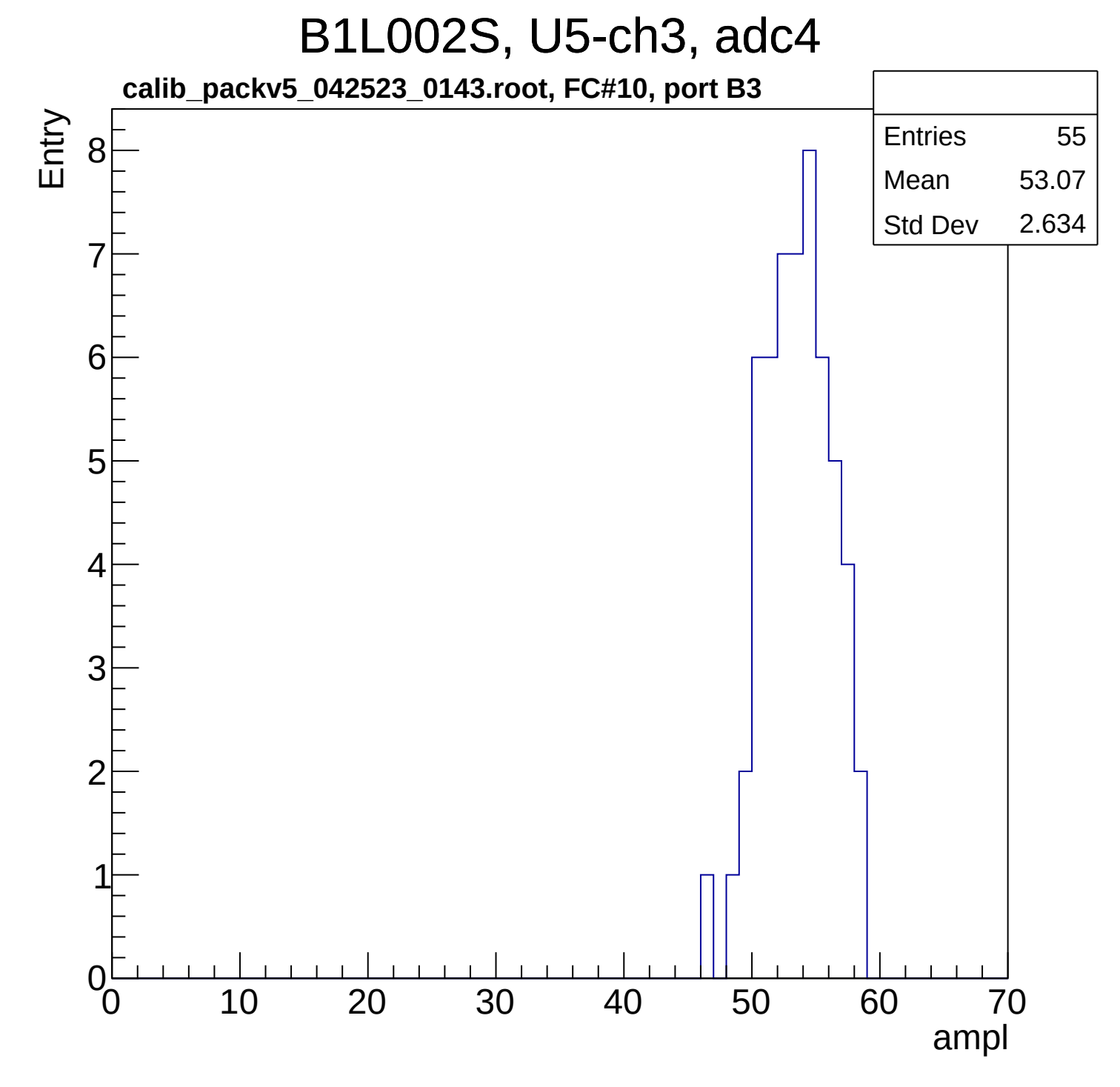
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.07
Std Dev 2.634
Entry
ampl
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2
3
4
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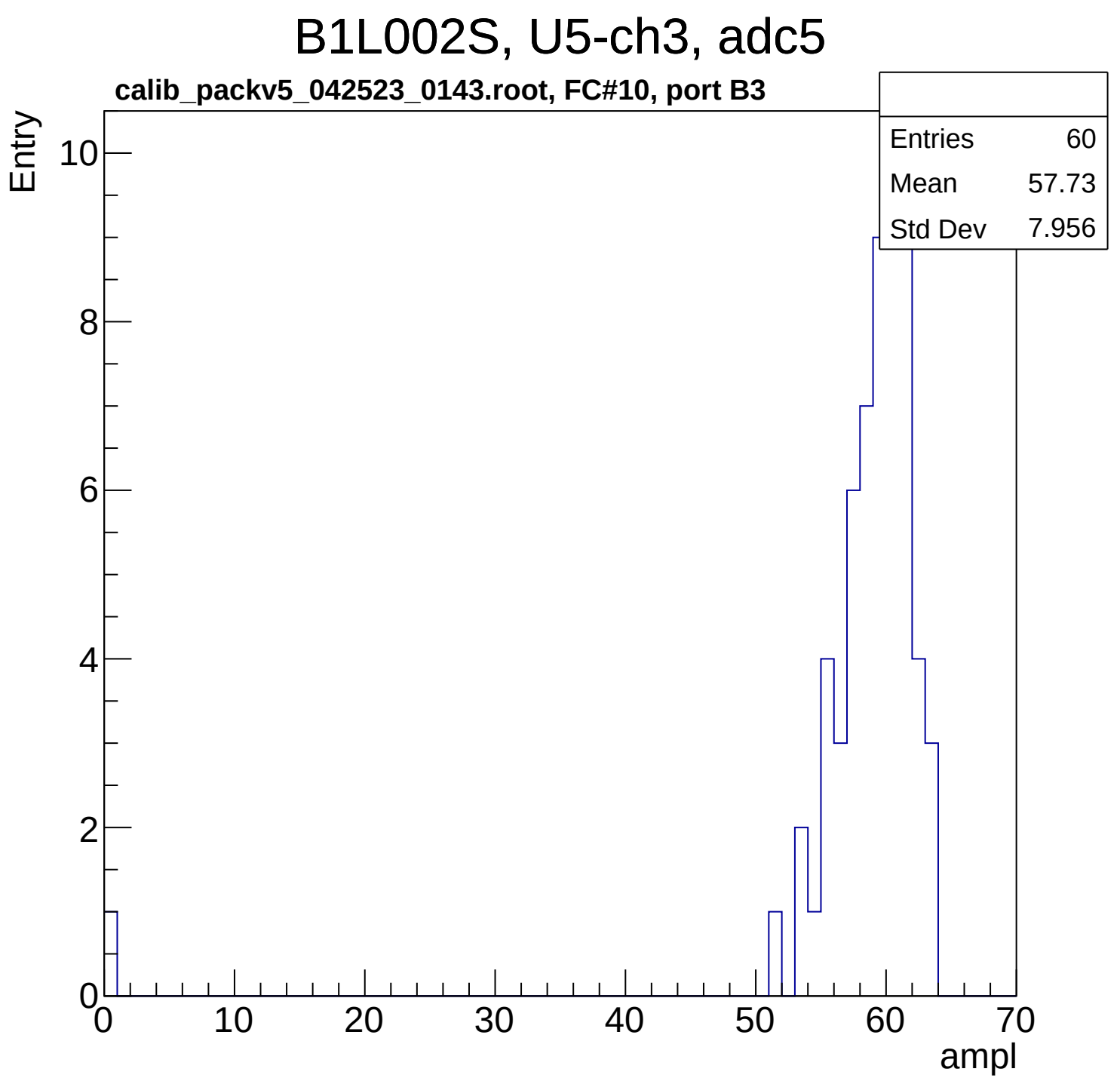
B1L002S, U5-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.73
Std Dev 7.956
Entry
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2
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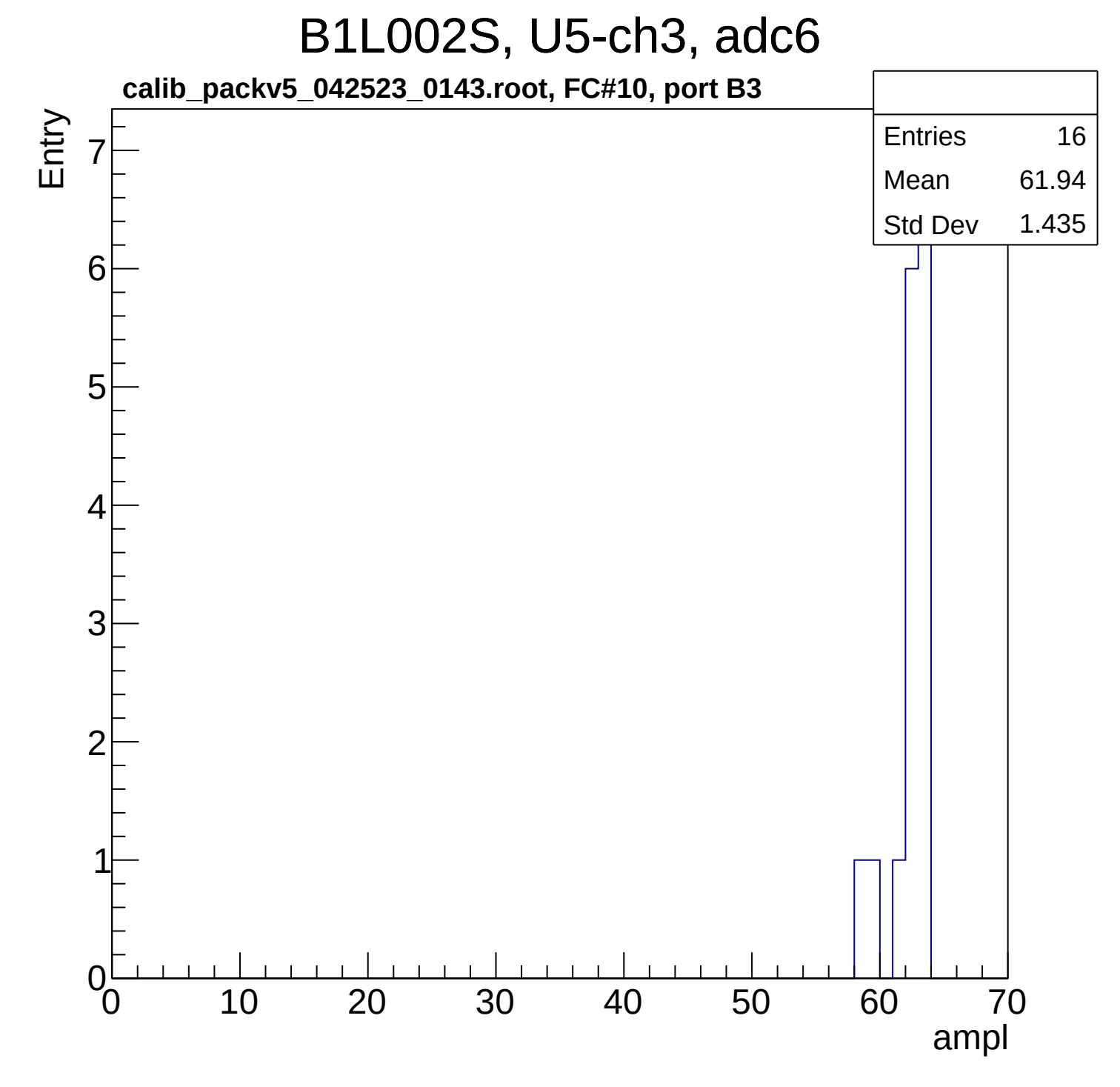
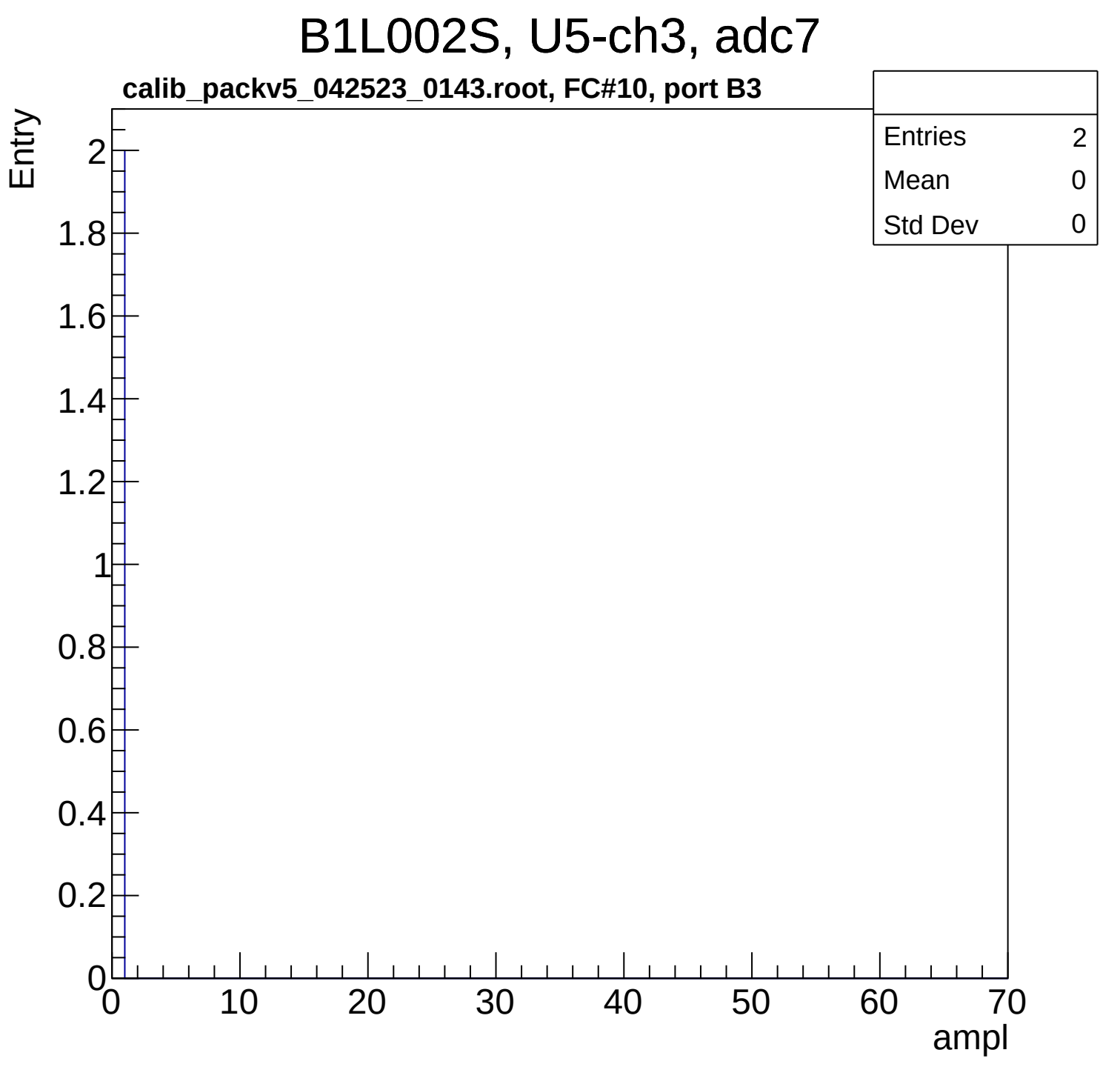
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.94
Std Dev 1.435
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
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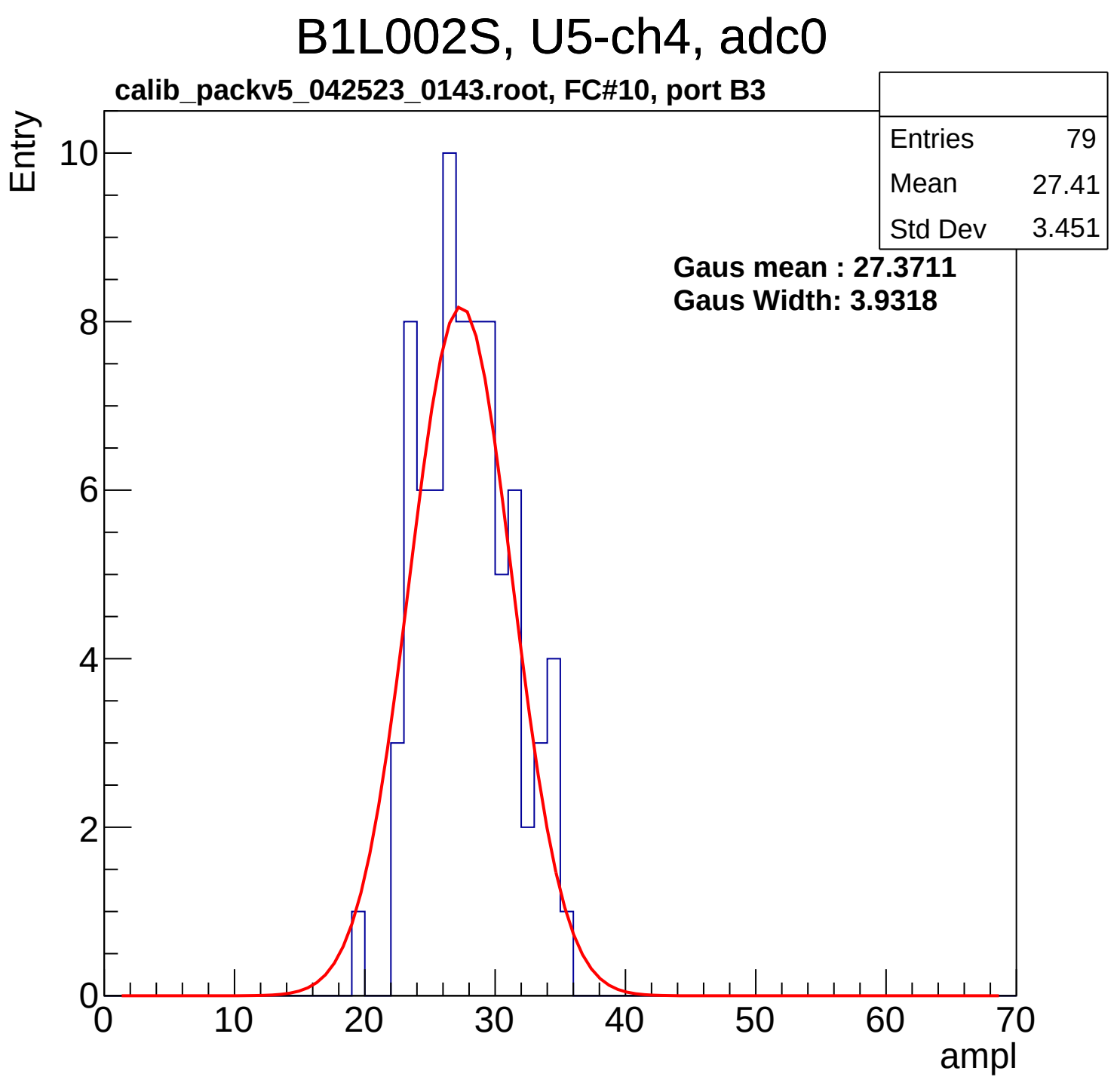
50
60
70
ampl


B1L002S, U5-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.41
Std Dev 3.451
Gaus mean : 27.3711
Gaus Width: 3.9318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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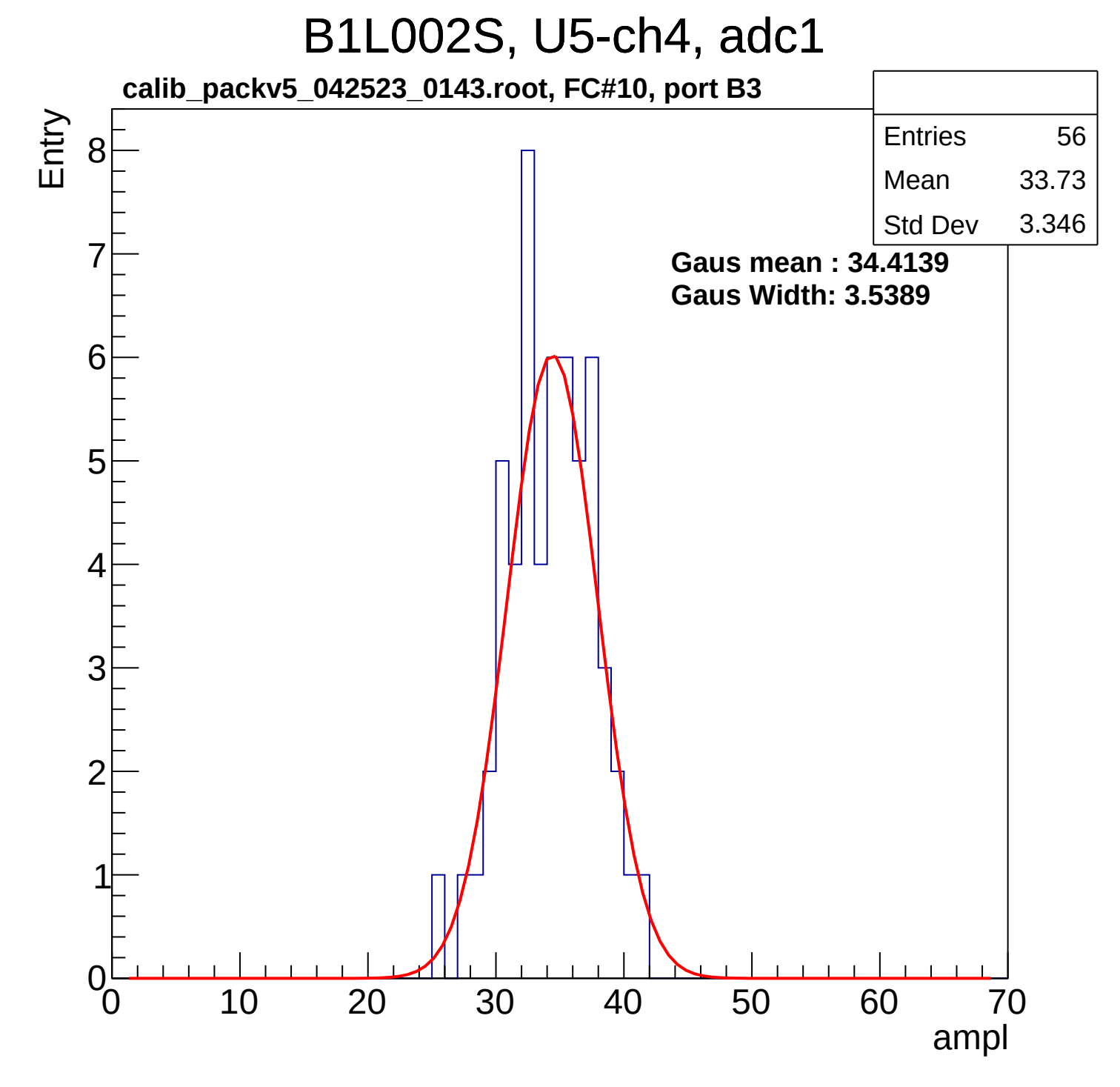
B1L002S, U5-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.73
Std Dev 3.346
Gaus mean : 34.4139
Gaus Width: 3.5389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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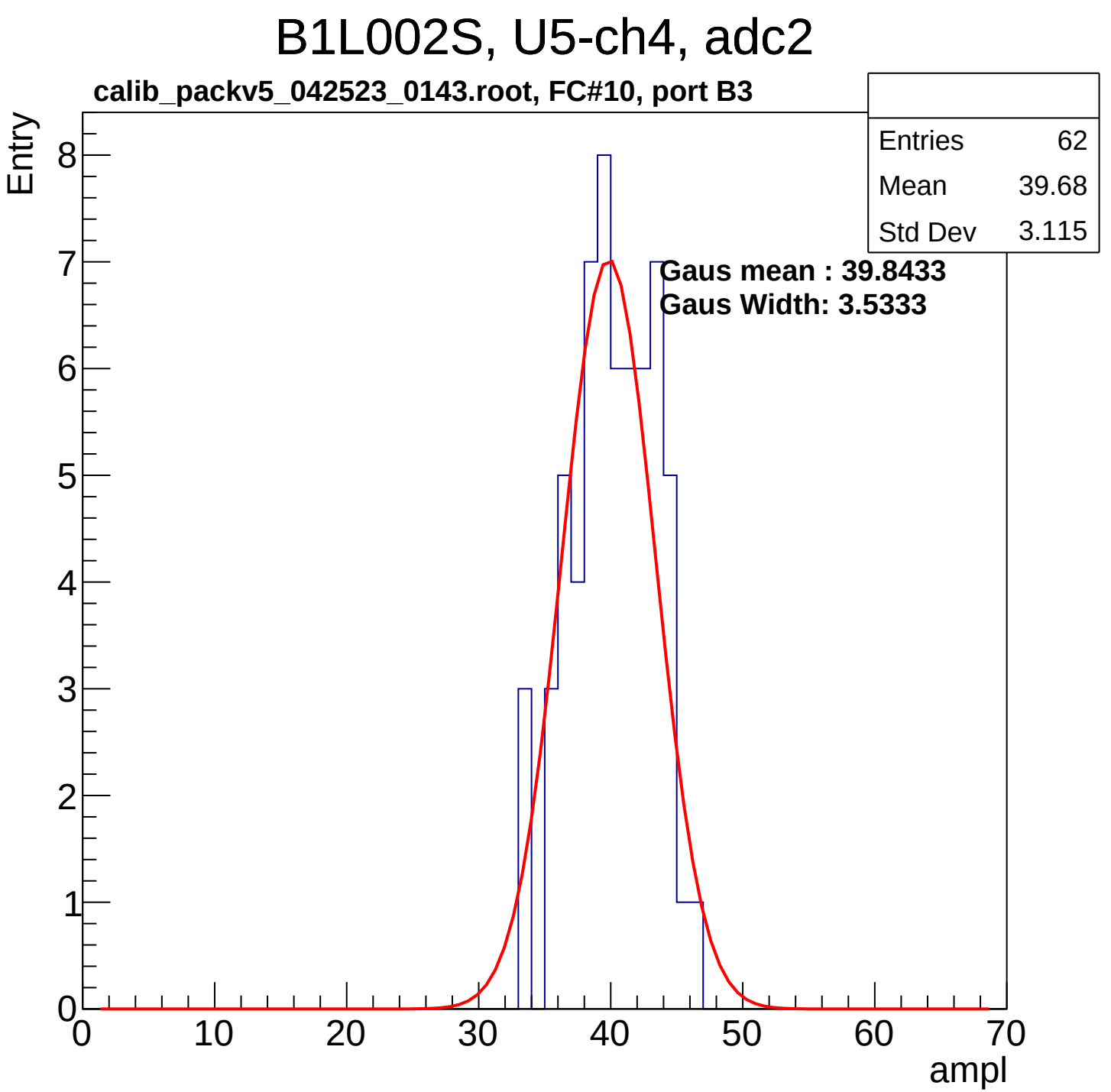
B1L002S, U5-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 39.68
Std Dev 3.115
Gaus mean : 39.8433
Gaus Width: 3.5333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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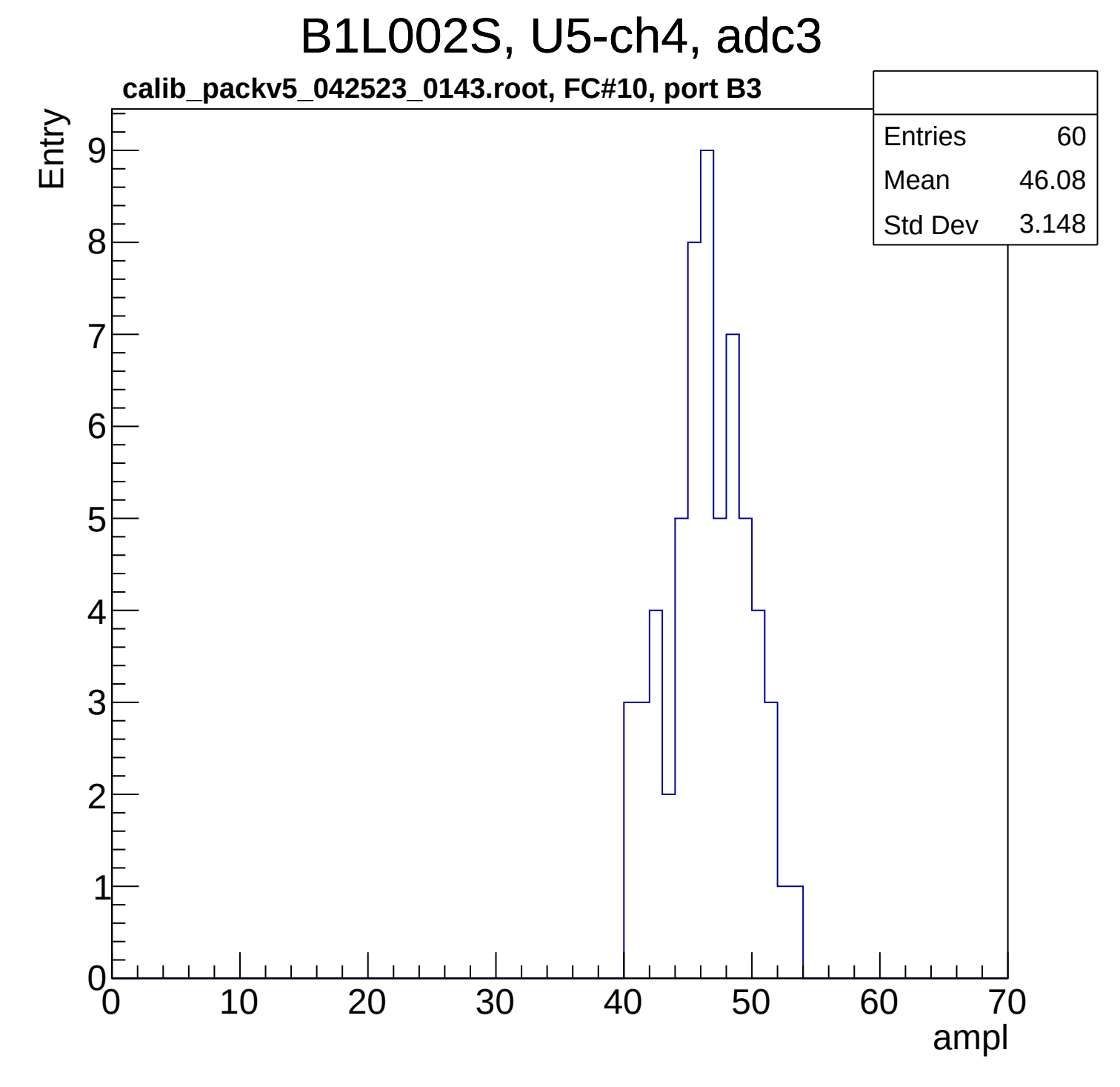
B1L002S, U5-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.08
Std Dev 3.148
Entry
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1
2
3
4
5
6
7
8
9
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10
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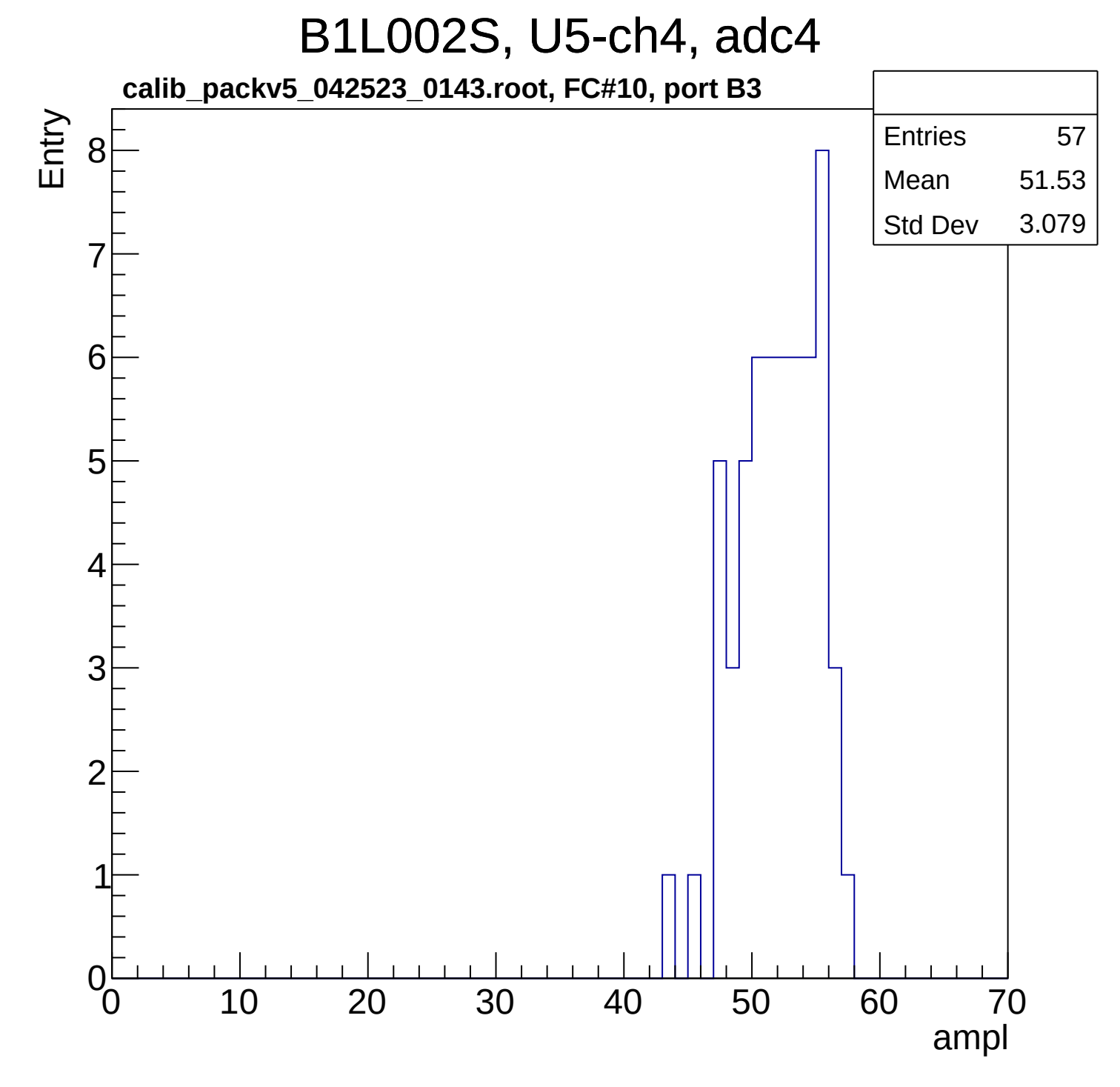
60
70
ampl


B1L002S, U5-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.53
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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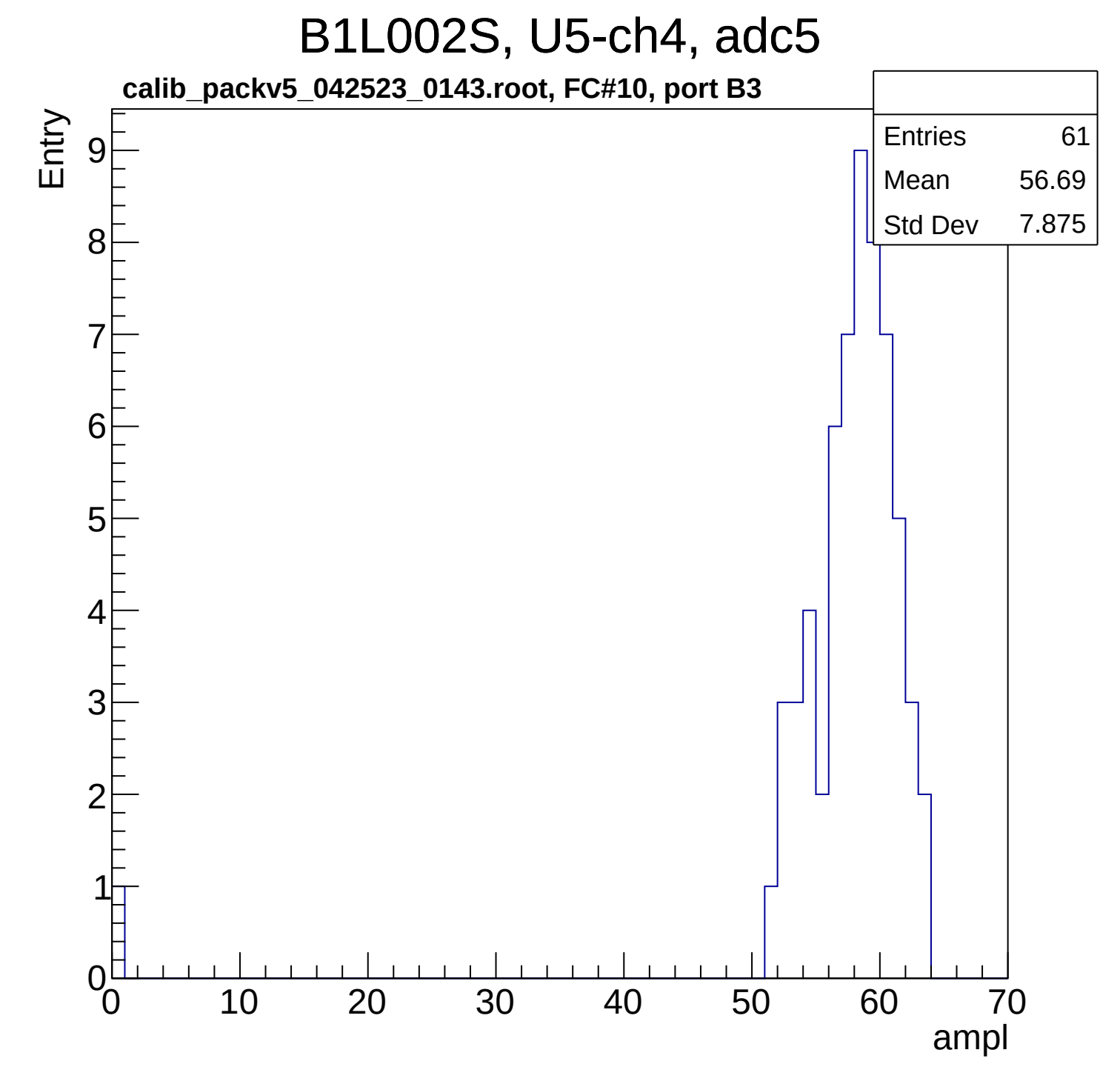
B1L002S, U5-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.69
Std Dev 7.875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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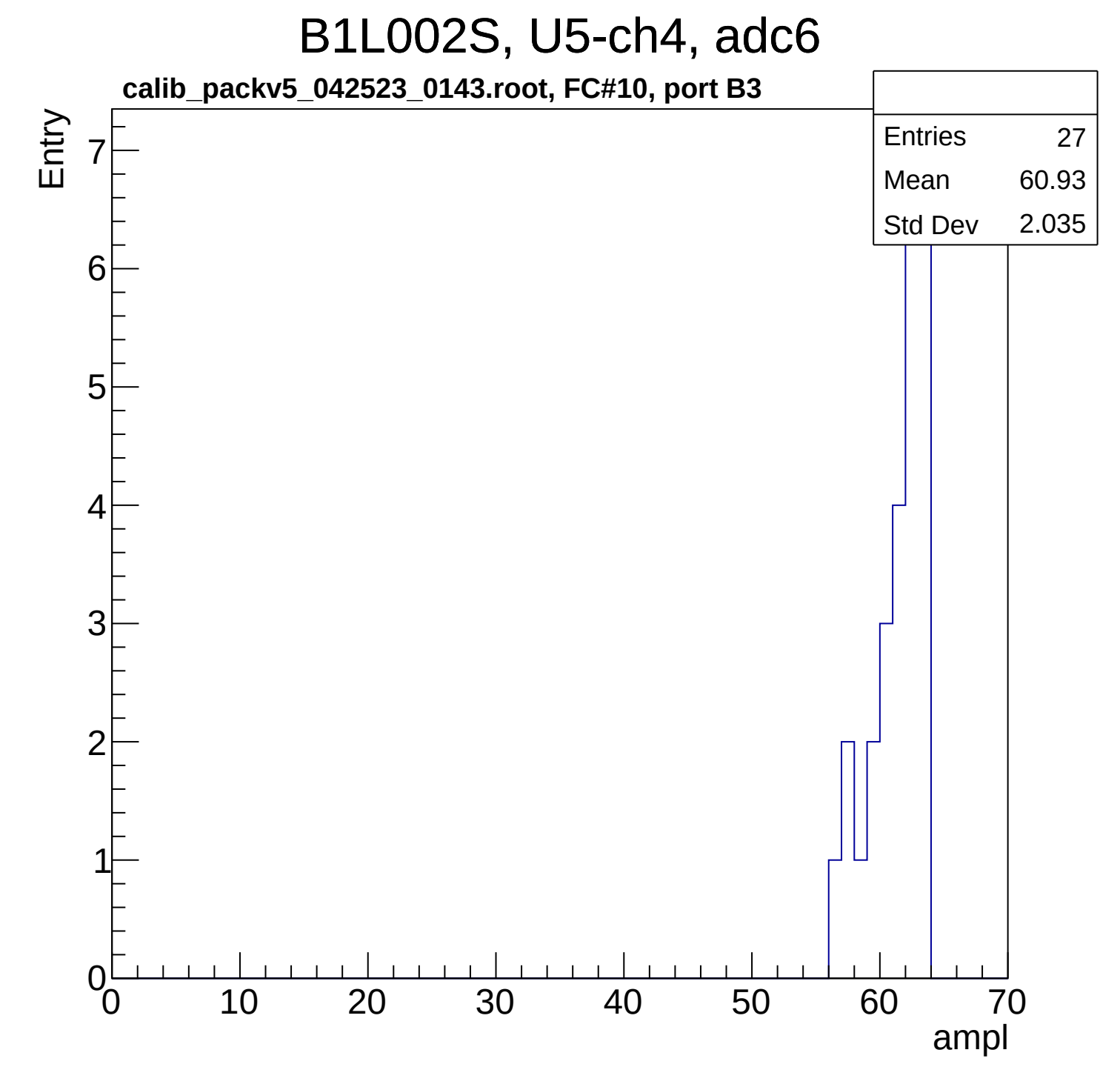
B1L002S, U5-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.93
Std Dev 2.035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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50
60
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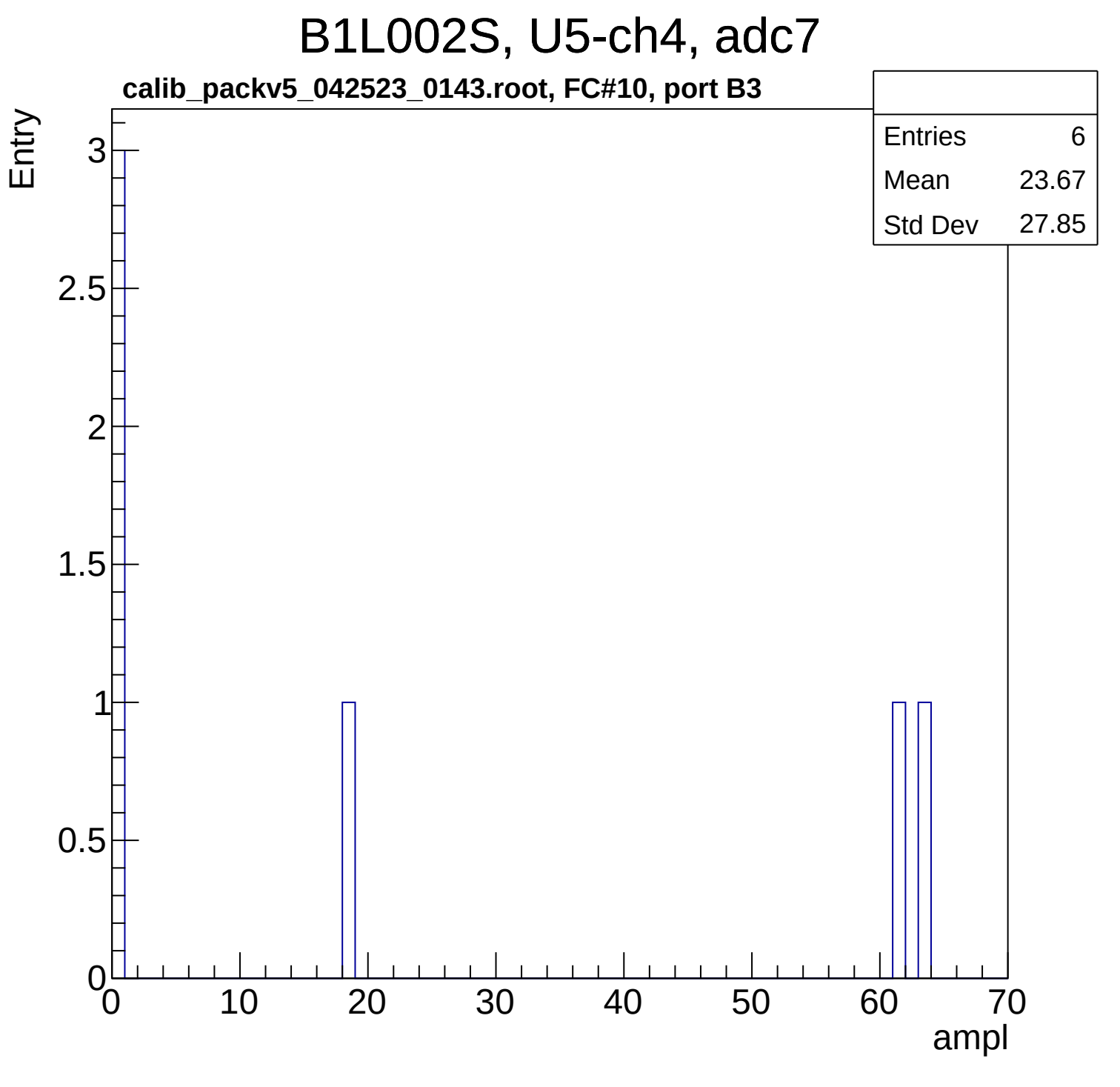
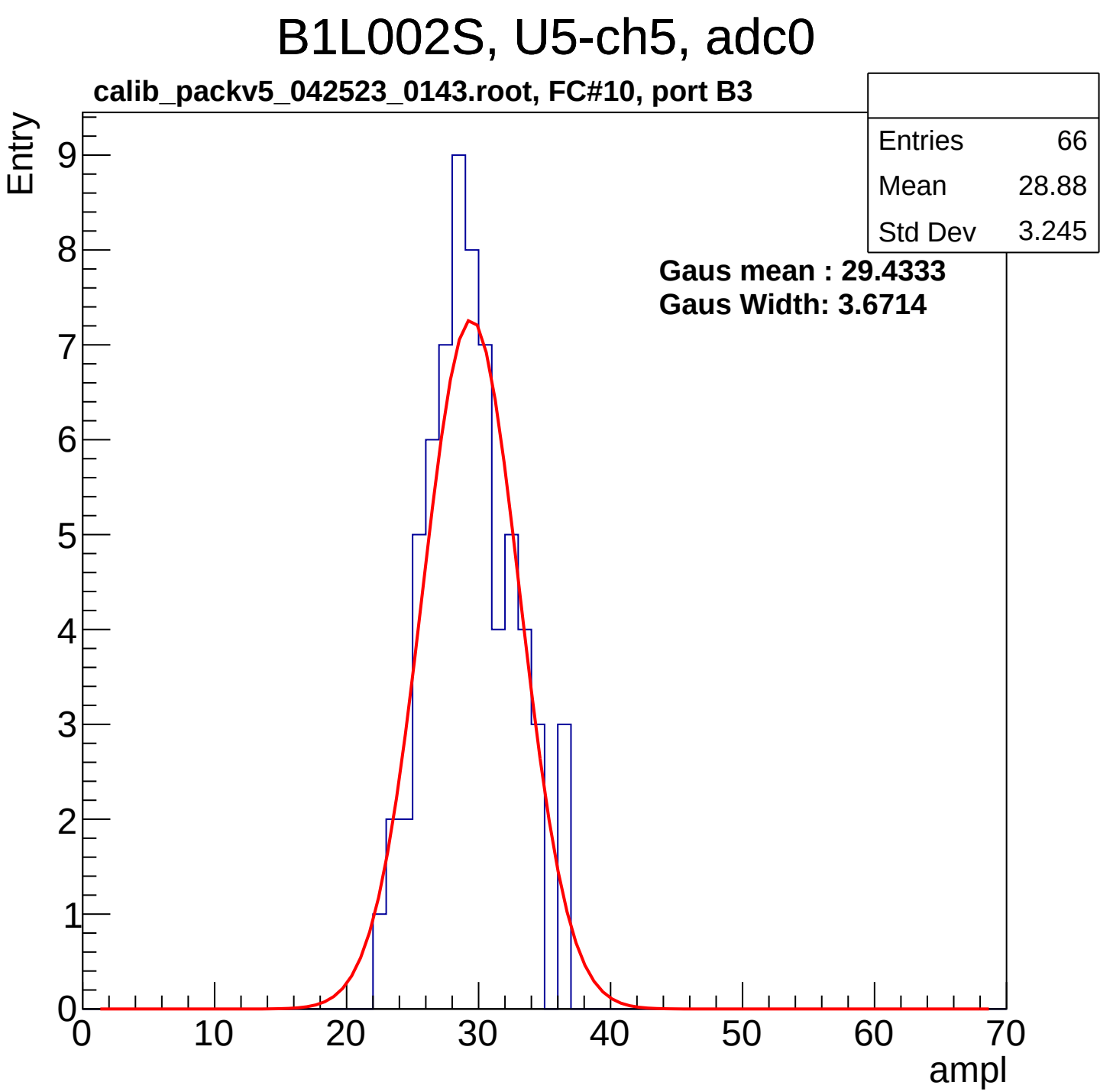
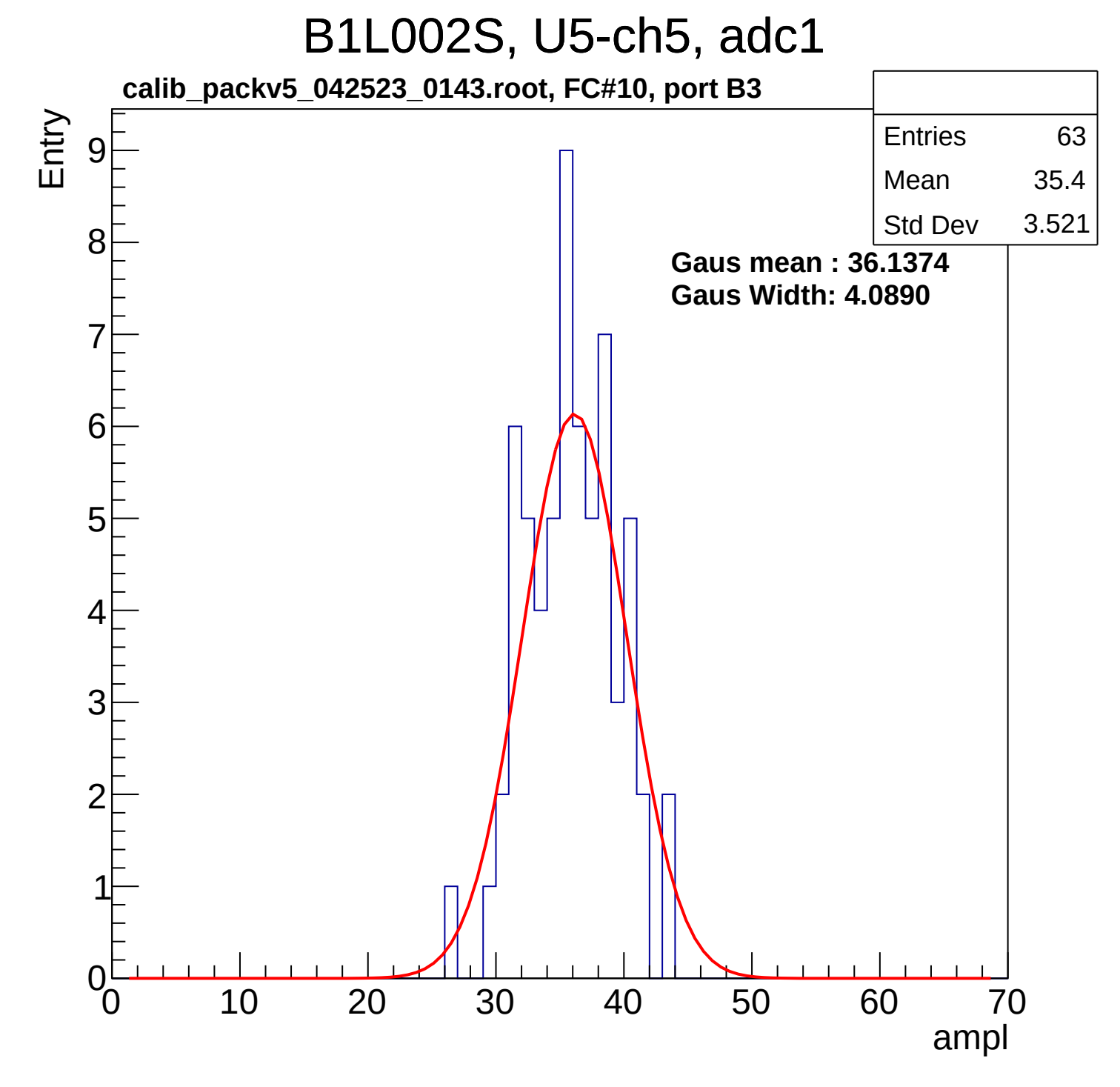

B1L002S, U5-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 23.67
Std Dev 27.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U5-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.88
Std Dev 3.245
Gaus mean : 29.4333
Gaus Width: 3.6714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.4
Std Dev 3.521
Gaus mean : 36.1374
Gaus Width: 4.0890
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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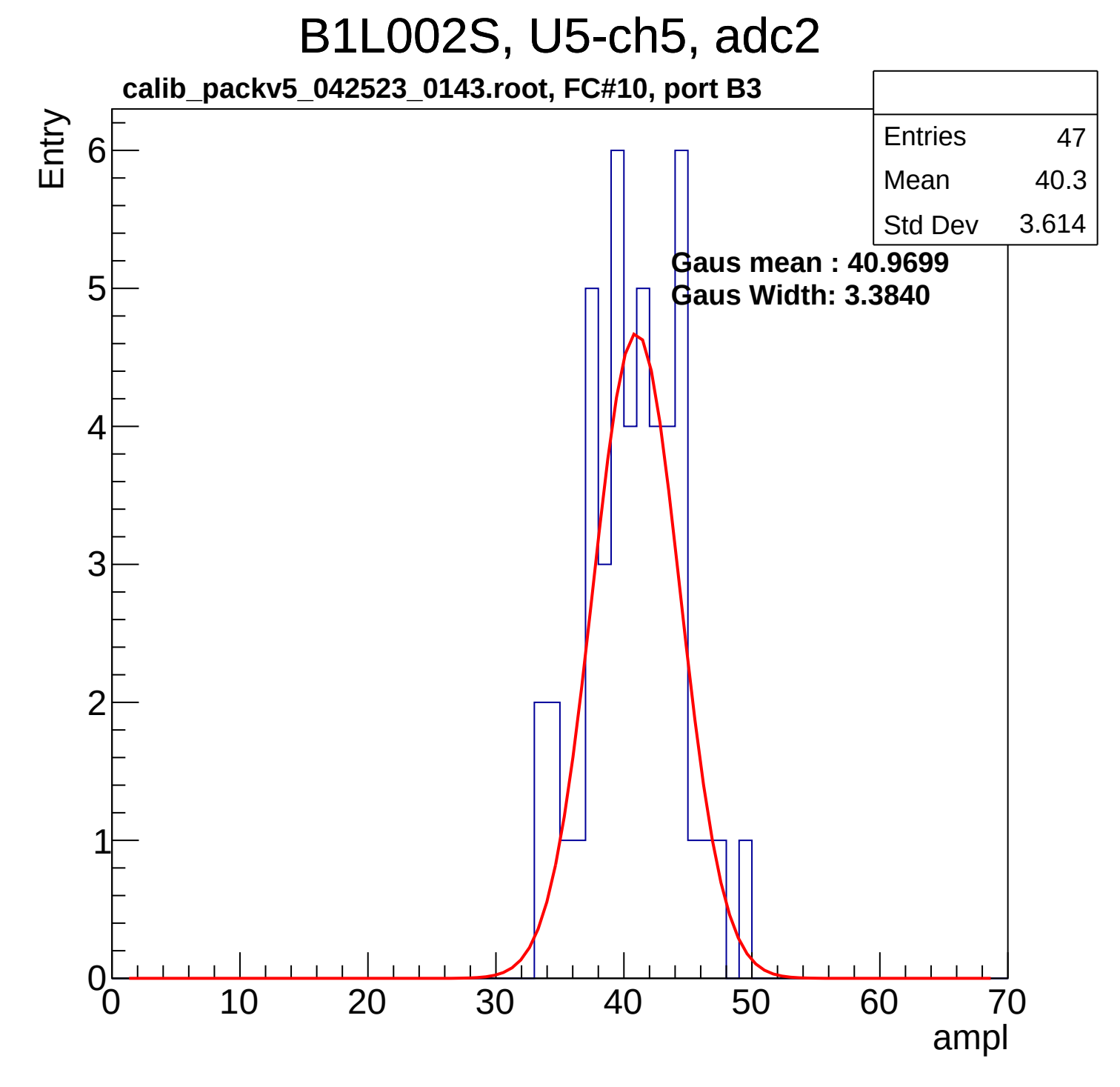
B1L002S, U5-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.3
Std Dev 3.614
Gaus mean : 40.9699
Gaus Width: 3.3840
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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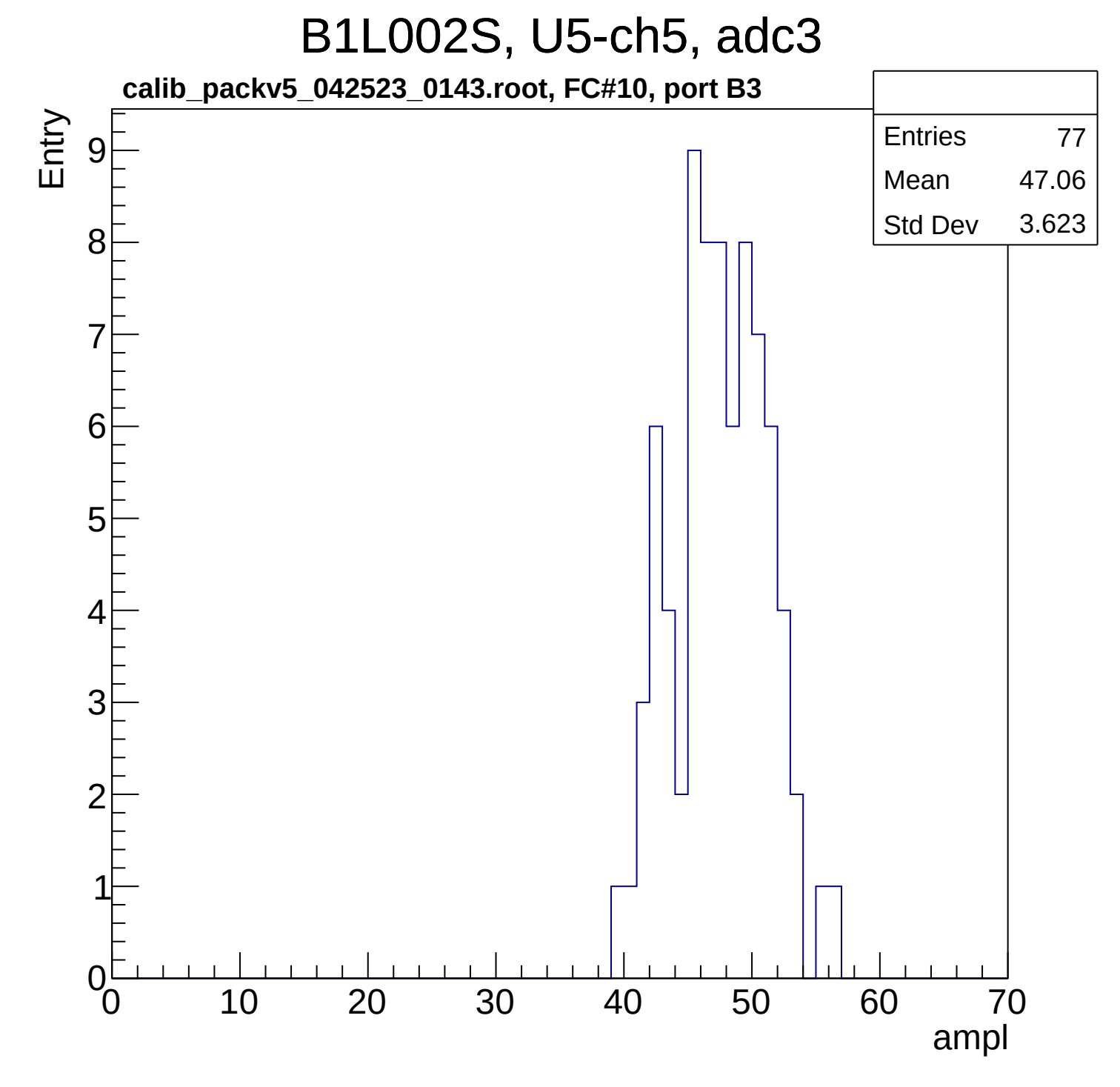
B1L002S, U5-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 47.06
Std Dev 3.623
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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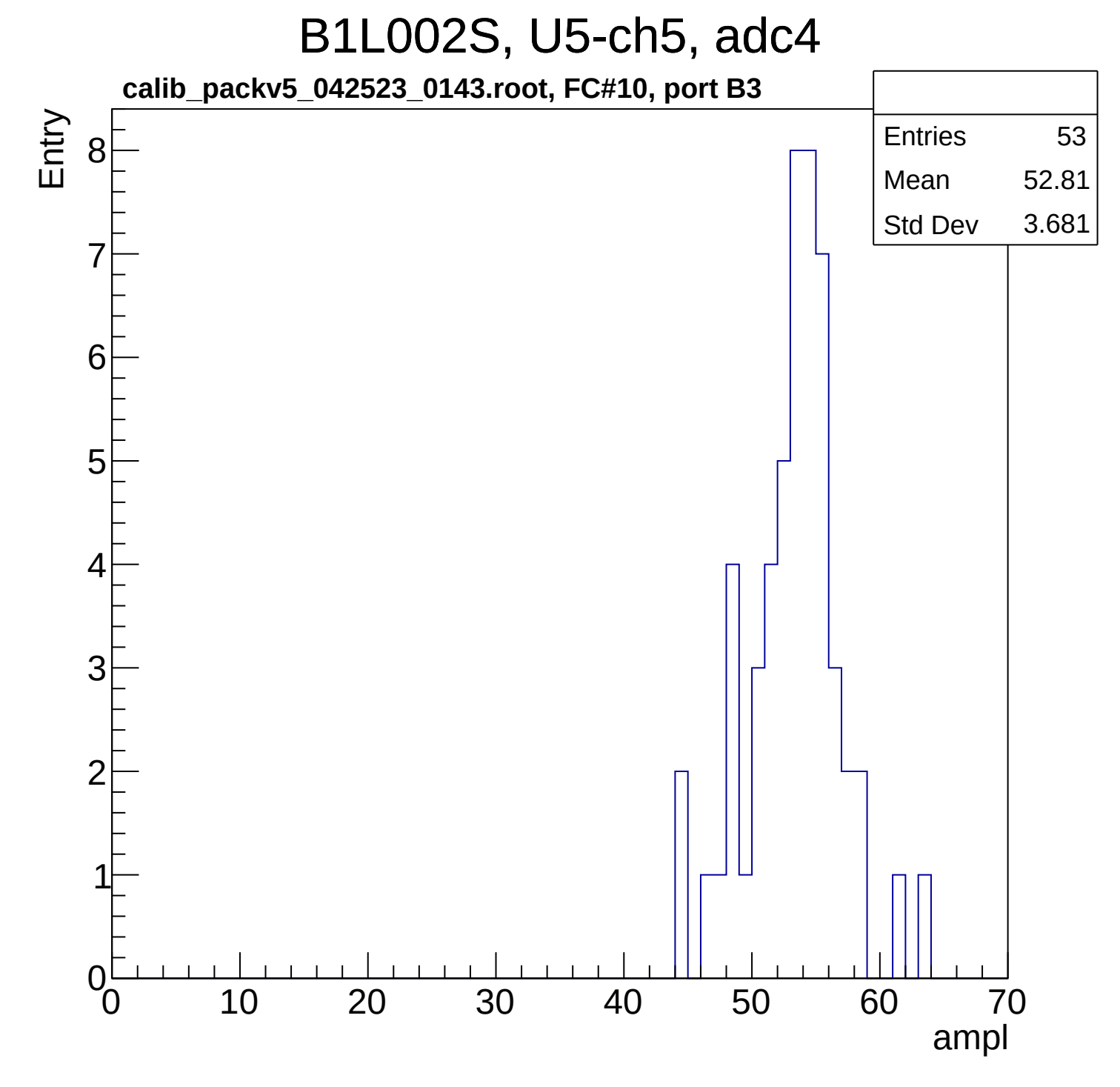
B1L002S, U5-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.81
Std Dev 3.681
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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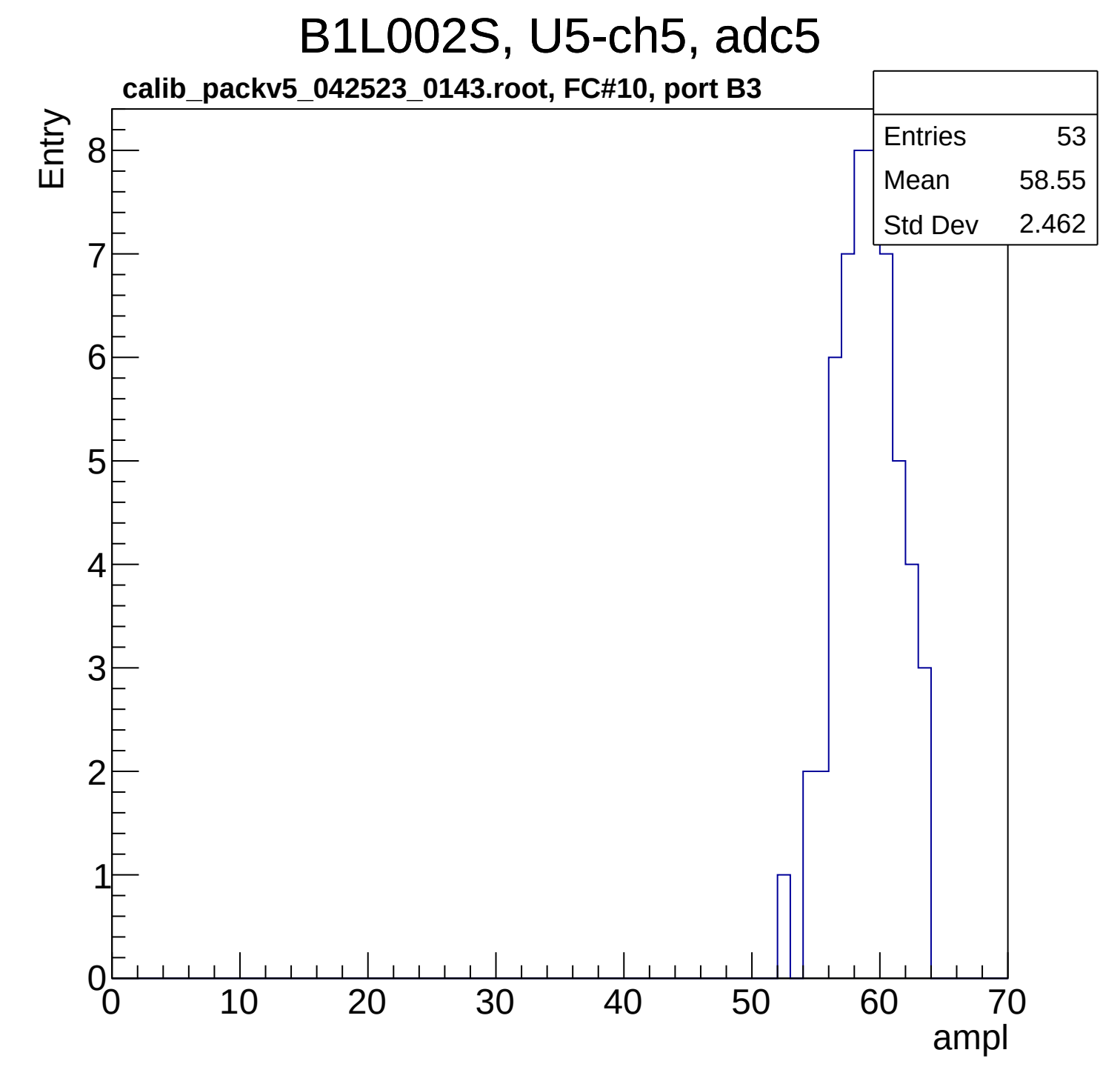
B1L002S, U5-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.55
Std Dev 2.462
Entry
ampl
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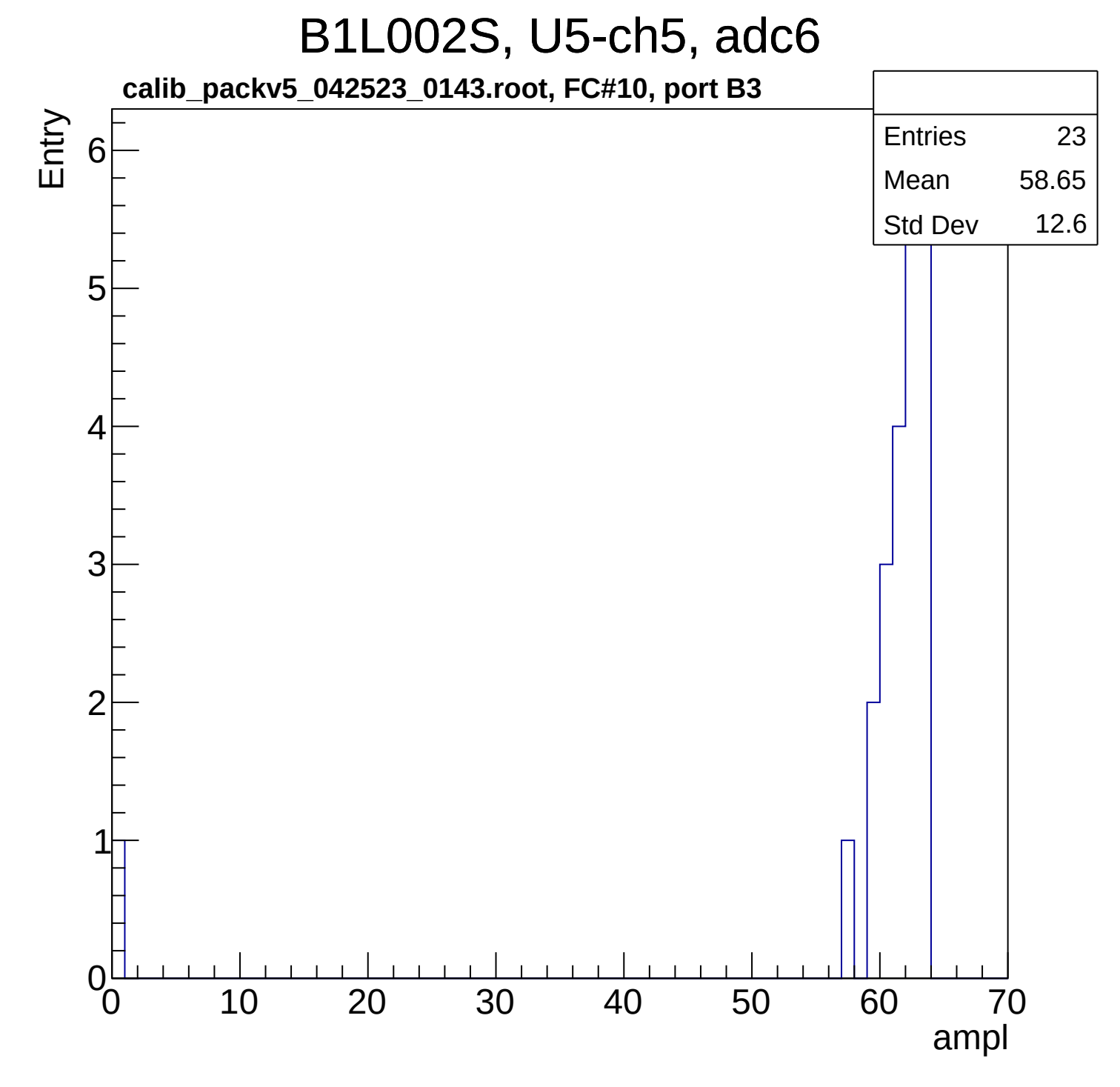
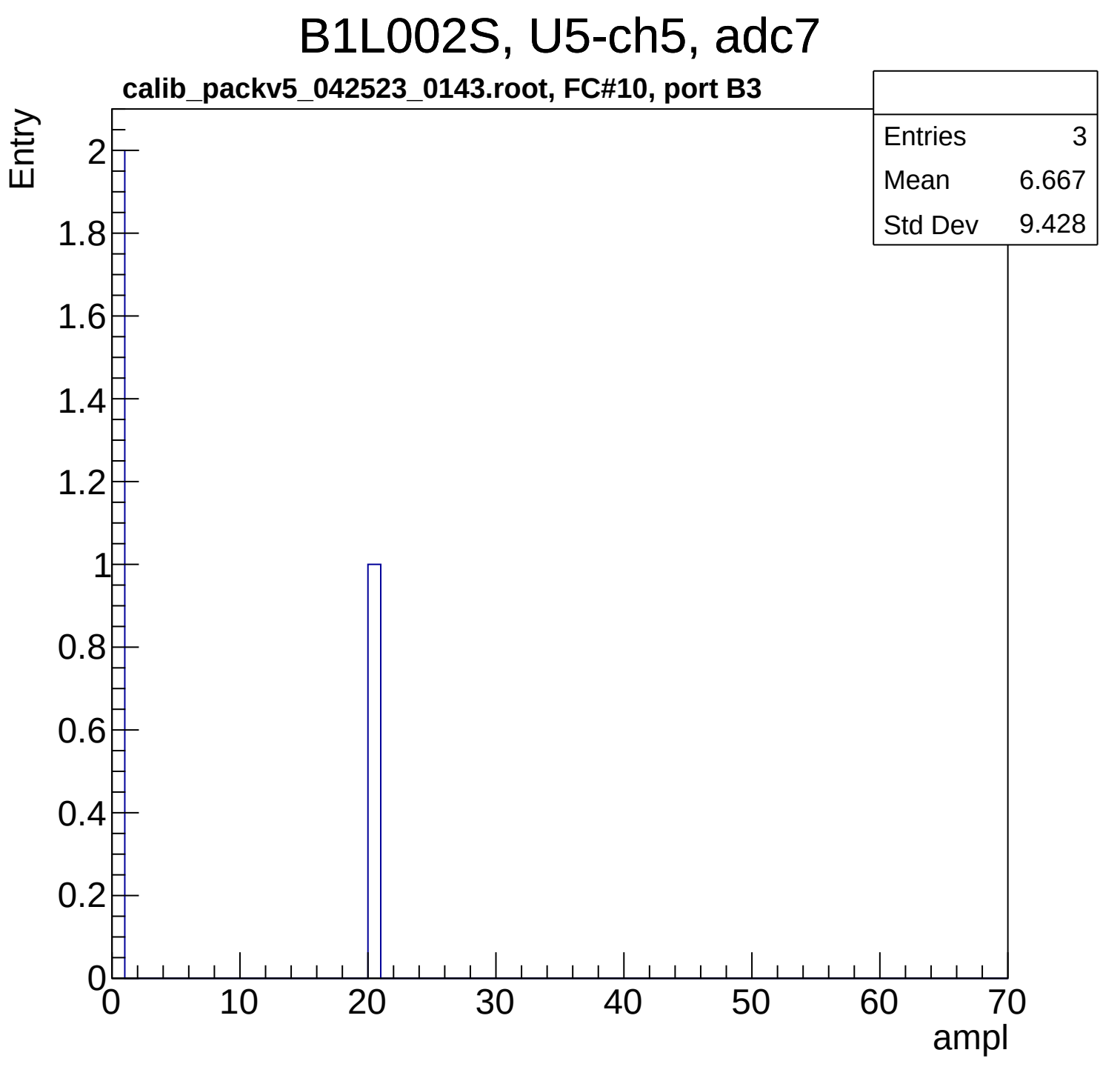
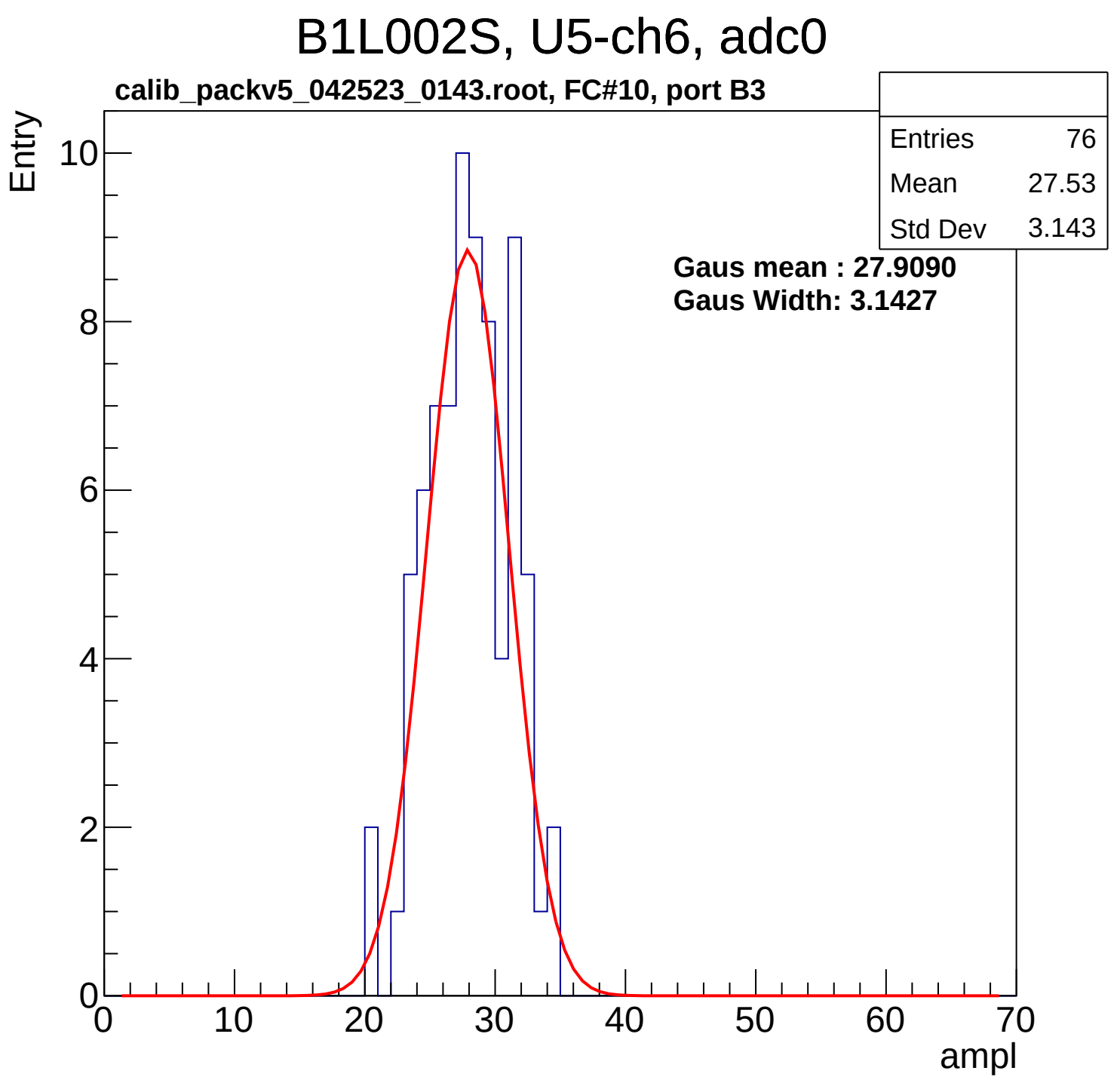
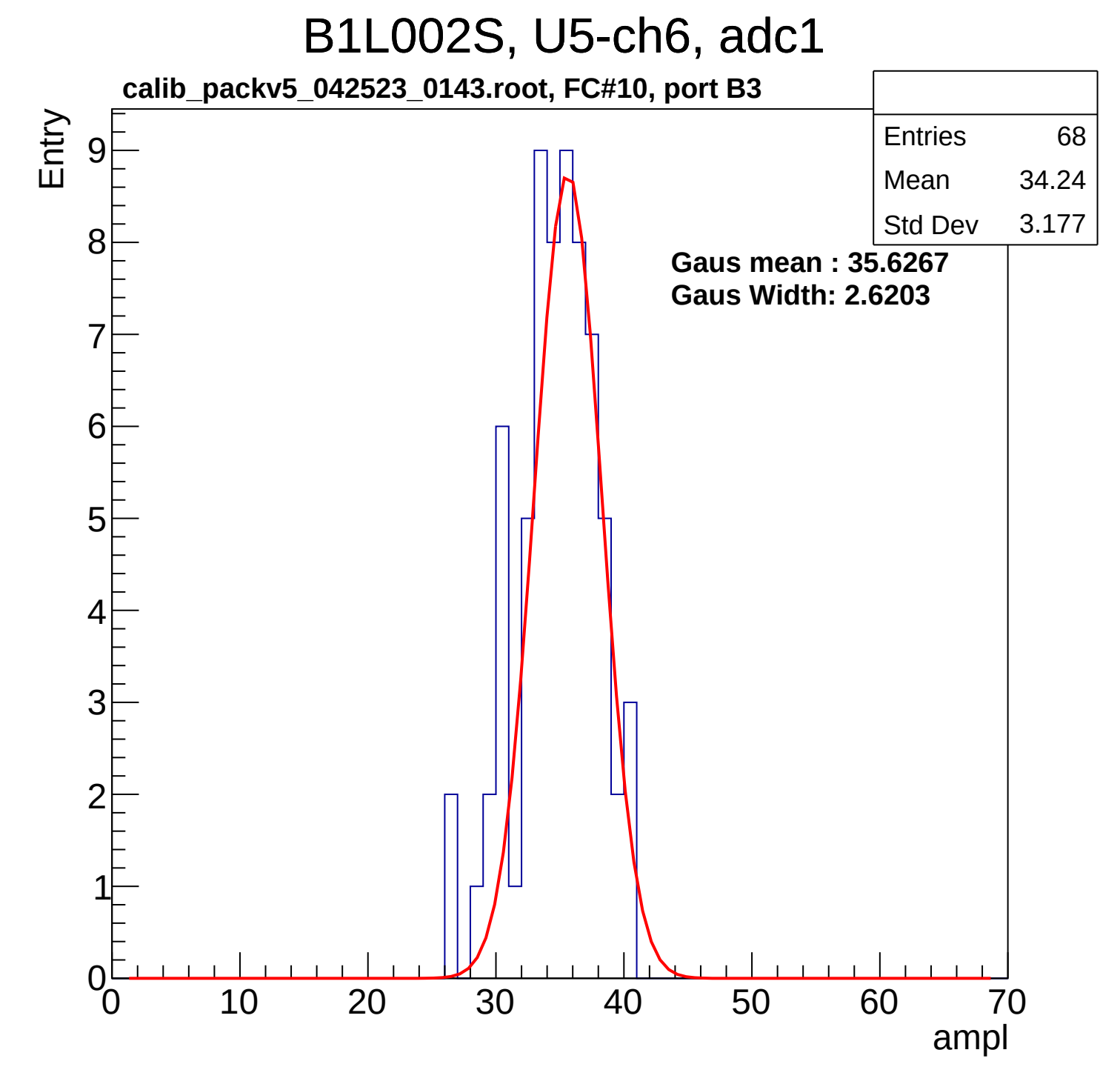
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.65
Std Dev 12.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.53
Std Dev 3.143
Gaus mean : 27.9090
Gaus Width: 3.1427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.24
Std Dev 3.177
Gaus mean : 35.6267
Gaus Width: 2.6203
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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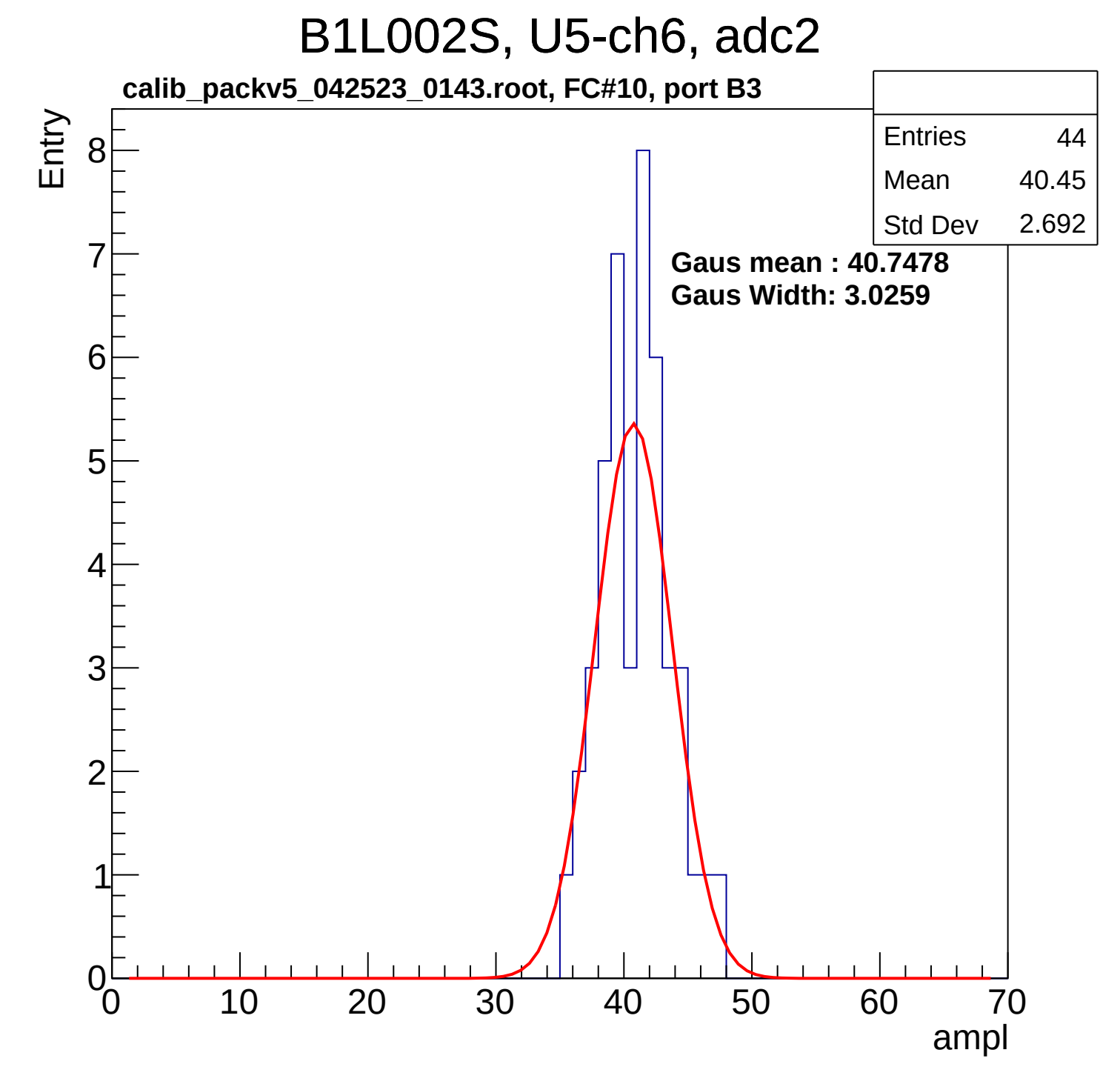
60
70


B1L002S, U5-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 40.45
Std Dev 2.692
Gaus mean : 40.7478
Gaus Width: 3.0259
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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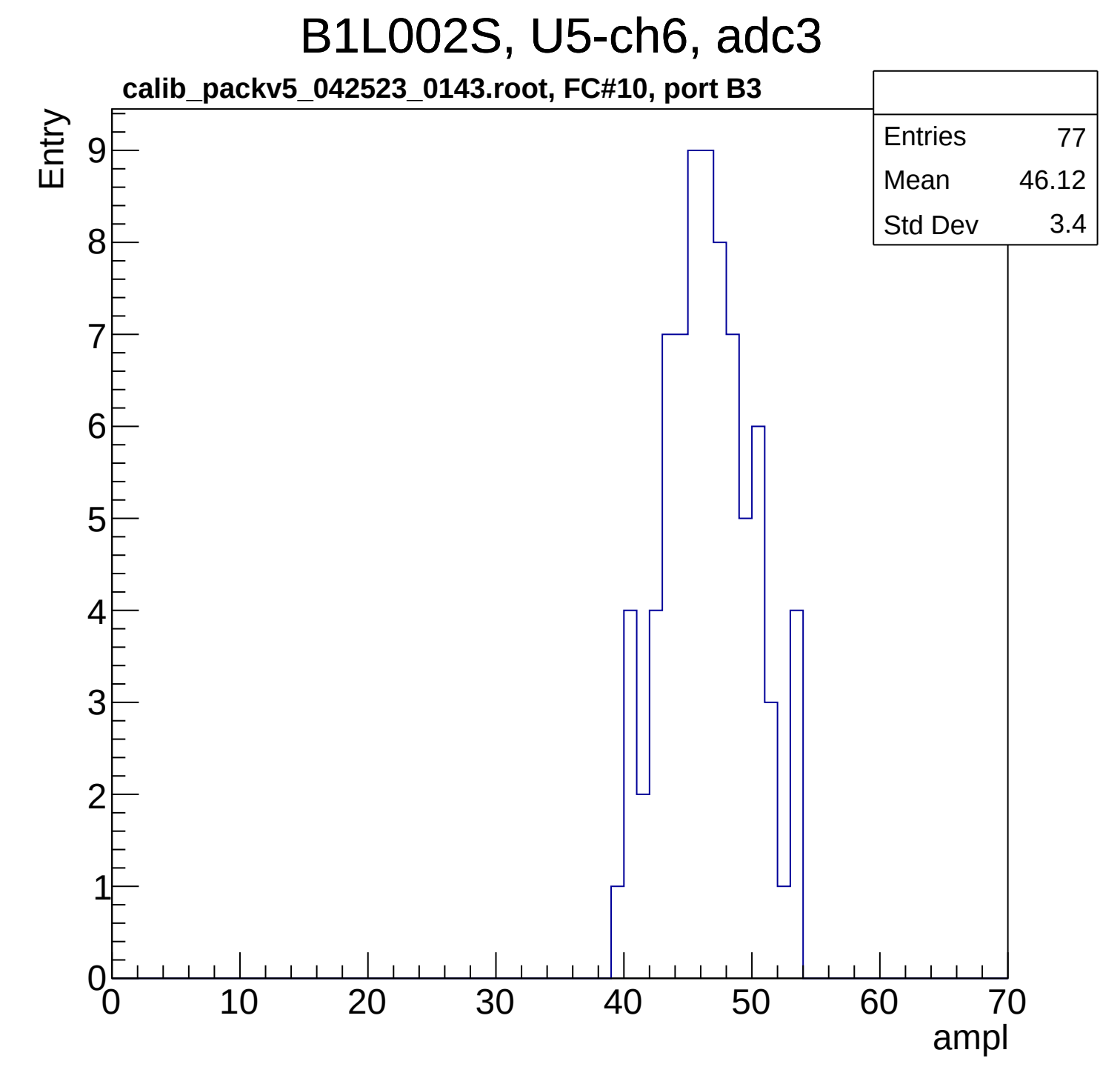
B1L002S, U5-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 46.12
Std Dev 3.4
Entry
ampl
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2
3
4
5
6
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8
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0
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60
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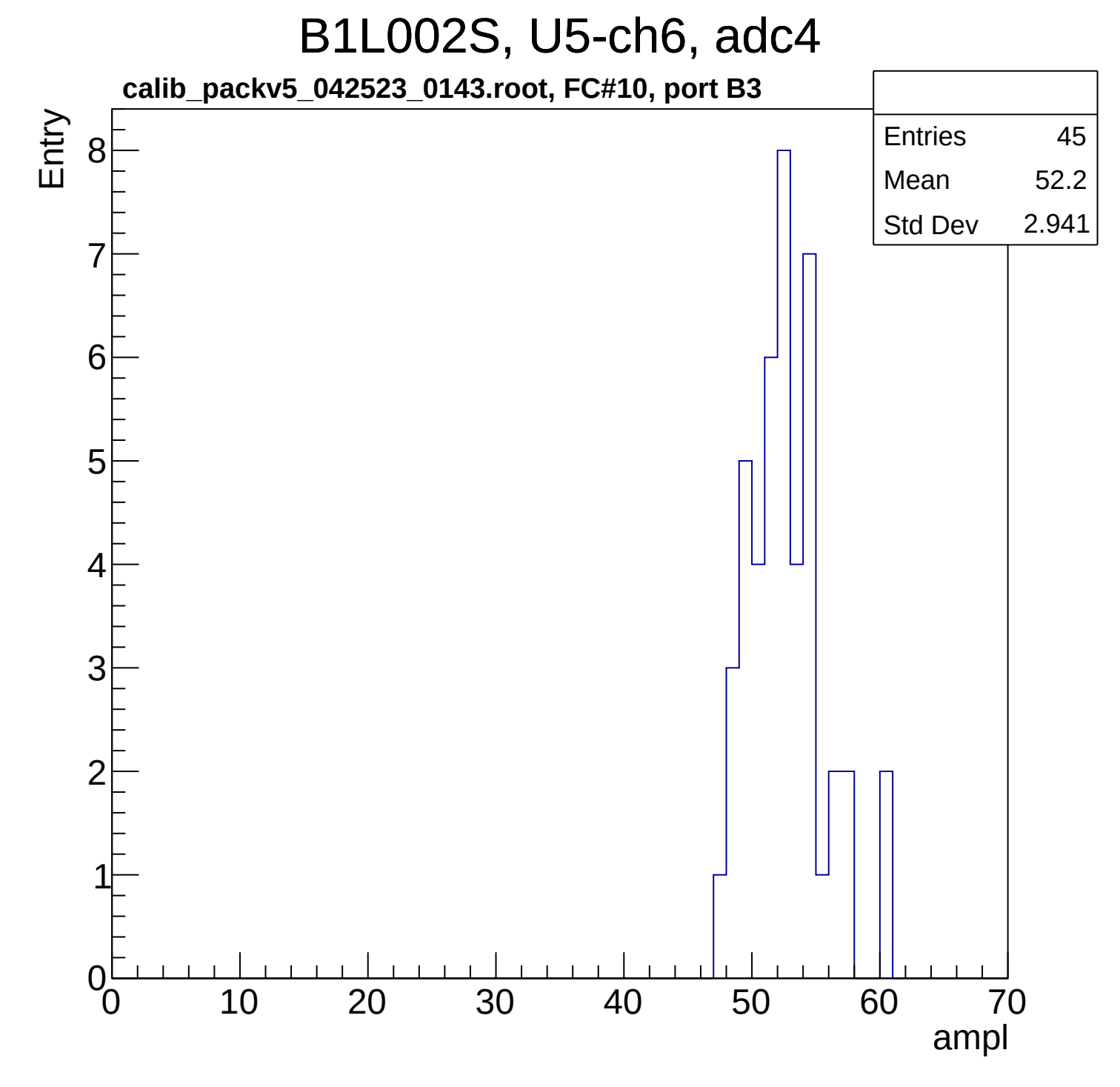
B1L002S, U5-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52.2
Std Dev 2.941
Entry
ampl
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2
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7
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0
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60
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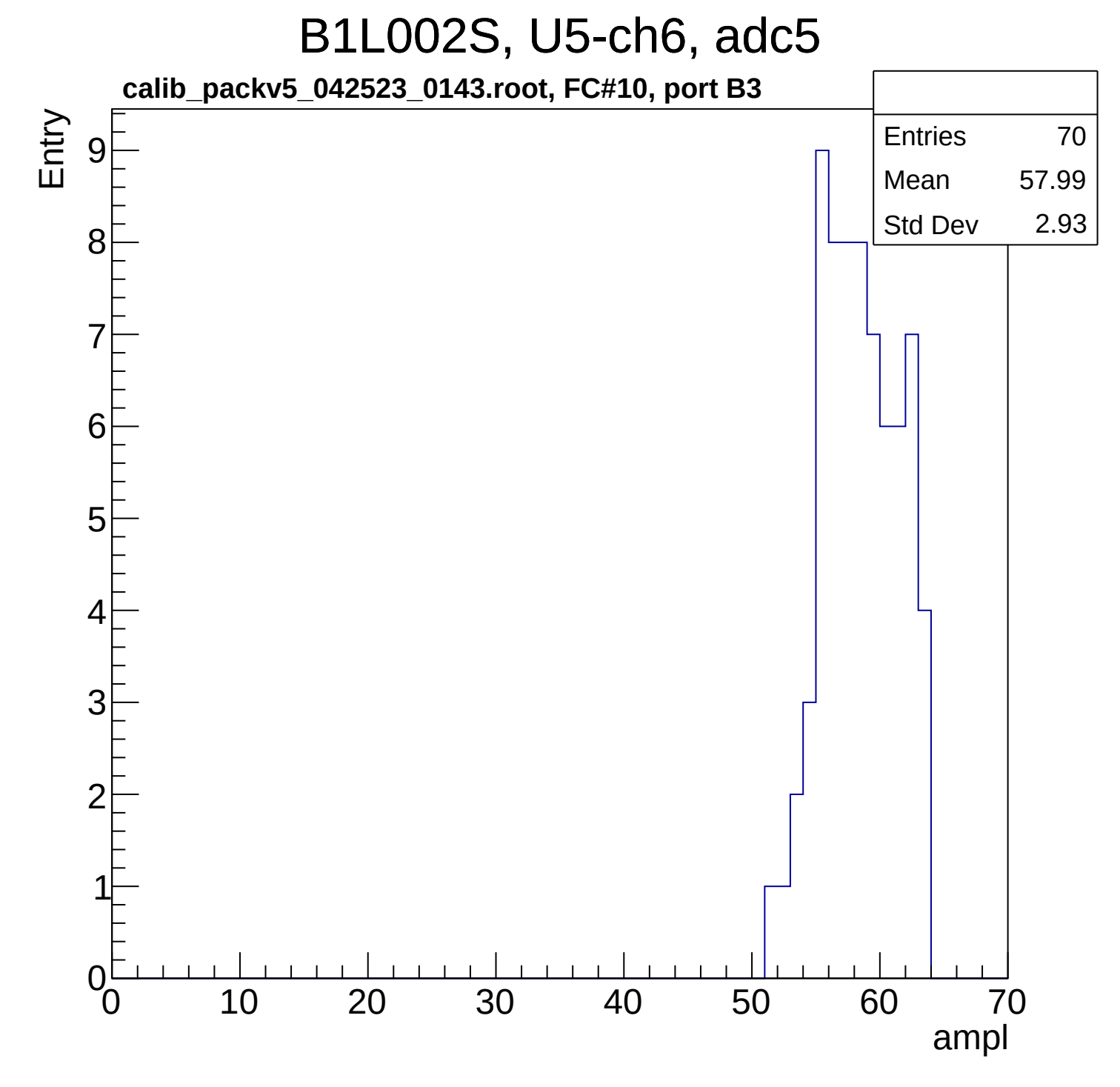
B1L002S, U5-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 57.99
Std Dev 2.93
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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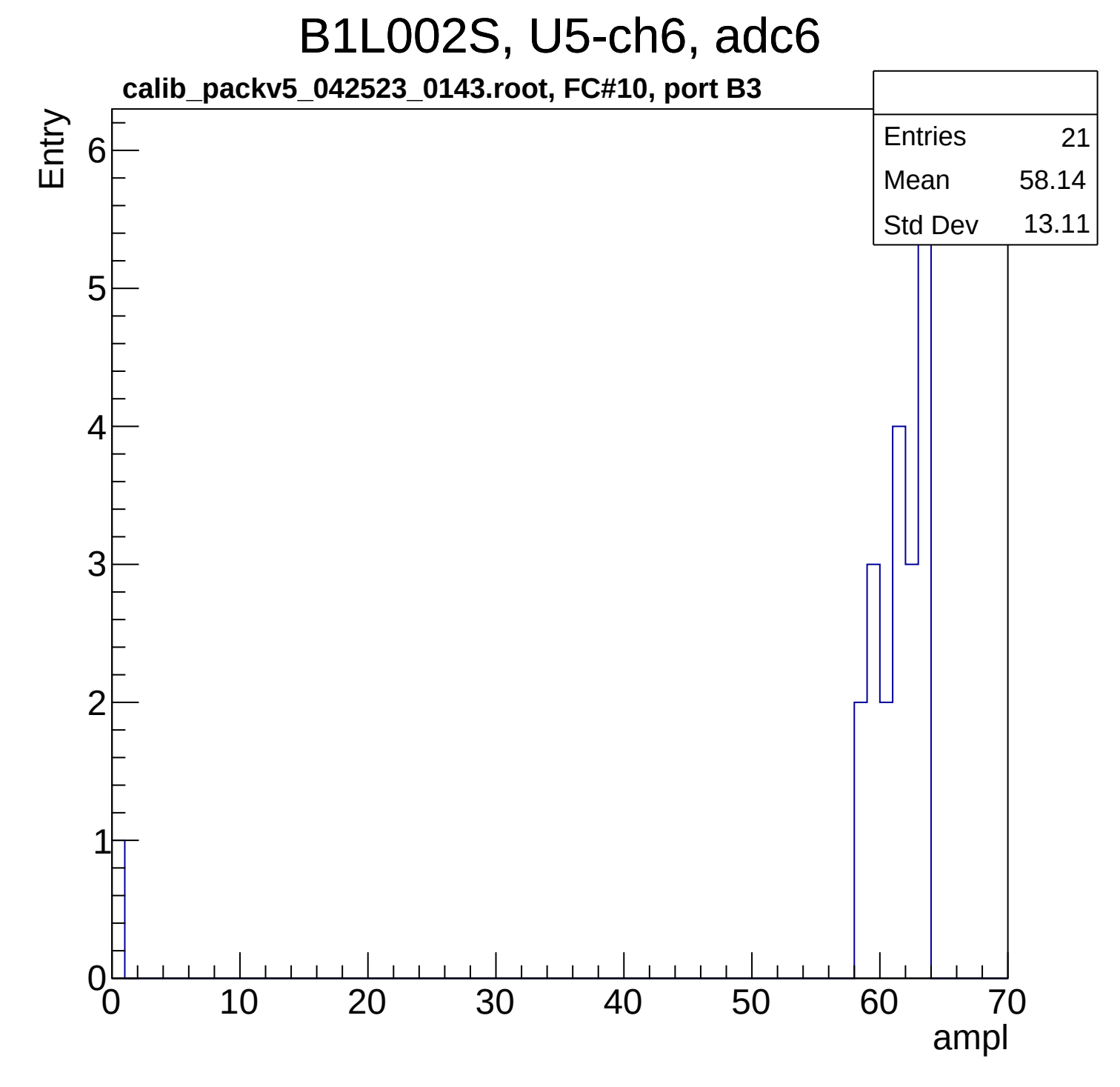
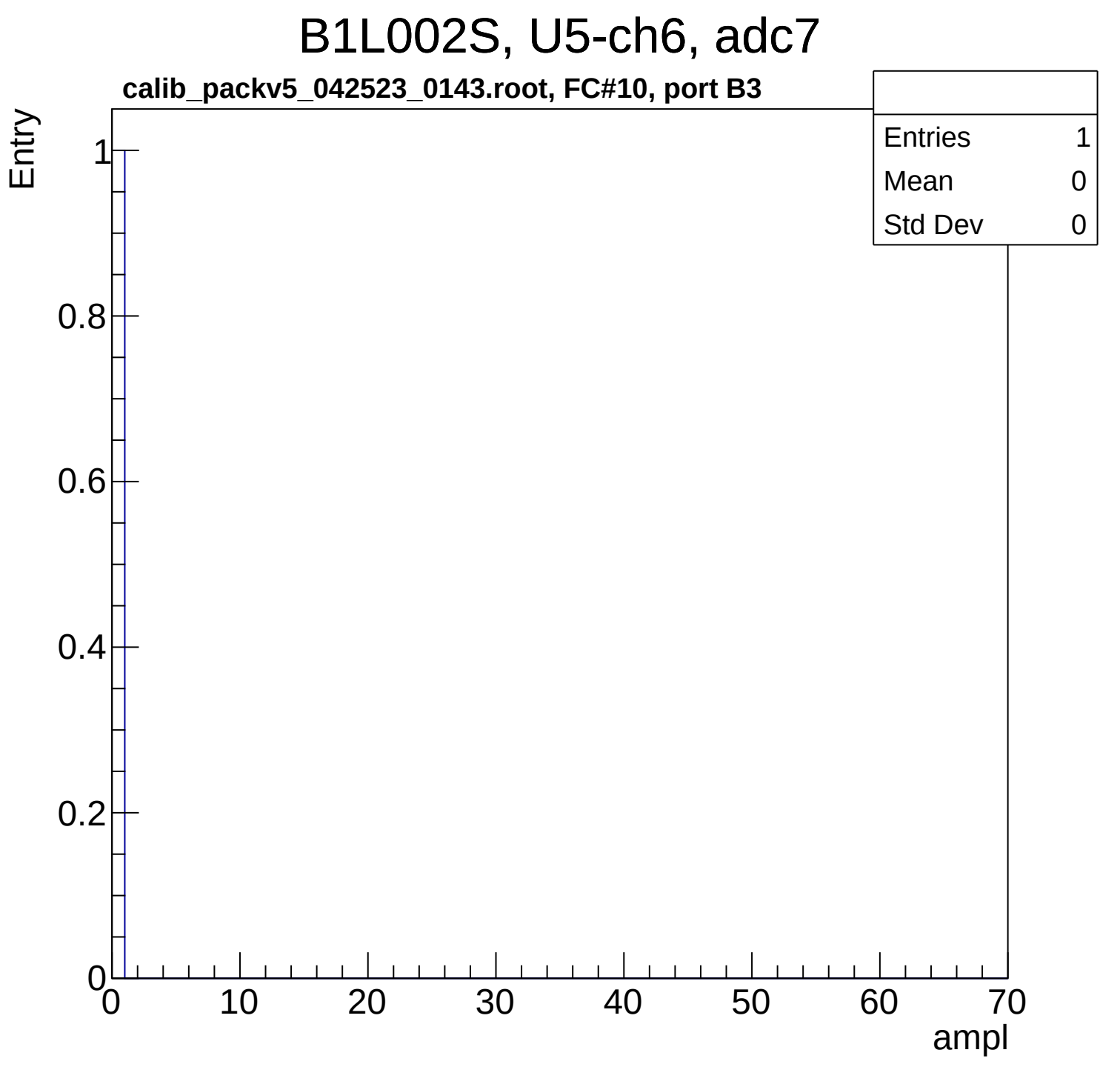

B1L002S, U5-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.14
Std Dev 13.11
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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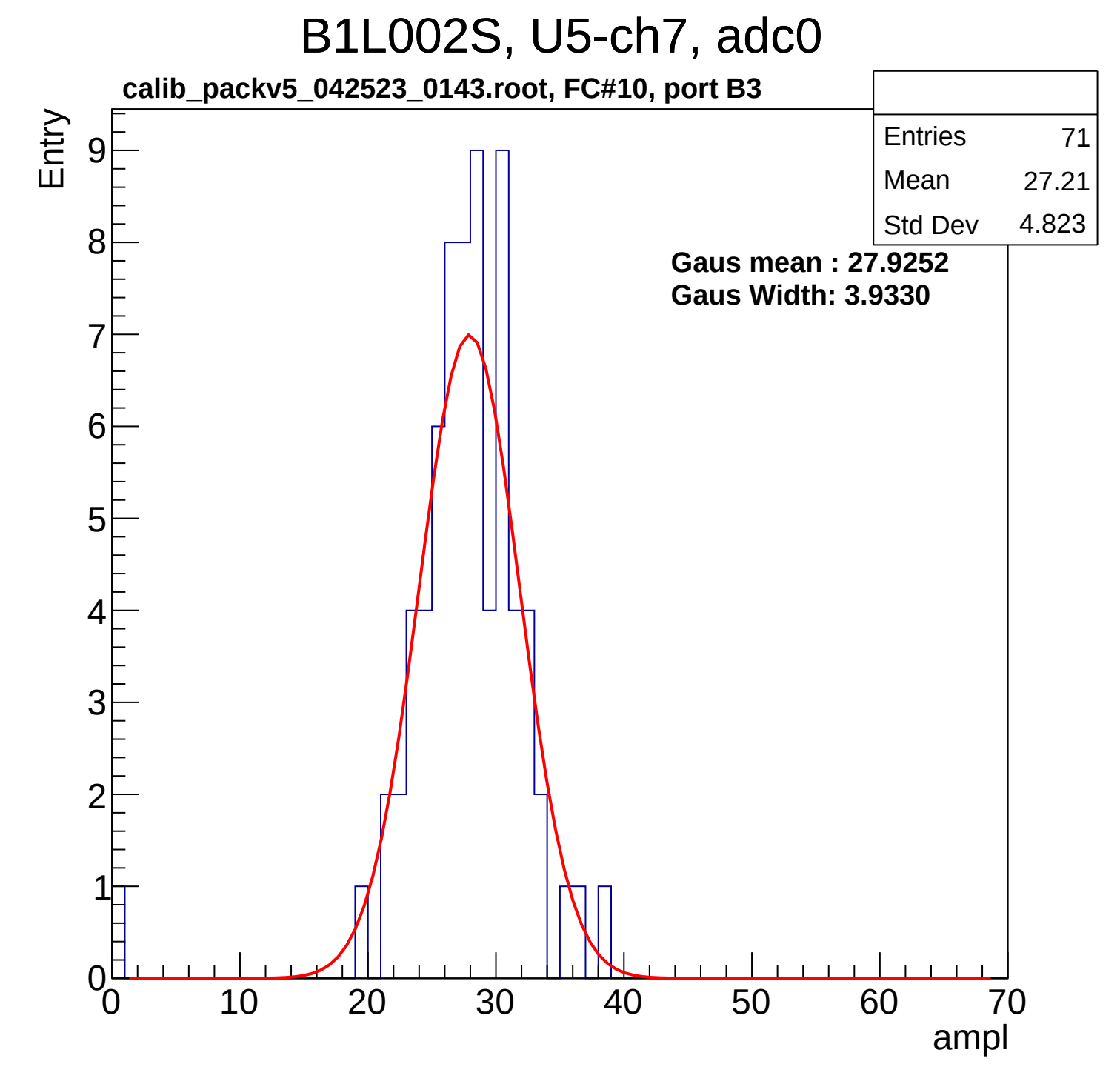
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.21
Std Dev 4.823
Gaus mean : 27.9252
Gaus Width: 3.9330
Entry
ampl
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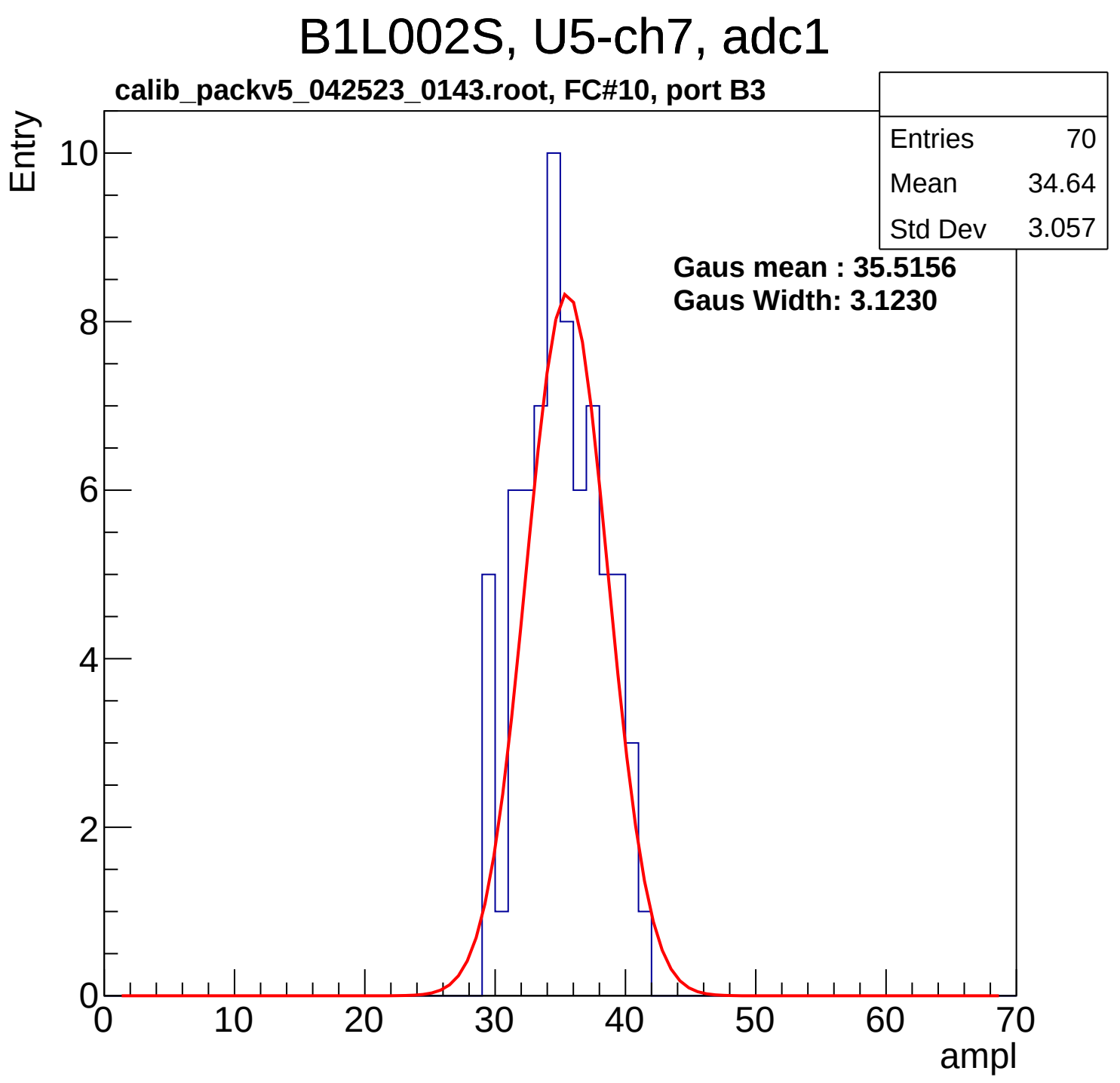
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.64
Std Dev 3.057
Gaus mean : 35.5156
Gaus Width: 3.1230
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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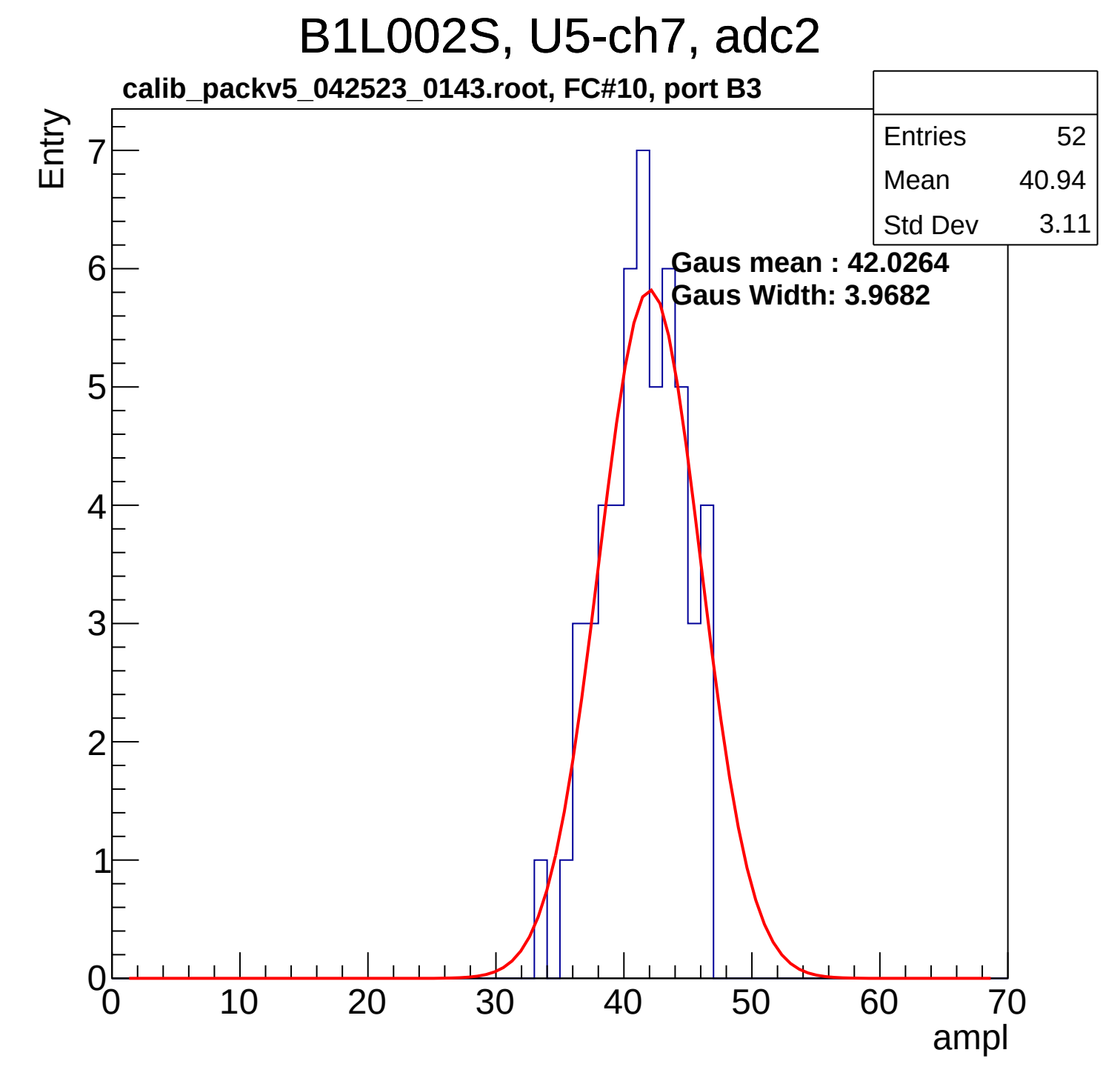
B1L002S, U5-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.94
Std Dev 3.11
Gaus mean : 42.0264
Gaus Width: 3.9682
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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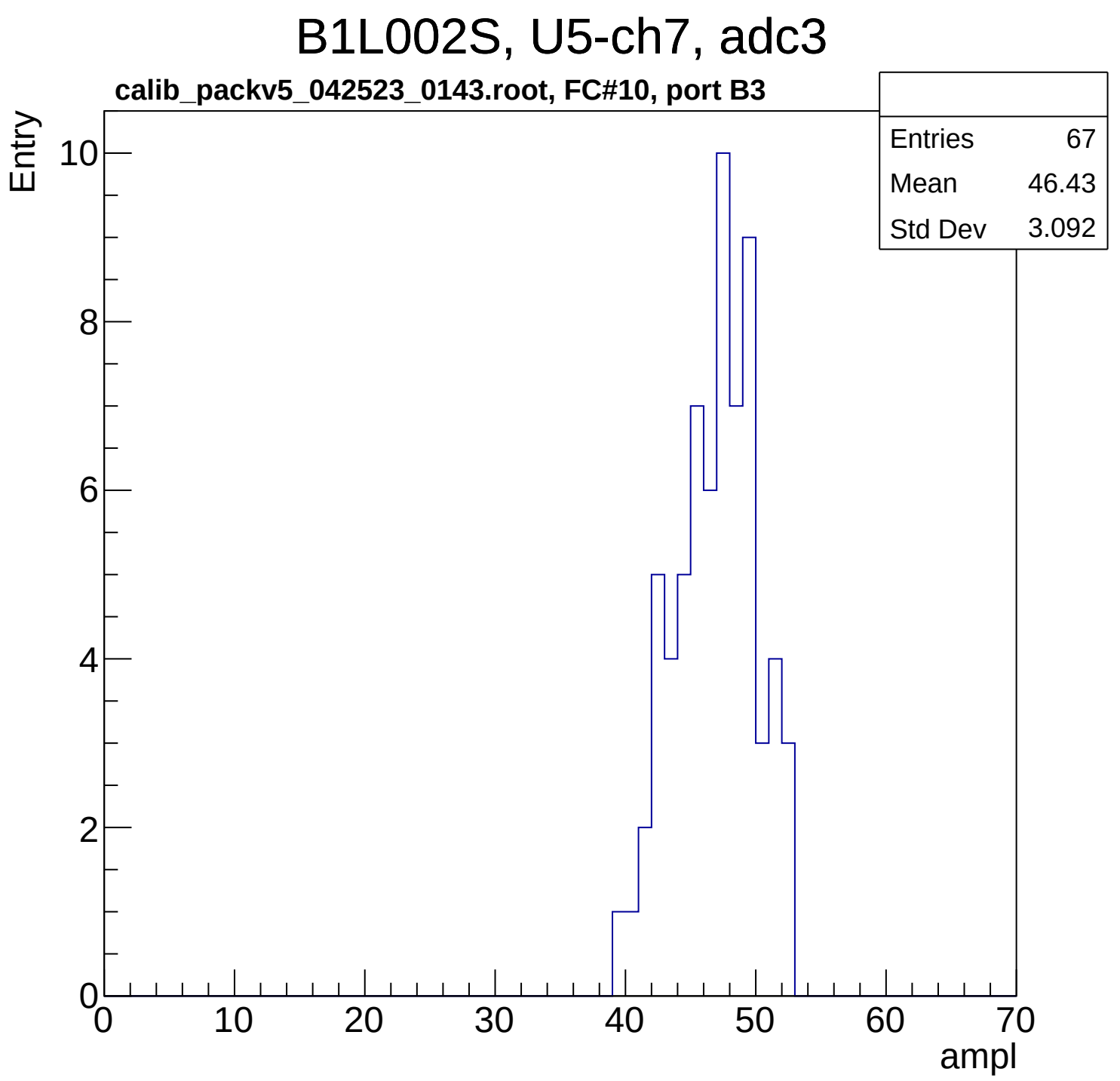
B1L002S, U5-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.43
Std Dev 3.092
Entry
0
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4
6
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10
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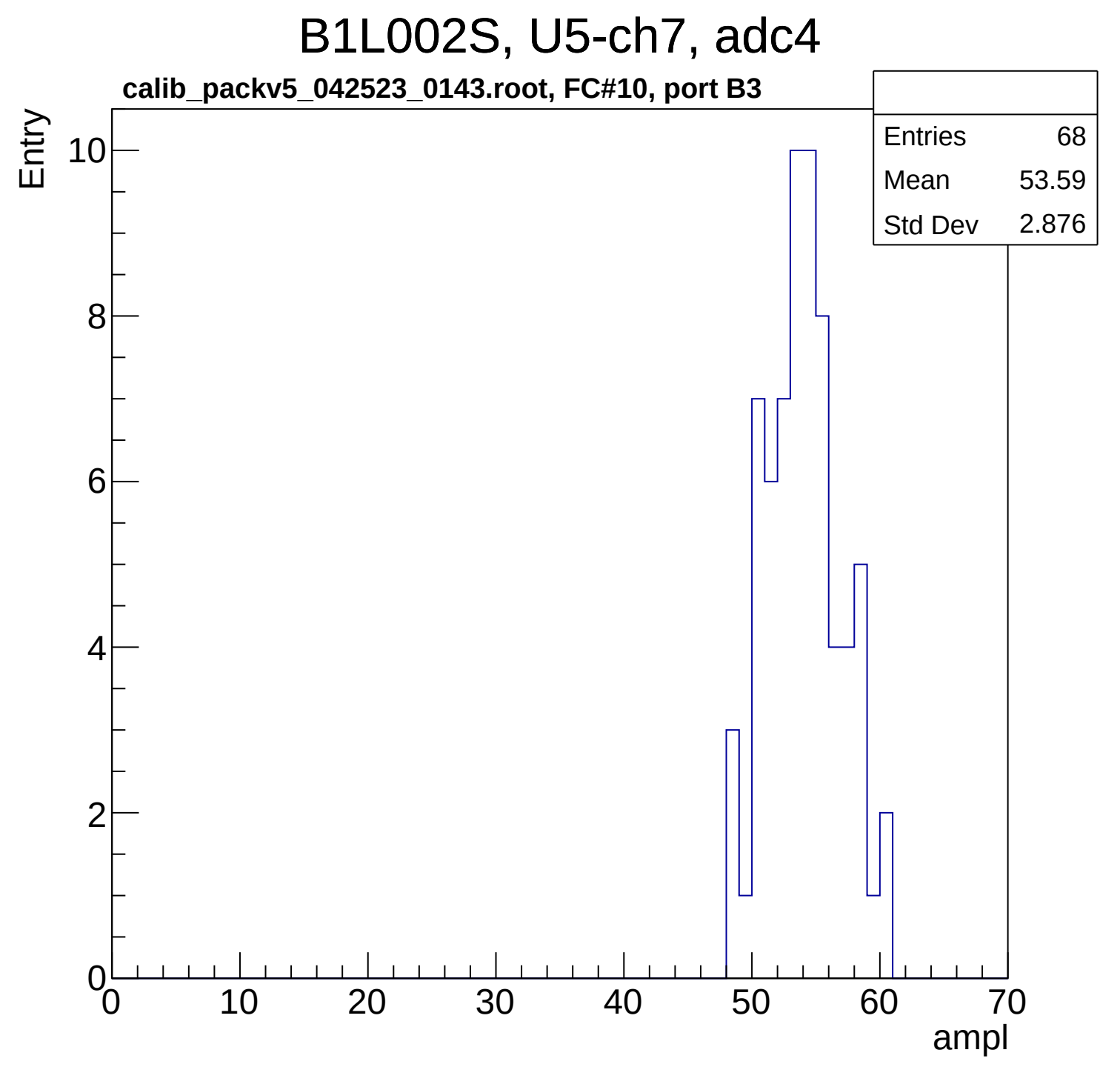
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.59
Std Dev 2.876
Entry
ampl
0
2
4
6
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10
0
10
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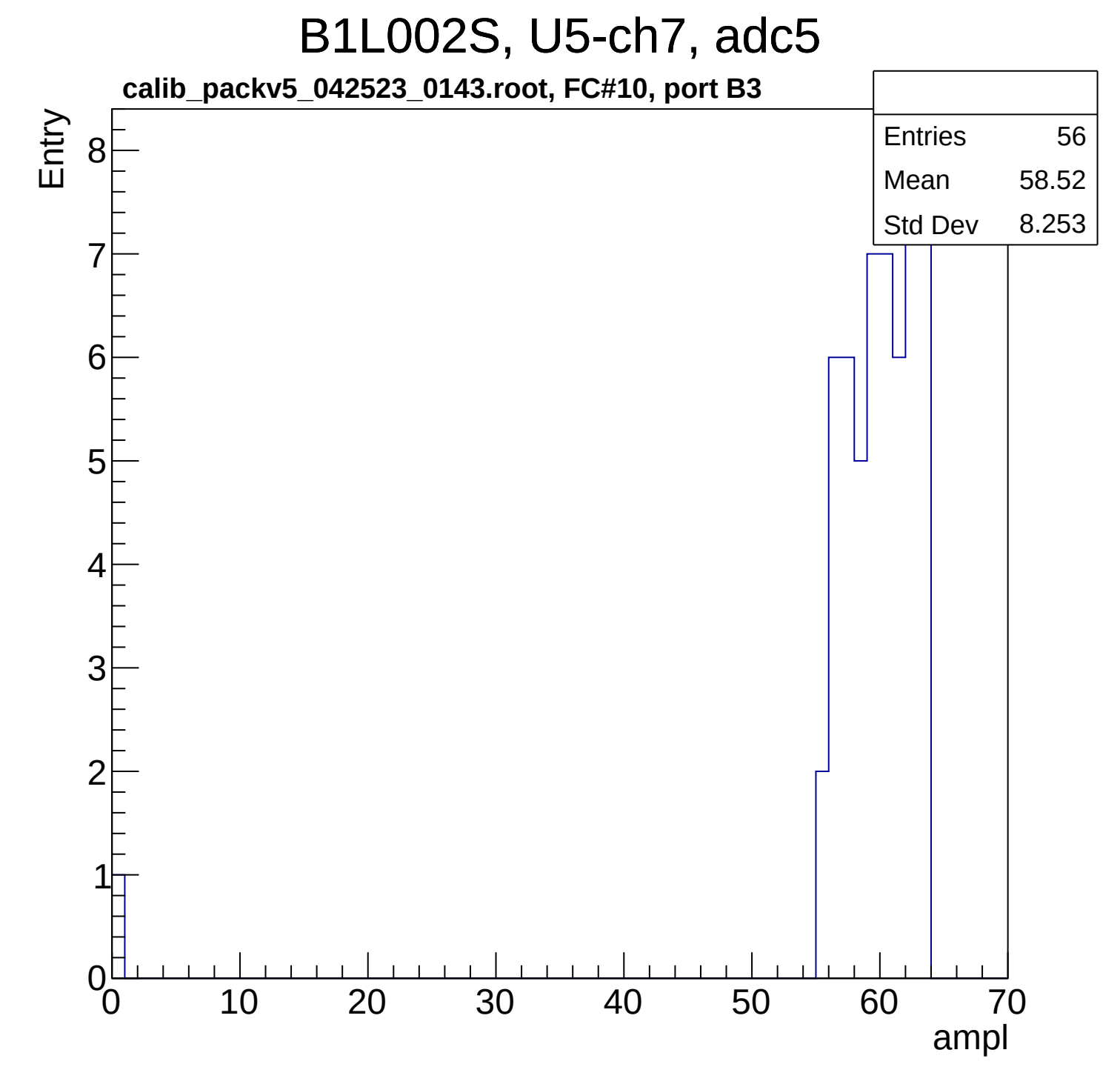
30
40
50
60
70


B1L002S, U5-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.52
Std Dev 8.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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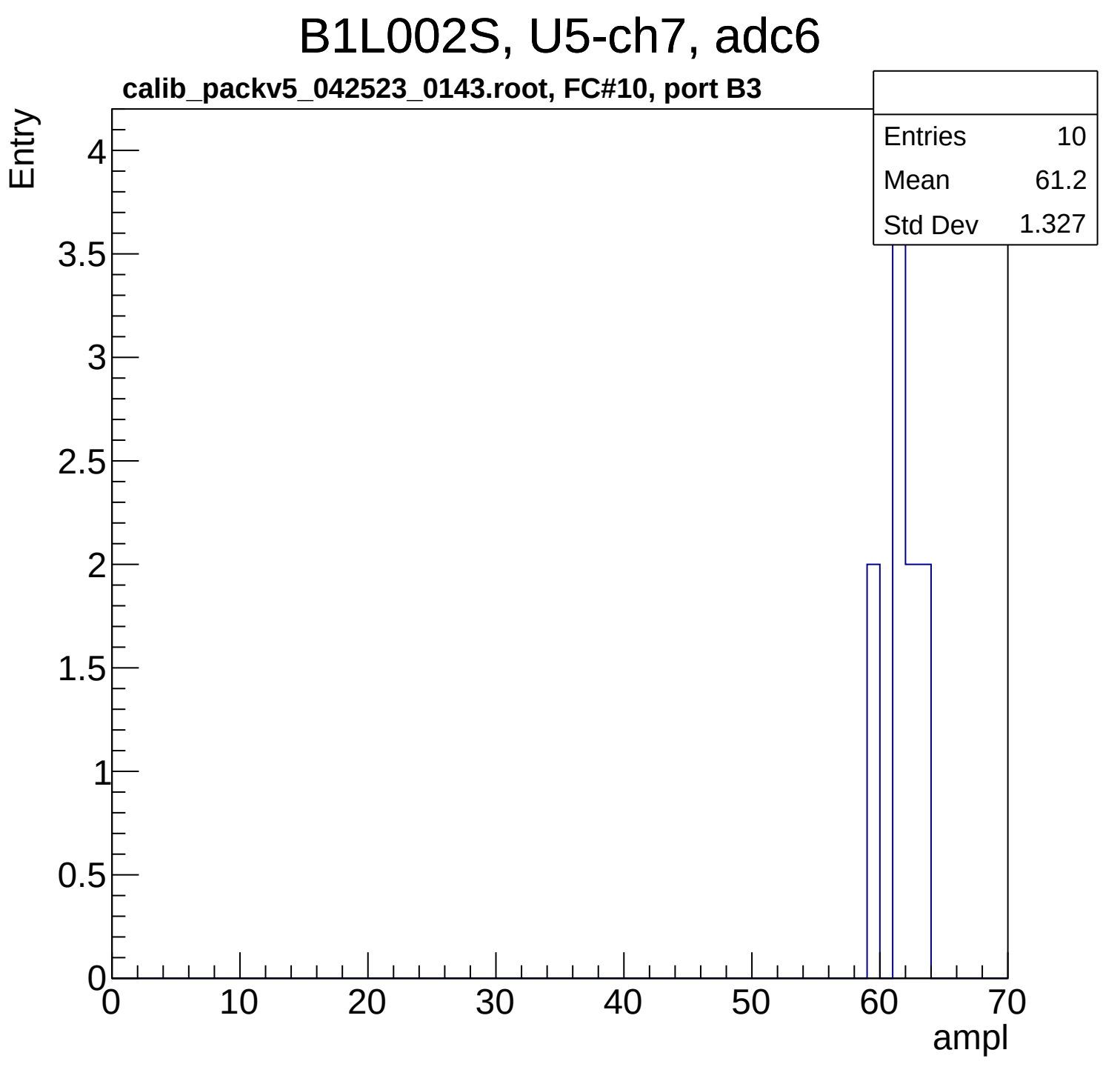
B1L002S, U5-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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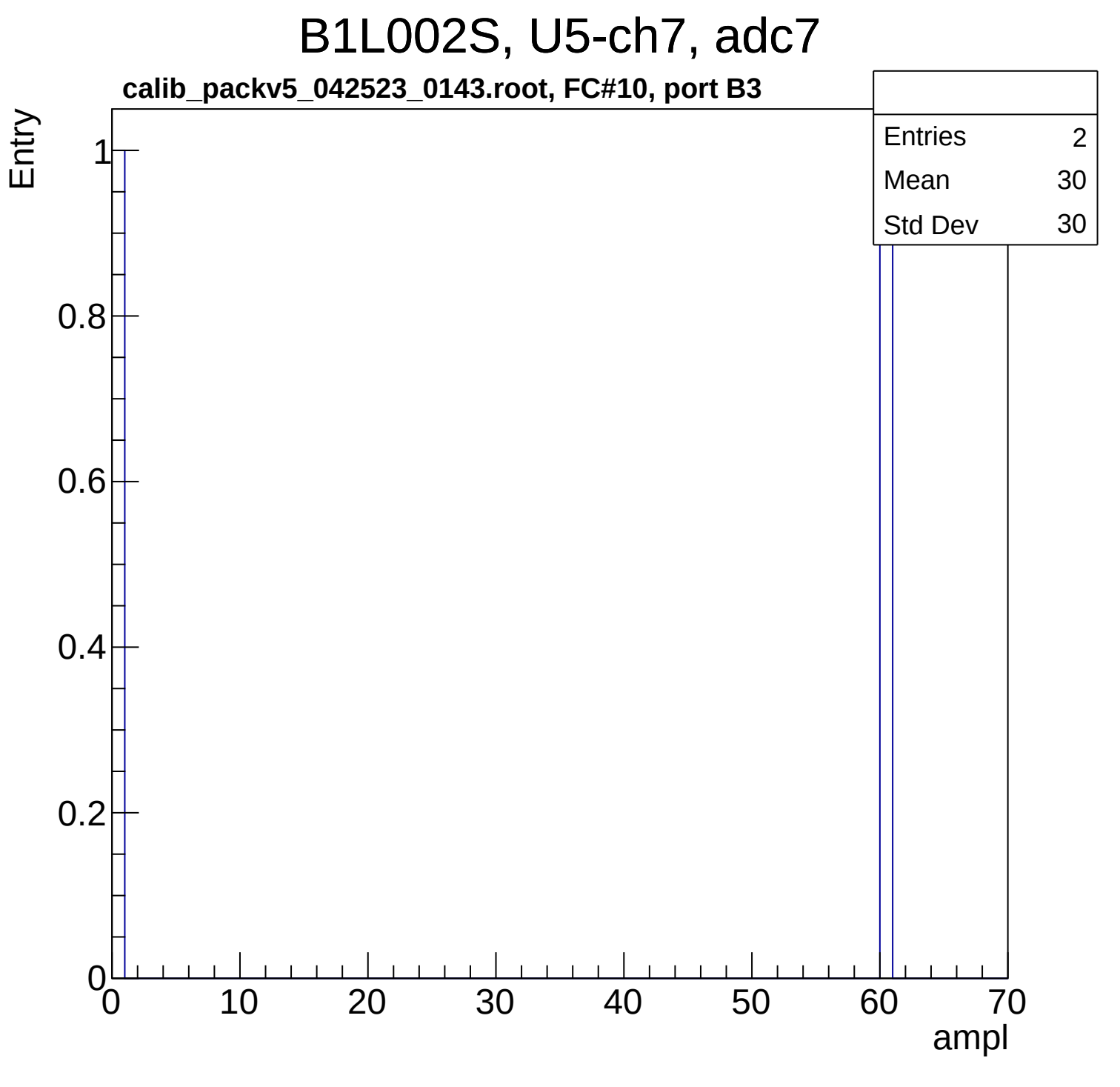
B1L002S, U5-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30
Std Dev 30
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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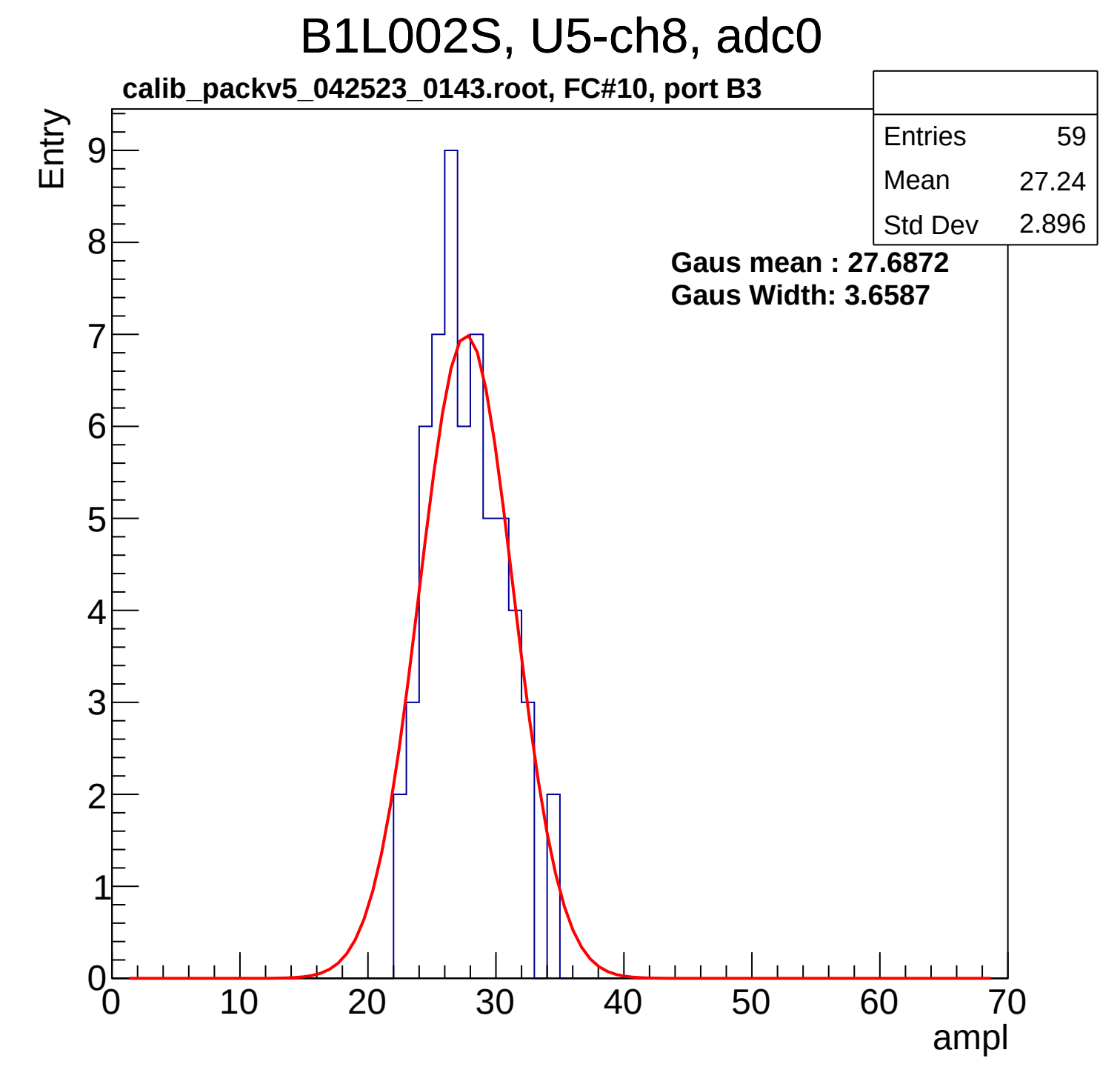
50
60
70
ampl


B1L002S, U5-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.24
Std Dev 2.896
Gaus mean : 27.6872
Gaus Width: 3.6587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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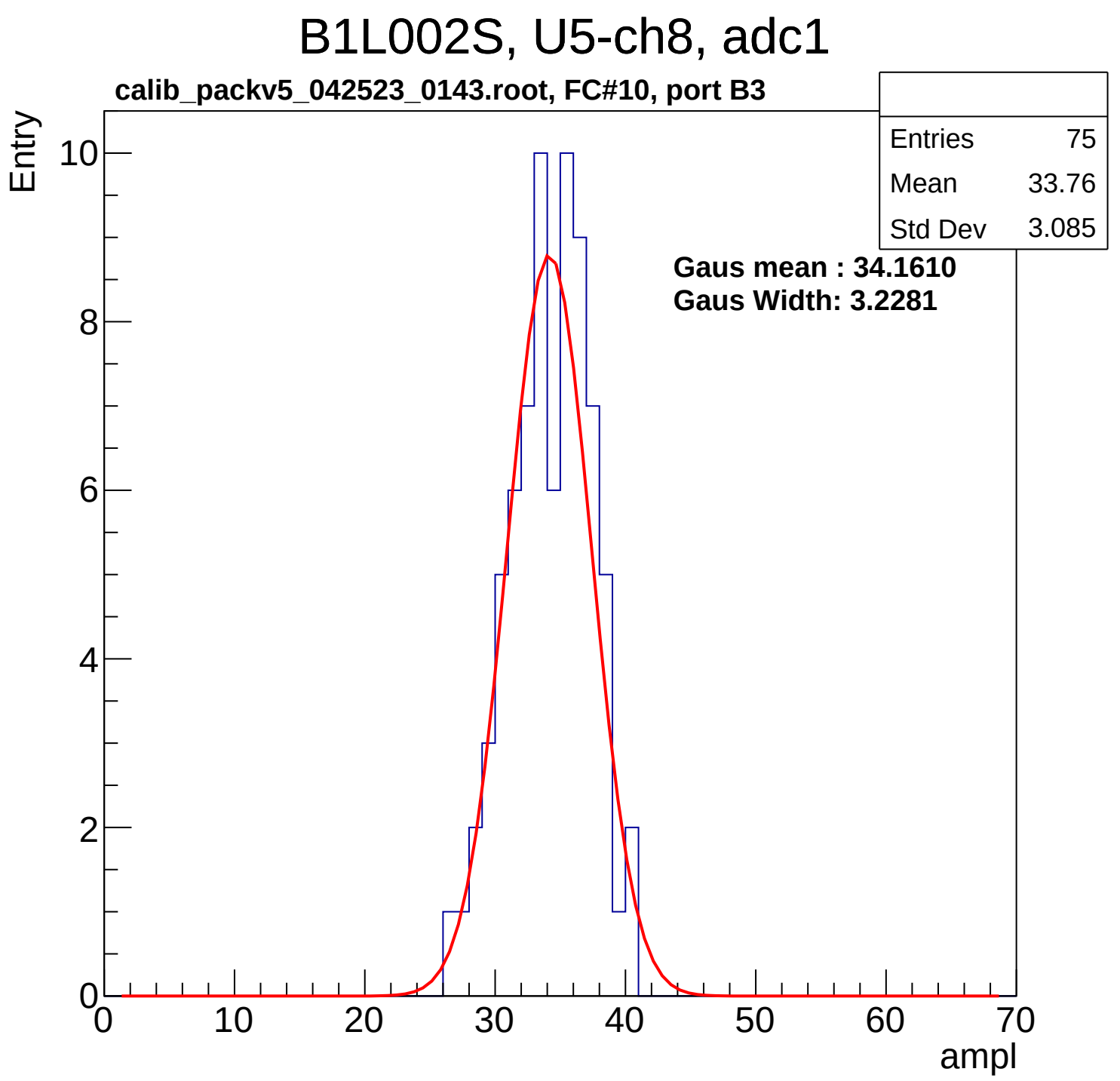
B1L002S, U5-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.76
Std Dev 3.085
Gaus mean : 34.1610
Gaus Width: 3.2281
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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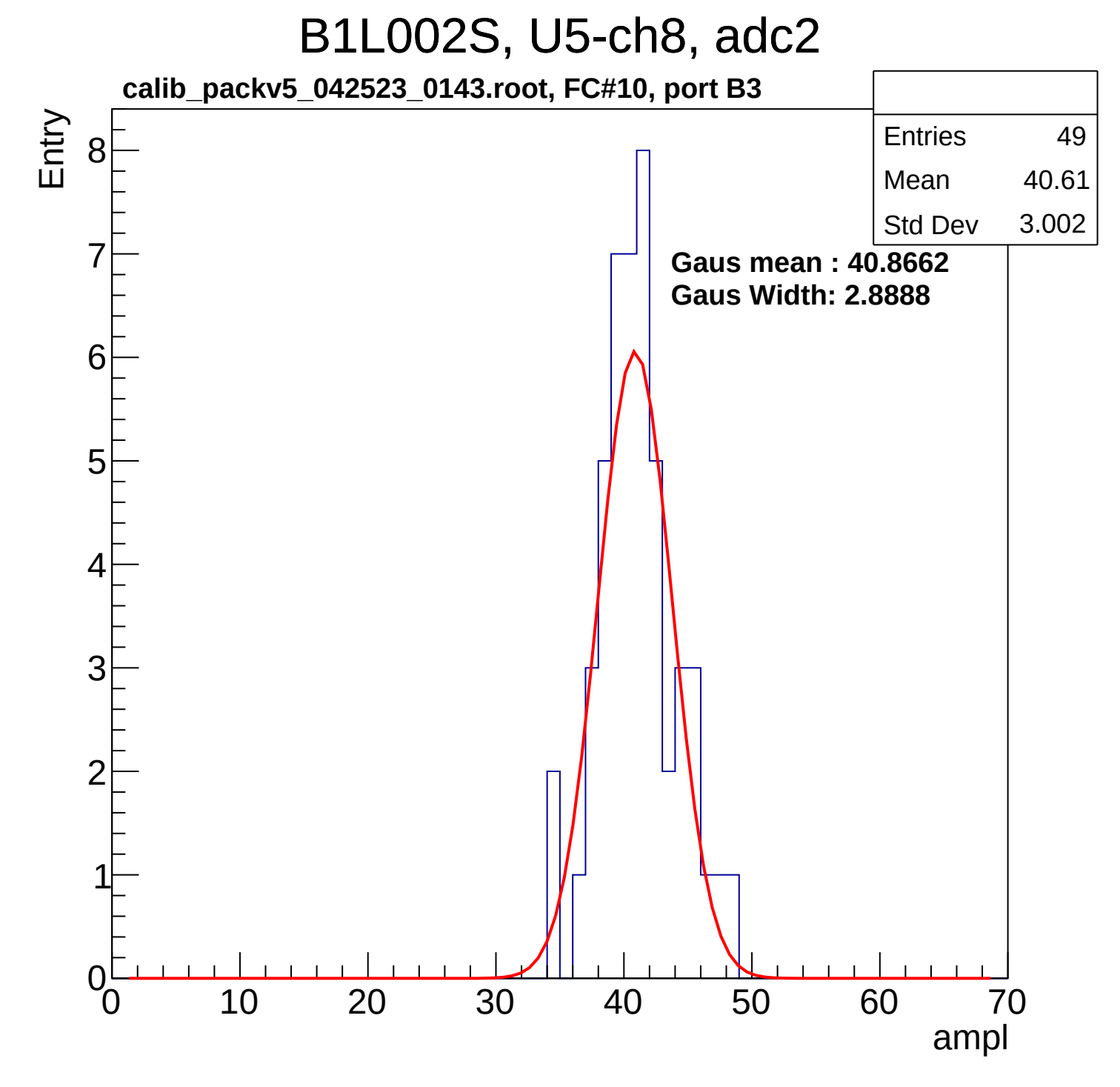
B1L002S, U5-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.61
Std Dev 3.002
Gaus mean : 40.8662
Gaus Width: 2.8888
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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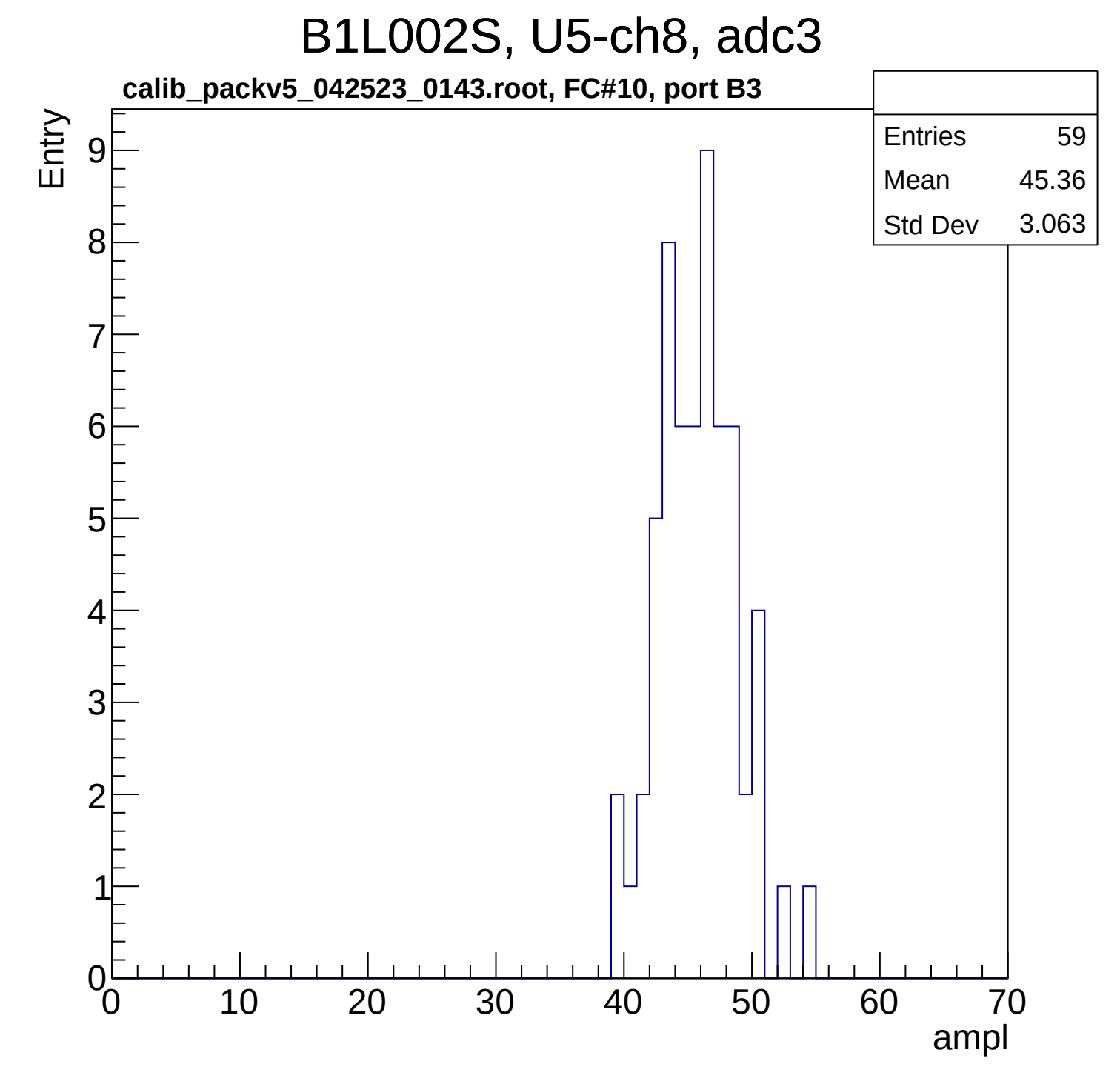
B1L002S, U5-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.36
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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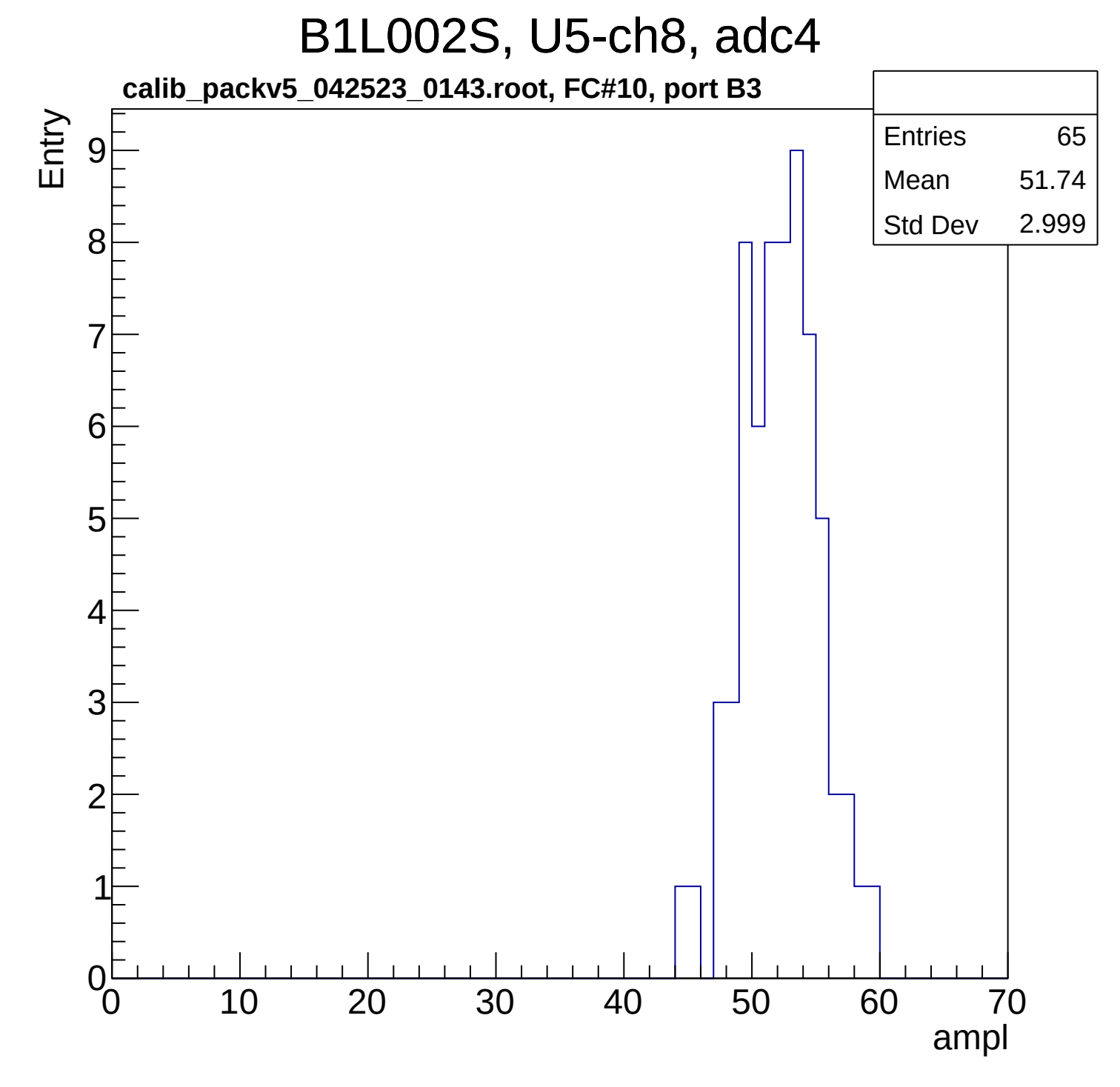
B1L002S, U5-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 51.74
Std Dev 2.999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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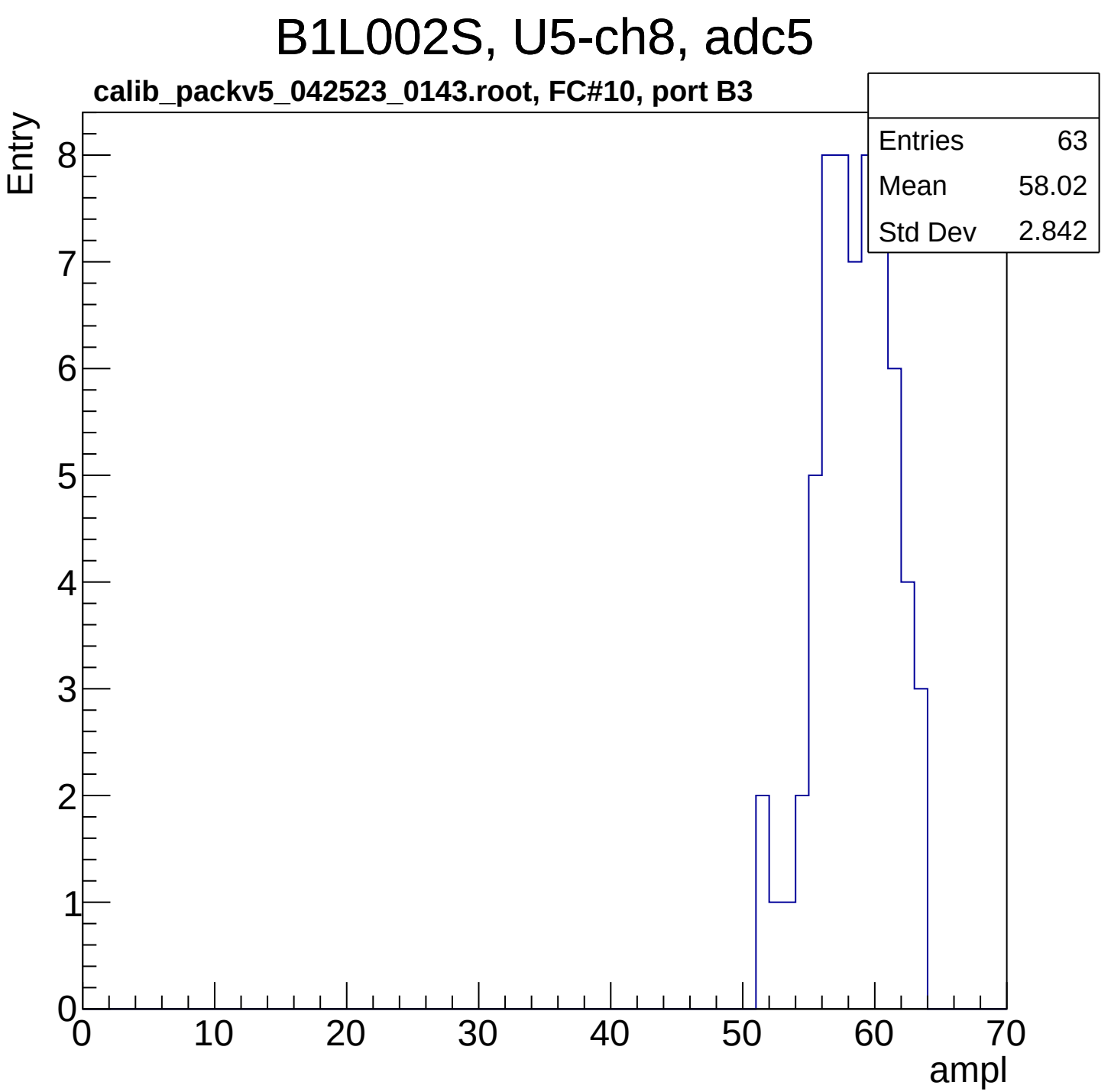
B1L002S, U5-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.02
Std Dev 2.842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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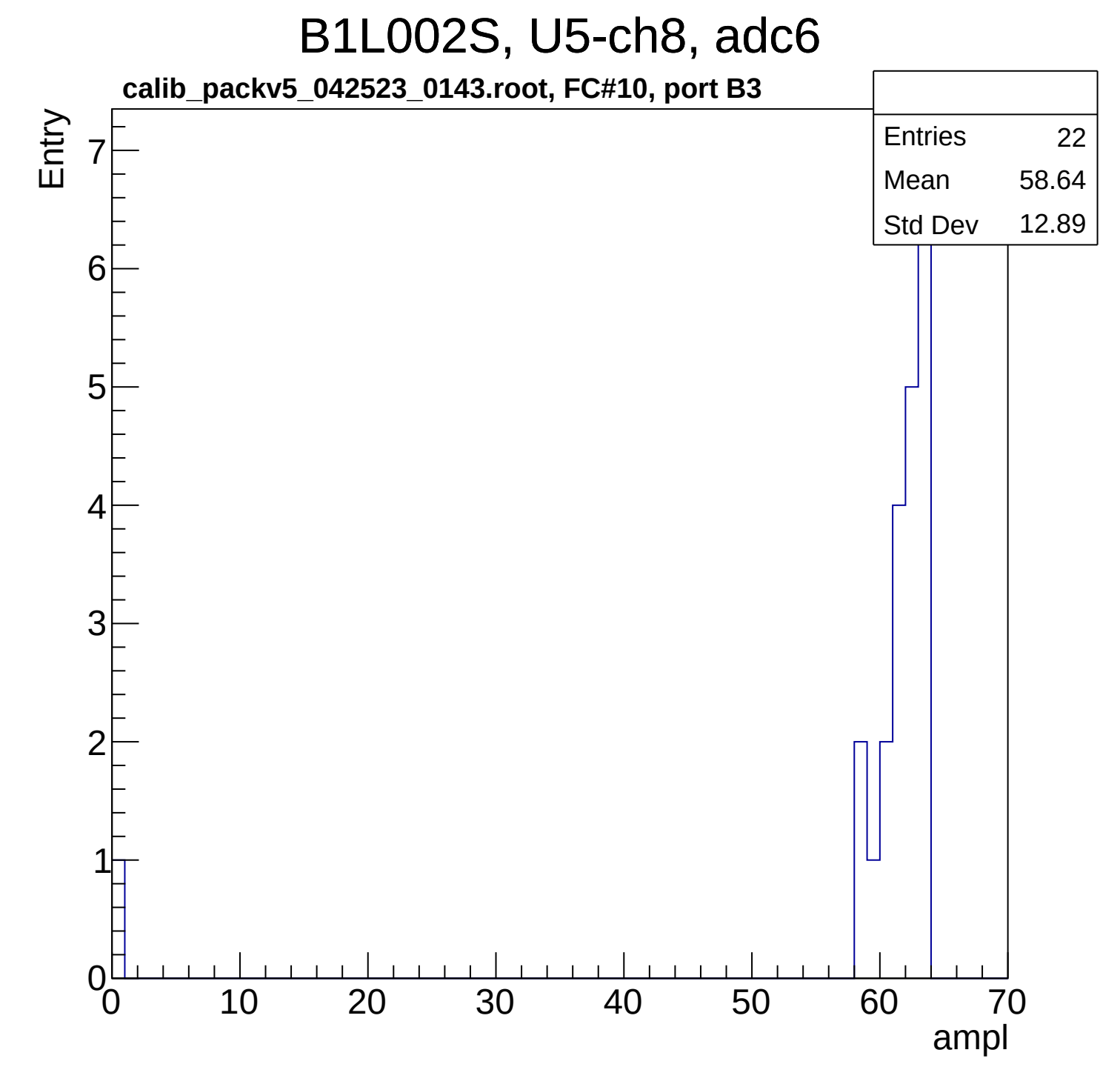
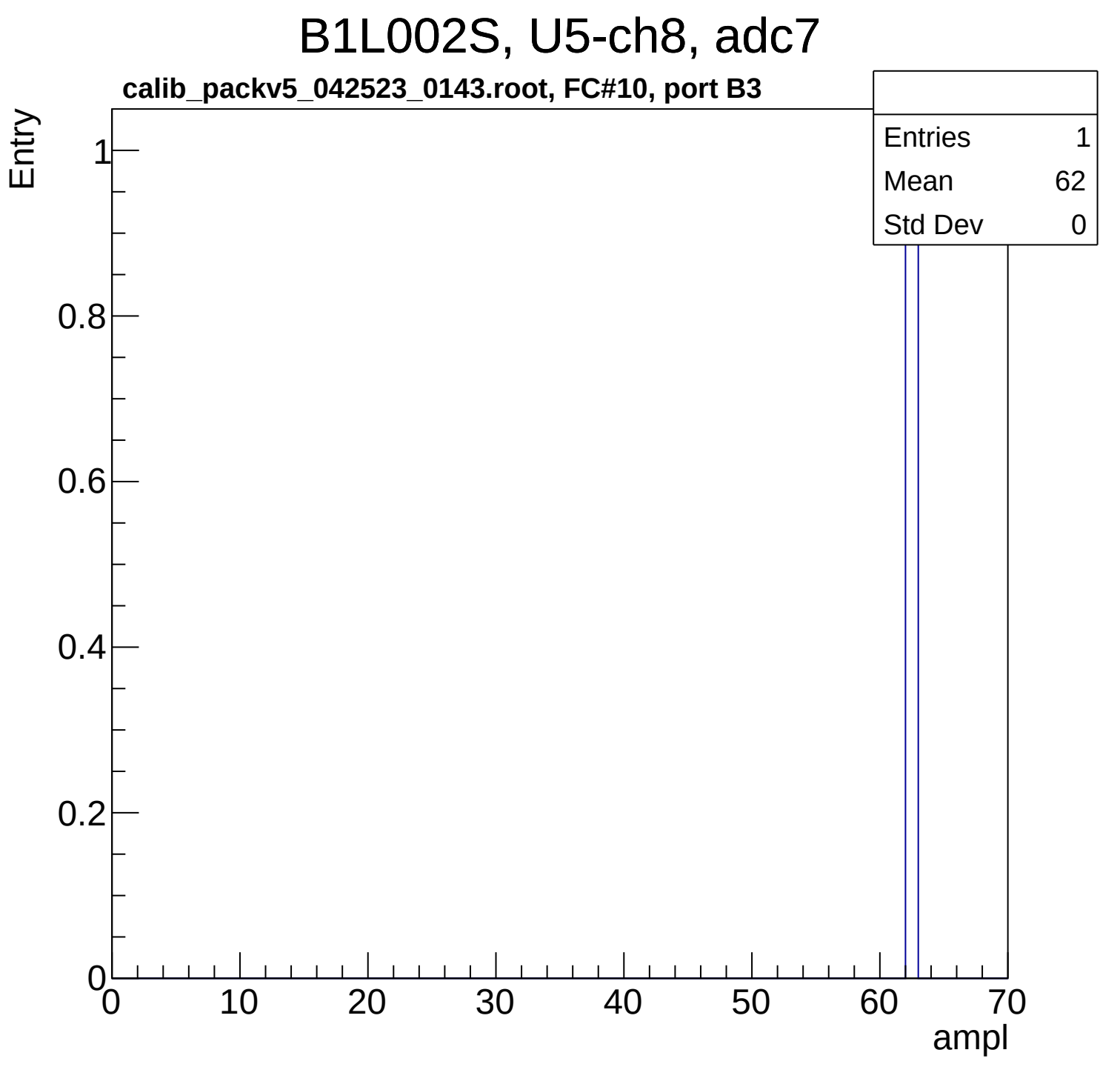
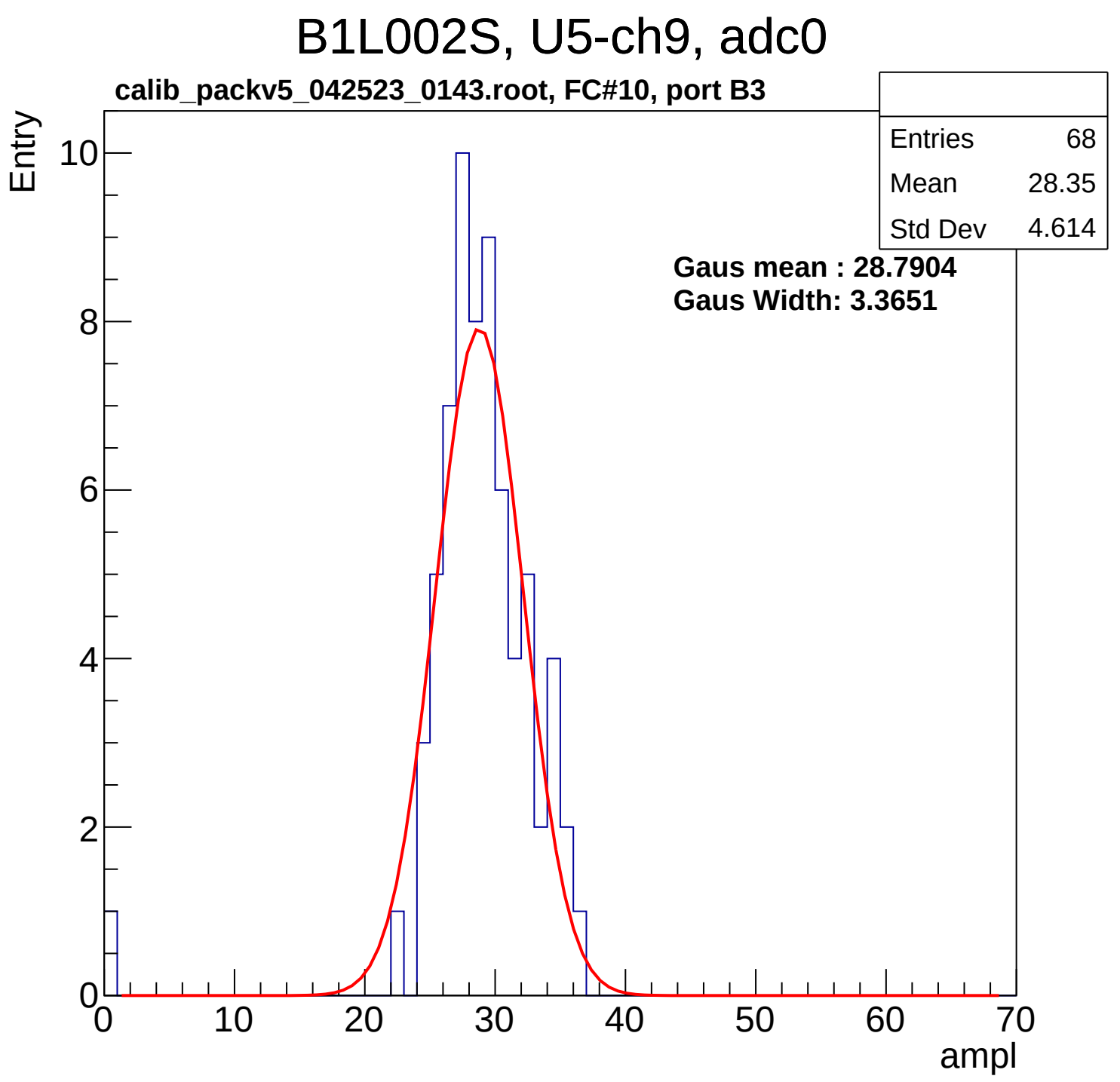
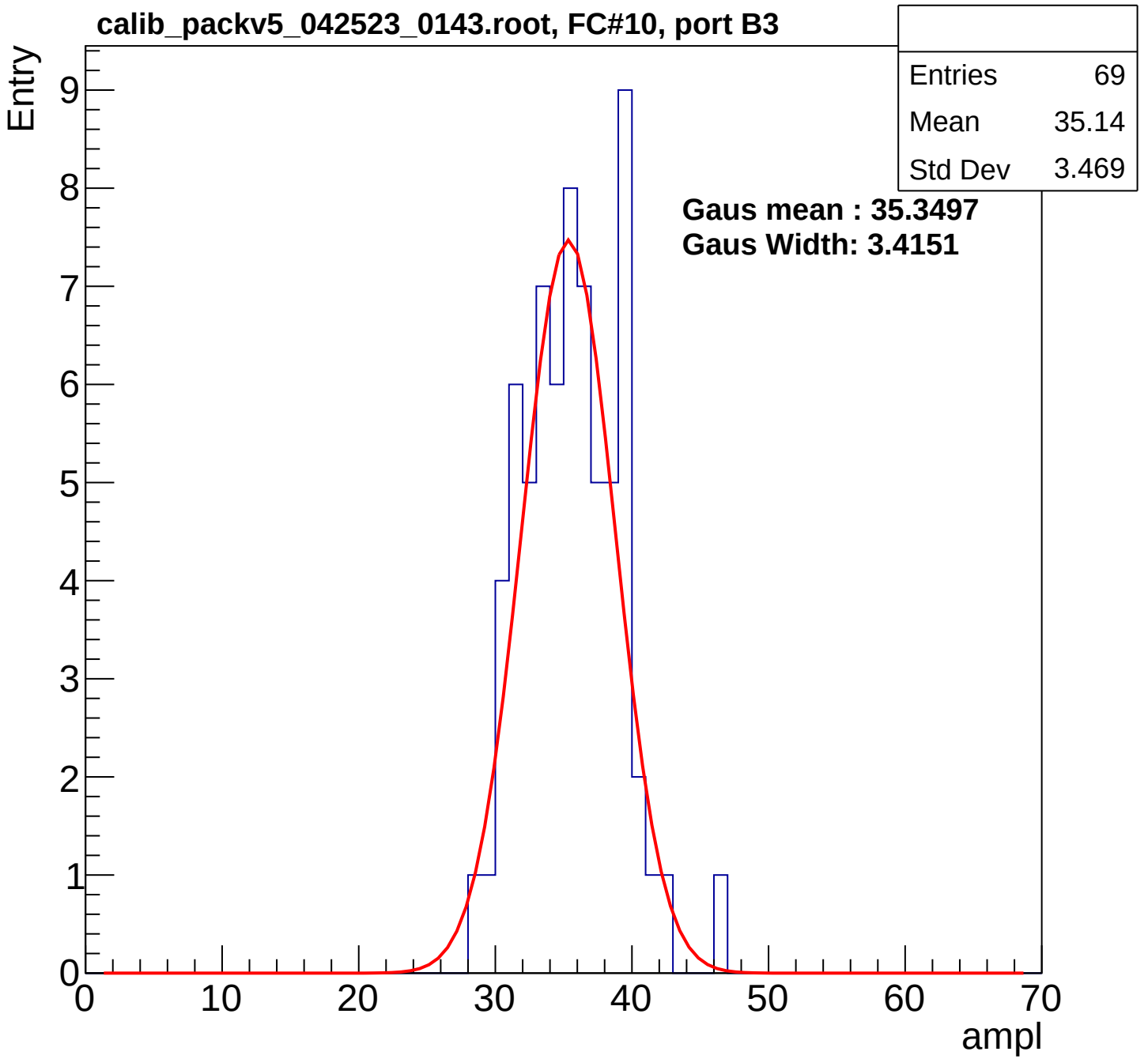
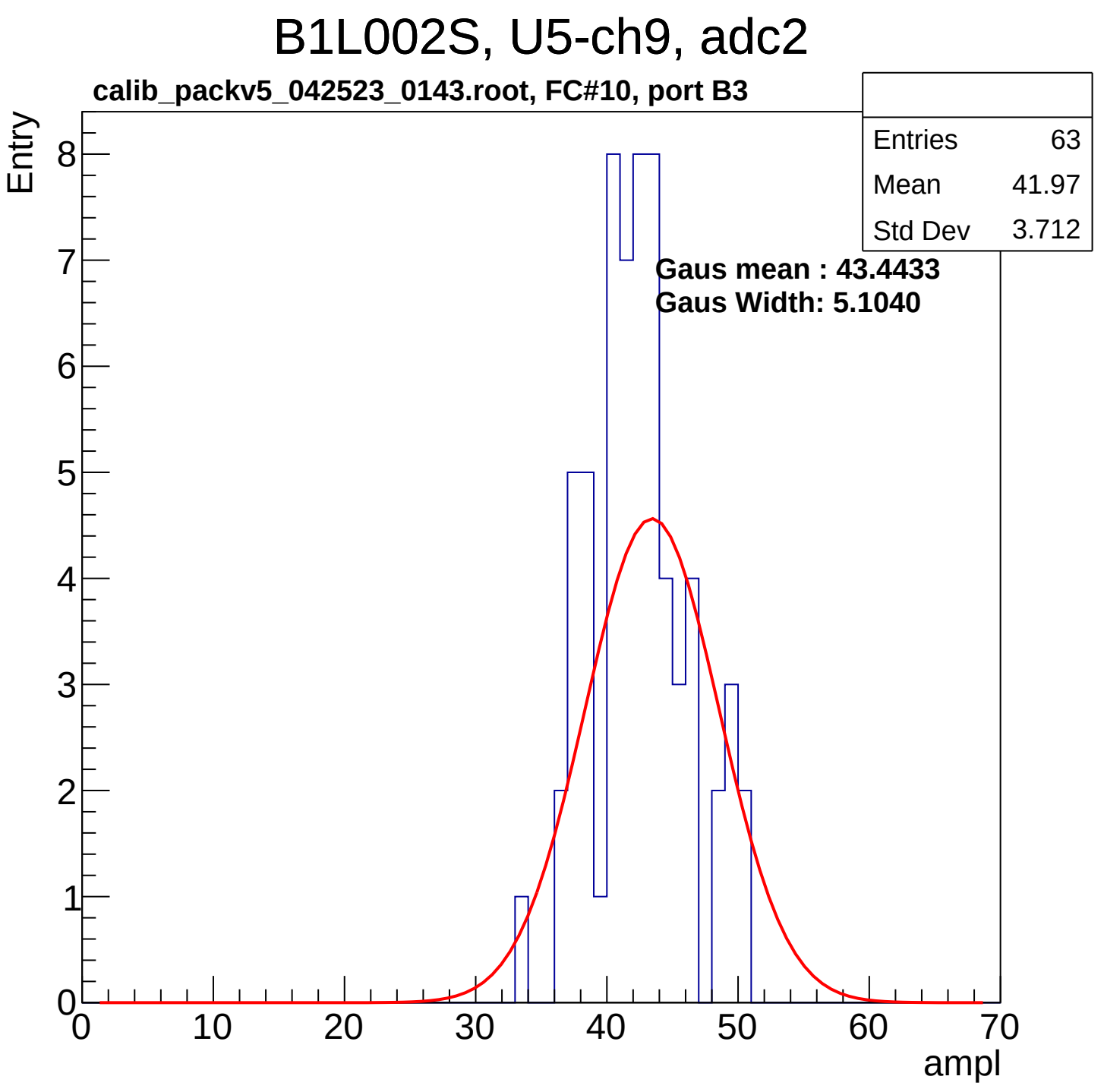
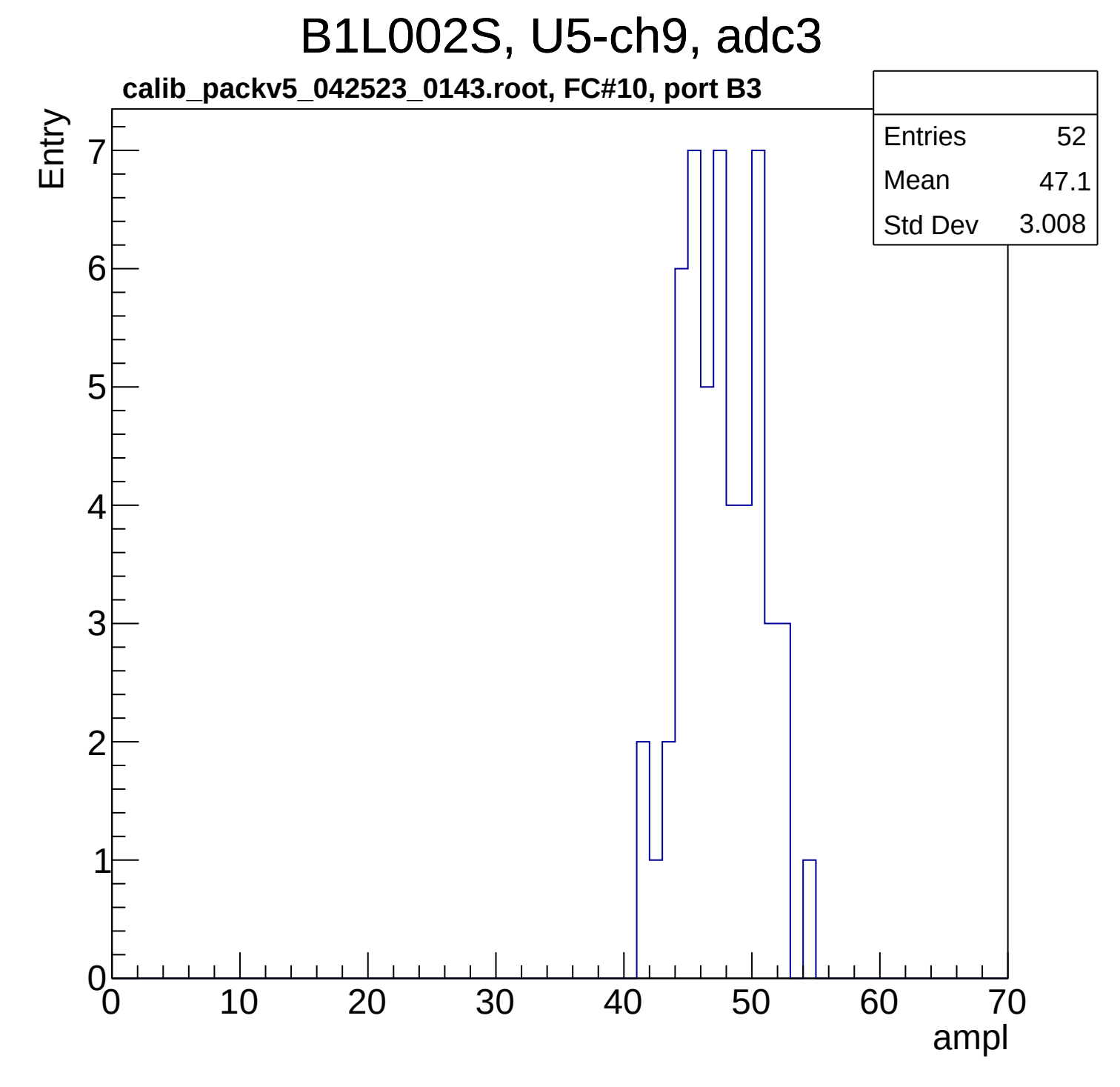

B1L002S, U5-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.64
Std Dev 12.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

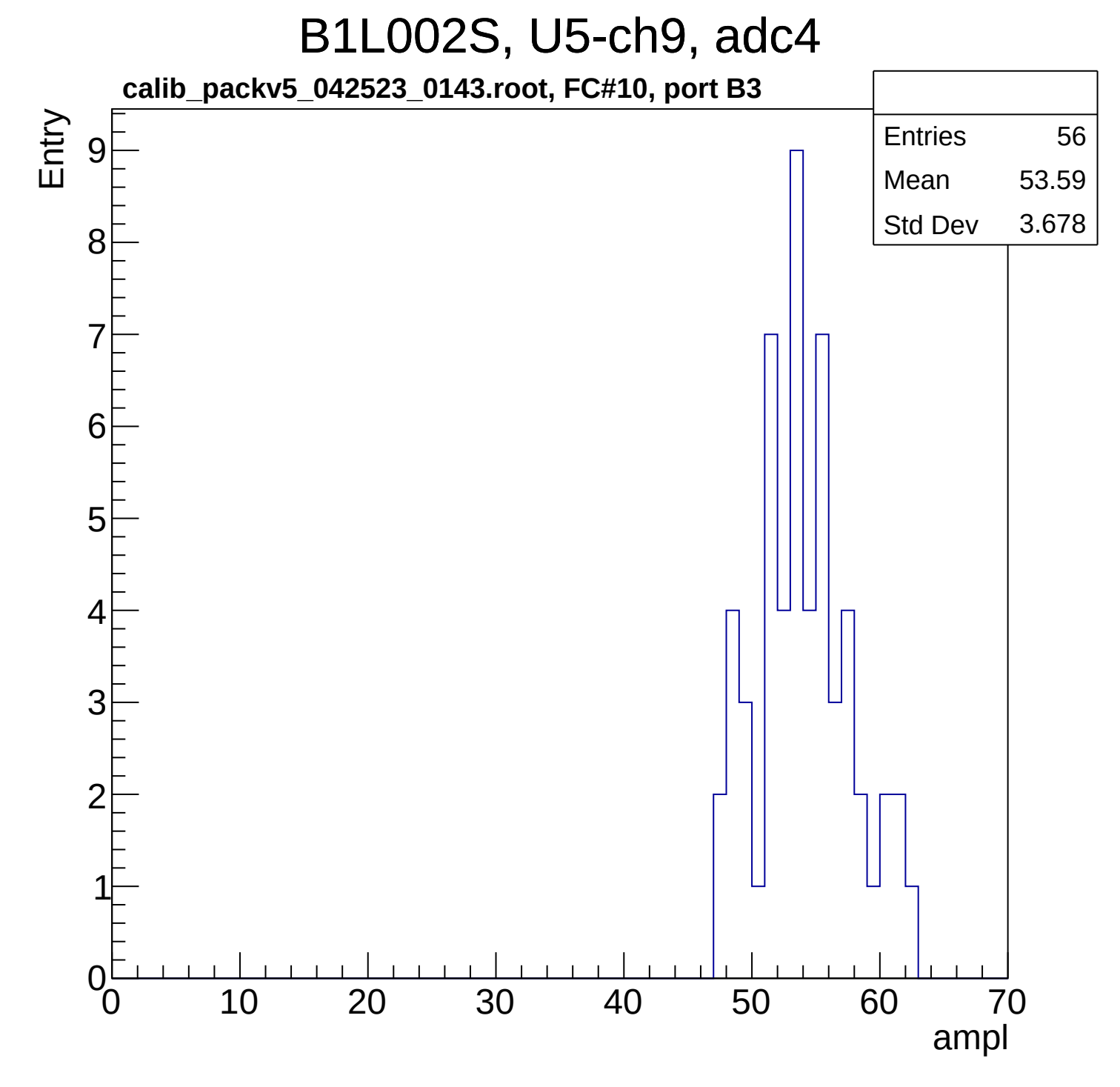

B1L002S, U5-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

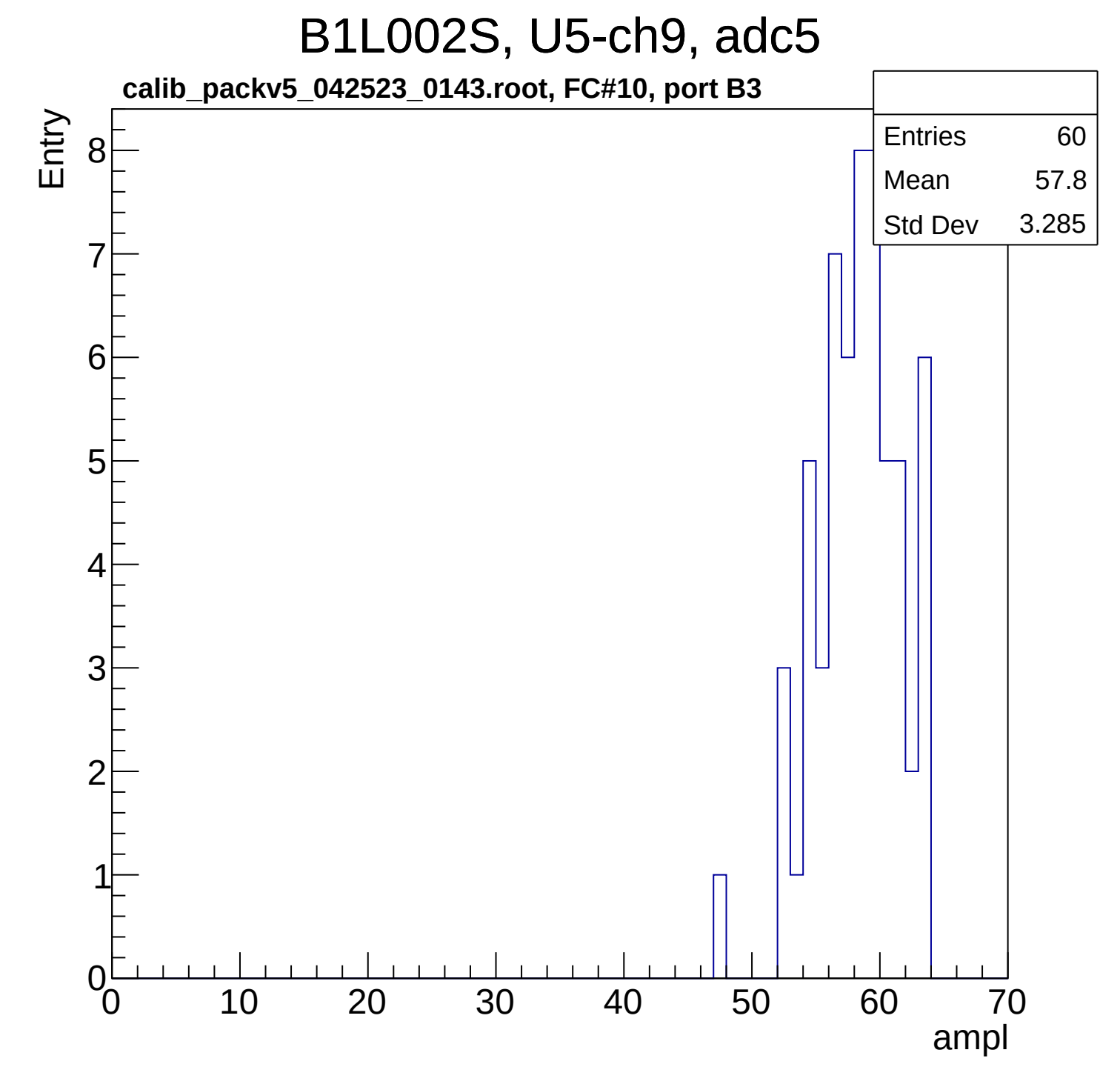

B1L002S, U5-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.35
Std Dev 4.614
Gaus mean : 28.7904
Gaus Width: 3.3651
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

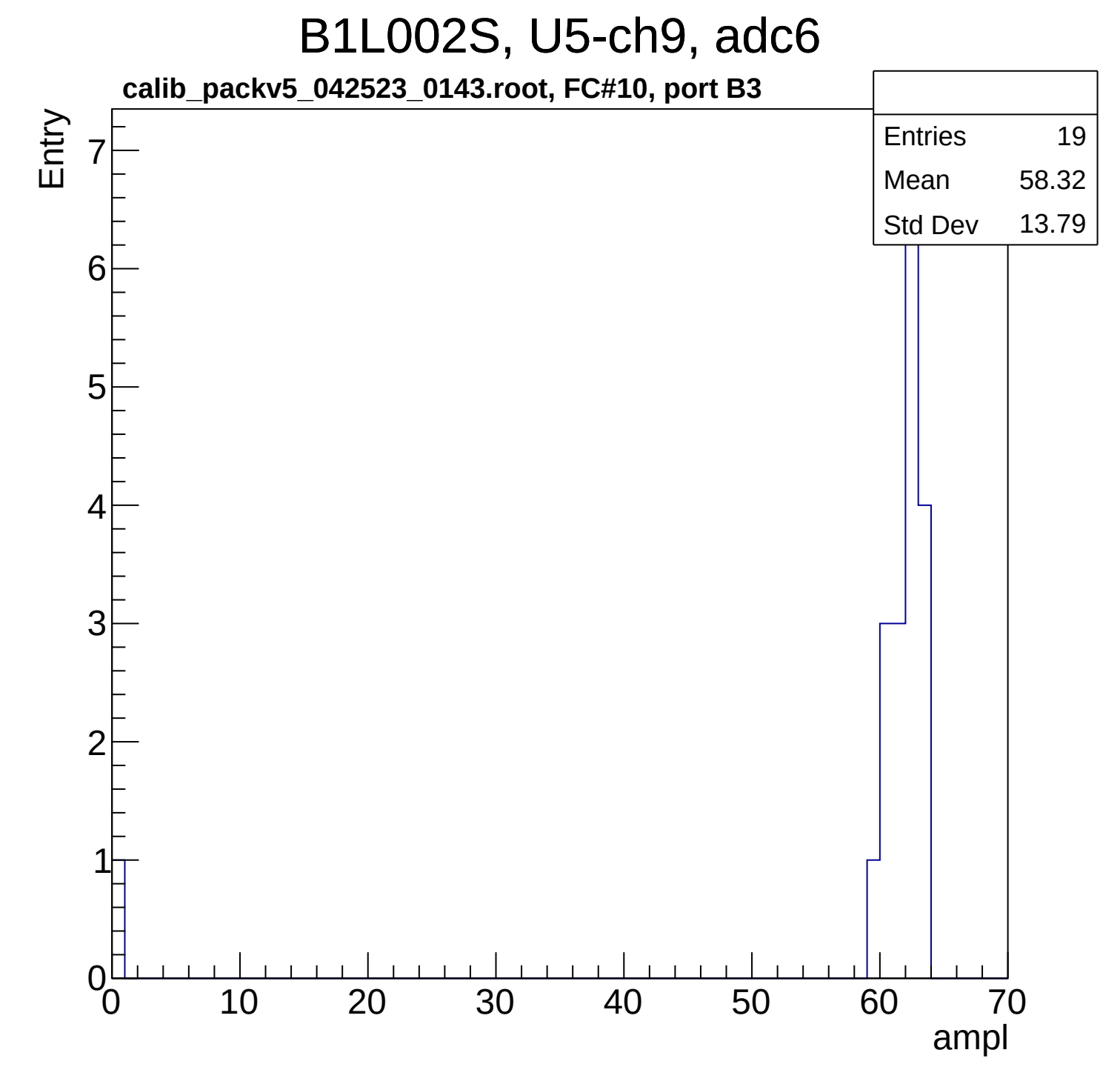

B1L002S, U5-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.14
Std Dev 3.469
Gaus mean : 35.3497
Gaus Width: 3.4151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

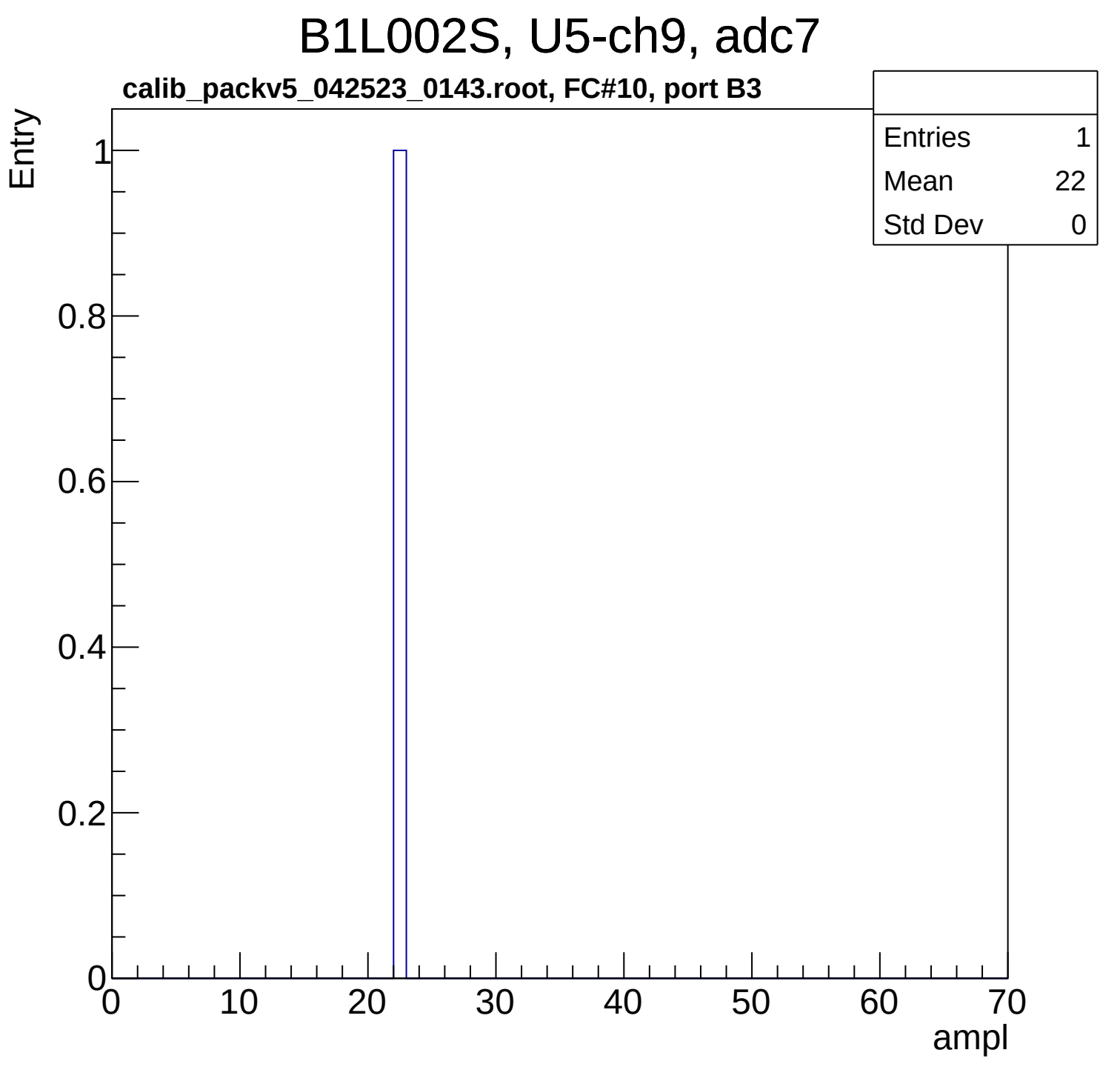

B1L002S, U5-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.97
Std Dev 3.712
Gaus mean : 43.4433
Gaus Width: 5.1040
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

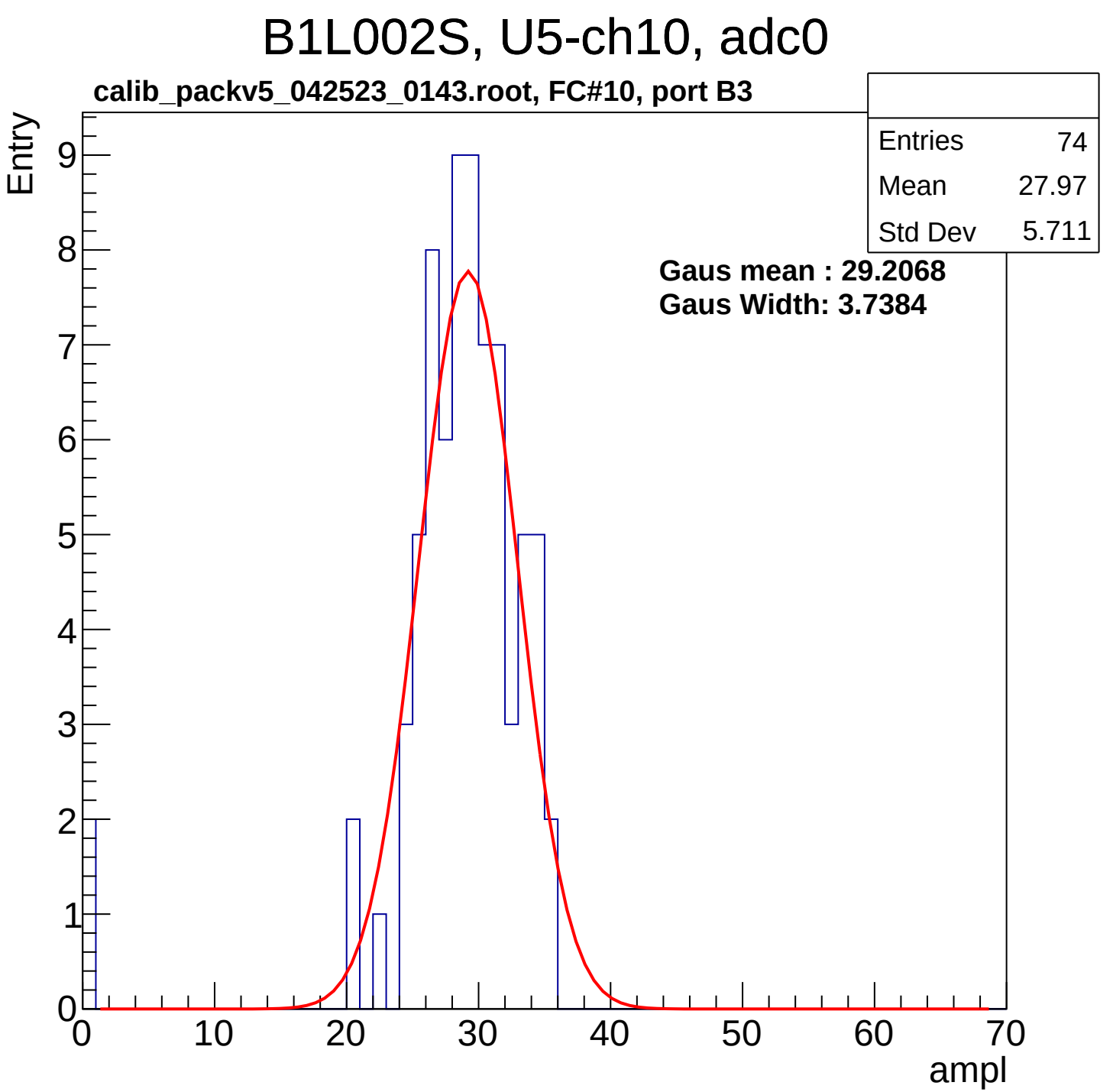

B1L002S, U5-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.1
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

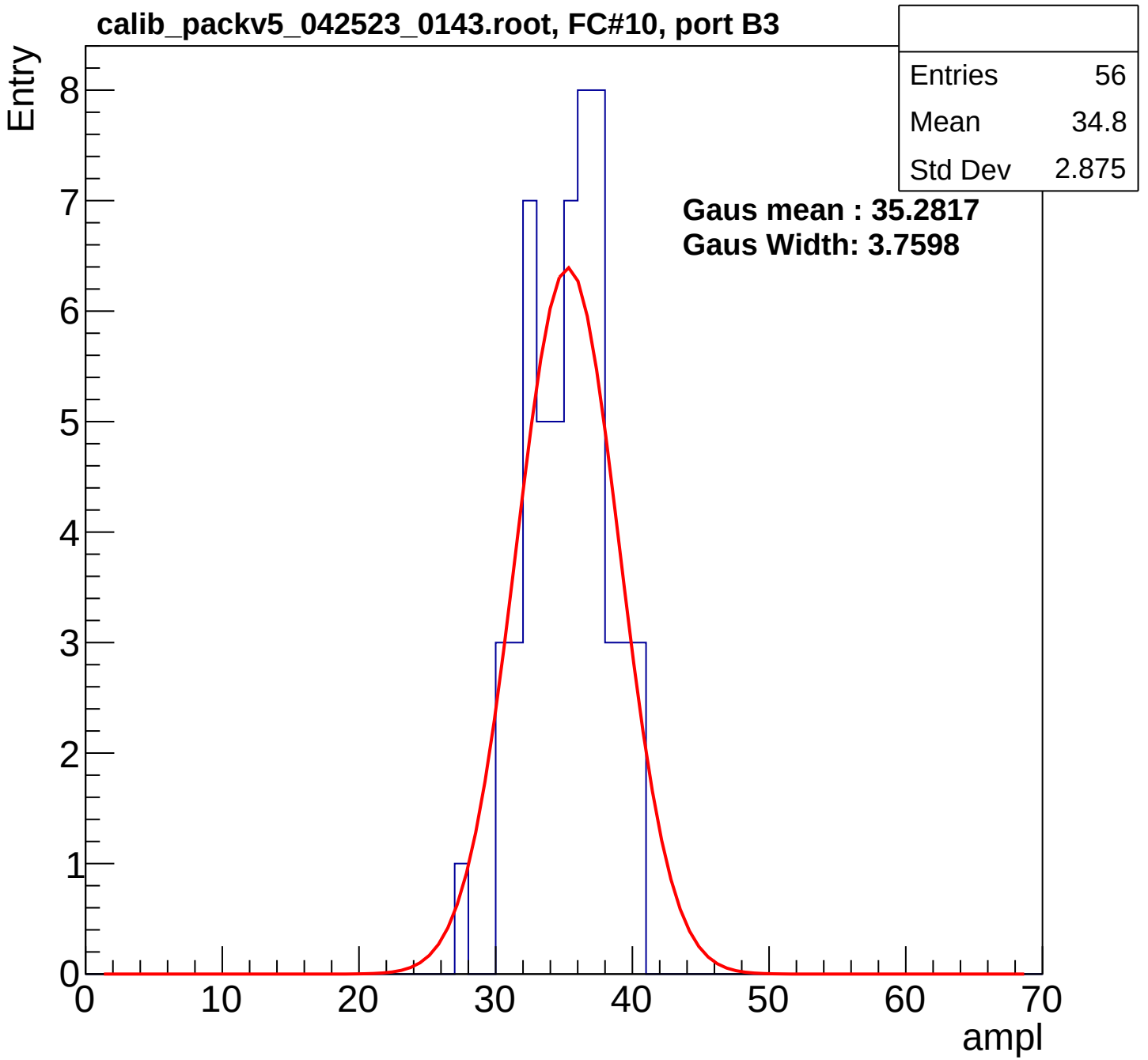

B1L002S, U5-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.59
Std Dev 3.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.8
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.32
Std Dev 13.79
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.97
Std Dev 5.711
Gaus mean : 29.2068
Gaus Width: 3.7384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.8
Std Dev 2.875
Gaus mean : 35.2817
Gaus Width: 3.7598
Entry
ampl
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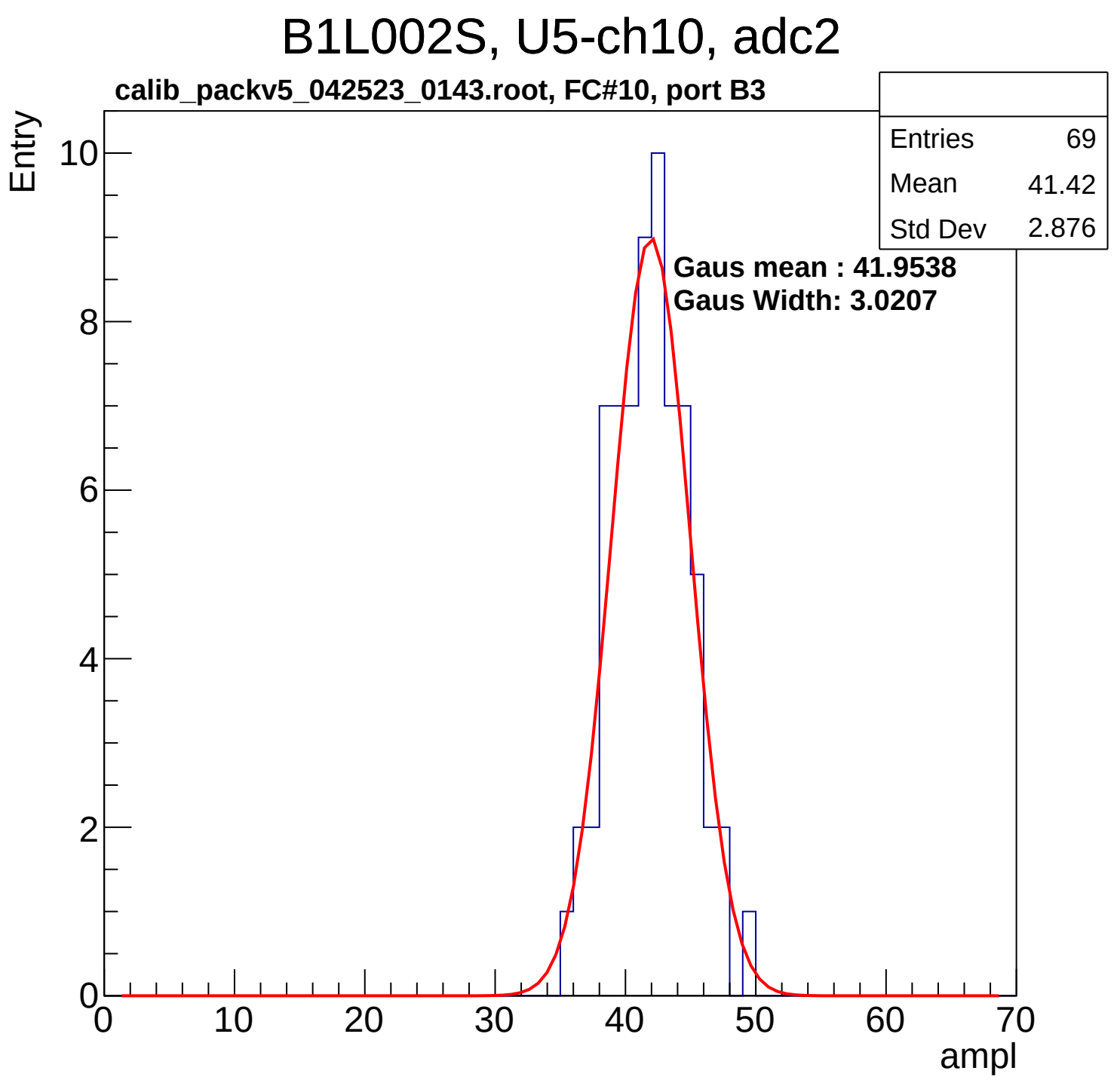
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.42
Std Dev 2.876
Gaus mean : 41.9538
Gaus Width: 3.0207
Entry
ampl
0
2
4
6
8
10
0
10
20
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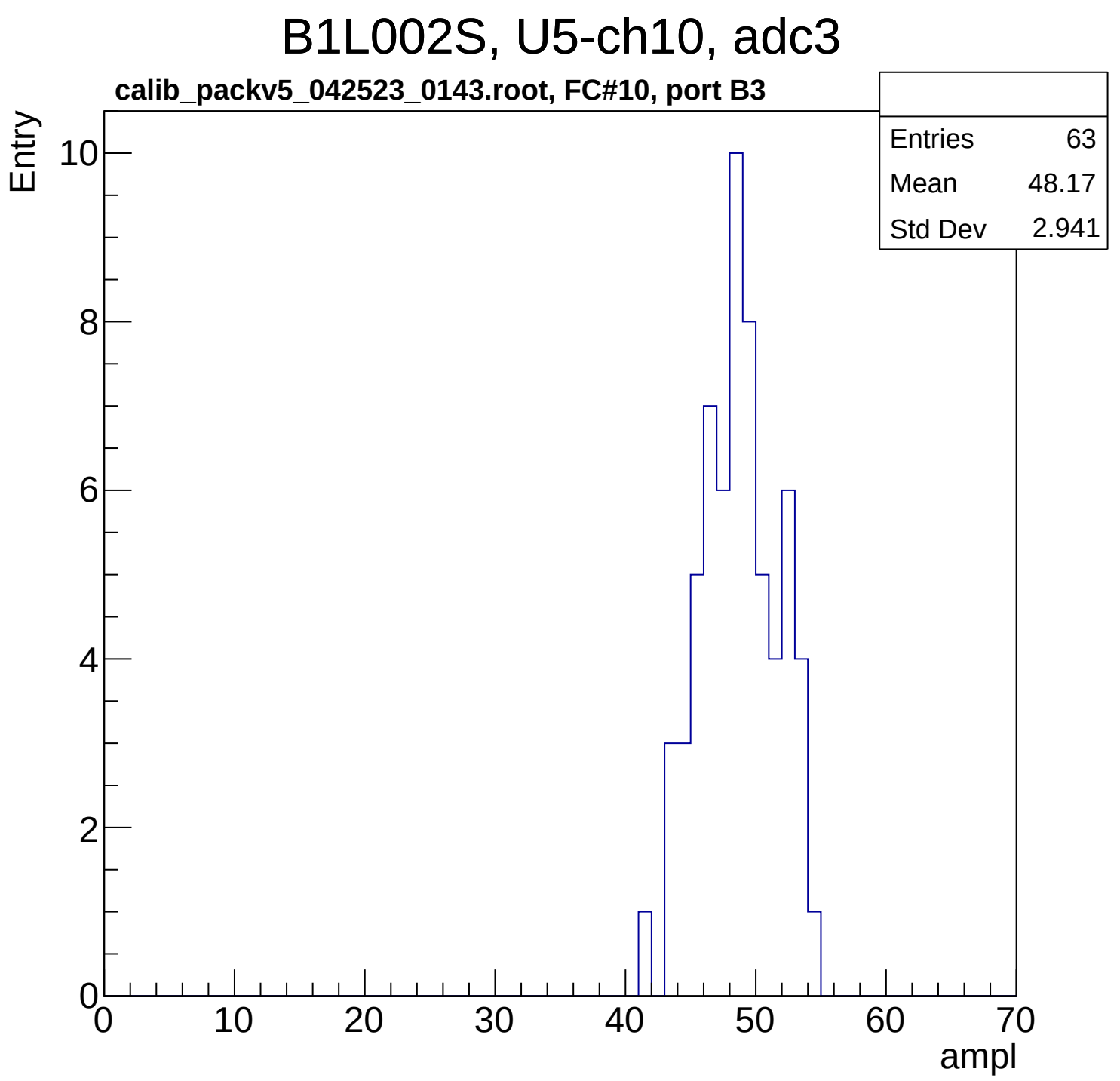
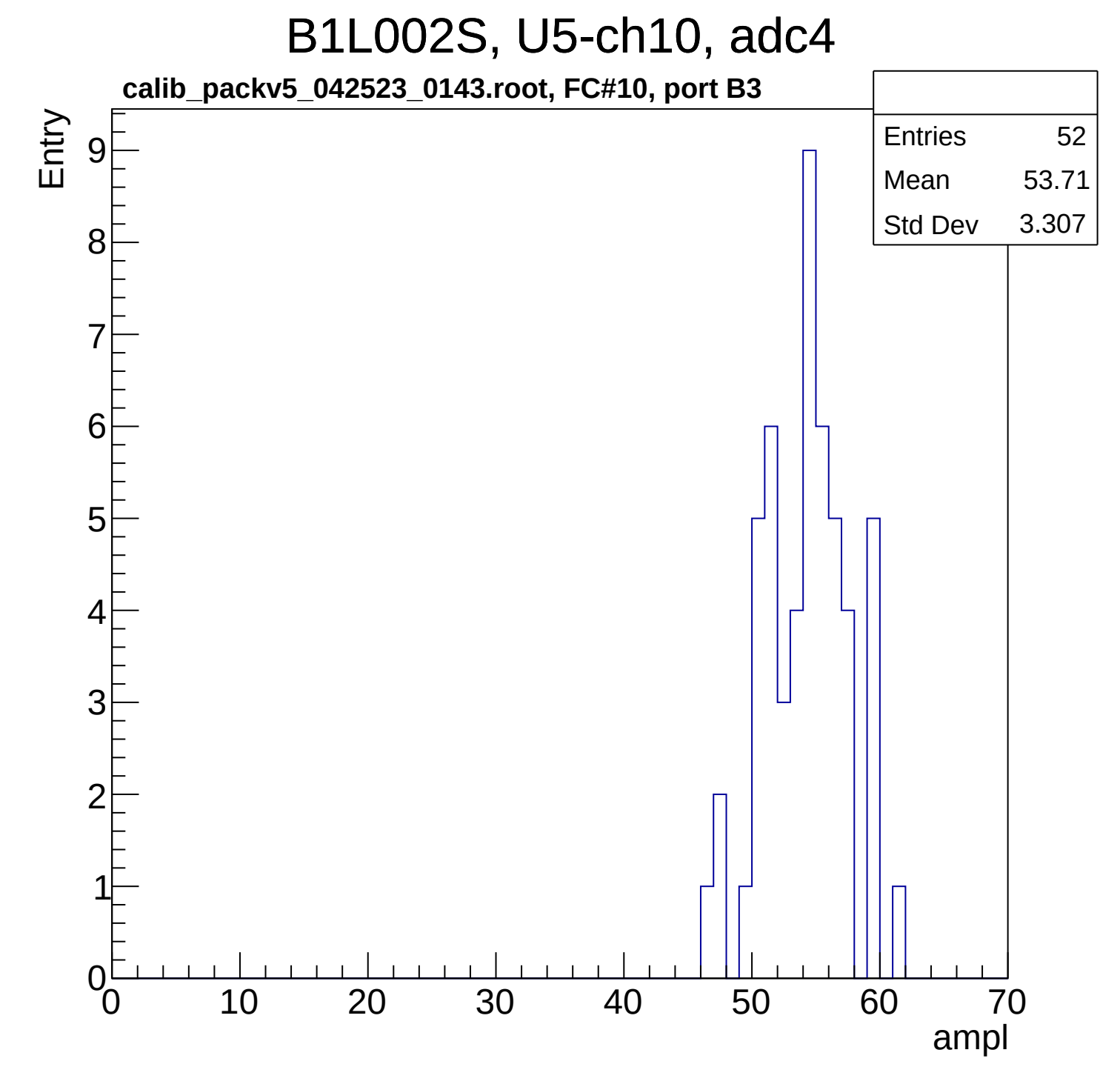
40
50
60
70


B1L002S, U5-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.17
Std Dev 2.941
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.71
Std Dev 3.307
Entry
ampl
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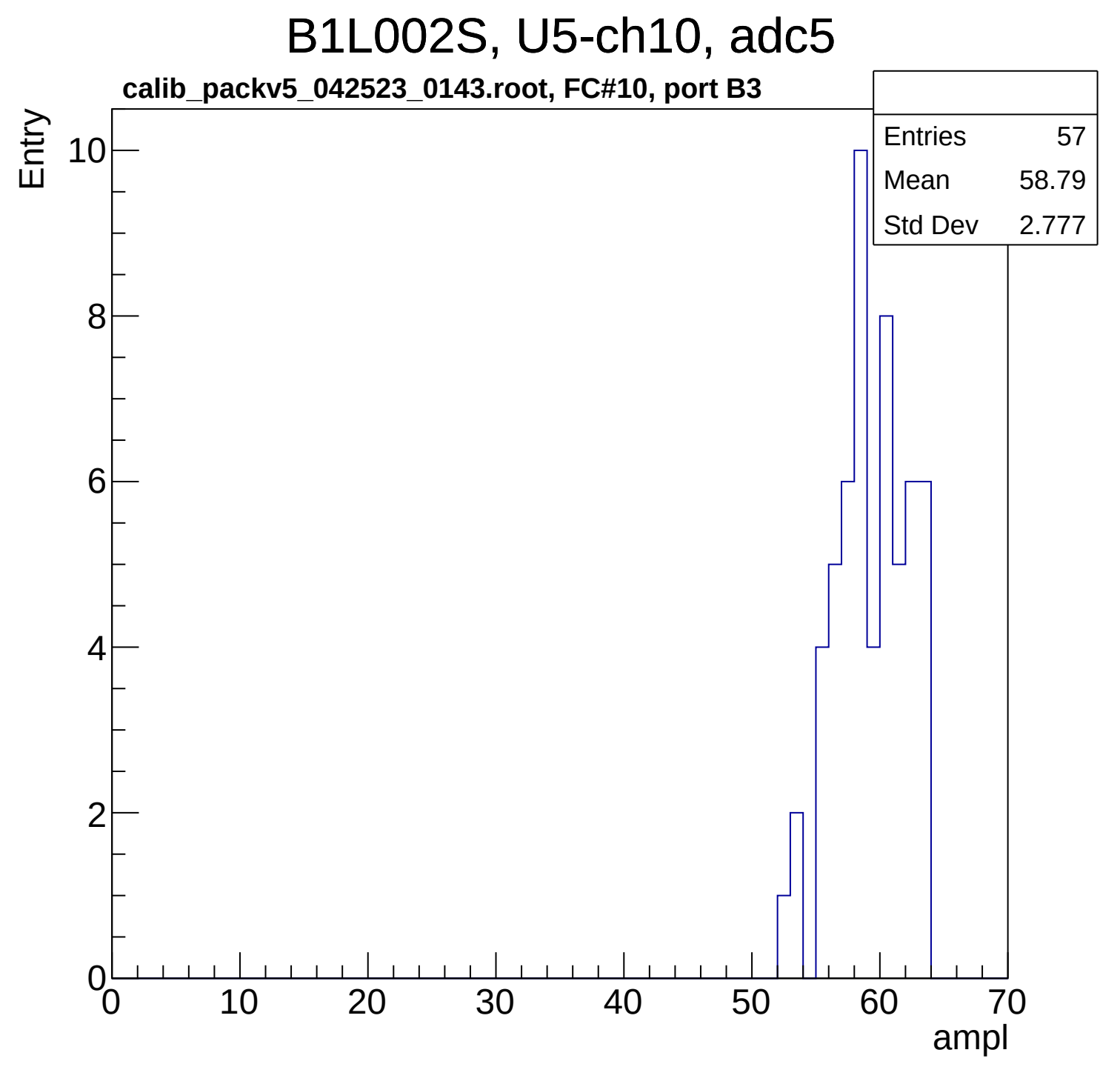
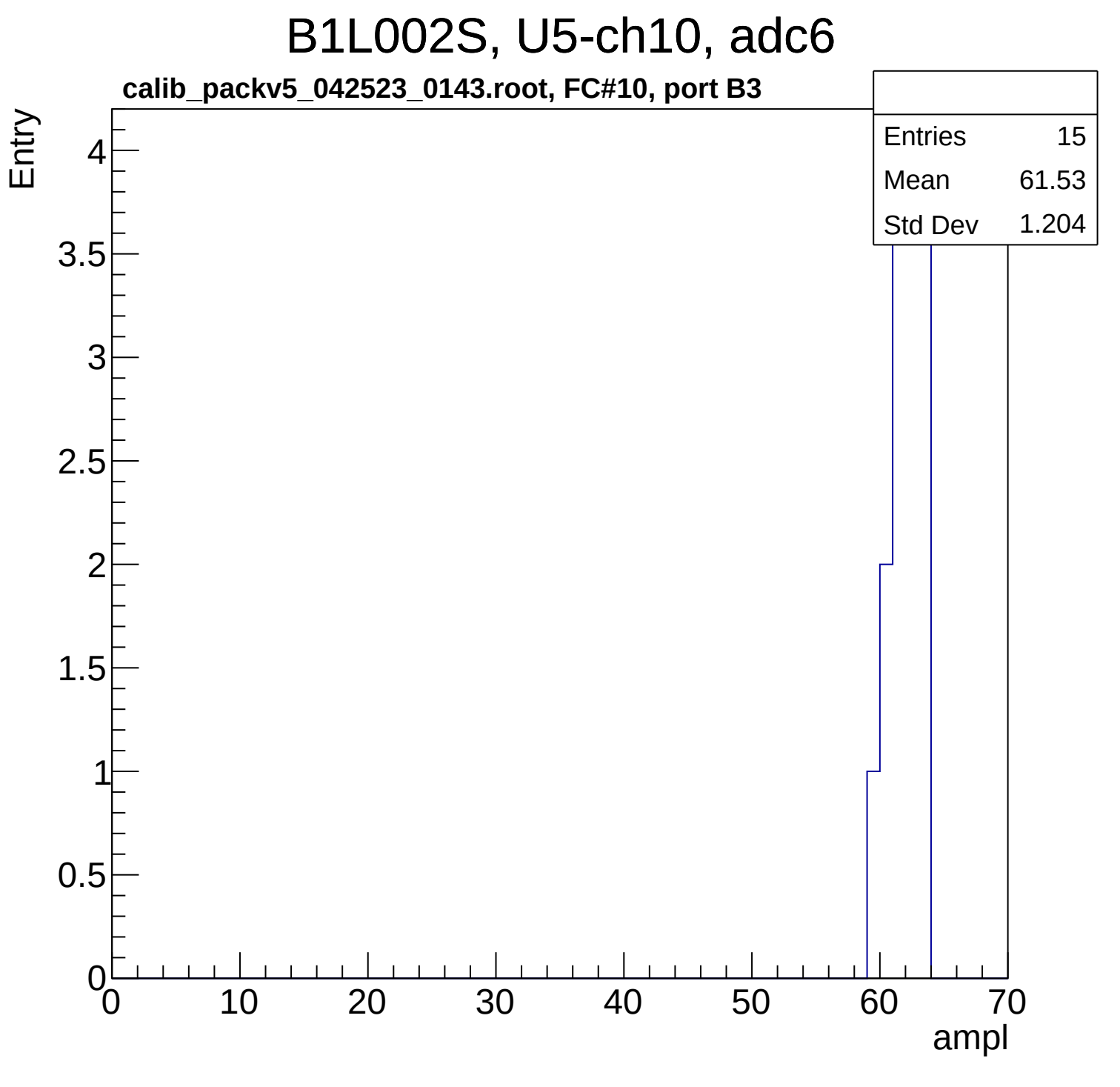
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.79
Std Dev 2.777
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.53
Std Dev 1.204
Entry
ampl
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0.5
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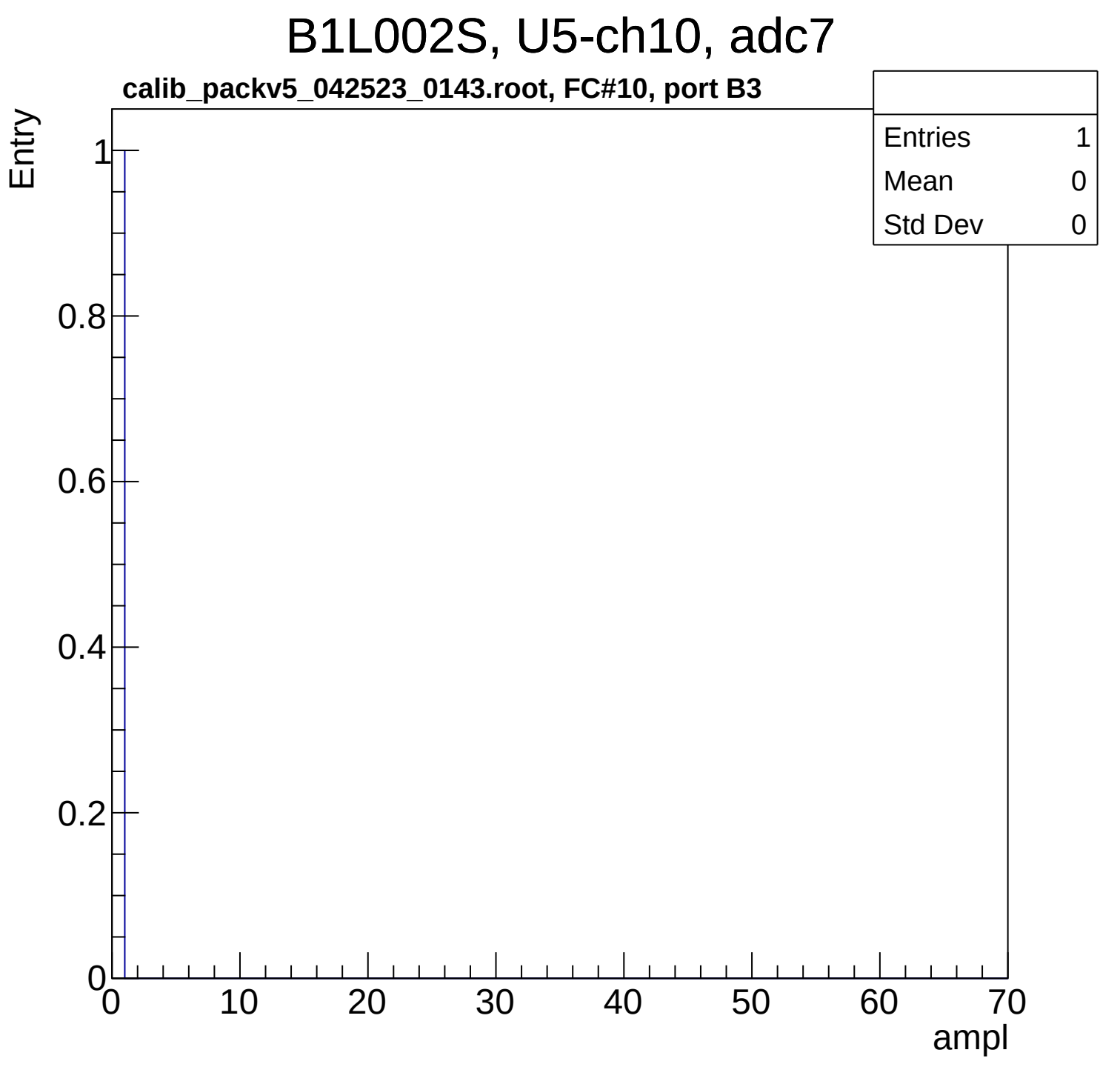
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U5-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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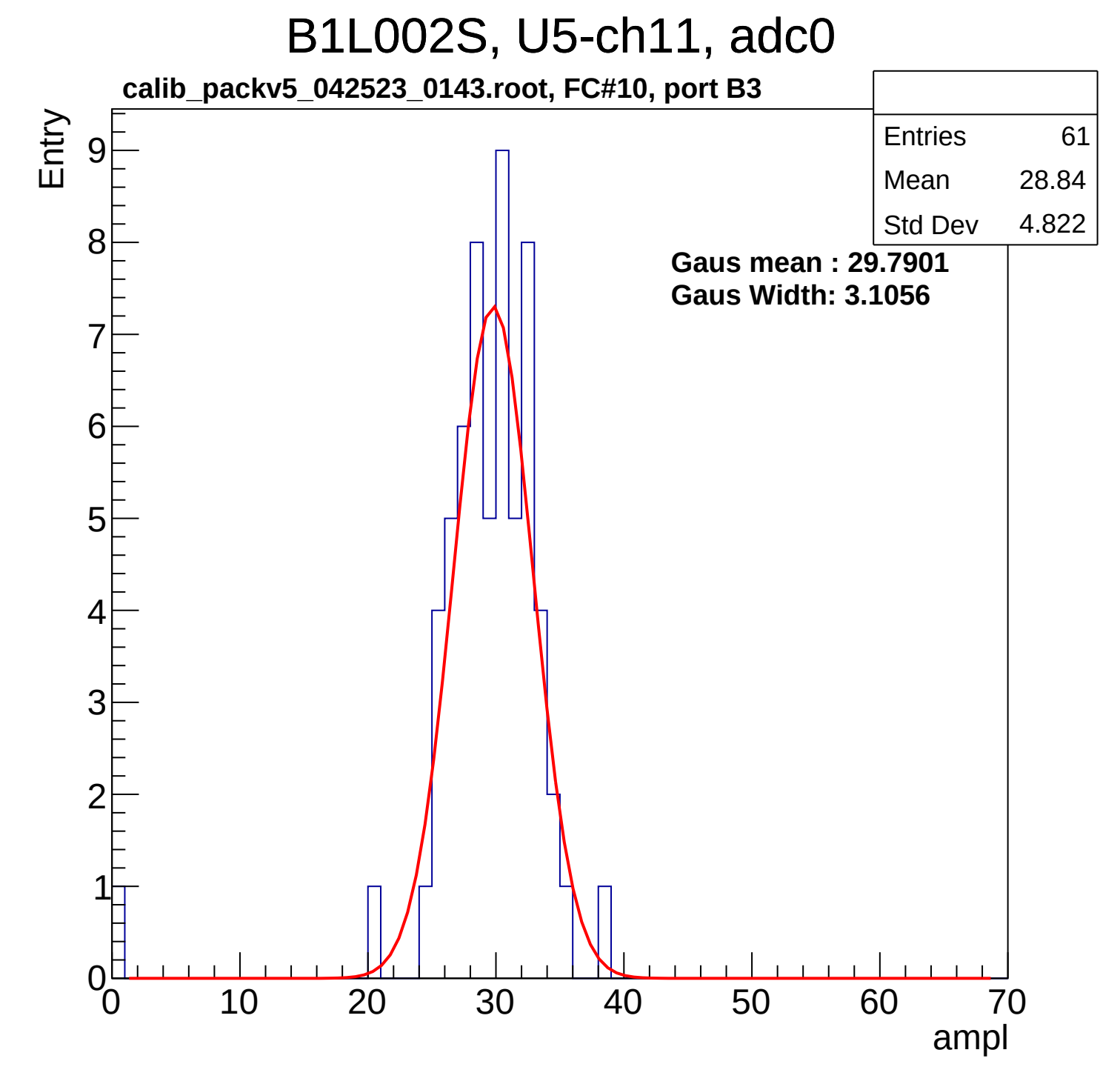
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.84
Std Dev 4.822
Gaus mean : 29.7901
Gaus Width: 3.1056
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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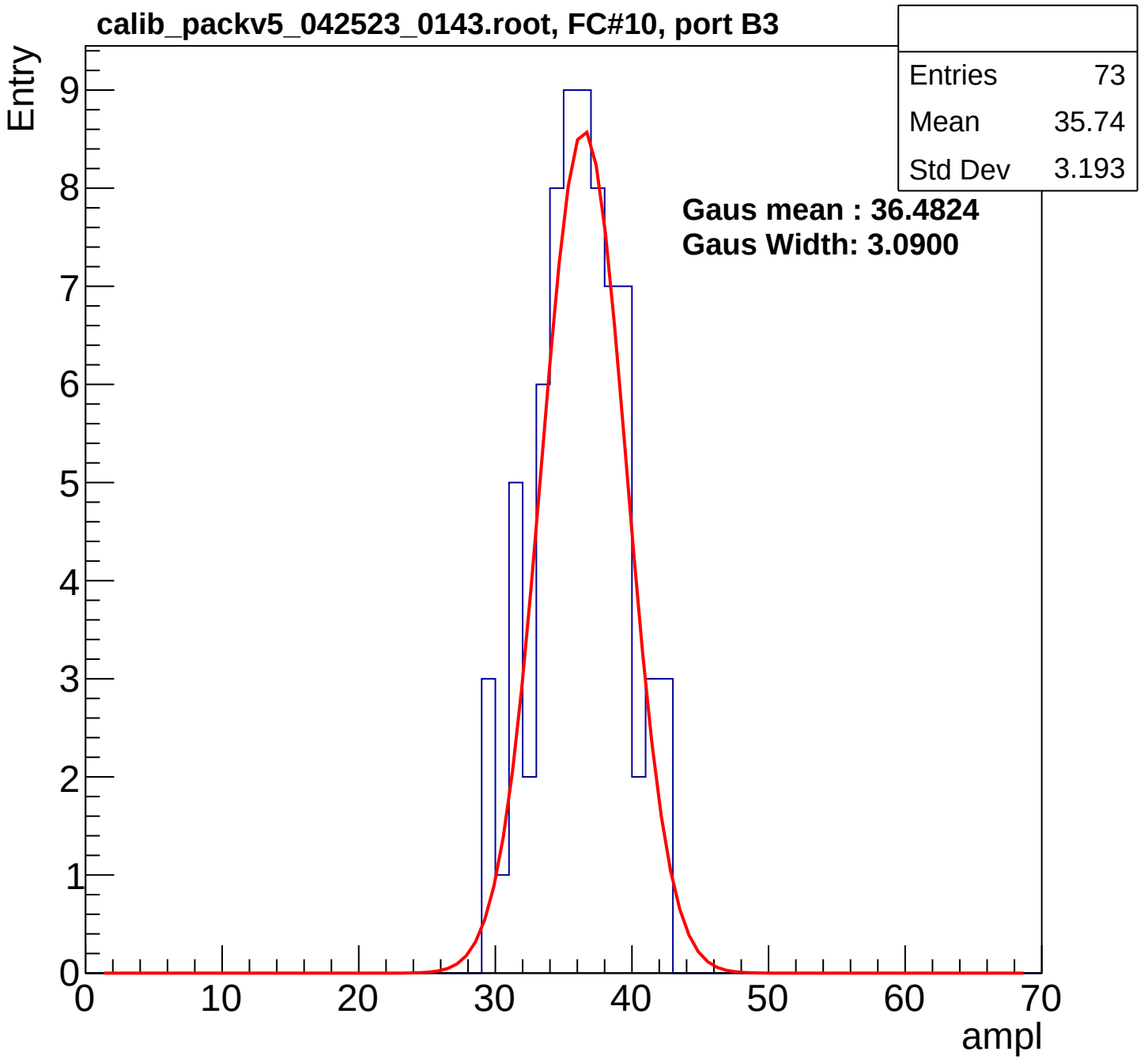
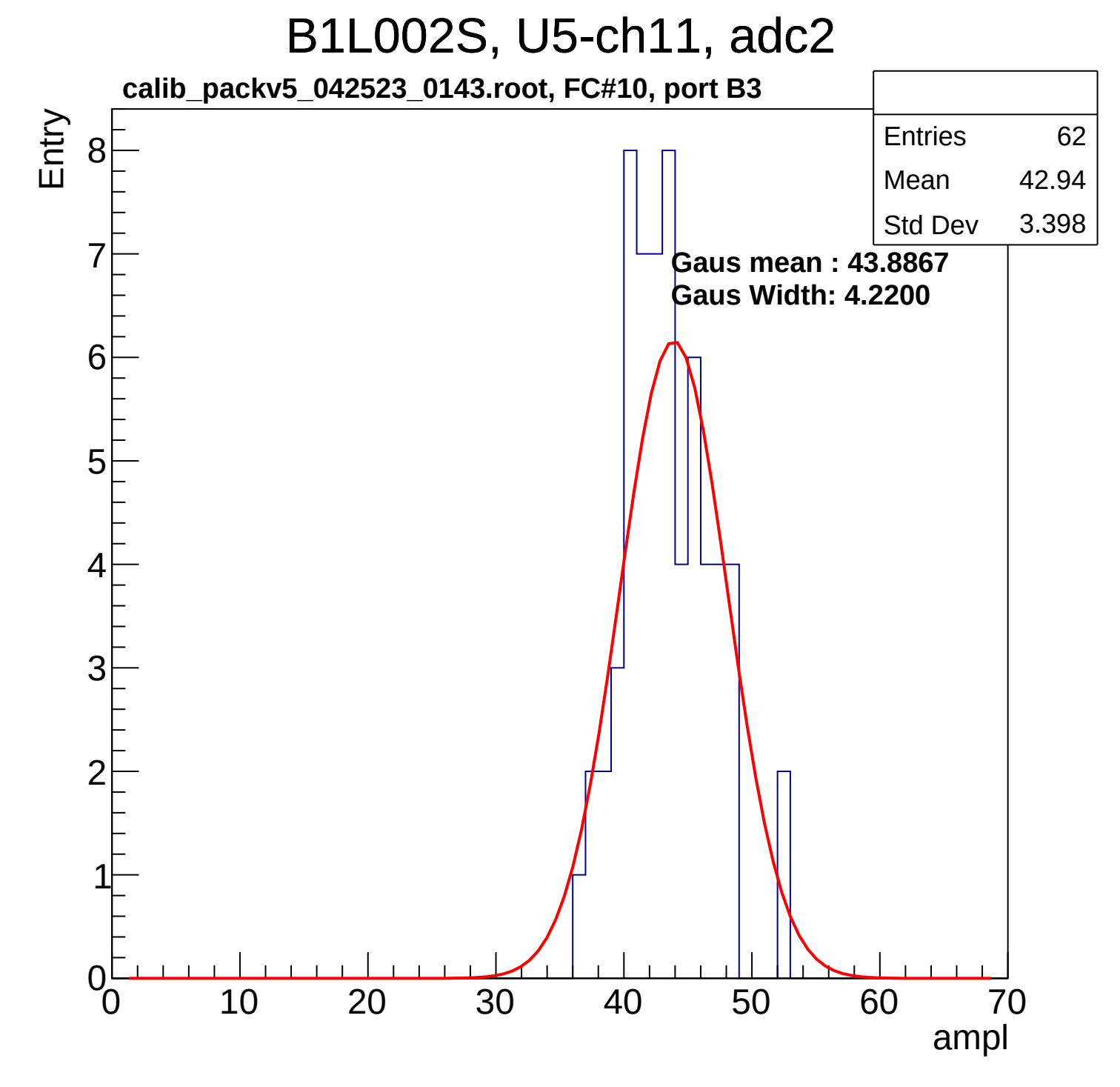

B1L002S, U5-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.74
Std Dev 3.193
Gaus mean : 36.4824
Gaus Width: 3.0900
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.94
Std Dev 3.398
Gaus mean : 43.8867
Gaus Width: 4.2200
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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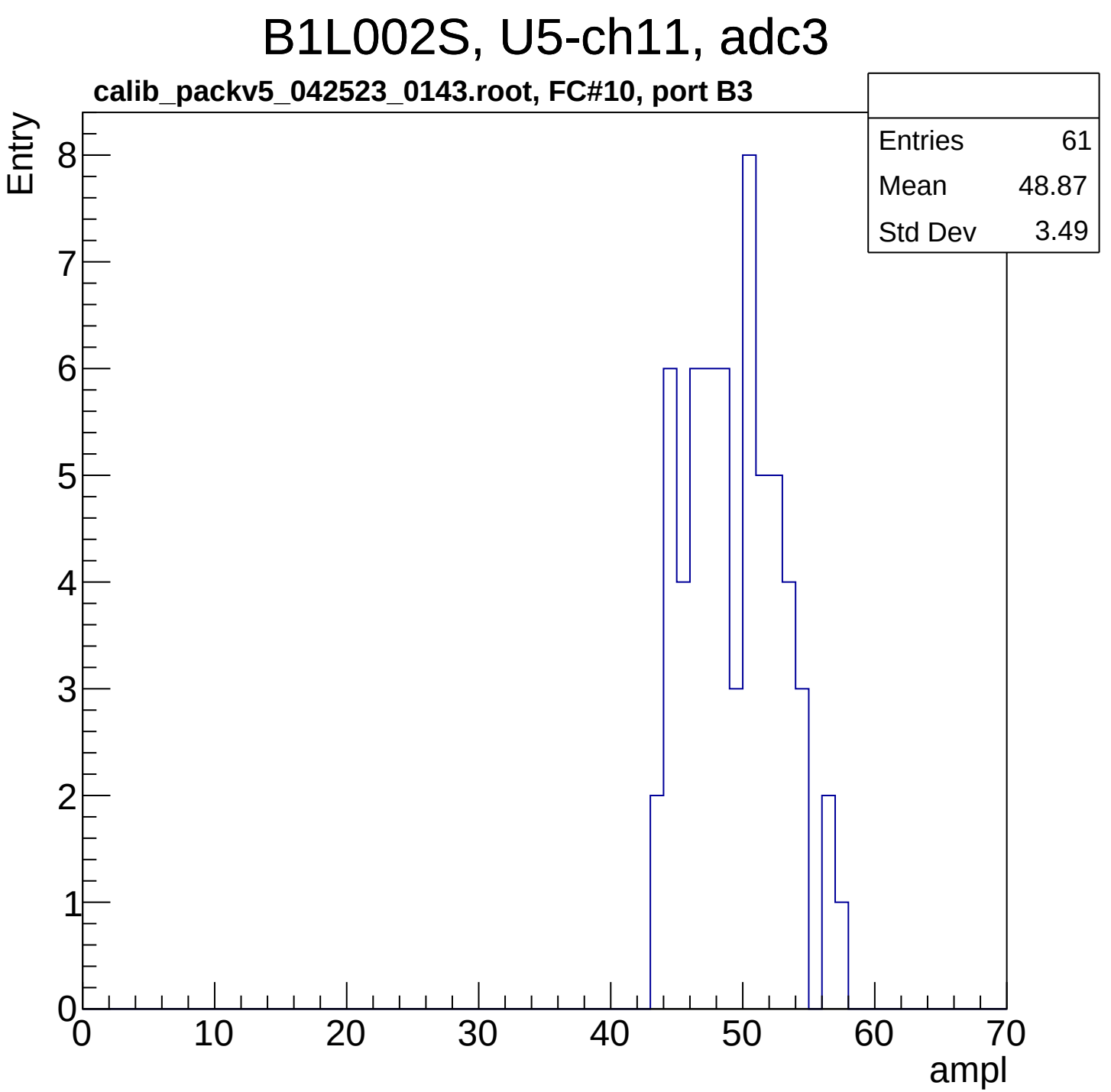
40
50
60
70


B1L002S, U5-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.87
Std Dev 3.49
Entry
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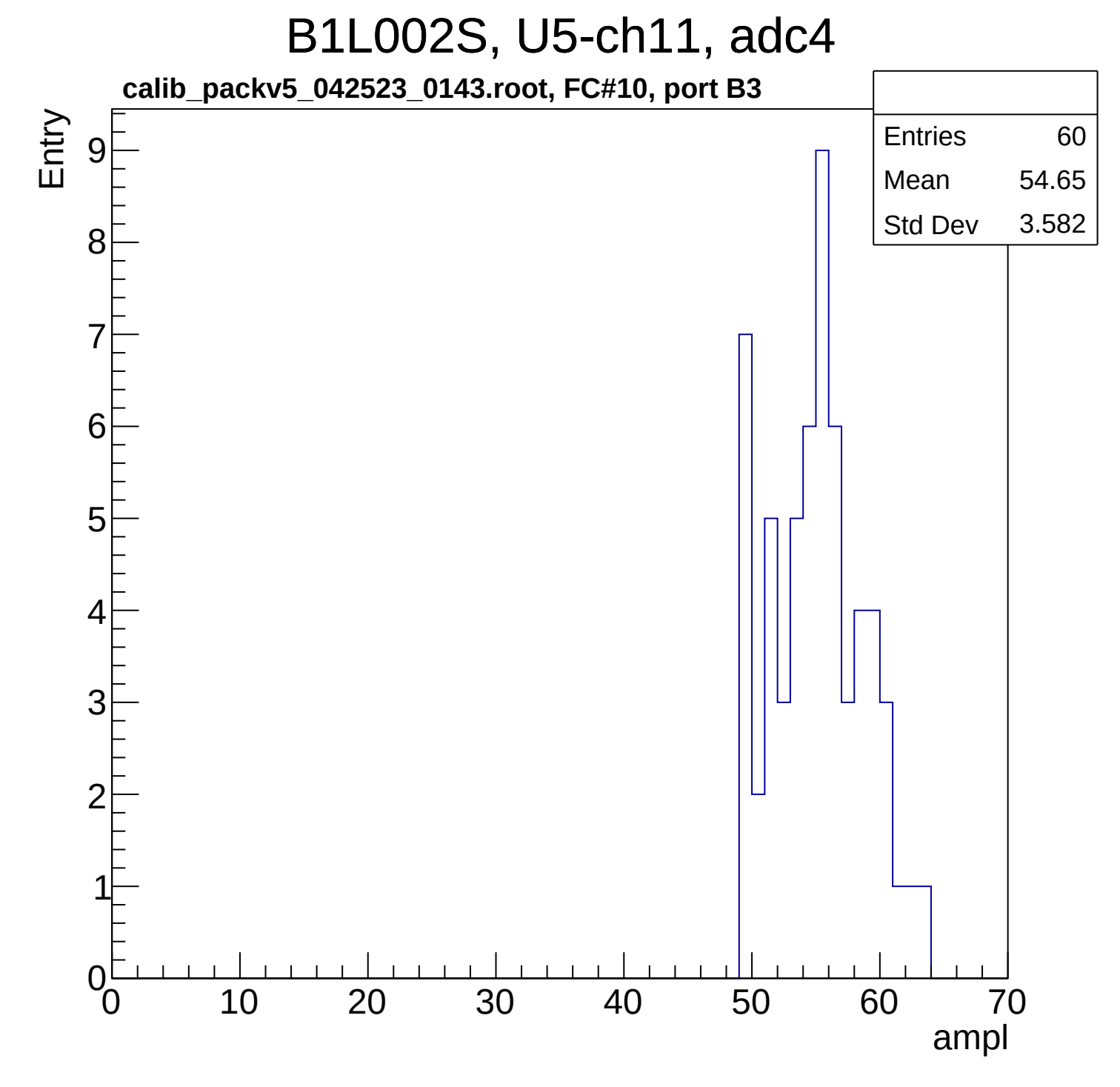
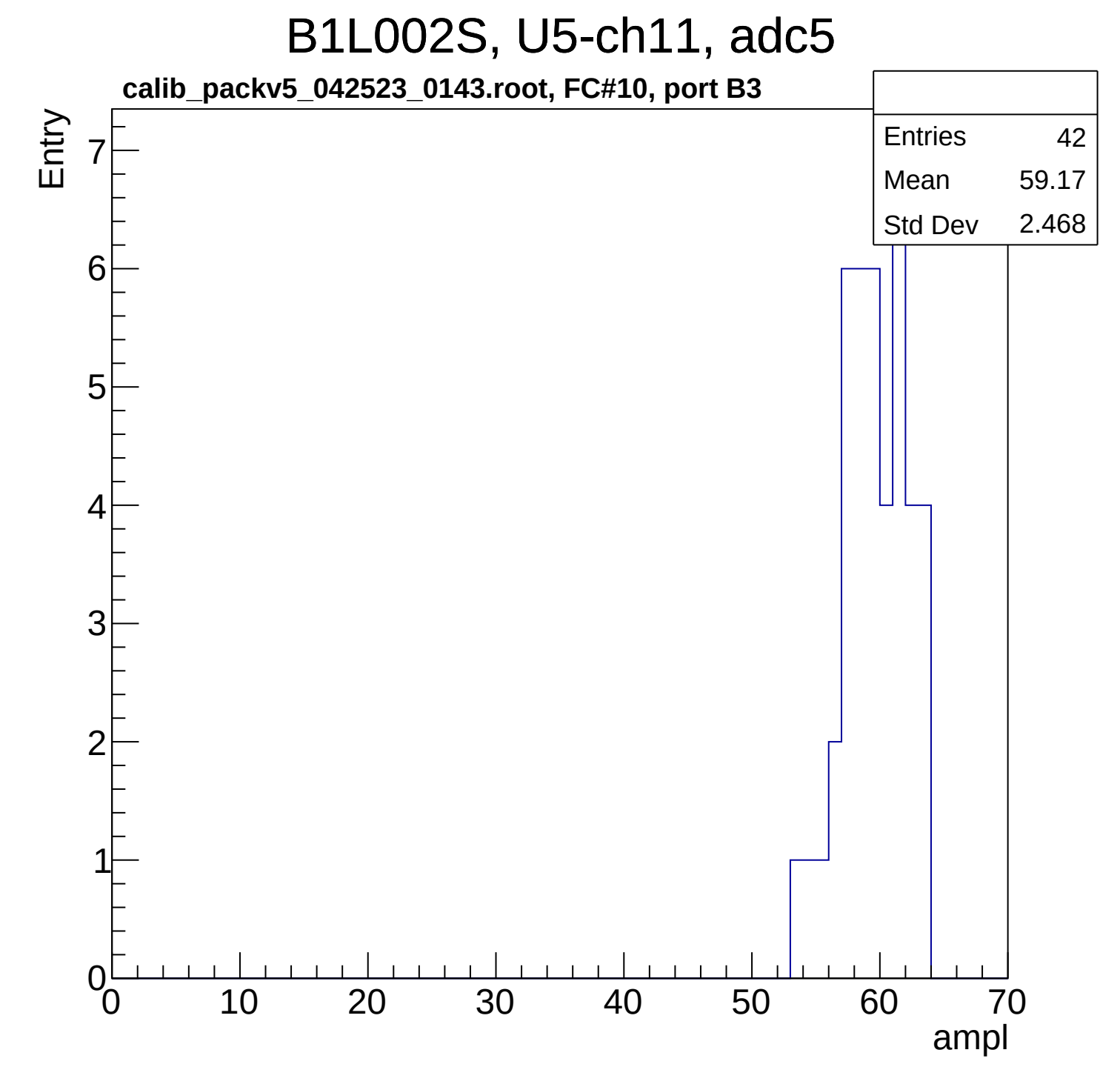
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.65
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.17
Std Dev 2.468
Entry
0
1
2
3
4
5
6
7
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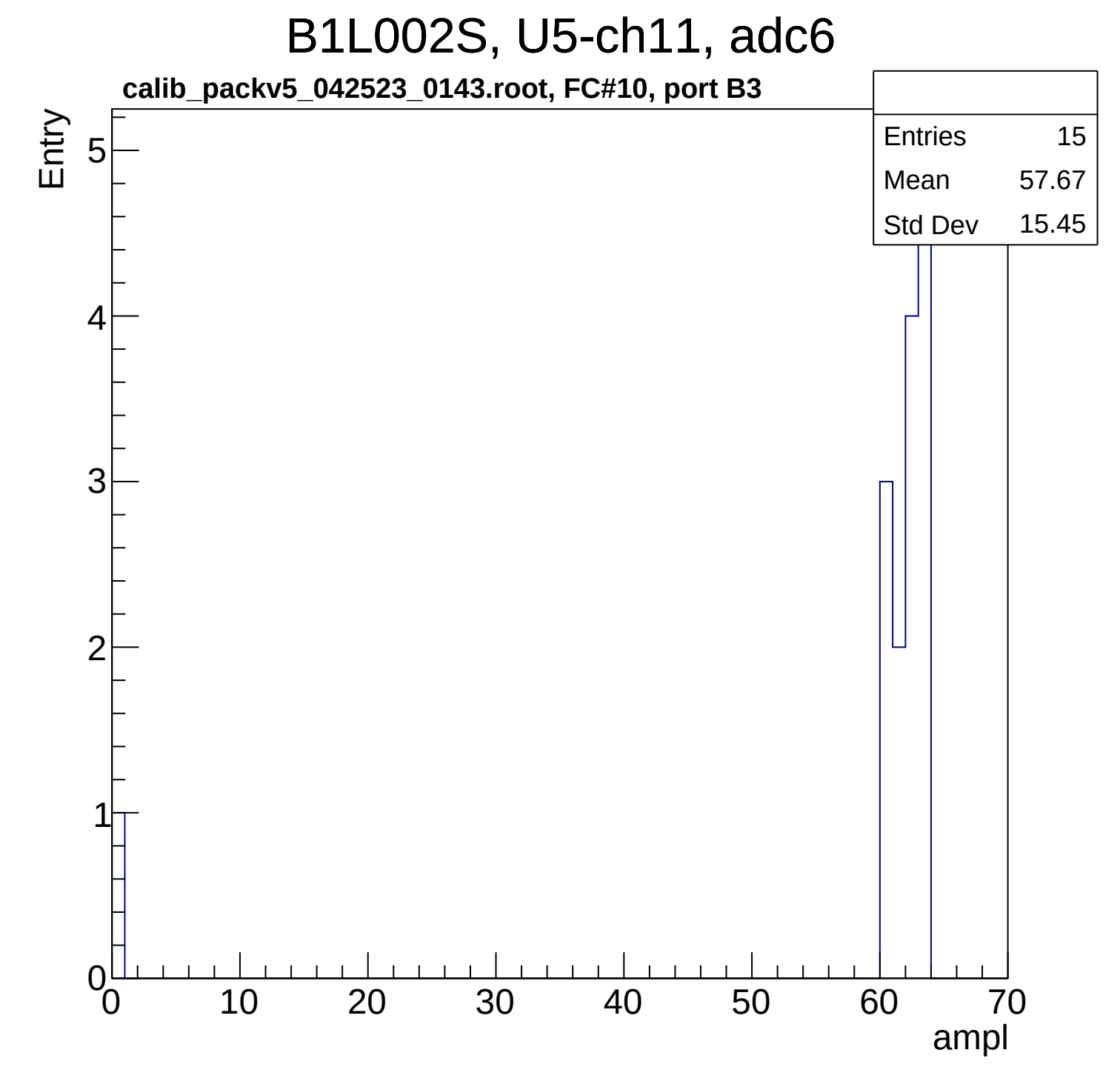
60
70
ampl


B1L002S, U5-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.67
Std Dev 15.45
Entry
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1
2
3
4
5
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10
20
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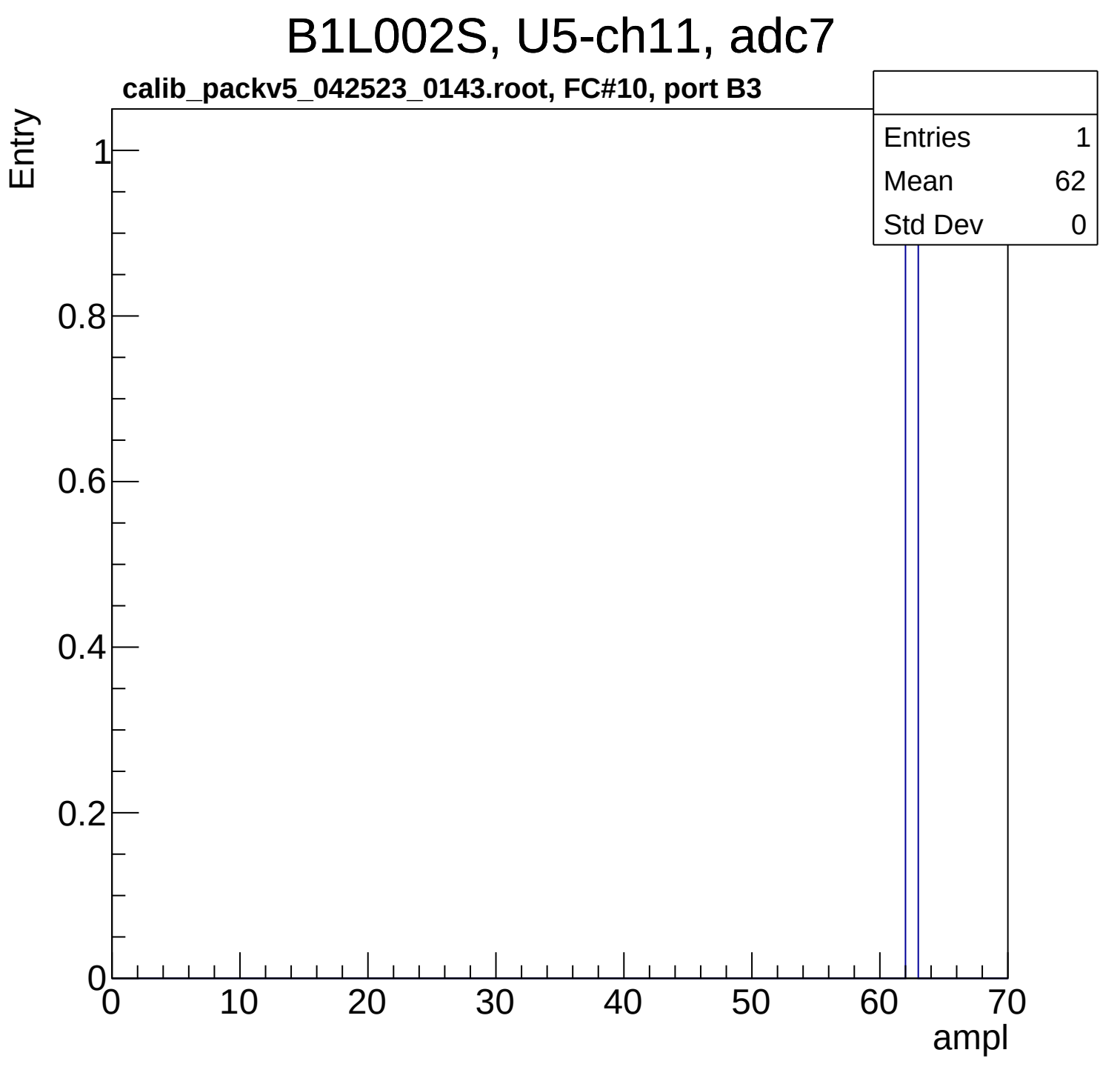
50
60
70
ampl


B1L002S, U5-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
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0.6
0.4
0.2
0
0
10
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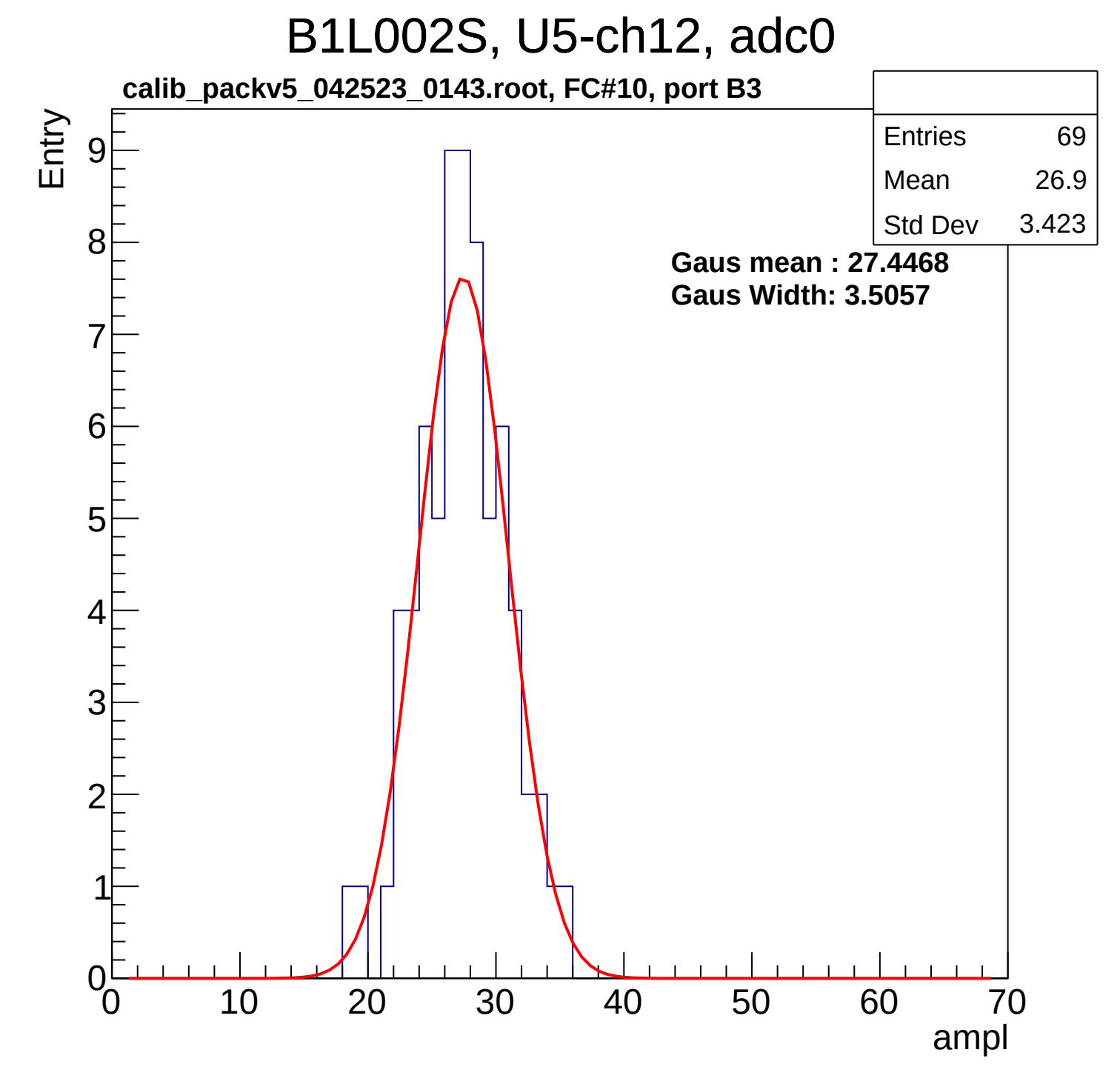
40
50
60
70
ampl


B1L002S, U5-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 26.9
Std Dev 3.423
Gaus mean : 27.4468
Gaus Width: 3.5057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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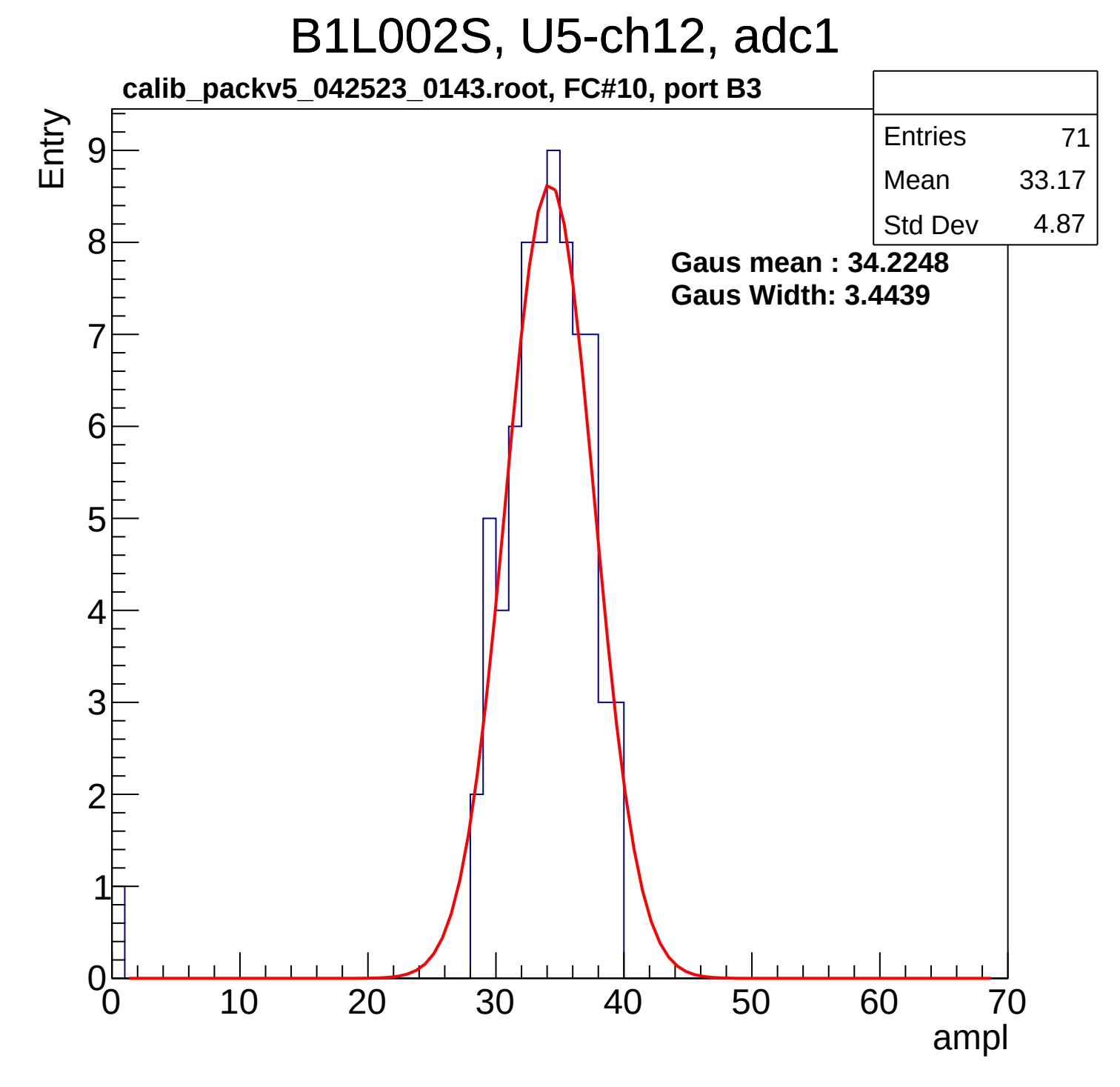
B1L002S, U5-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.17
Std Dev 4.87
Gaus mean : 34.2248
Gaus Width: 3.4439
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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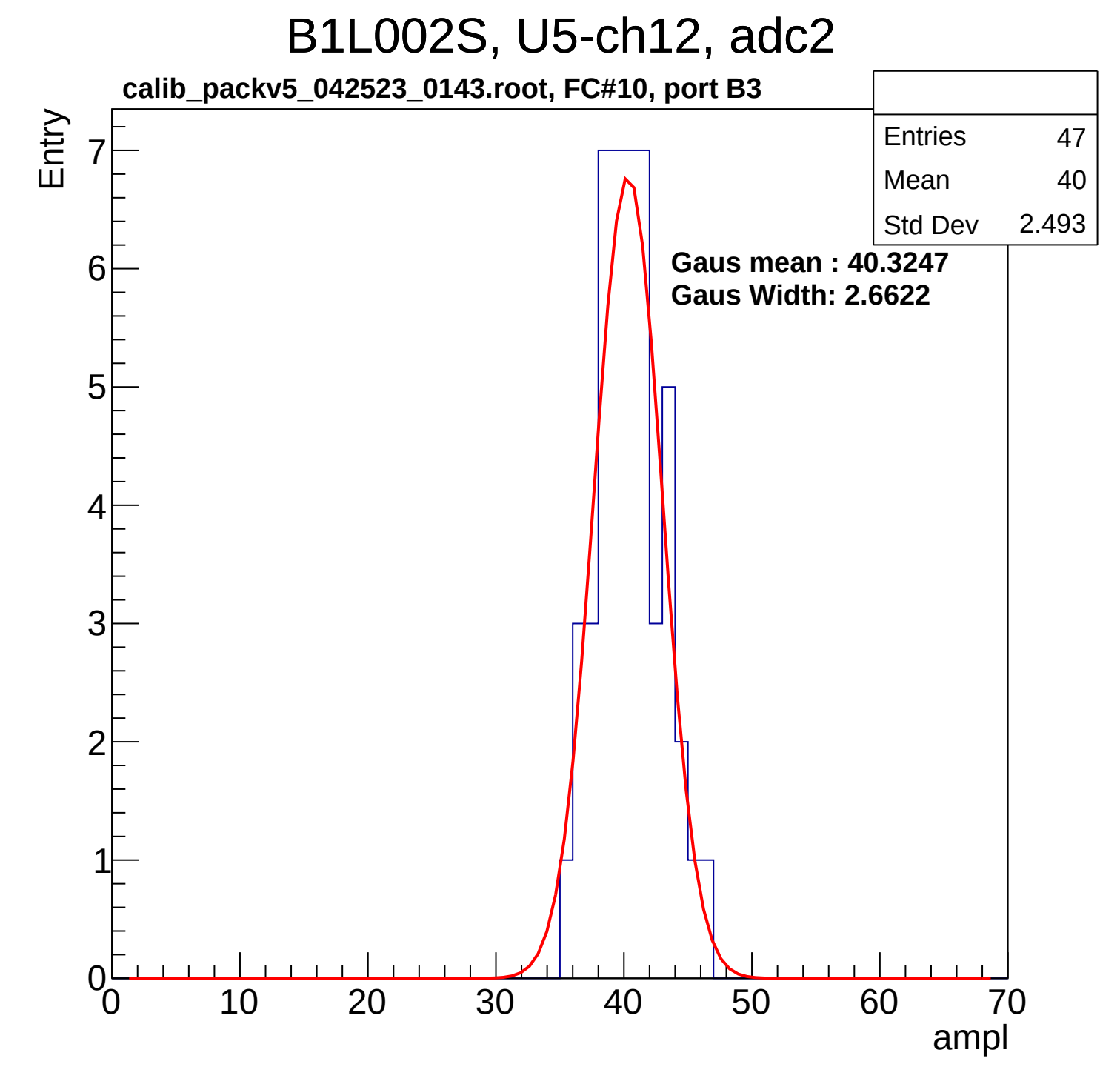
B1L002S, U5-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40
Std Dev 2.493
Gaus mean : 40.3247
Gaus Width: 2.6622
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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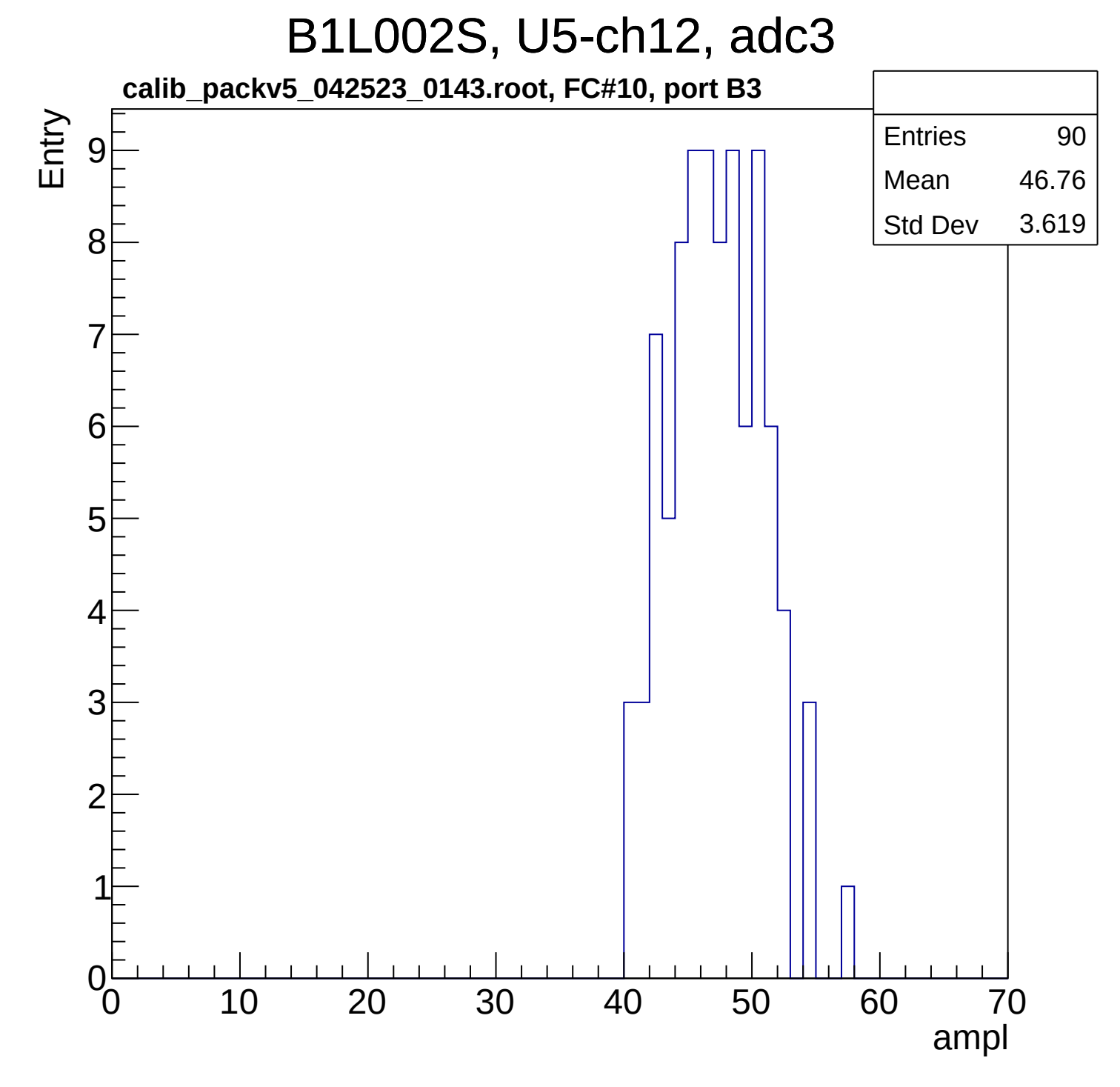
B1L002S, U5-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 46.76
Std Dev 3.619
Entry
ampl
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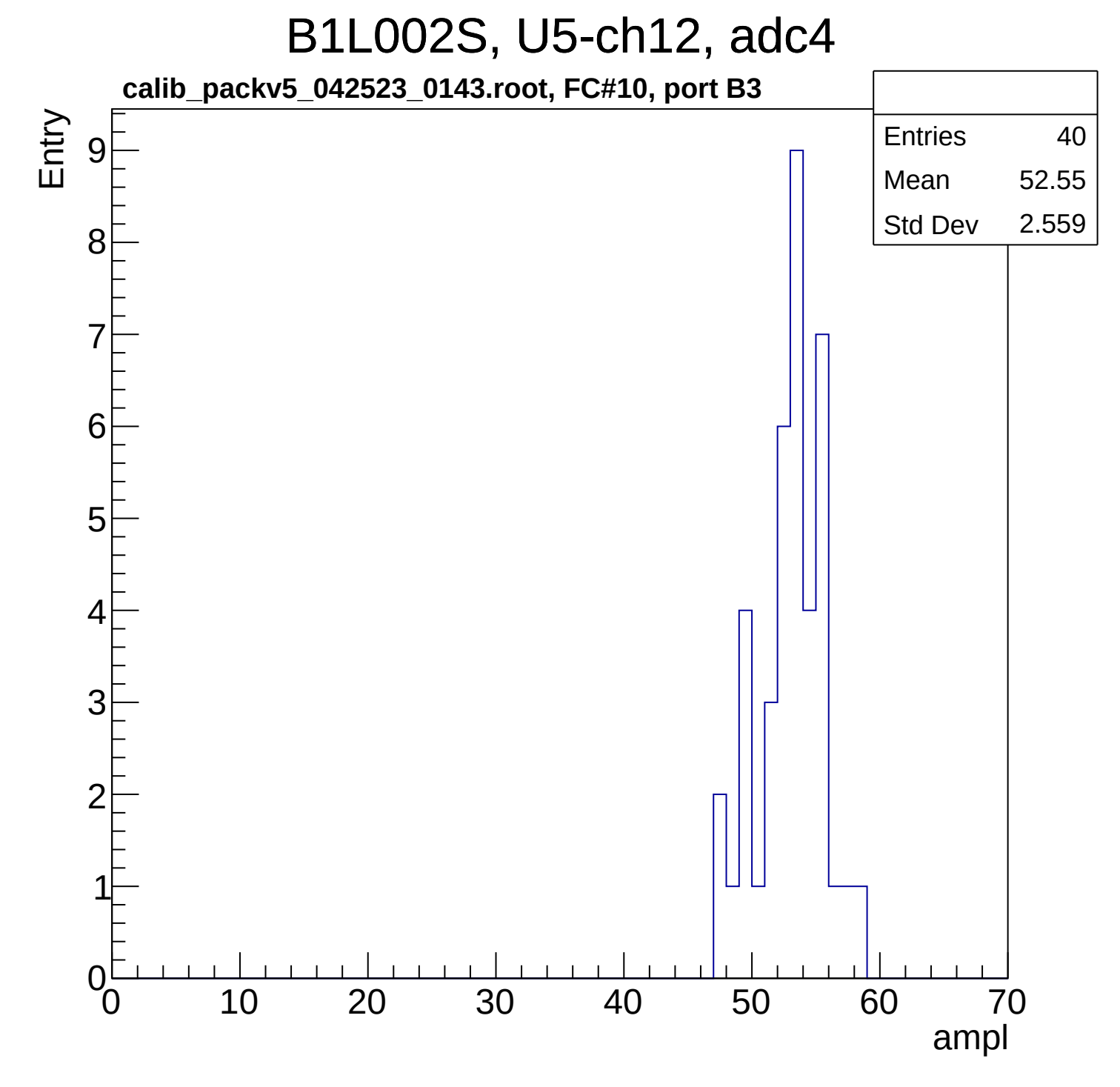
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 52.55
Std Dev 2.559
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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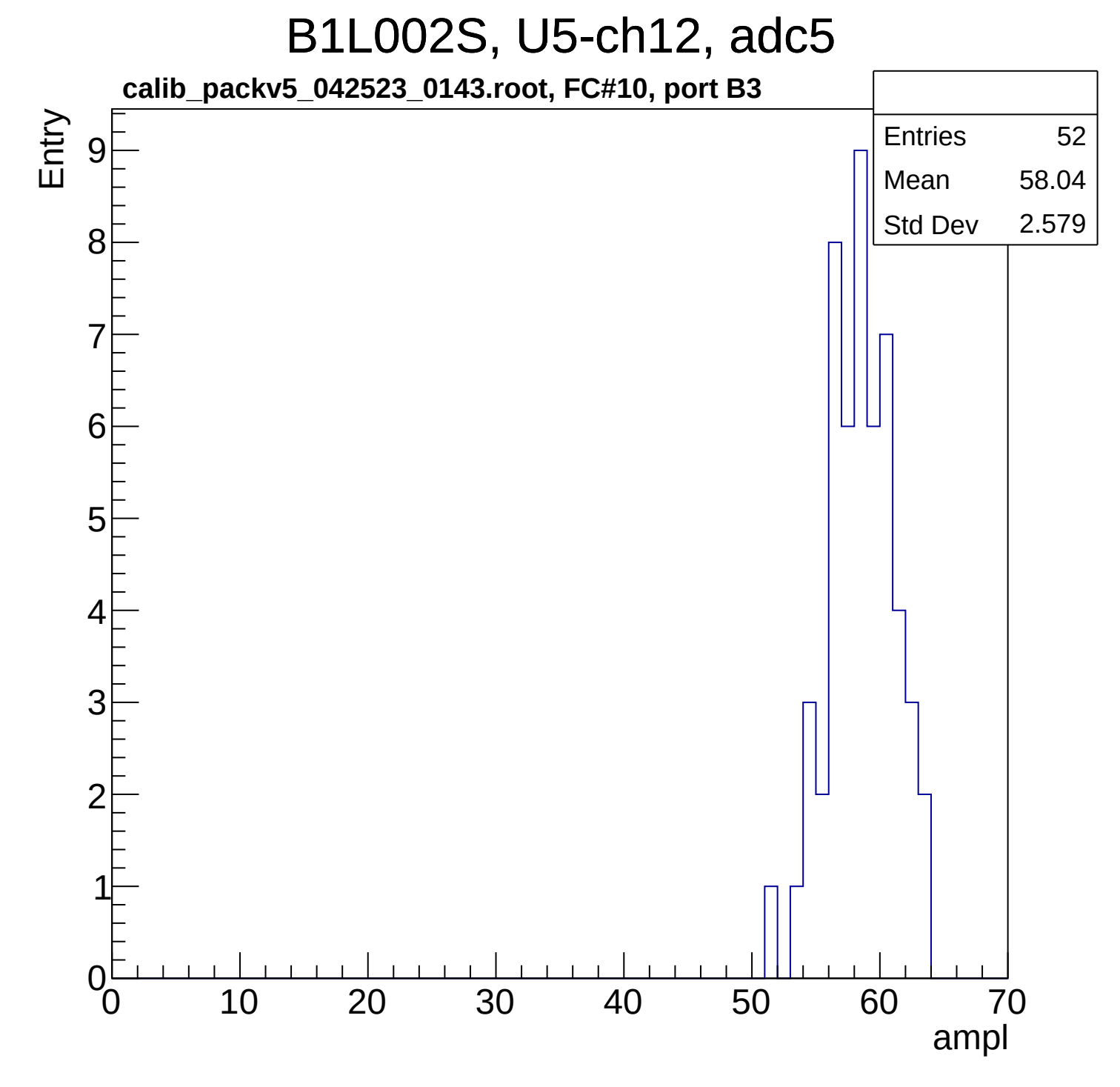
B1L002S, U5-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.04
Std Dev 2.579
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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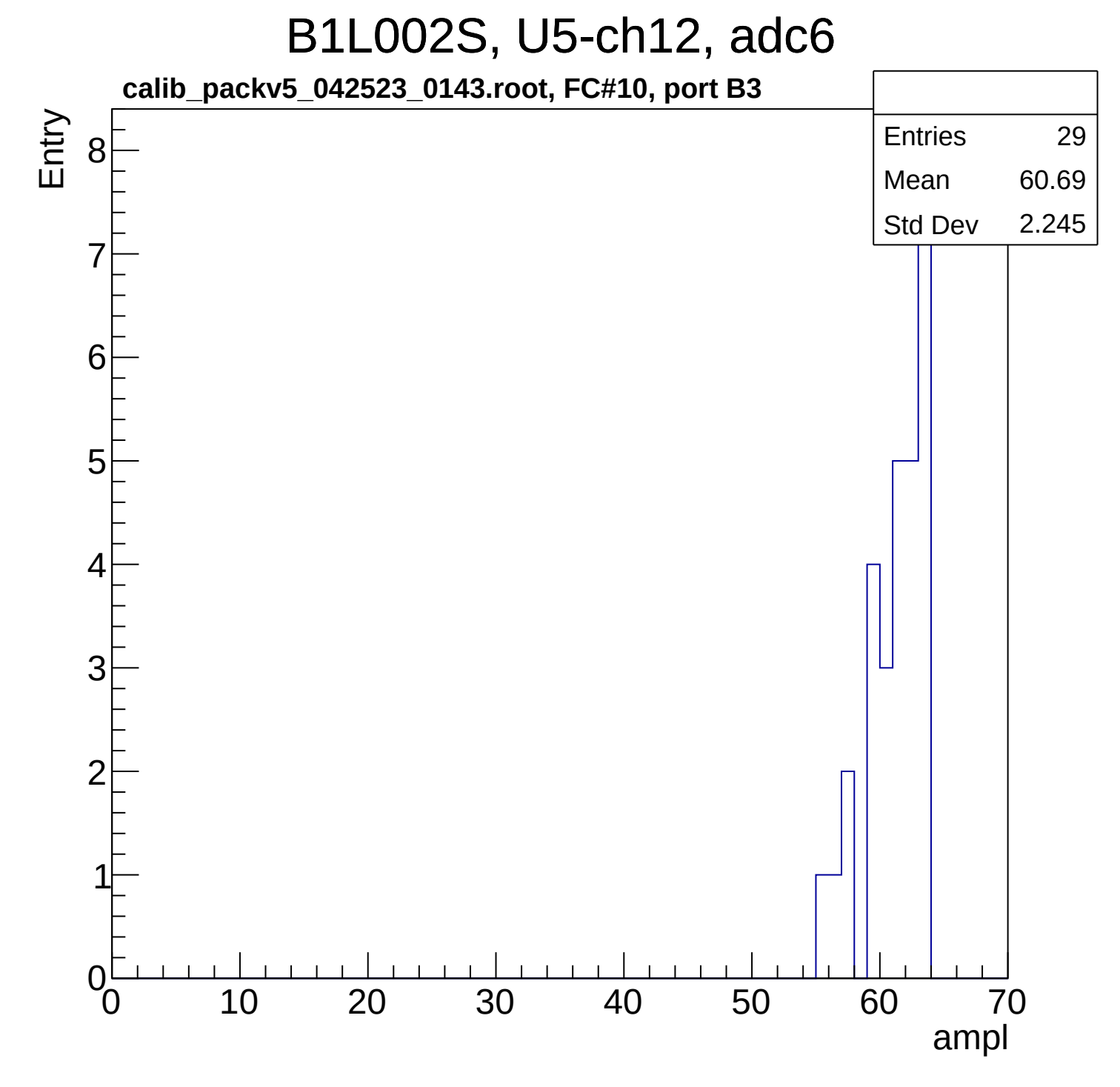
B1L002S, U5-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 60.69
Std Dev 2.245
Entry
ampl
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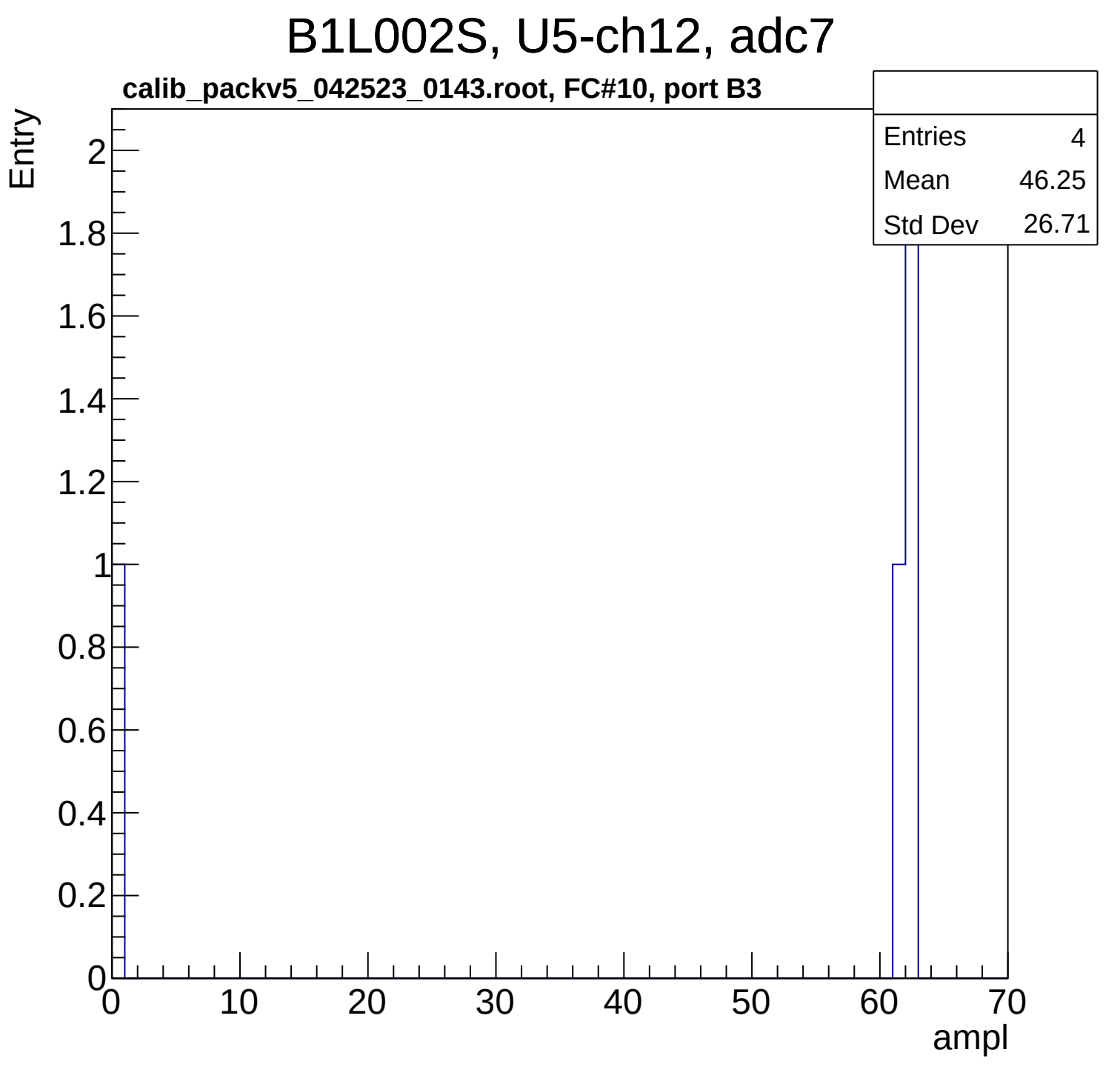
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.25
Std Dev 26.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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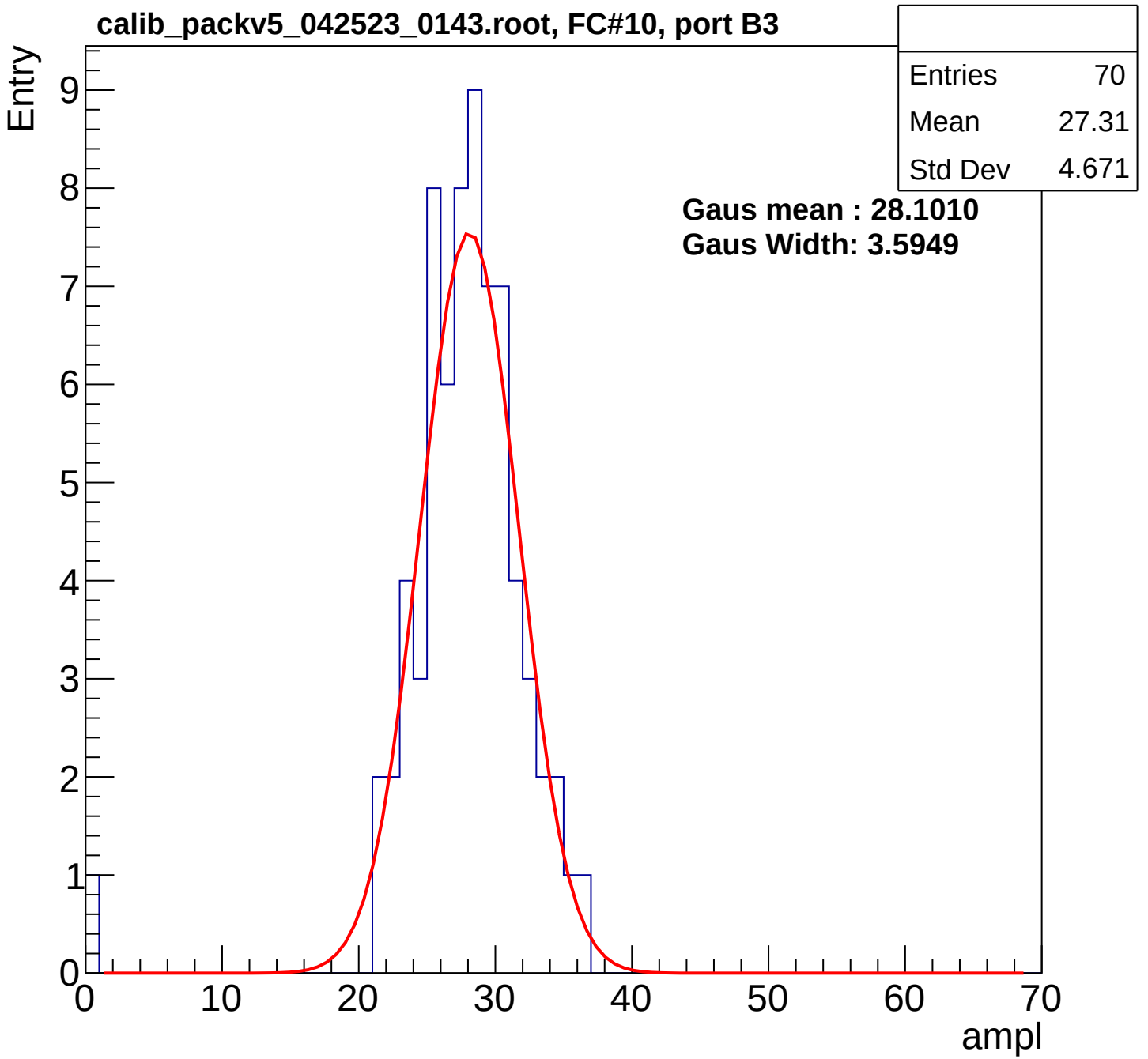
B1L002S, U5-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.31
Std Dev 4.671
Gaus mean : 28.1010
Gaus Width: 3.5949
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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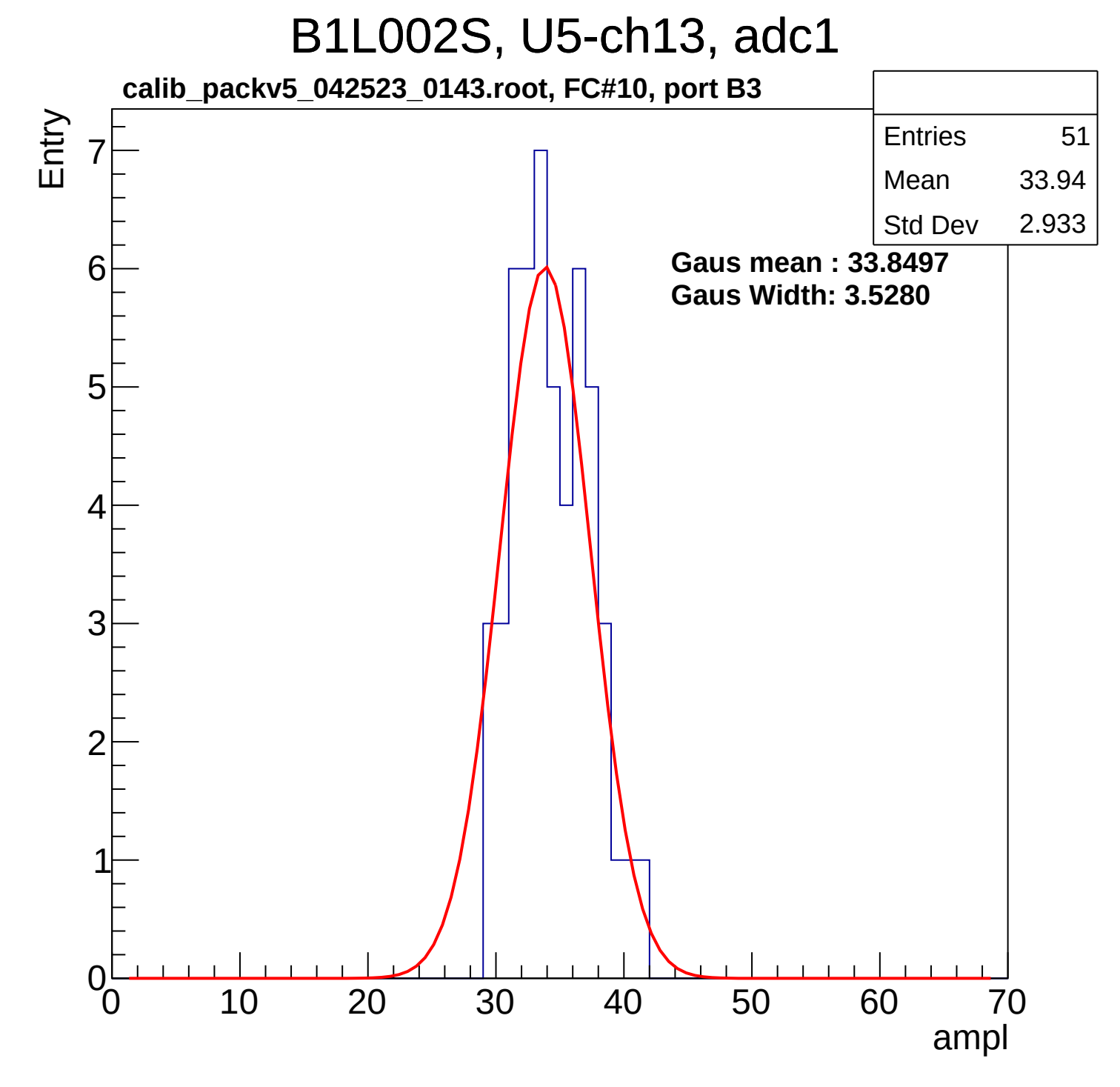
B1L002S, U5-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 33.94
Std Dev 2.933
Gaus mean : 33.8497
Gaus Width: 3.5280
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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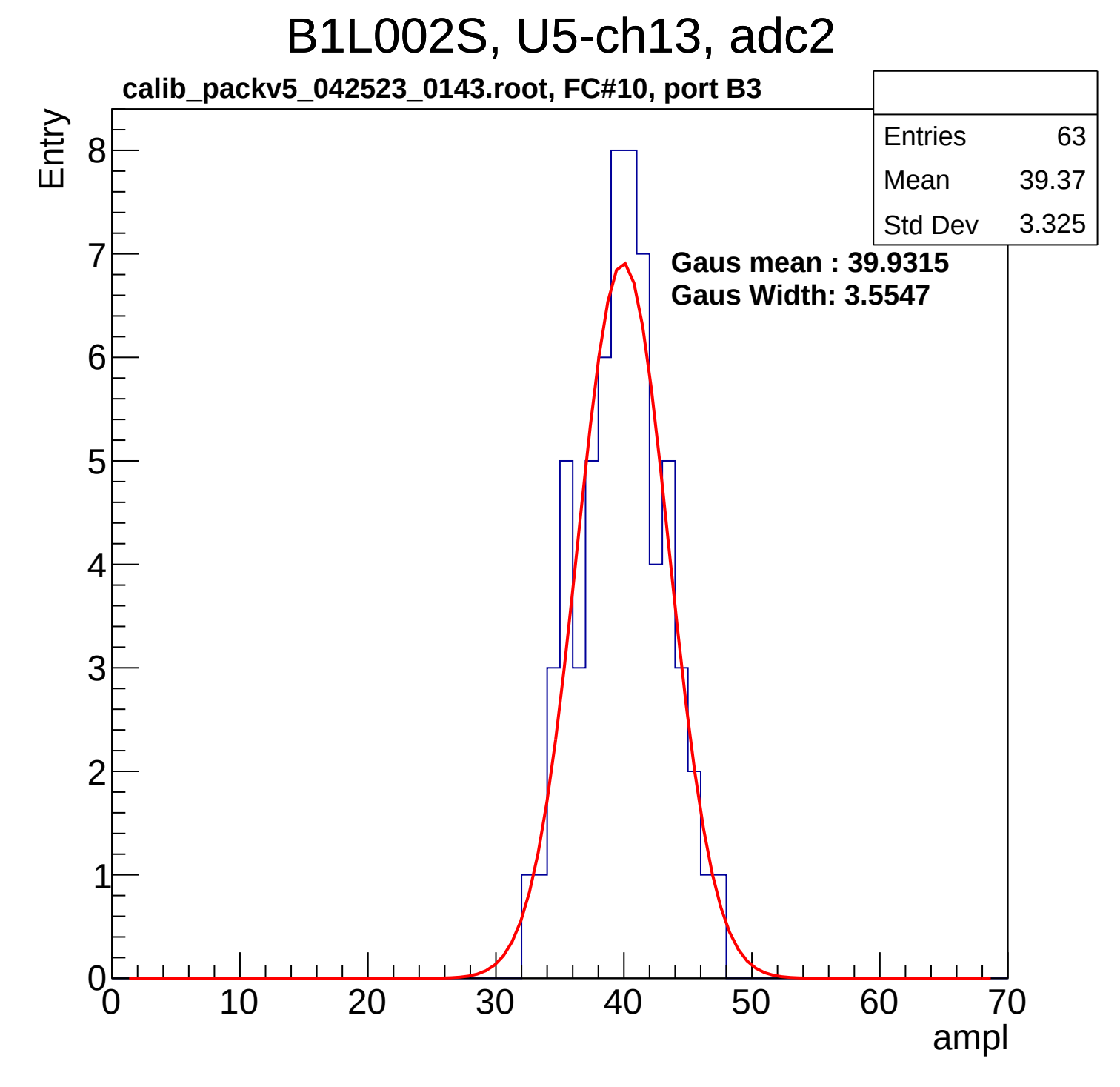
B1L002S, U5-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.37
Std Dev 3.325
Gaus mean : 39.9315
Gaus Width: 3.5547
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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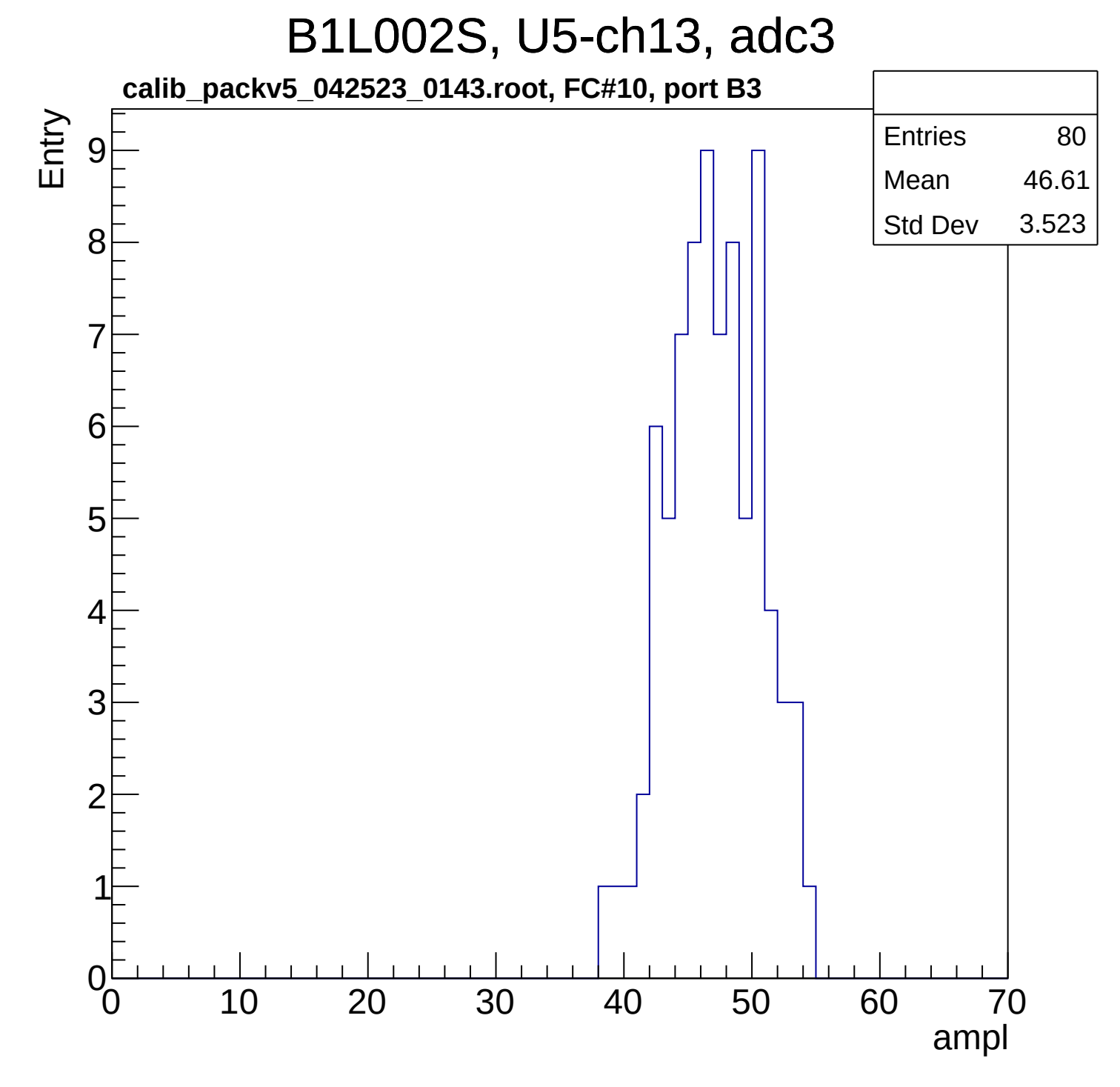
B1L002S, U5-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 46.61
Std Dev 3.523
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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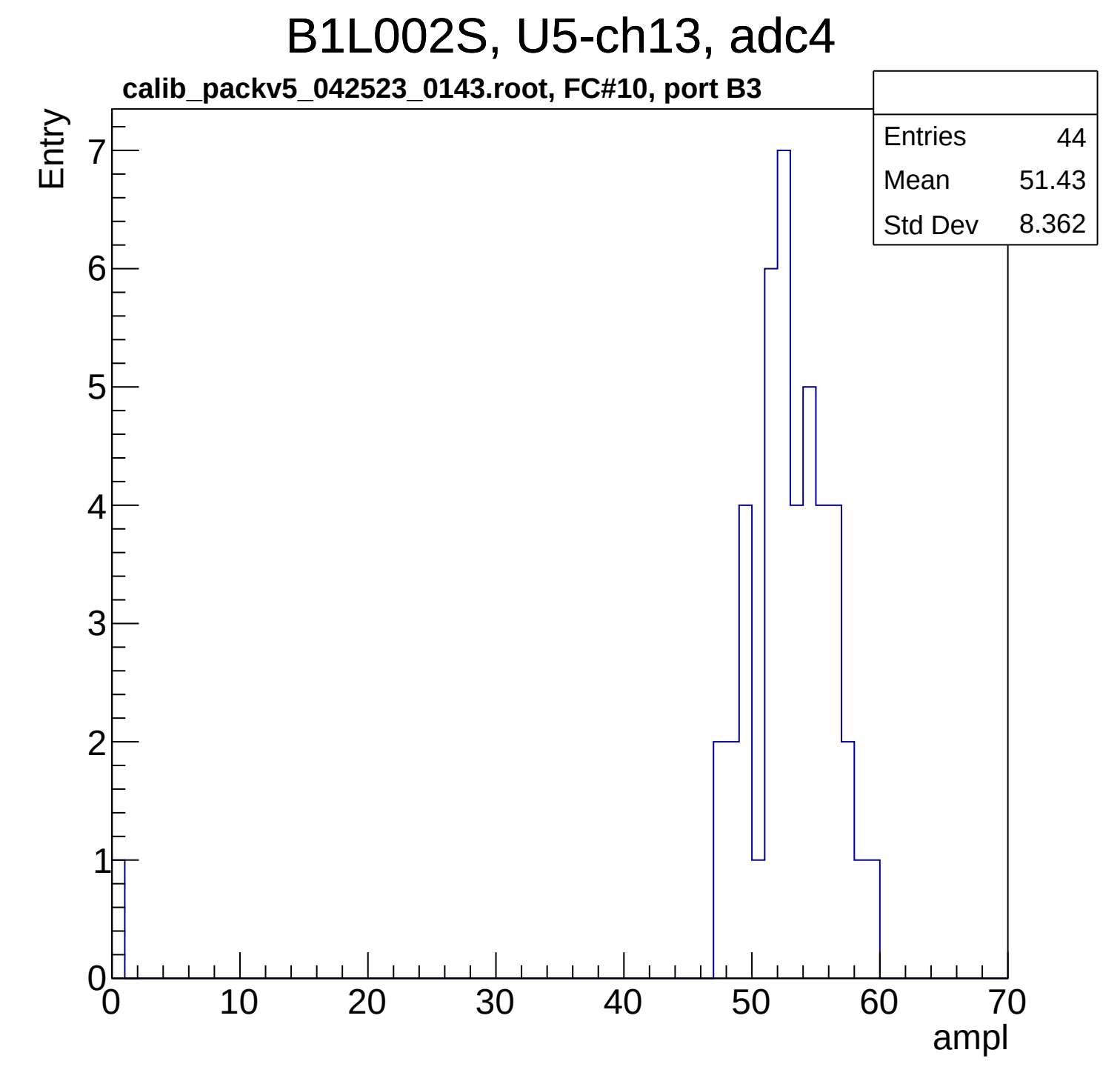
B1L002S, U5-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 51.43
Std Dev 8.362
Entry
ampl
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3
4
5
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0
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20
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60
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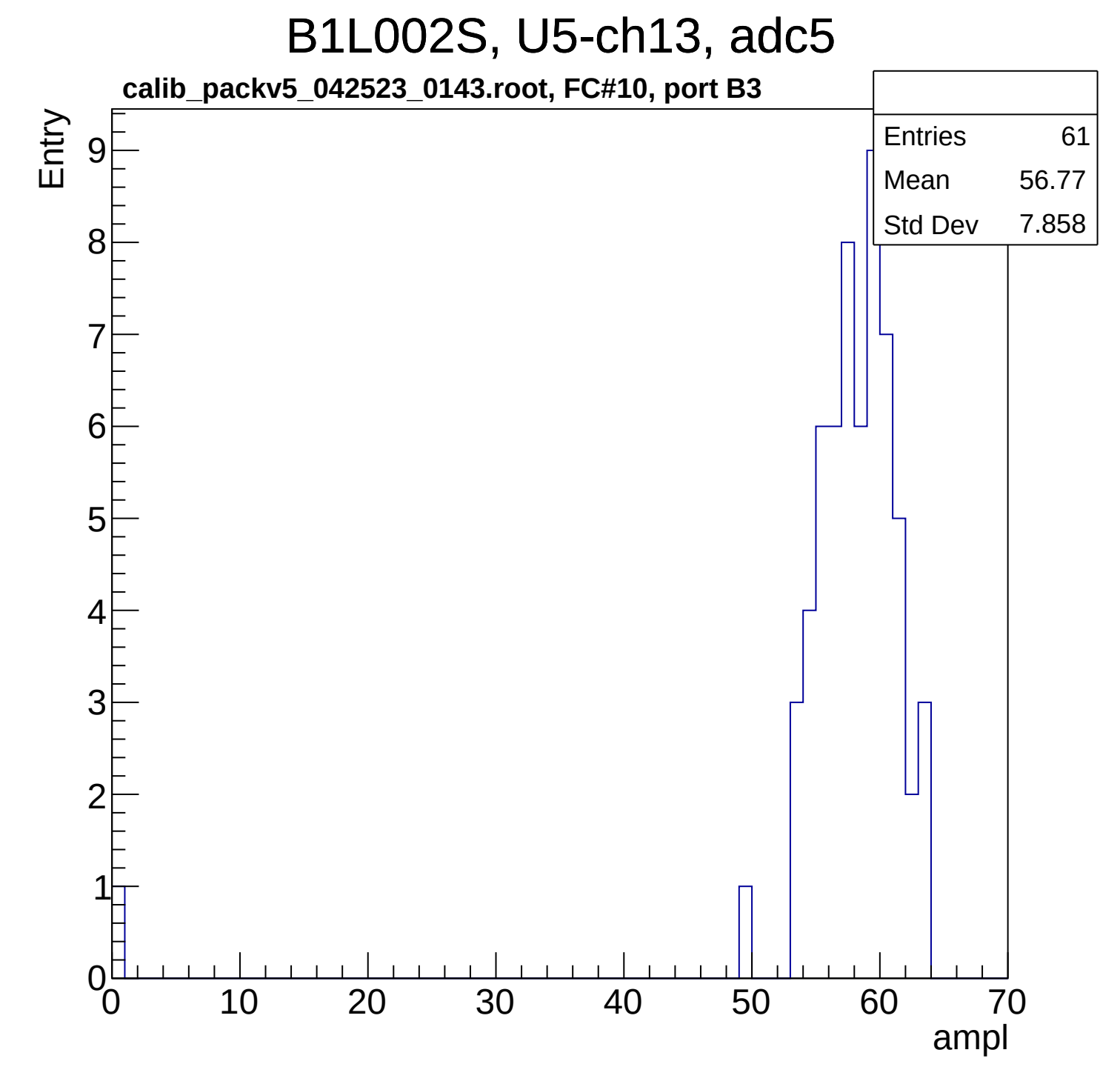
B1L002S, U5-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.77
Std Dev 7.858
Entry
ampl
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3
4
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6
7
8
9
0
10
20
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60
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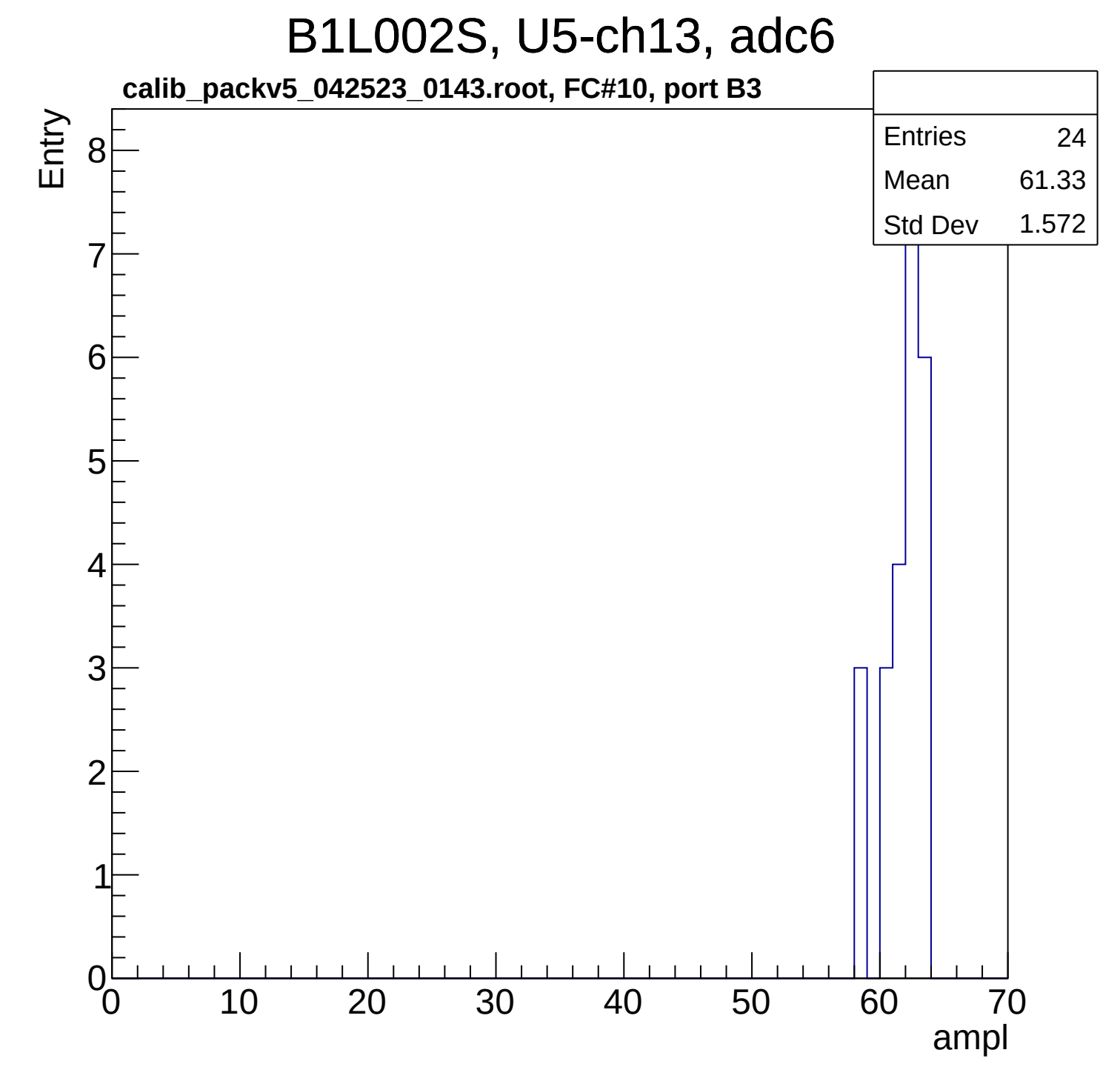
B1L002S, U5-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.33
Std Dev 1.572
Entry
ampl
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2
3
4
5
6
7
8
0
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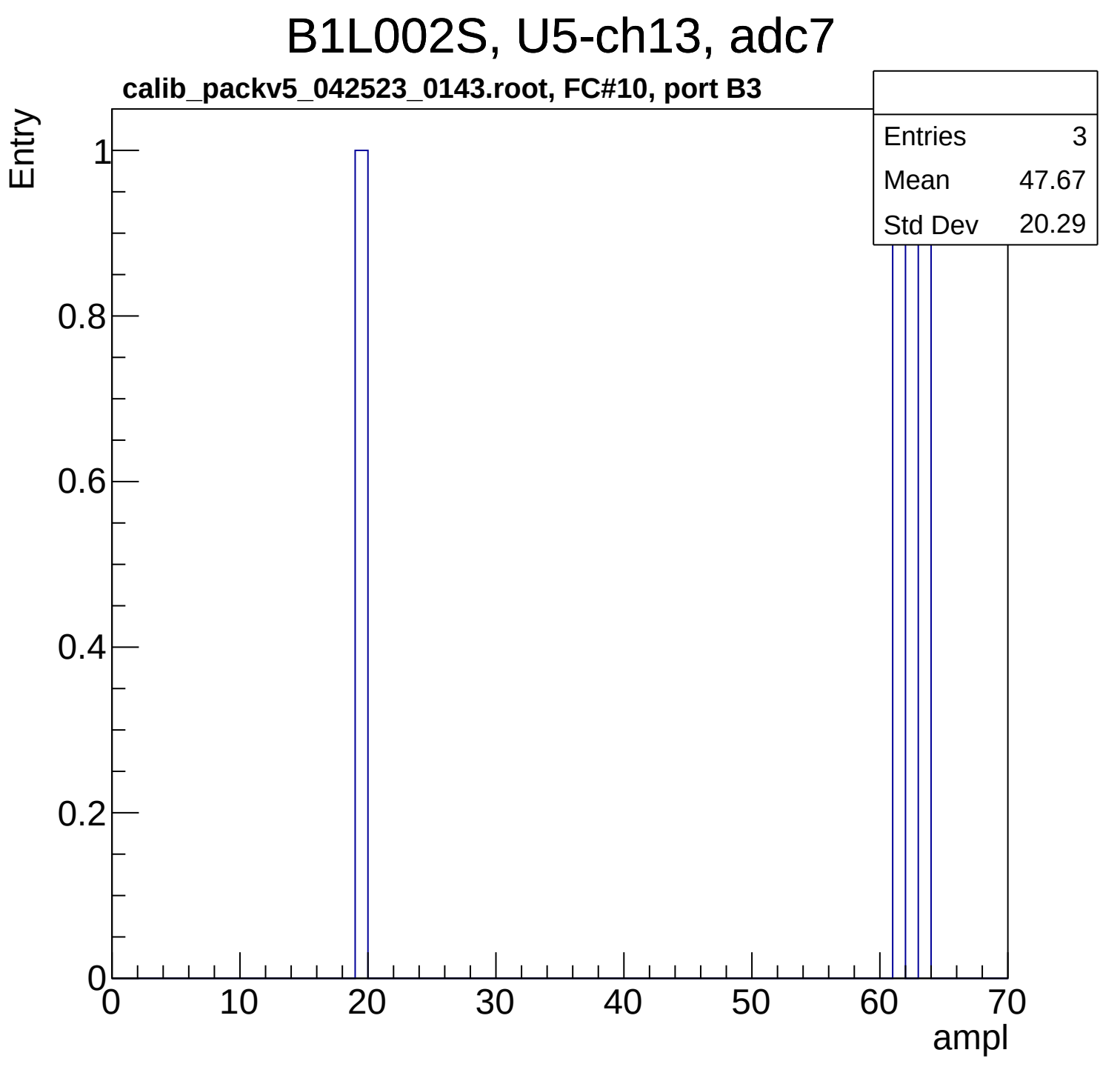
20
30
40
50
60
70


B1L002S, U5-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 47.67
Std Dev 20.29
Entry
ampl
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0.2
0.4
0.6
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0
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50
60
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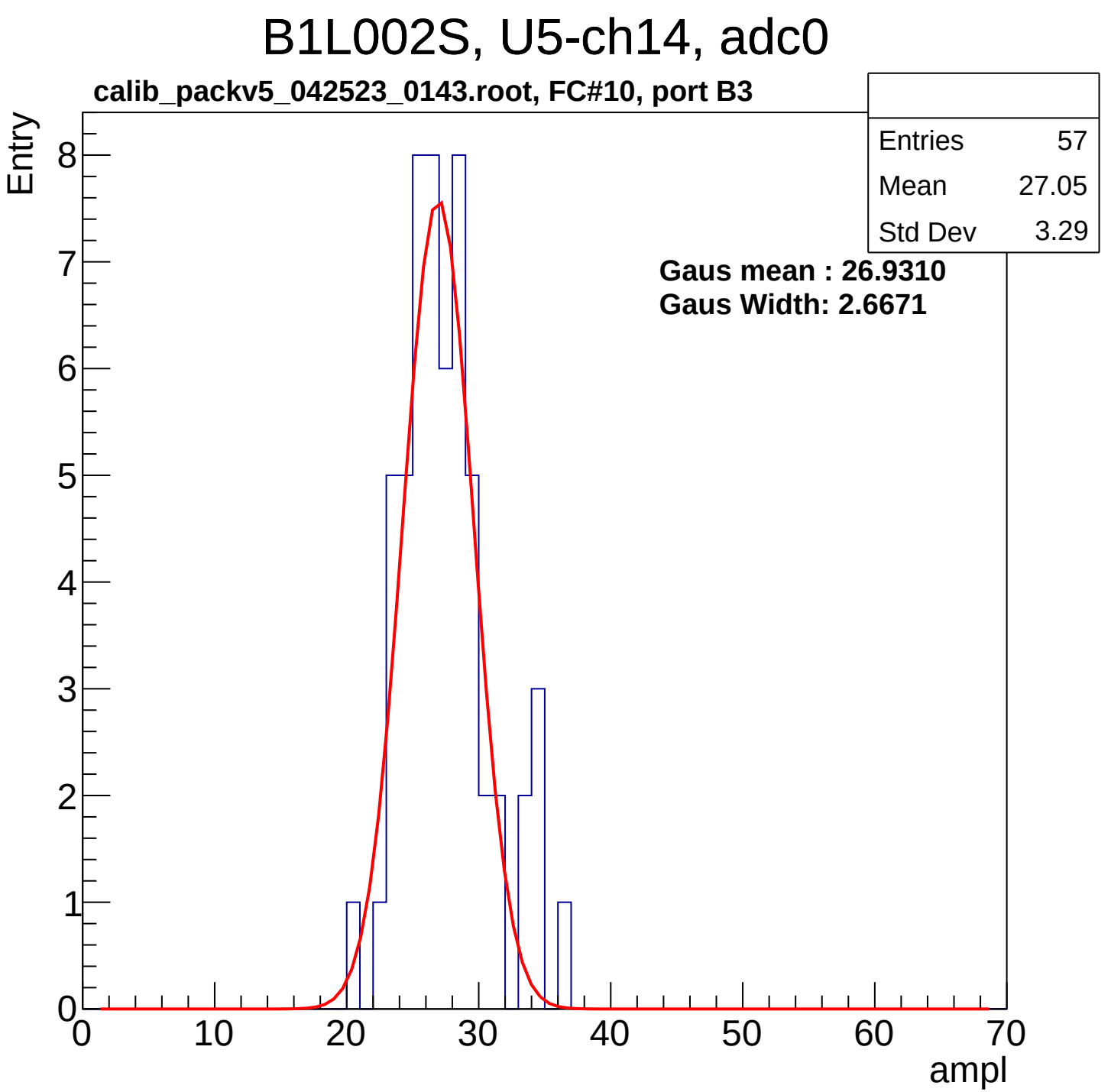
B1L002S, U5-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.05
Std Dev 3.29
Gaus mean : 26.9310
Gaus Width: 2.6671
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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60
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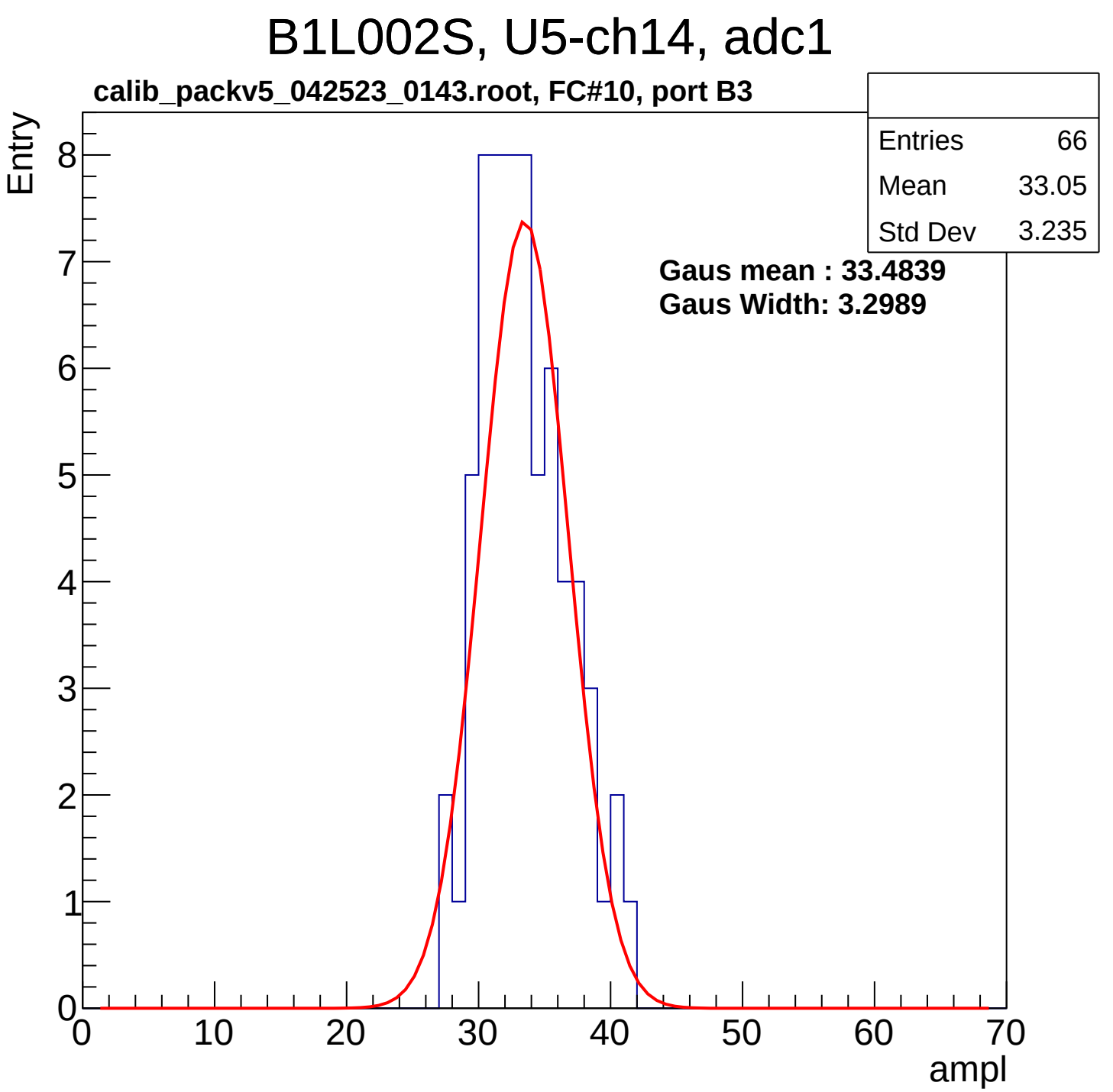
B1L002S, U5-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.05
Std Dev 3.235
Gaus mean : 33.4839
Gaus Width: 3.2989
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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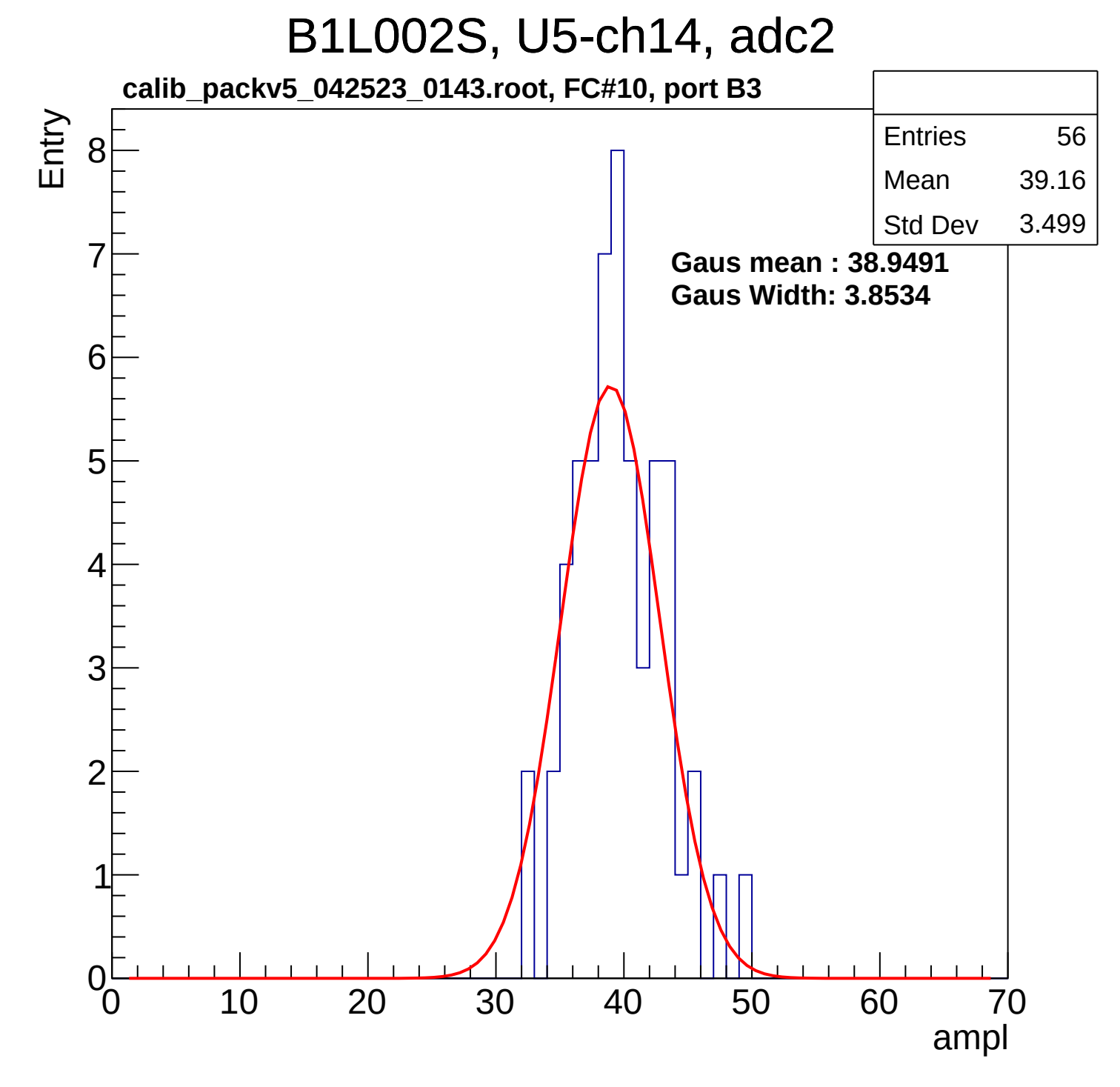
B1L002S, U5-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.16
Std Dev 3.499
Gaus mean : 38.9491
Gaus Width: 3.8534
Entry
ampl
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2
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4
5
6
7
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0
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20
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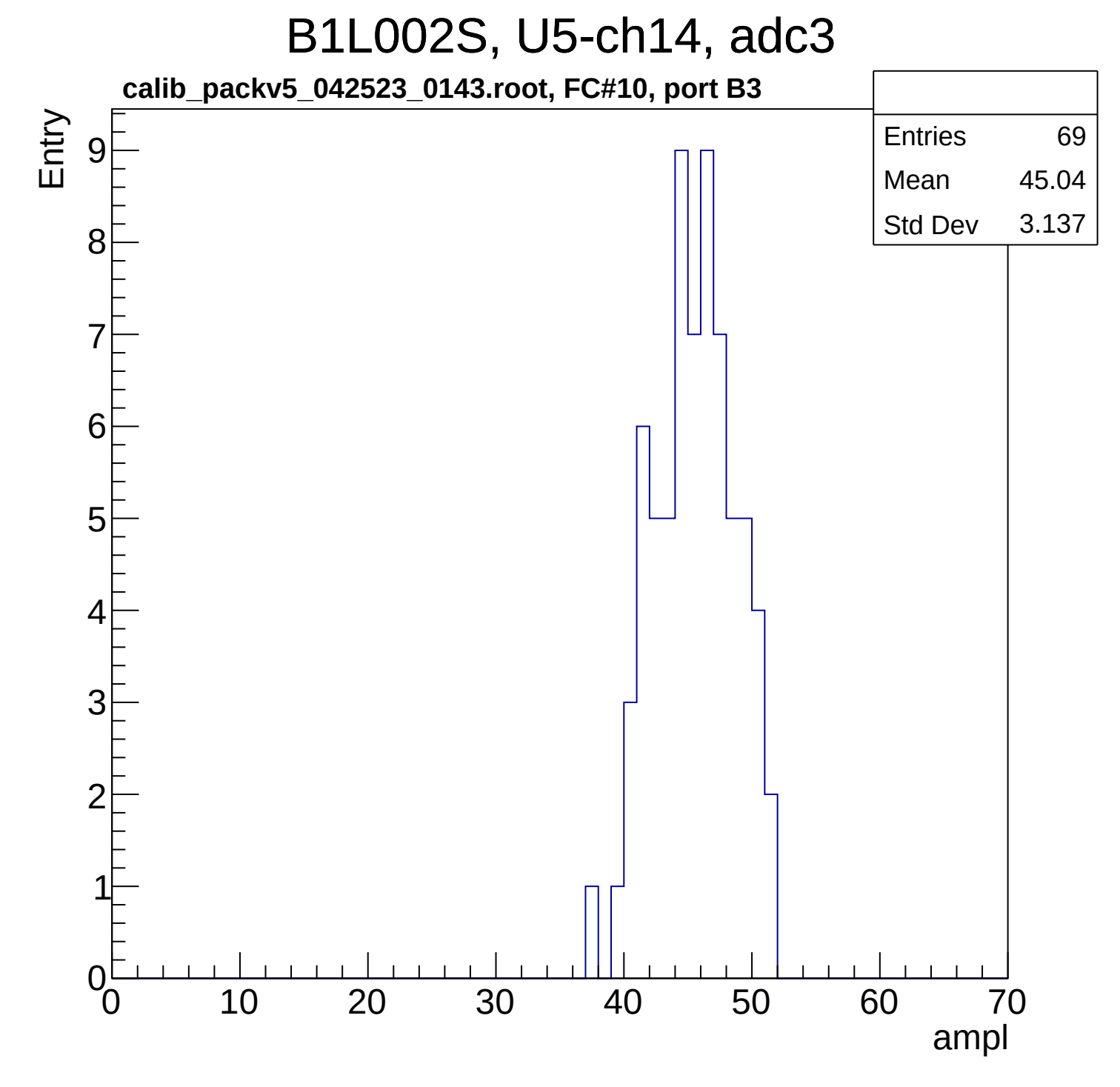
B1L002S, U5-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.04
Std Dev 3.137
Entry
ampl
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60
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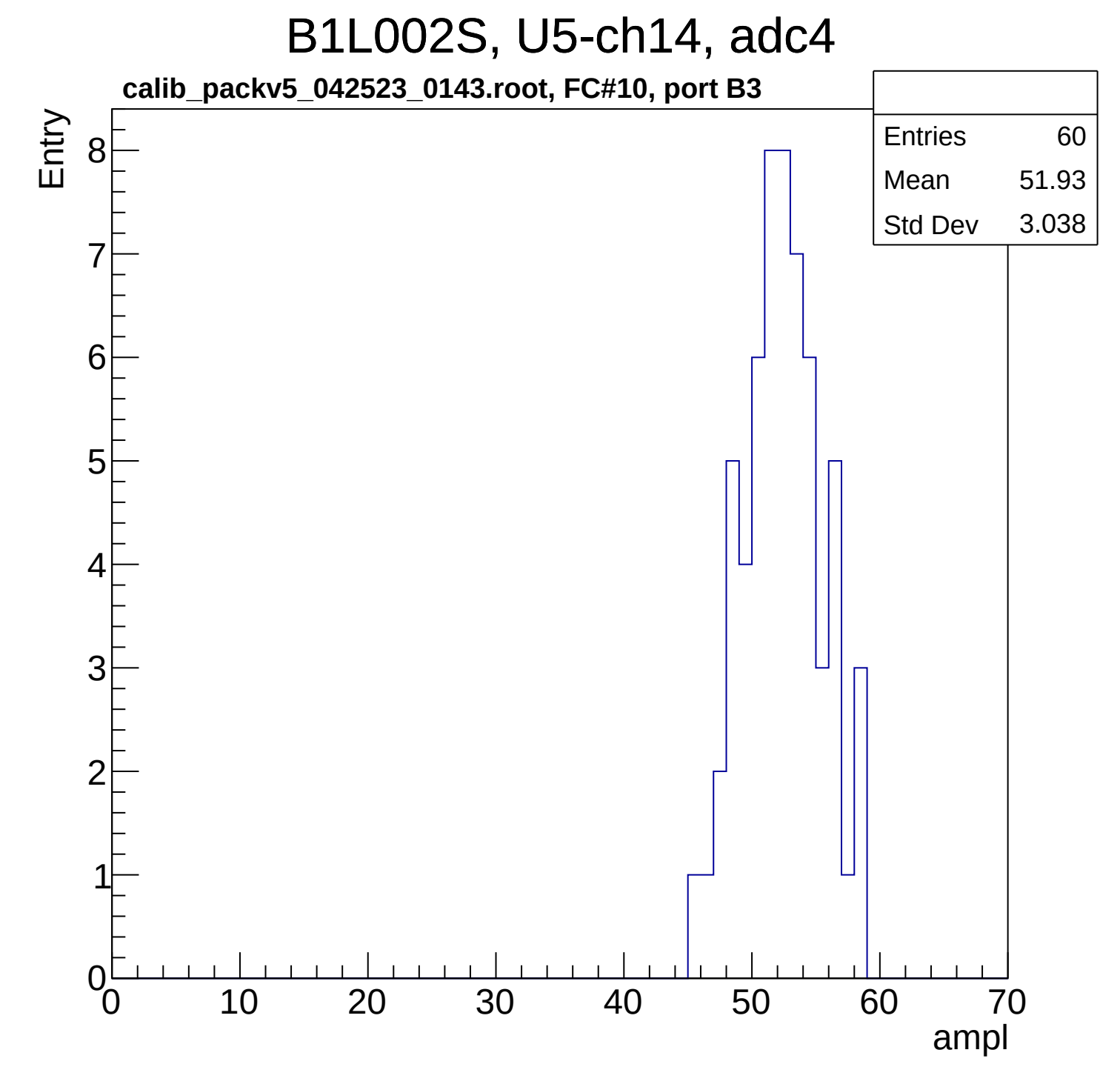
B1L002S, U5-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.93
Std Dev 3.038
Entry
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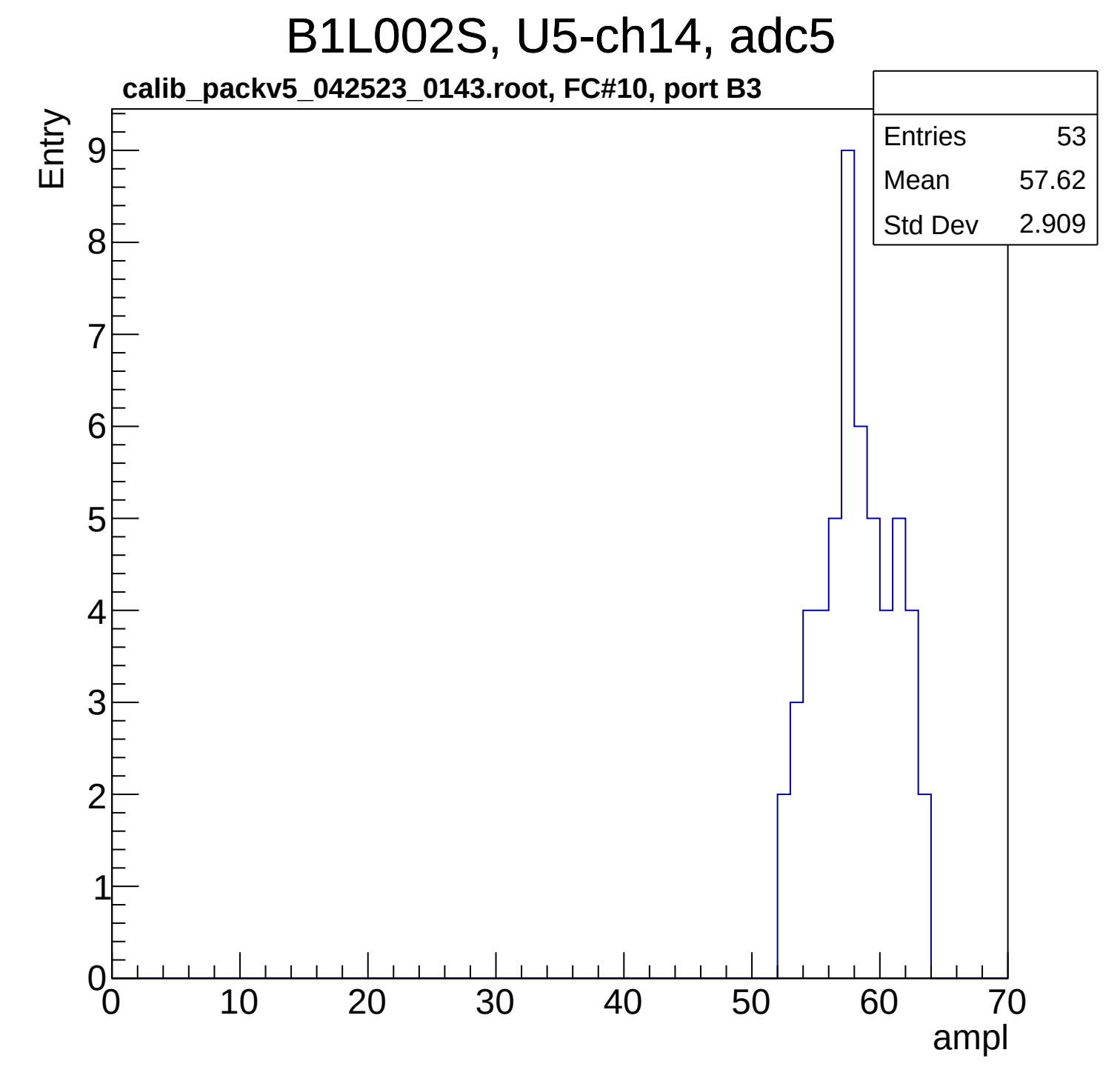
B1L002S, U5-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.62
Std Dev 2.909
Entry
ampl
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2
3
4
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0
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60
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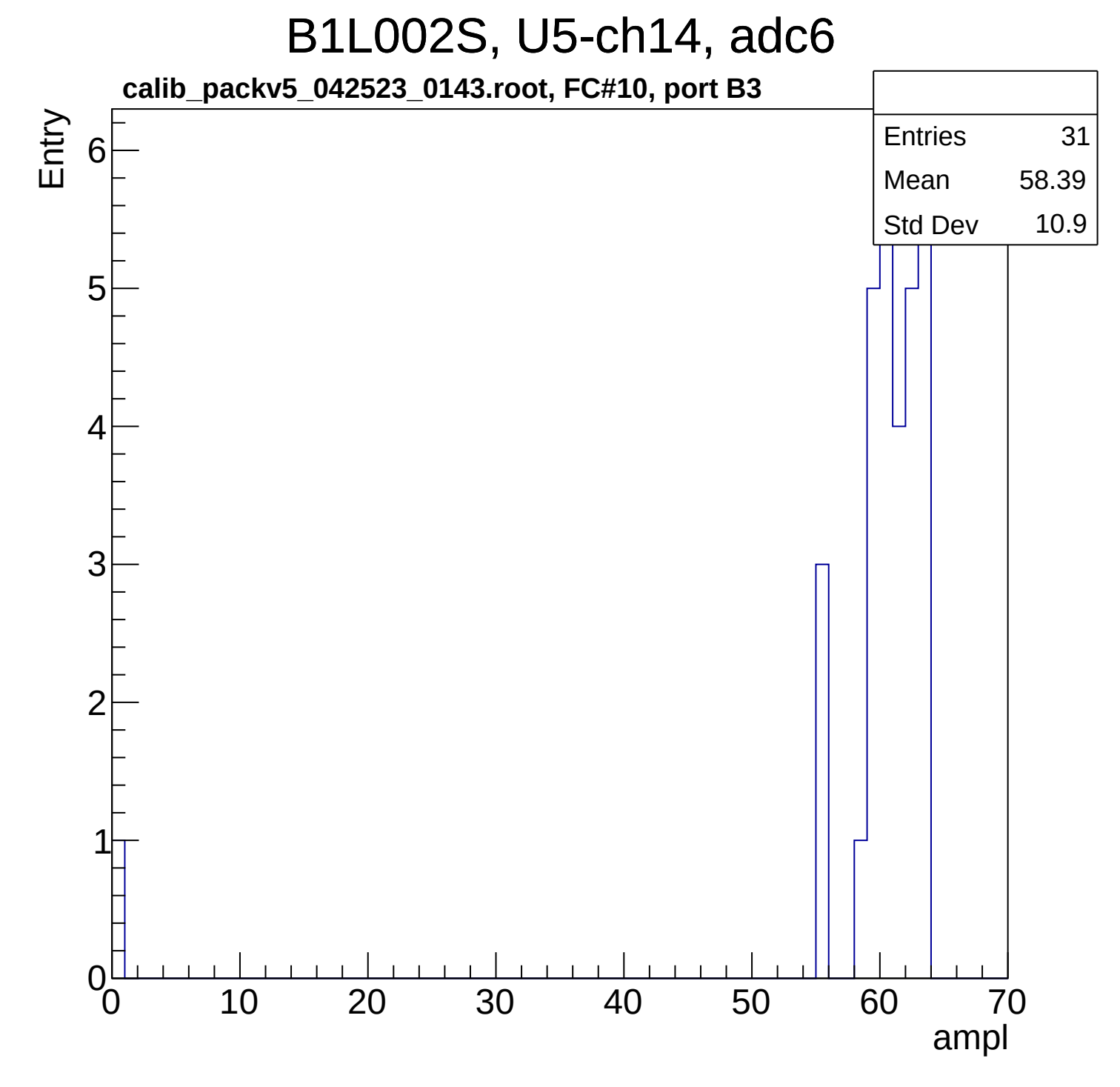
B1L002S, U5-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.39
Std Dev 10.9
Entry
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4
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6
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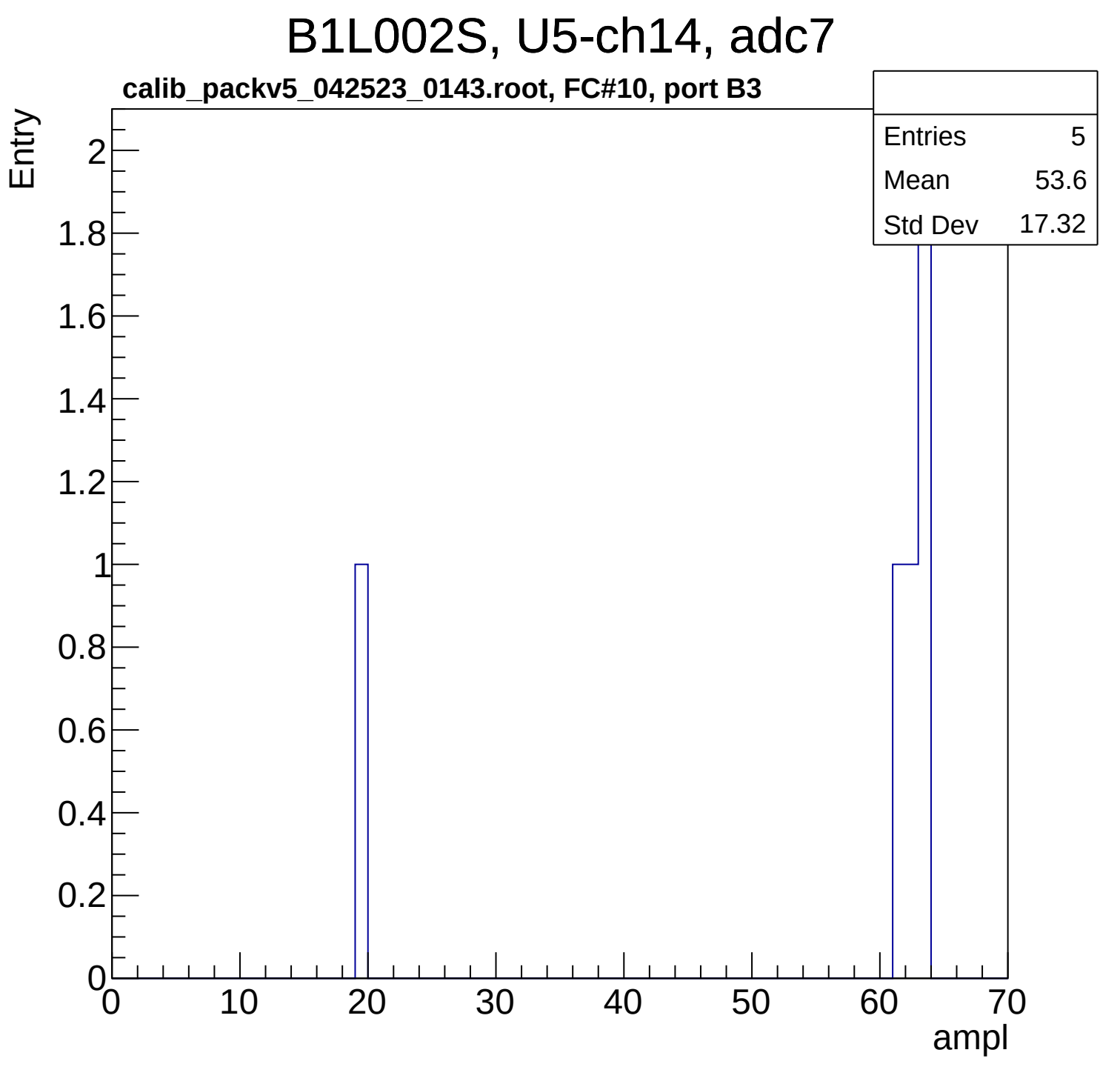
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 53.6
Std Dev 17.32
Entry
ampl
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0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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20
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40
50
60
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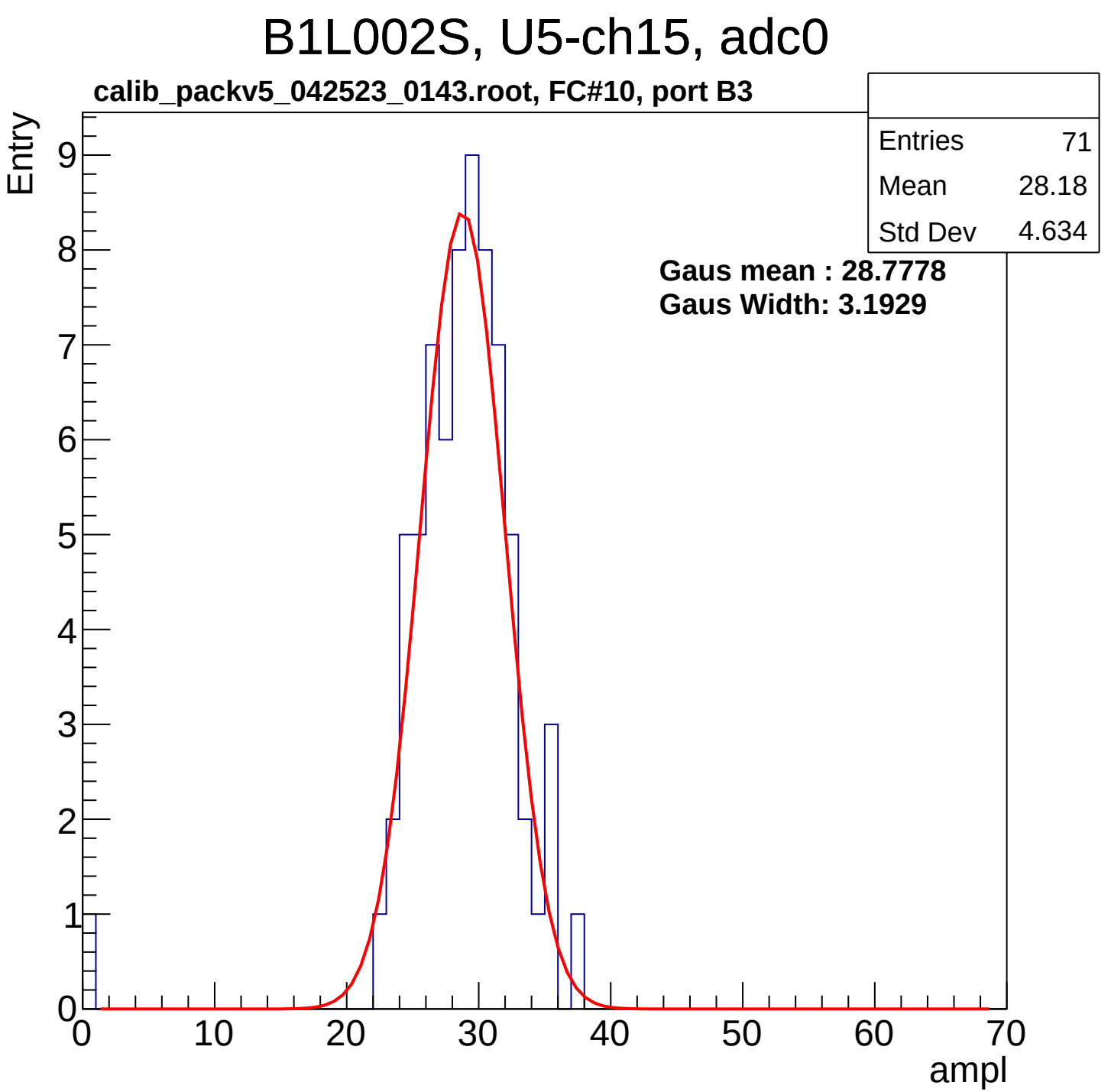
B1L002S, U5-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.18
Std Dev 4.634
Gaus mean : 28.7778
Gaus Width: 3.1929
Entry
ampl
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2
3
4
5
6
7
8
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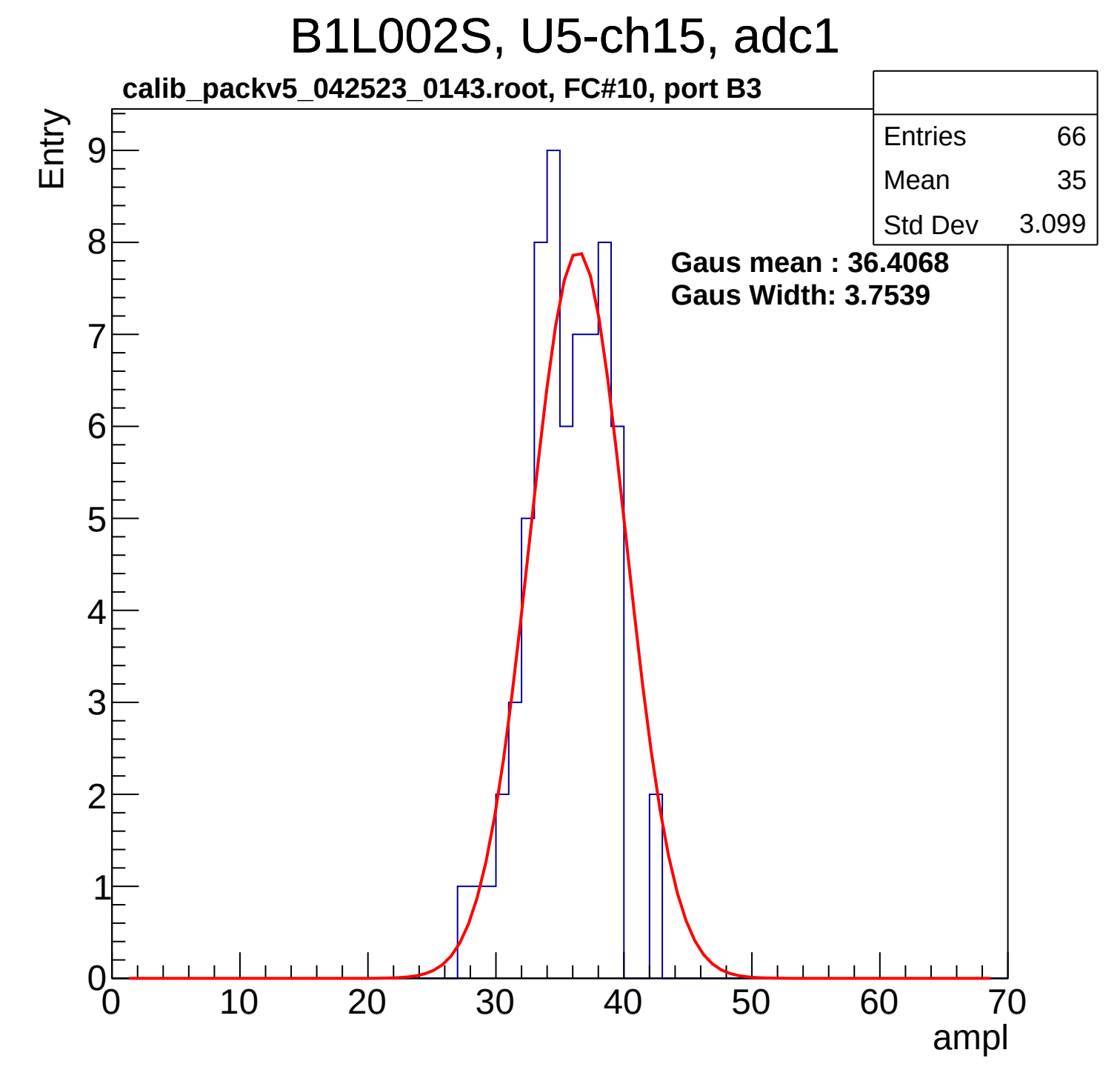
B1L002S, U5-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35
Std Dev 3.099
Gaus mean : 36.4068
Gaus Width: 3.7539
Entry
ampl
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2
3
4
5
6
7
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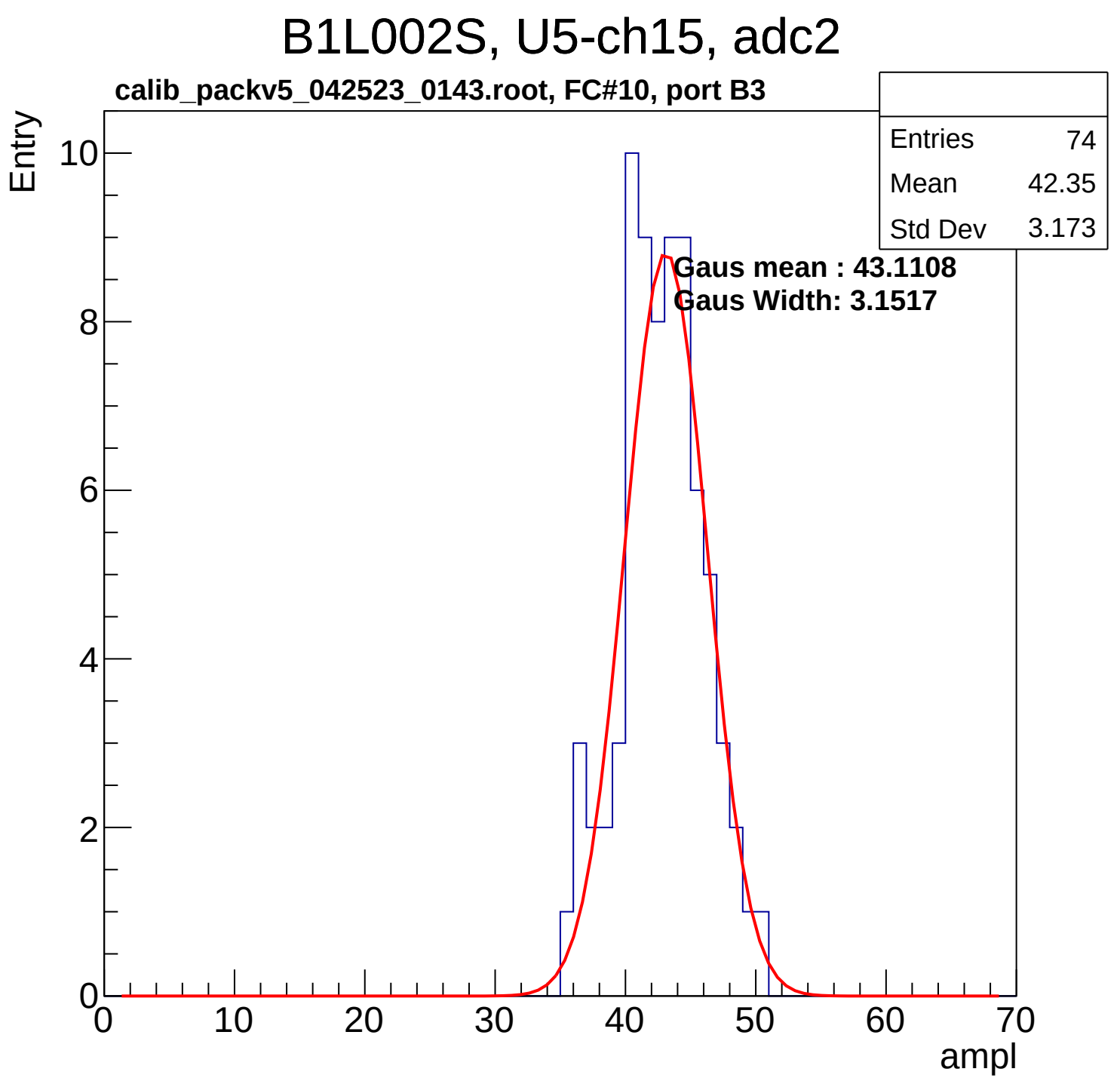
B1L002S, U5-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 42.35
Std Dev 3.173
Gaus mean : 43.1108
Gaus Width: 3.1517
Entry
ampl
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0
10
20
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40
50
60
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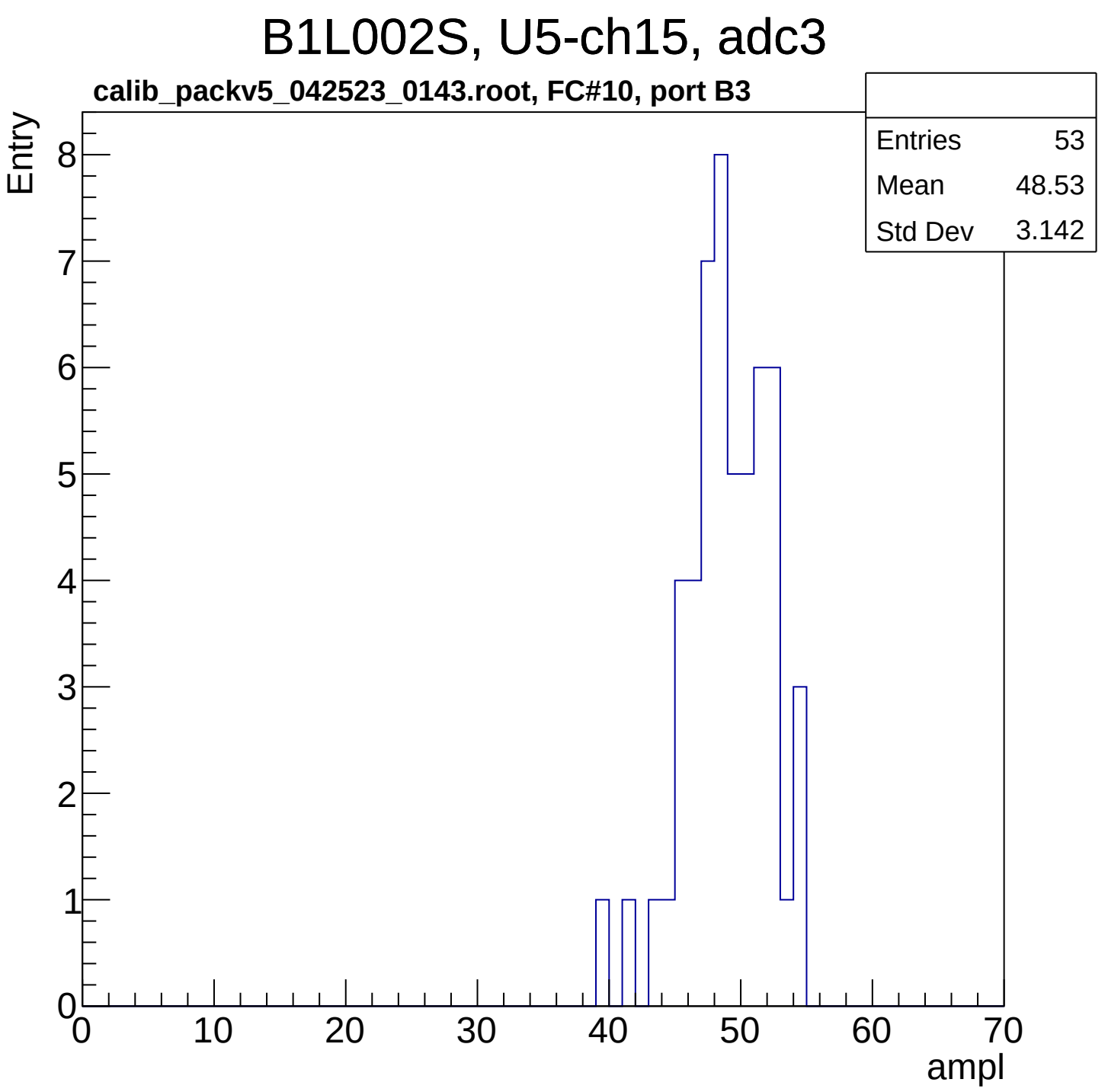
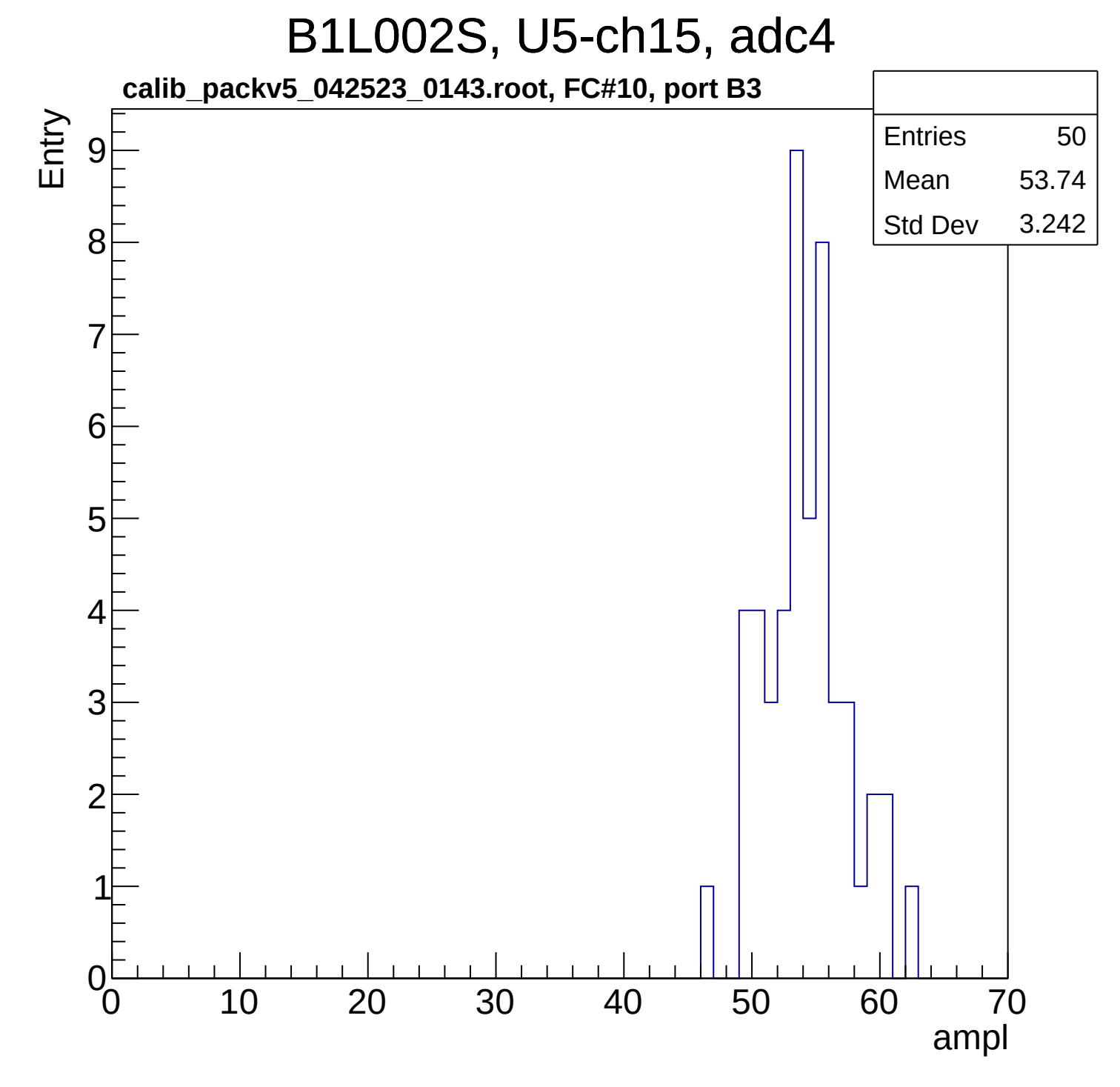

B1L002S, U5-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.53
Std Dev 3.142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.74
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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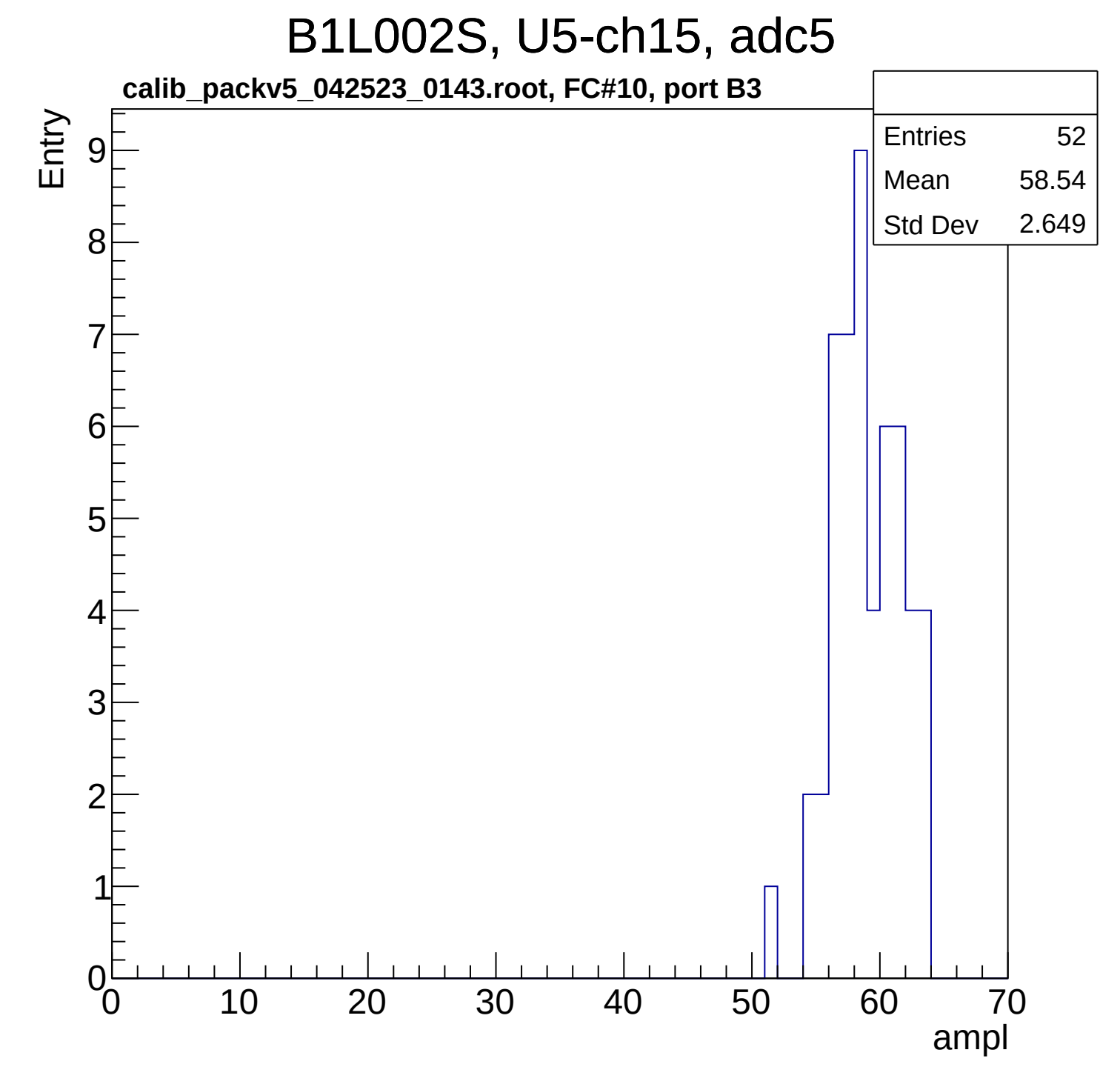
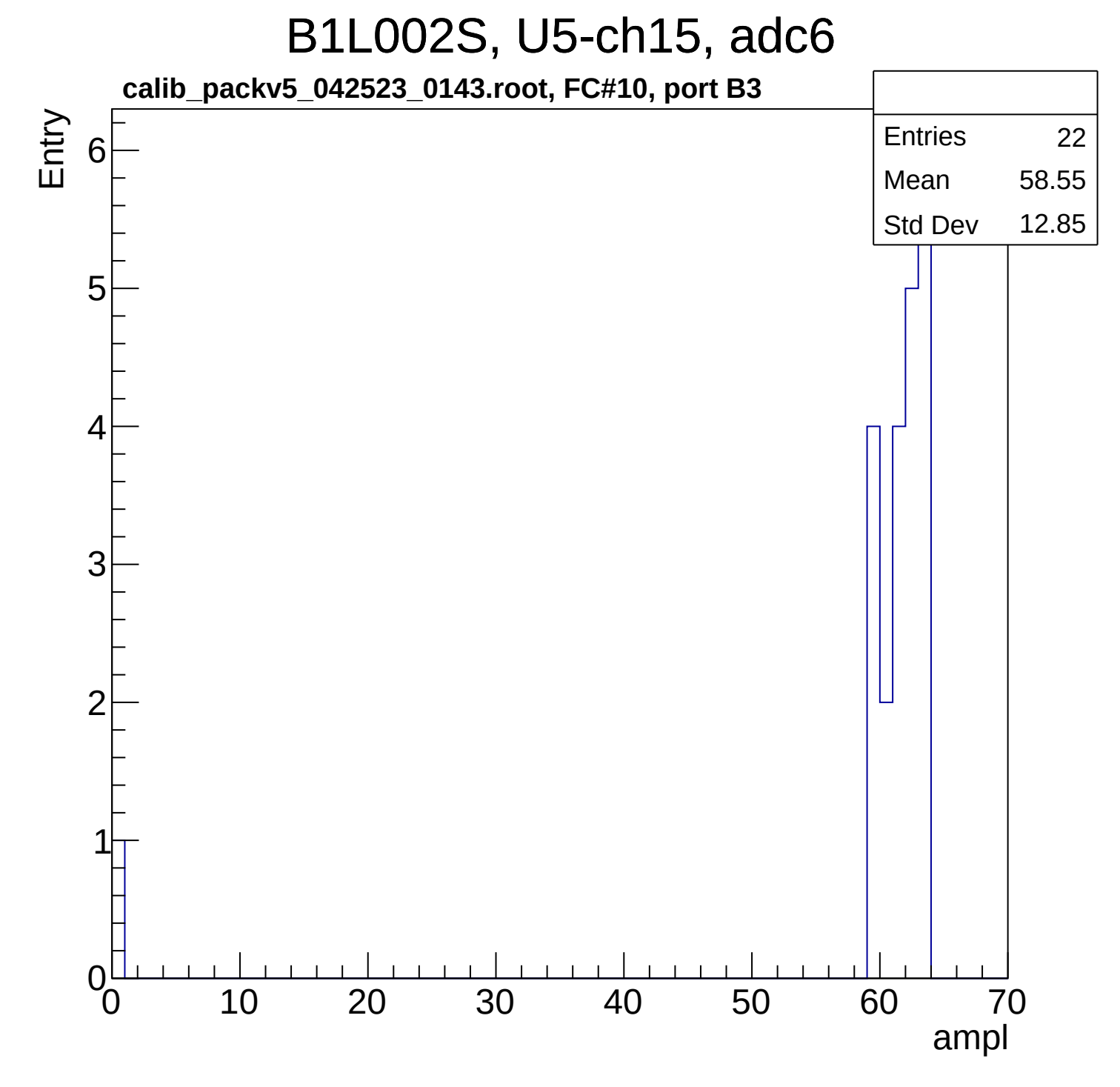
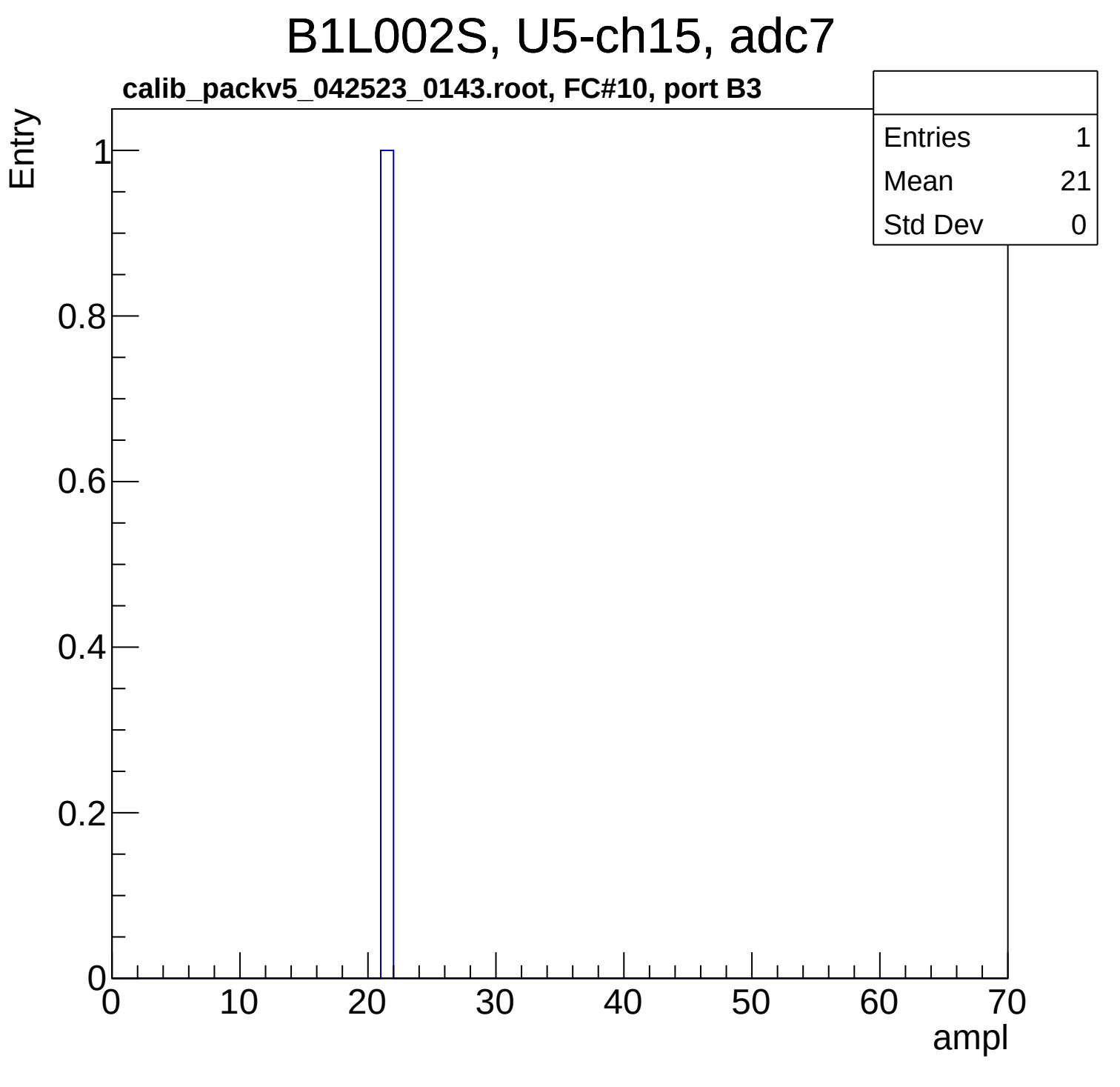
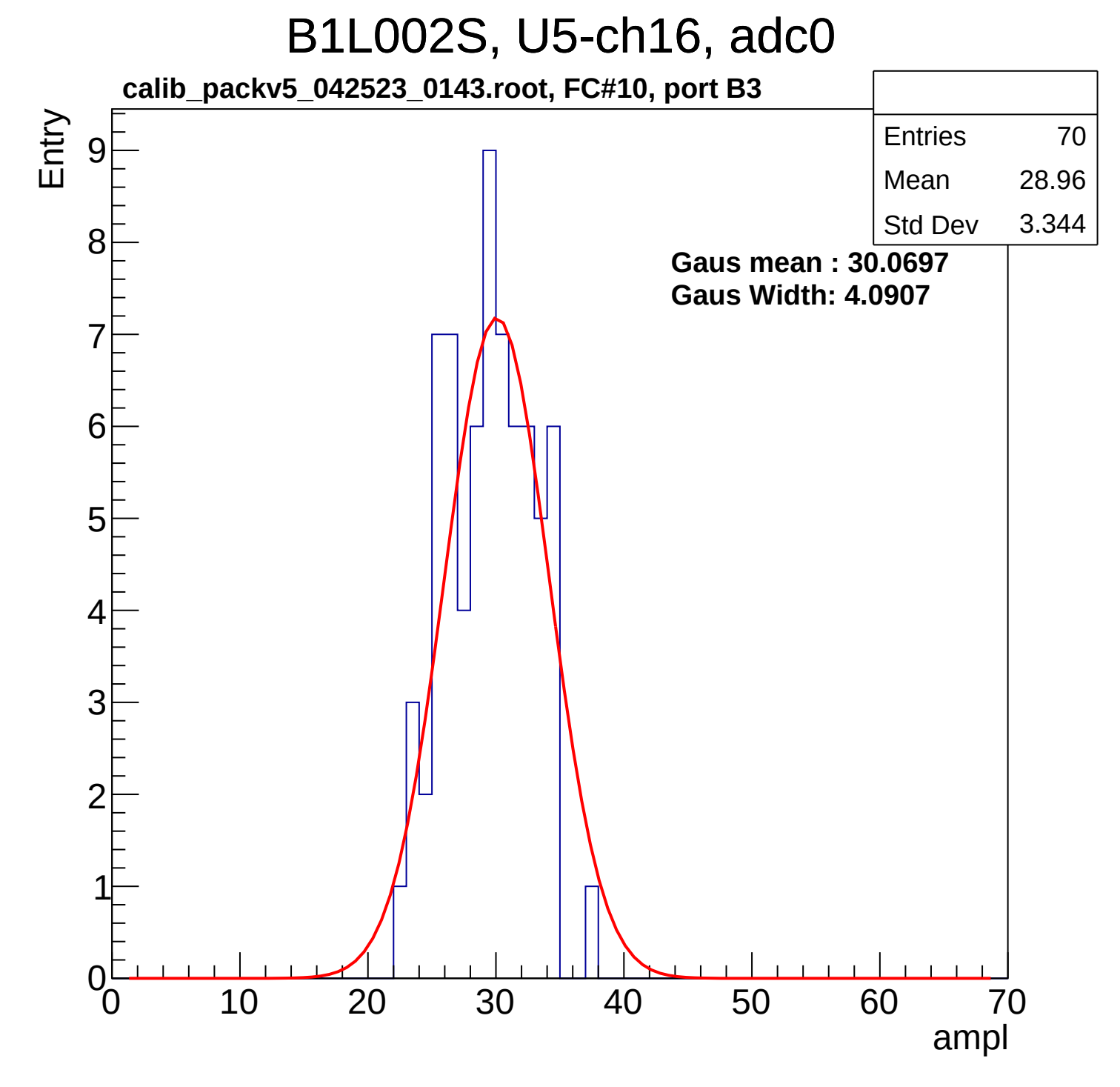
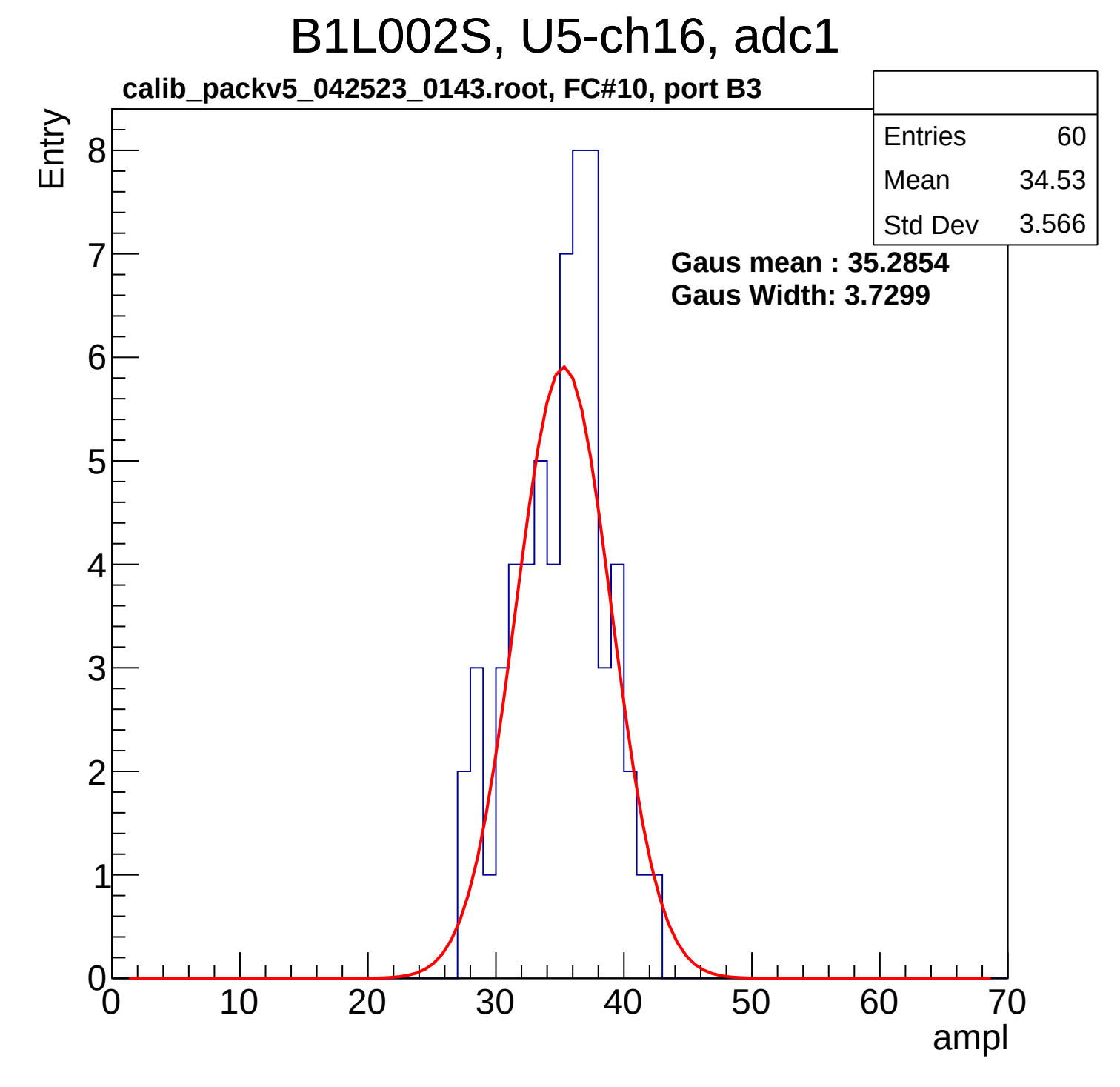
40
50
60
70

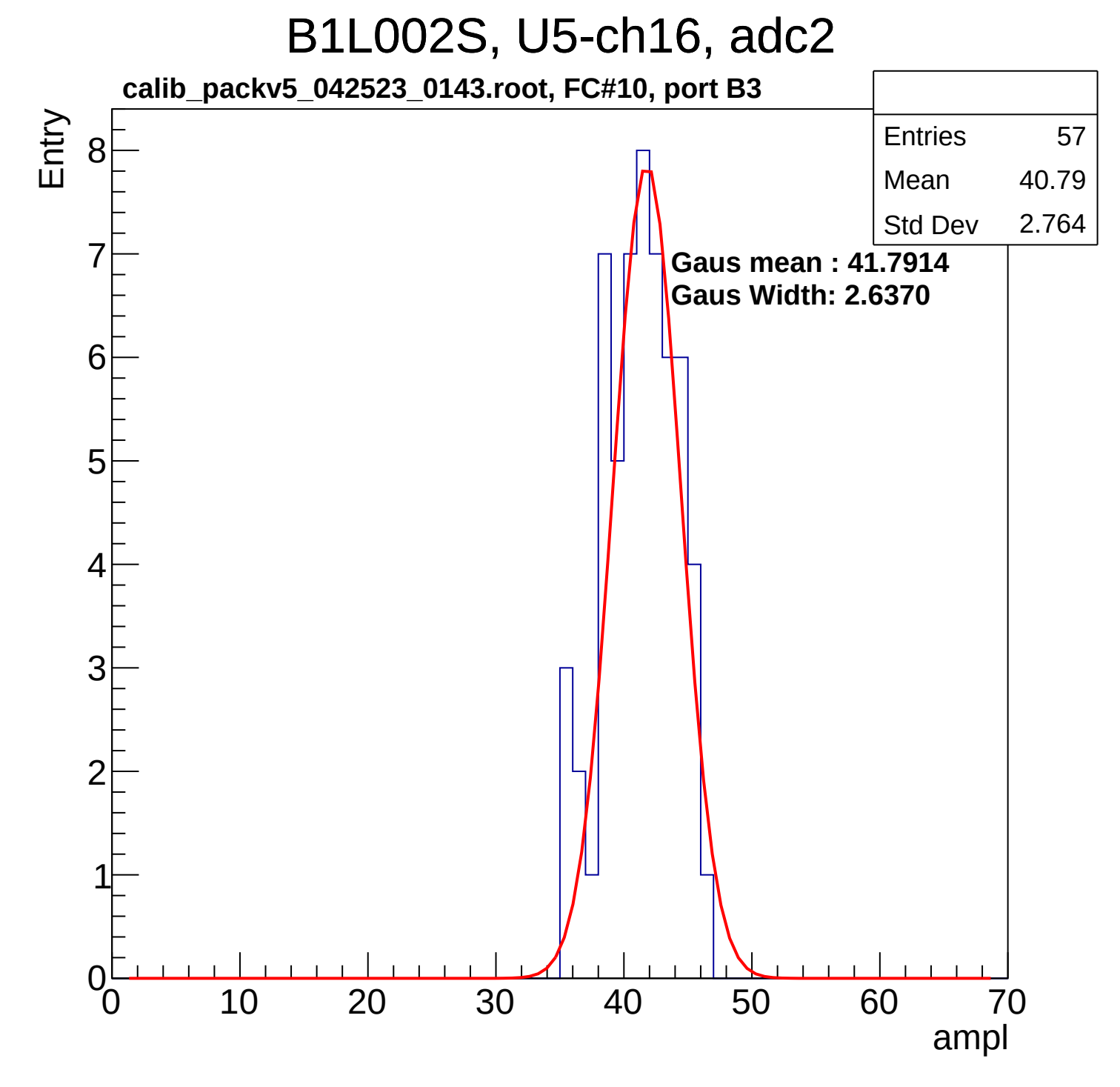

B1L002S, U5-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.54
Std Dev 2.649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

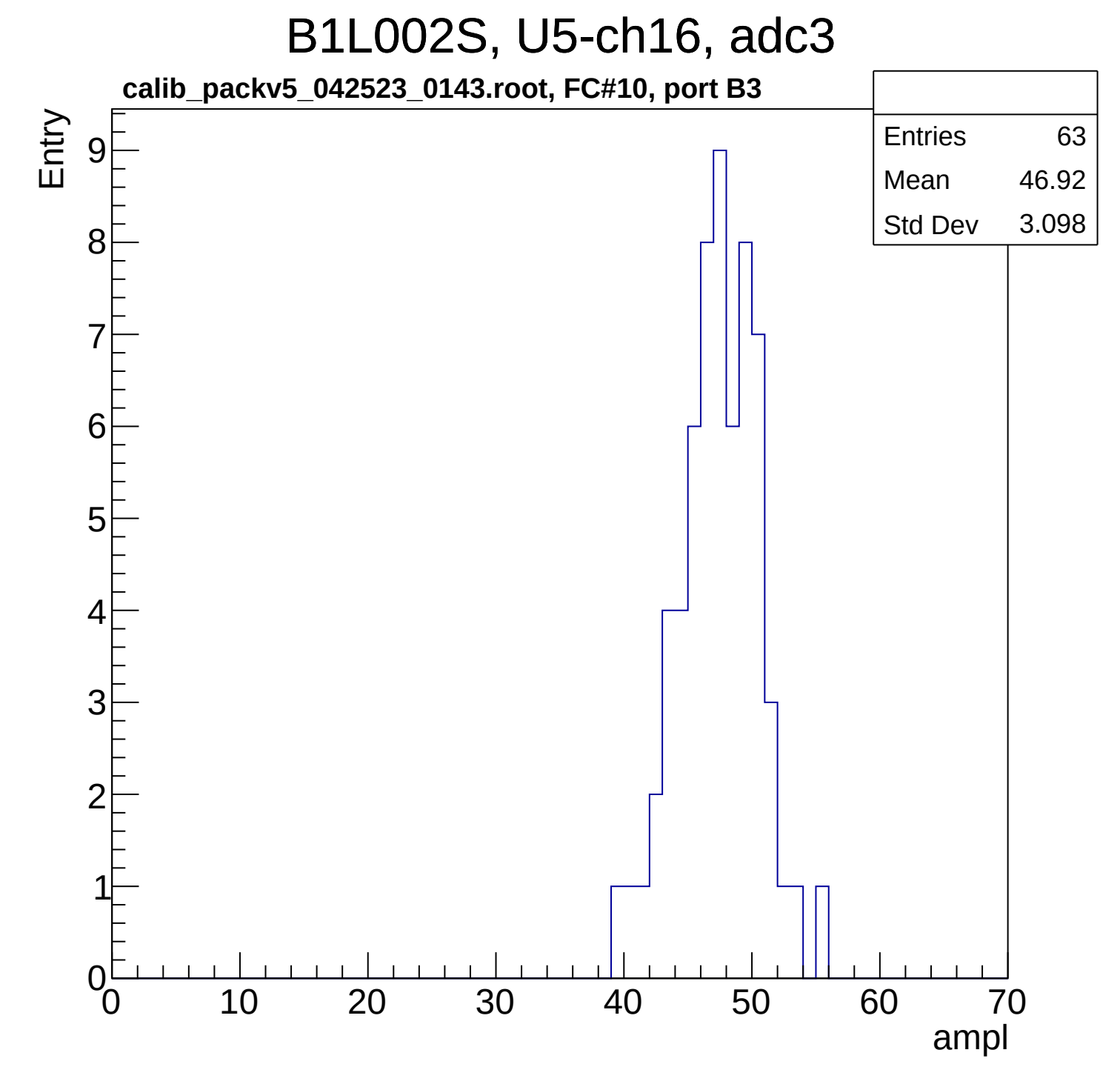

B1L002S, U5-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.55
Std Dev 12.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

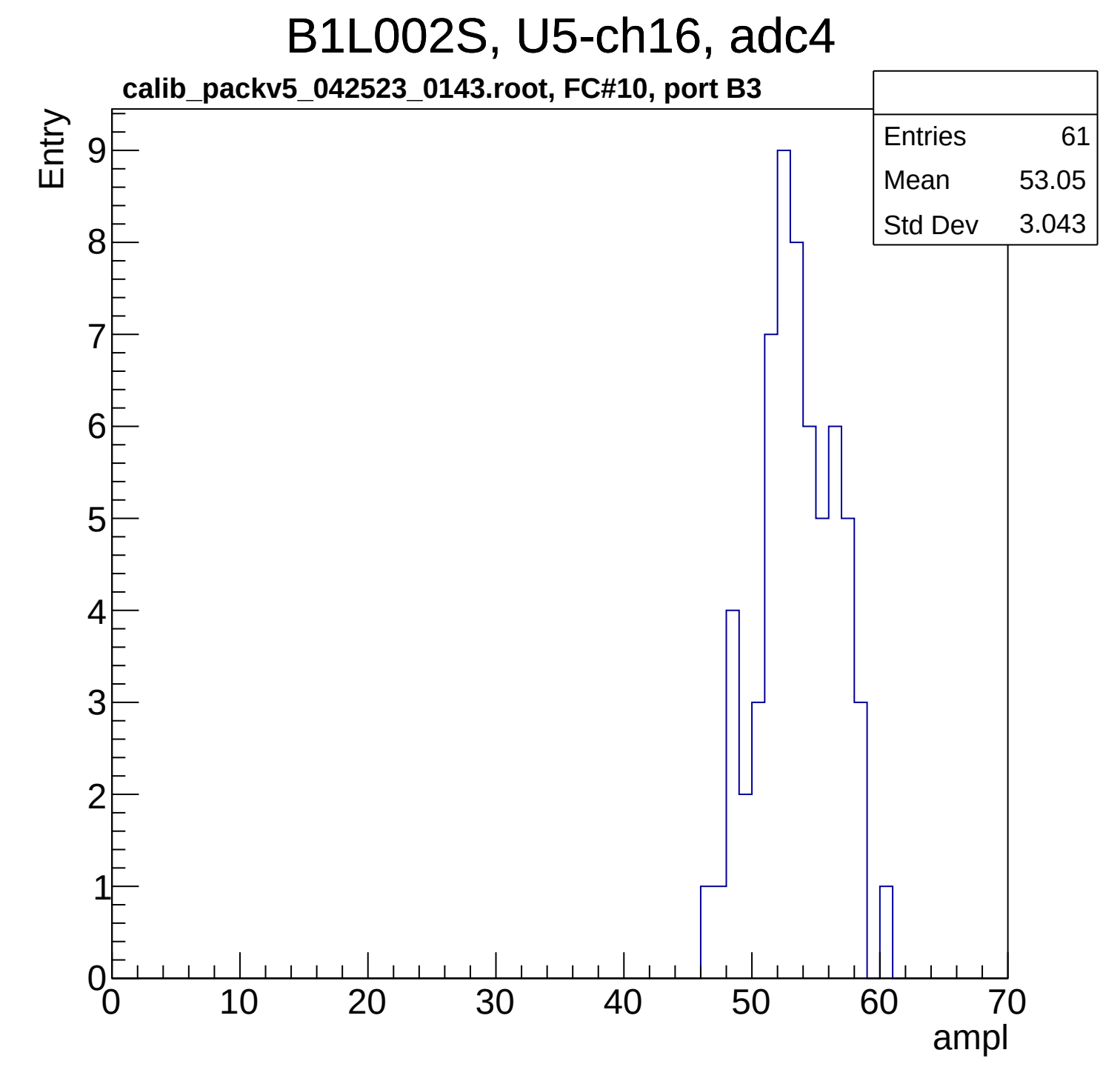

B1L002S, U5-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

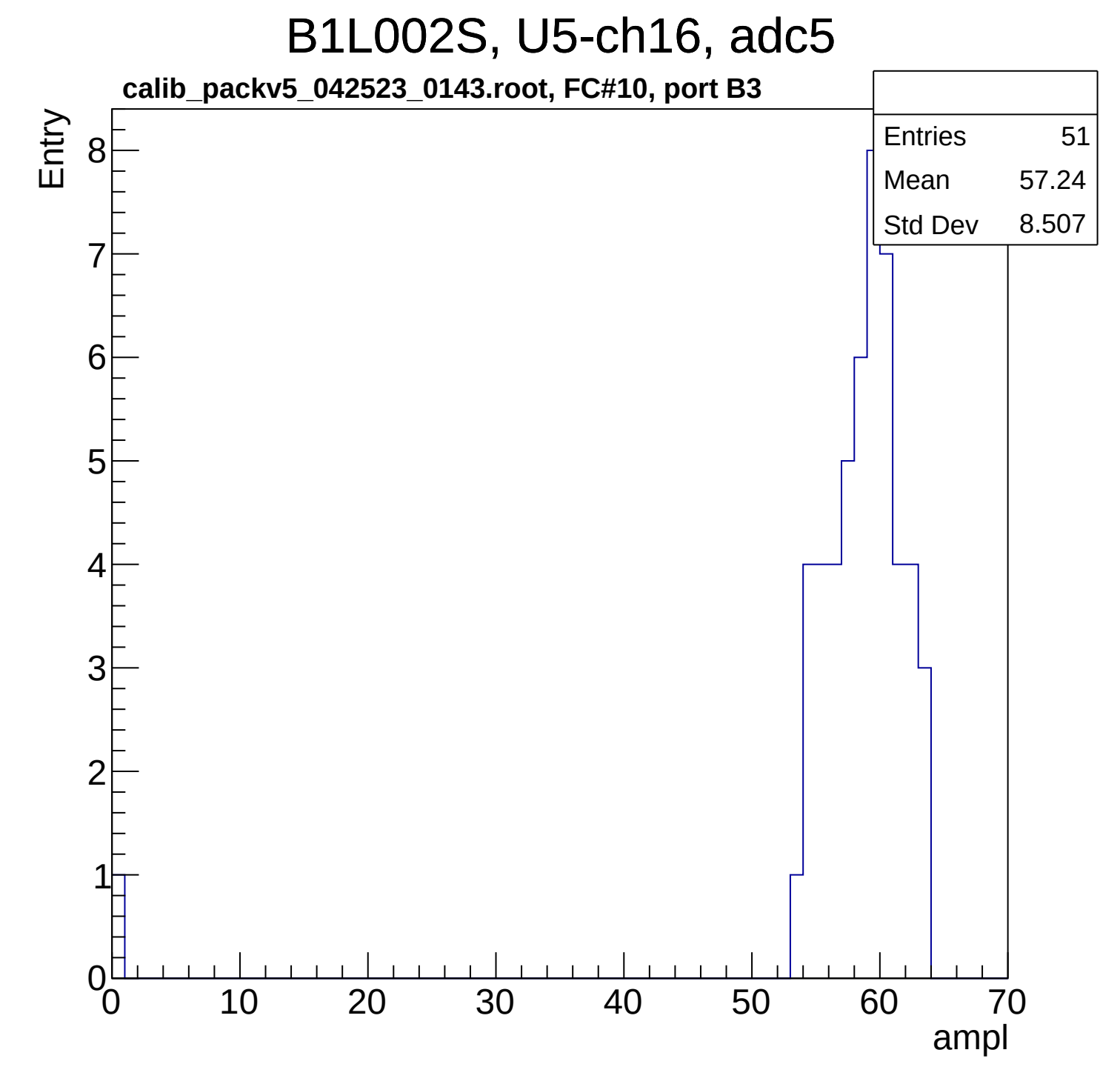

B1L002S, U5-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.96
Std Dev 3.344
Gaus mean : 30.0697
Gaus Width: 4.0907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

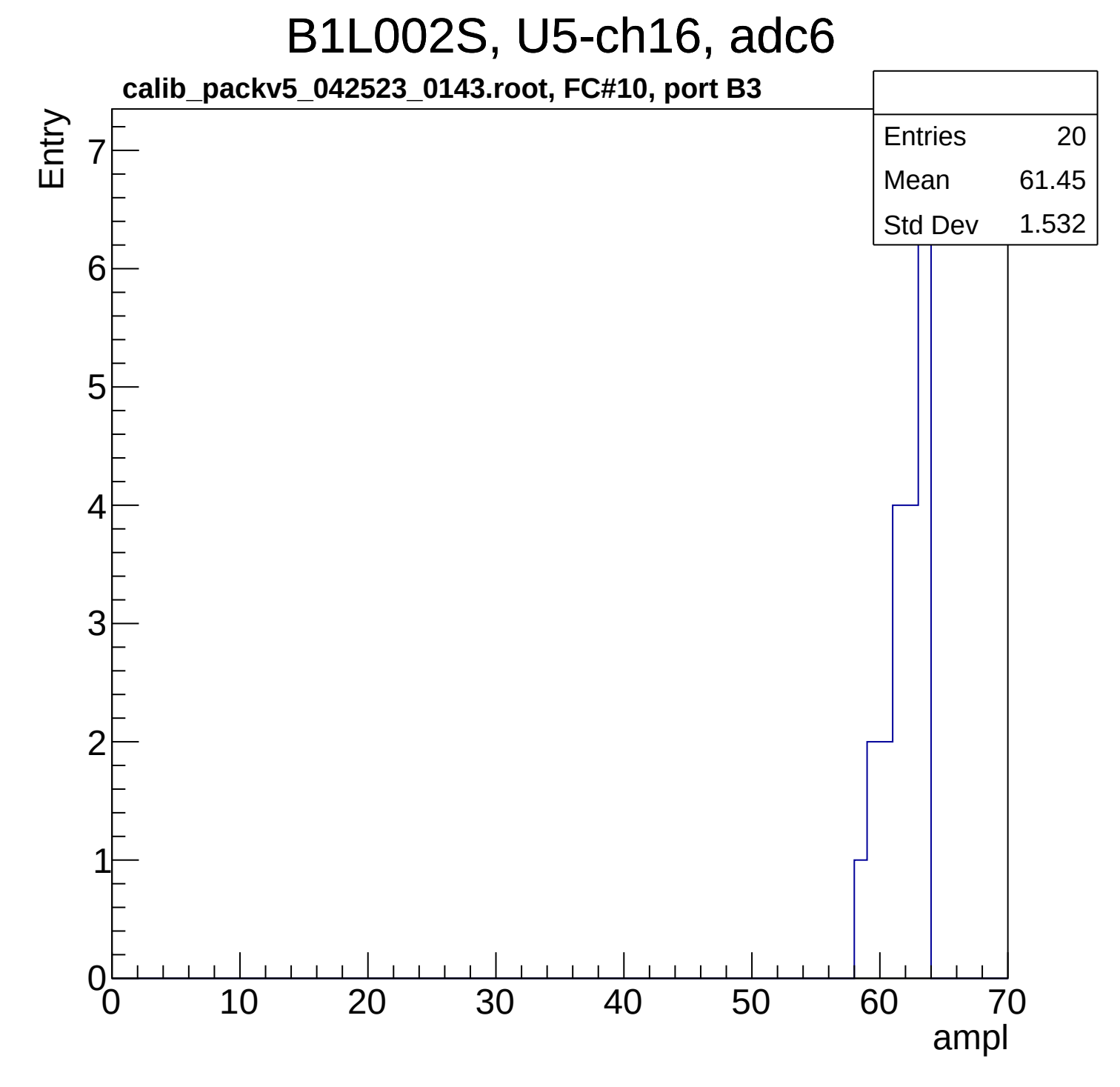

B1L002S, U5-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.53
Std Dev 3.566
Gaus mean : 35.2854
Gaus Width: 3.7299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

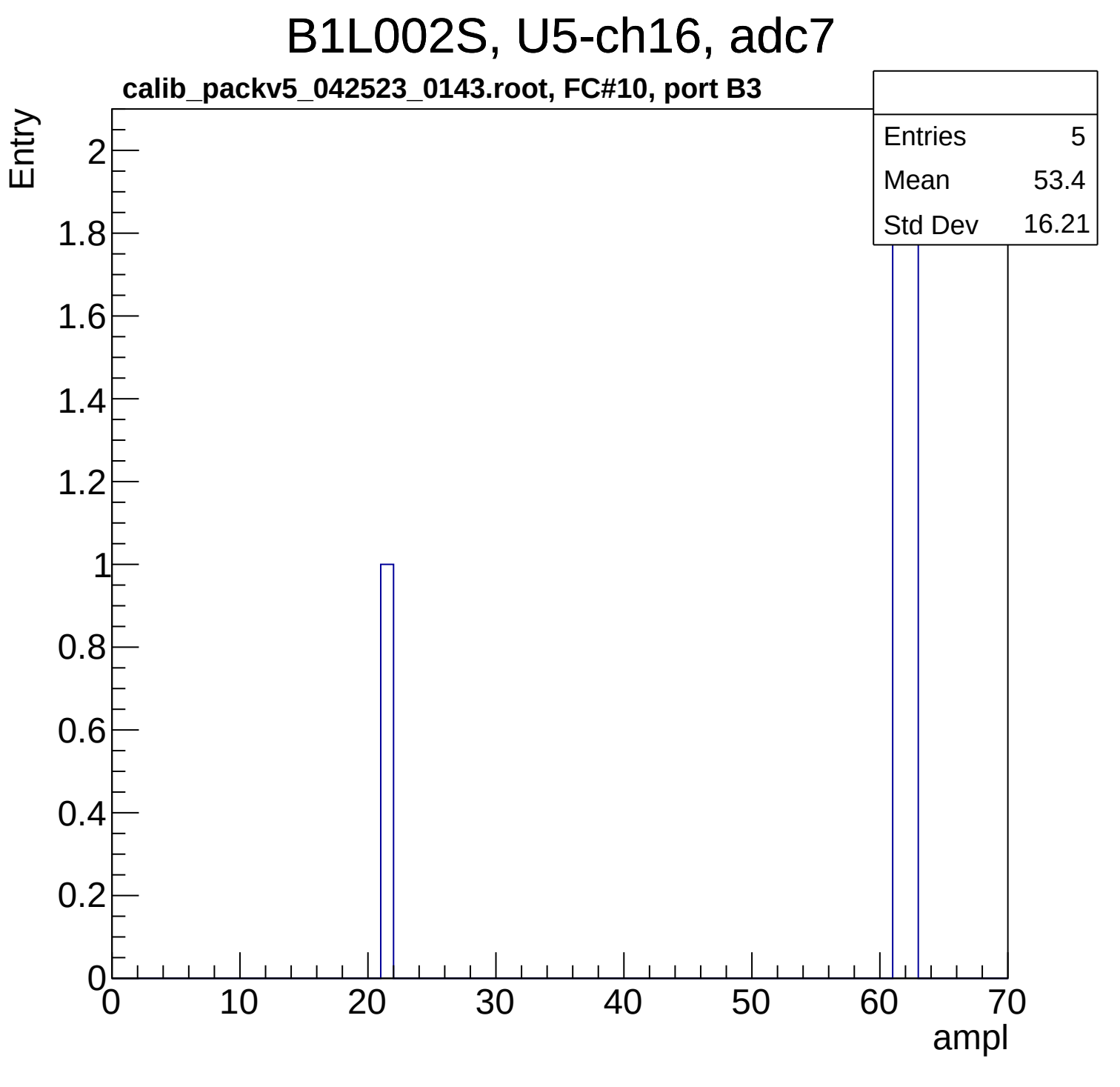

B1L002S, U5-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.79
Std Dev 2.764
Gaus mean : 41.7914
Gaus Width: 2.6370
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

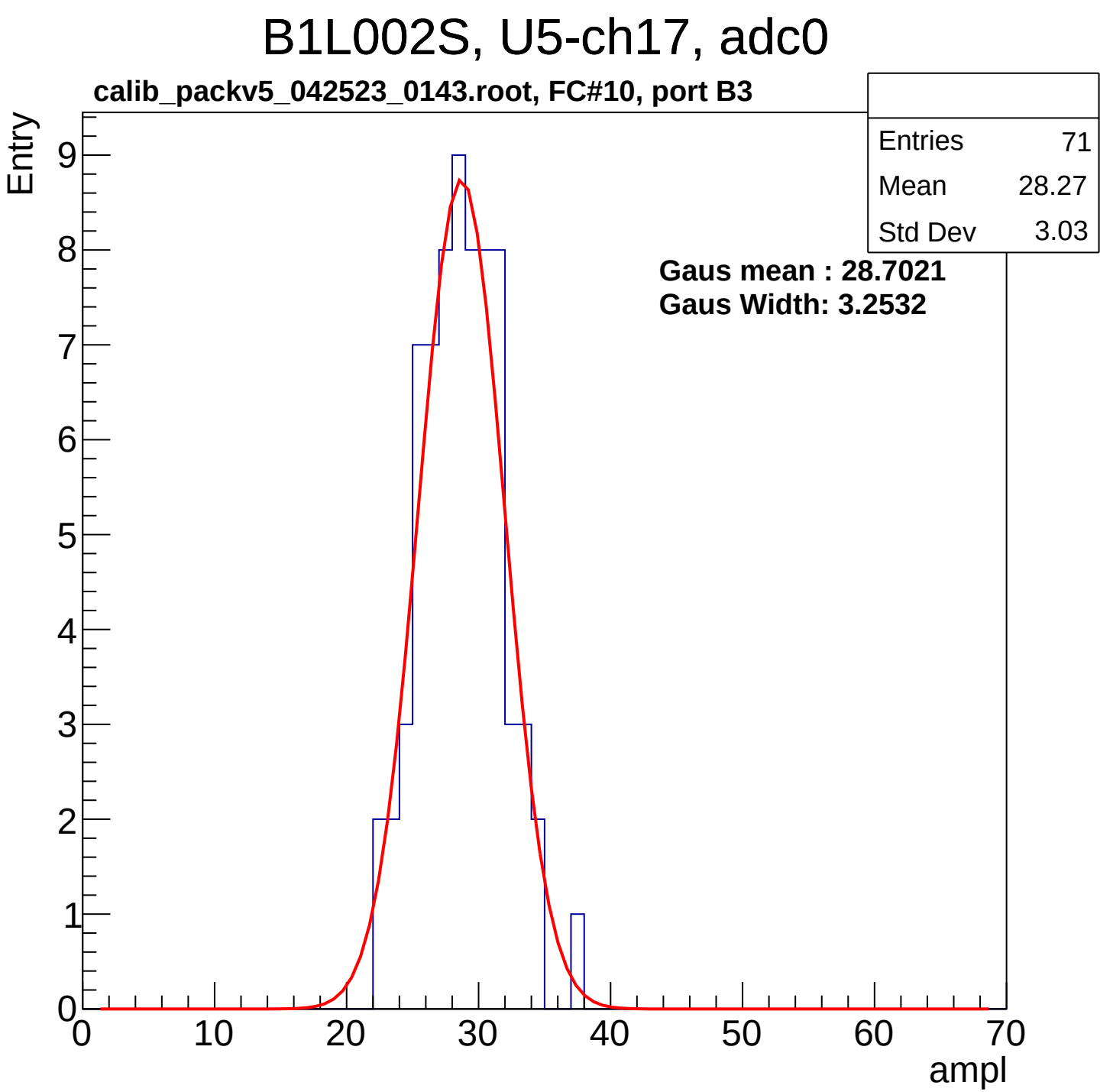

B1L002S, U5-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.92
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

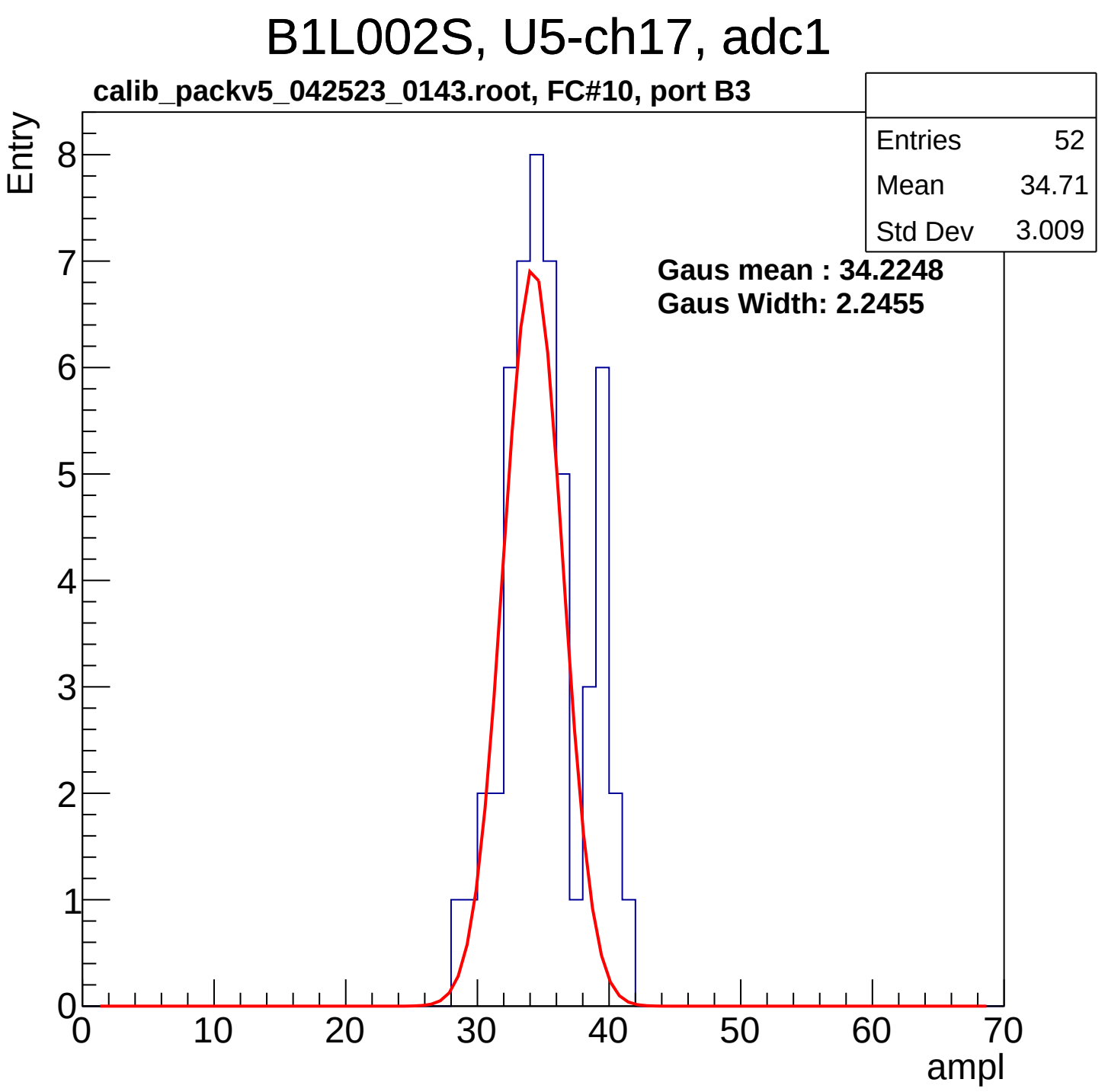

B1L002S, U5-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.05
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

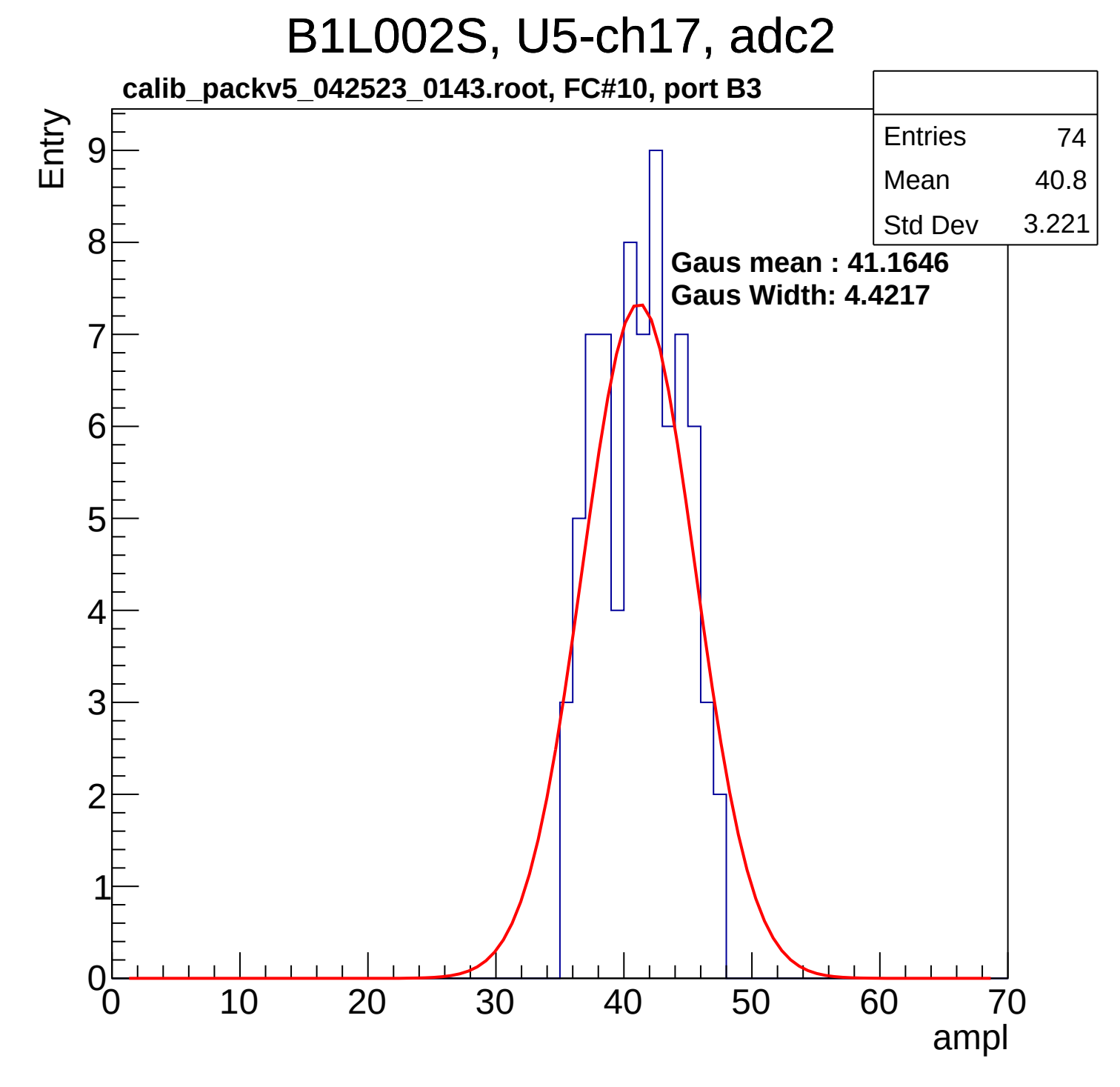

B1L002S, U5-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.24
Std Dev 8.507
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

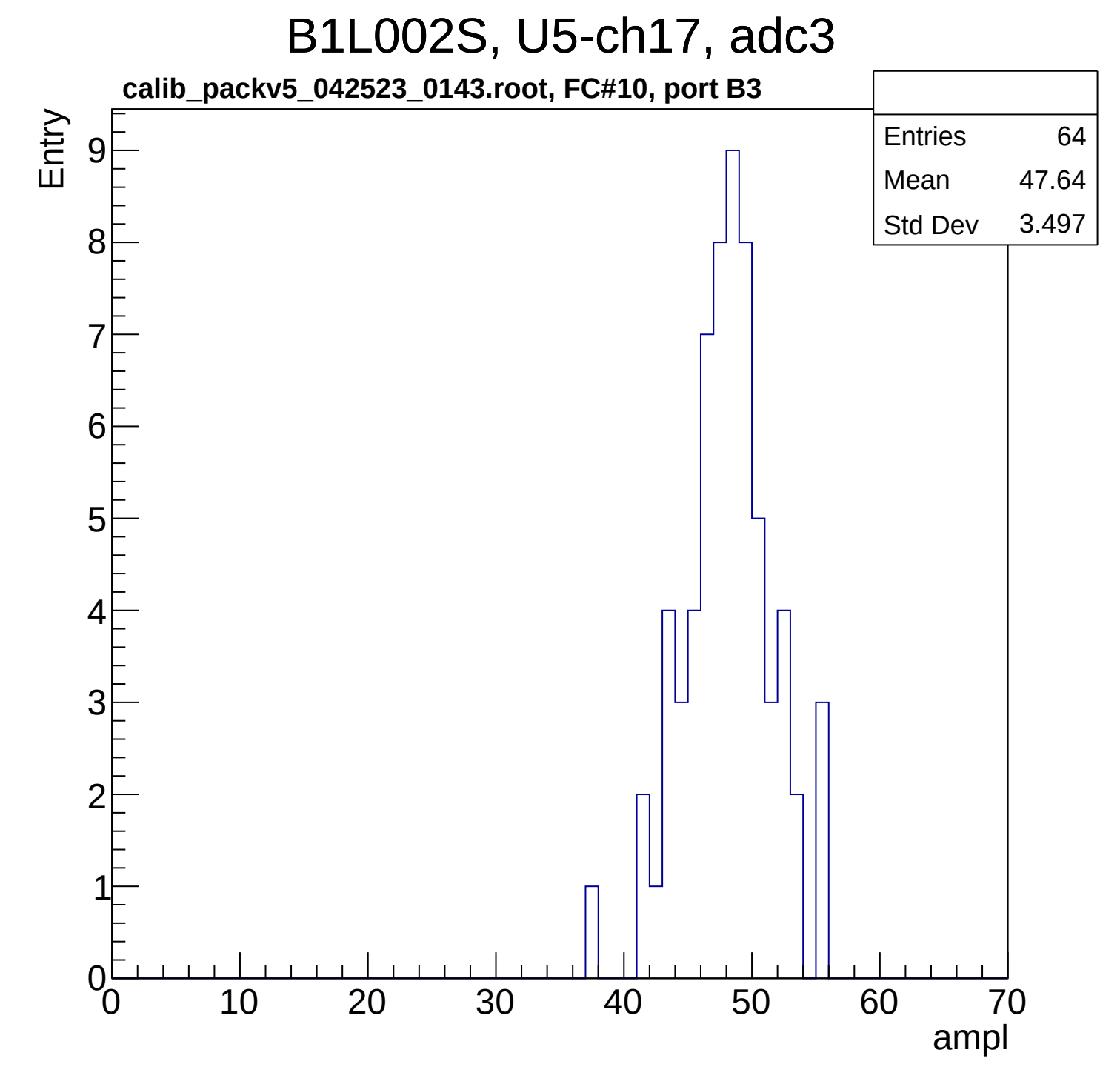

B1L002S, U5-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.45
Std Dev 1.532
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 53.4
Std Dev 16.21
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.27
Std Dev 3.03
Gaus mean : 28.7021
Gaus Width: 3.2532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.71
Std Dev 3.009
Gaus mean : 34.2248
Gaus Width: 2.2455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.8
Std Dev 3.221
Gaus mean : 41.1646
Gaus Width: 4.4217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.64
Std Dev 3.497
Entry
ampl
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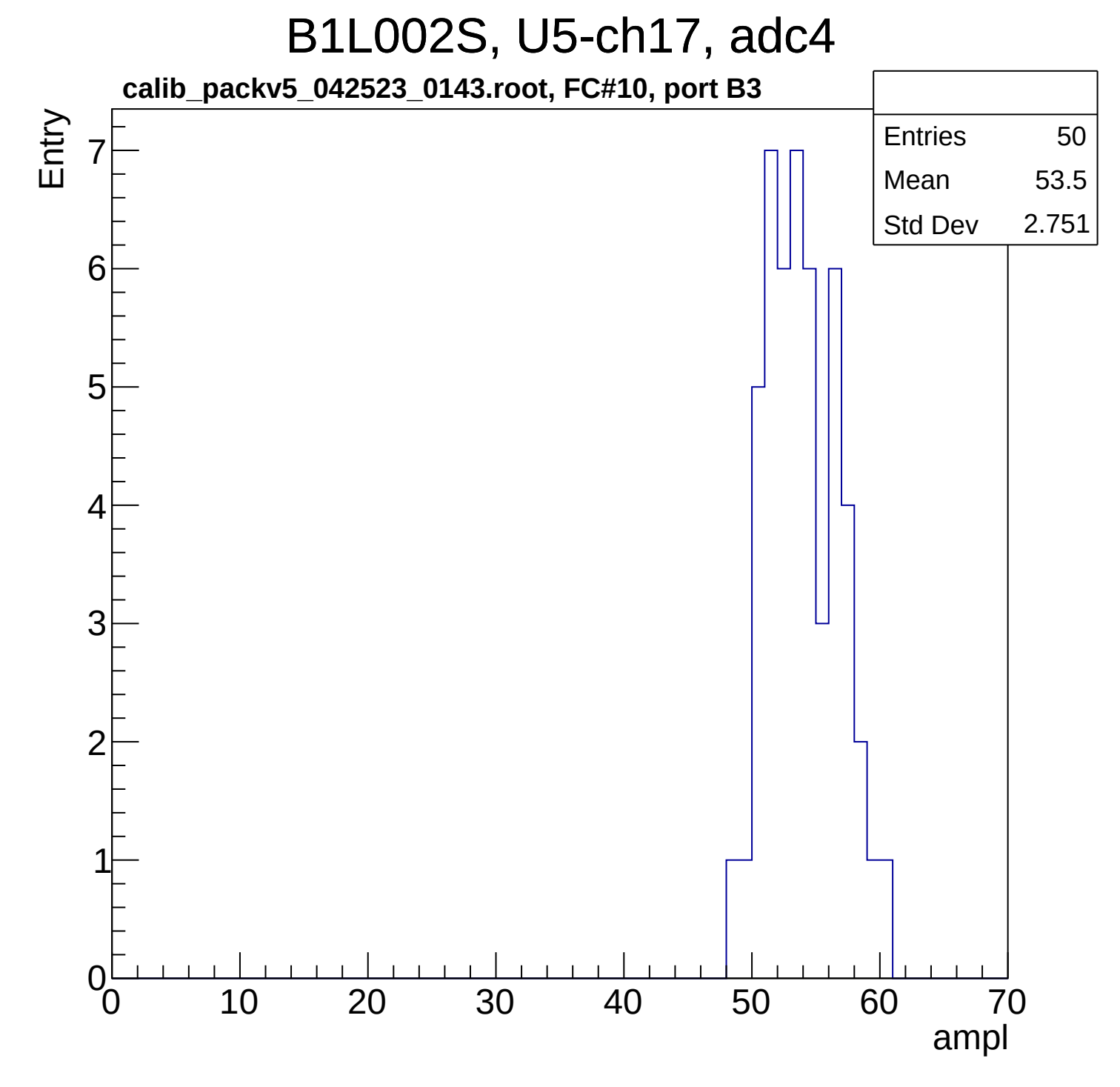
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.5
Std Dev 2.751
Entry
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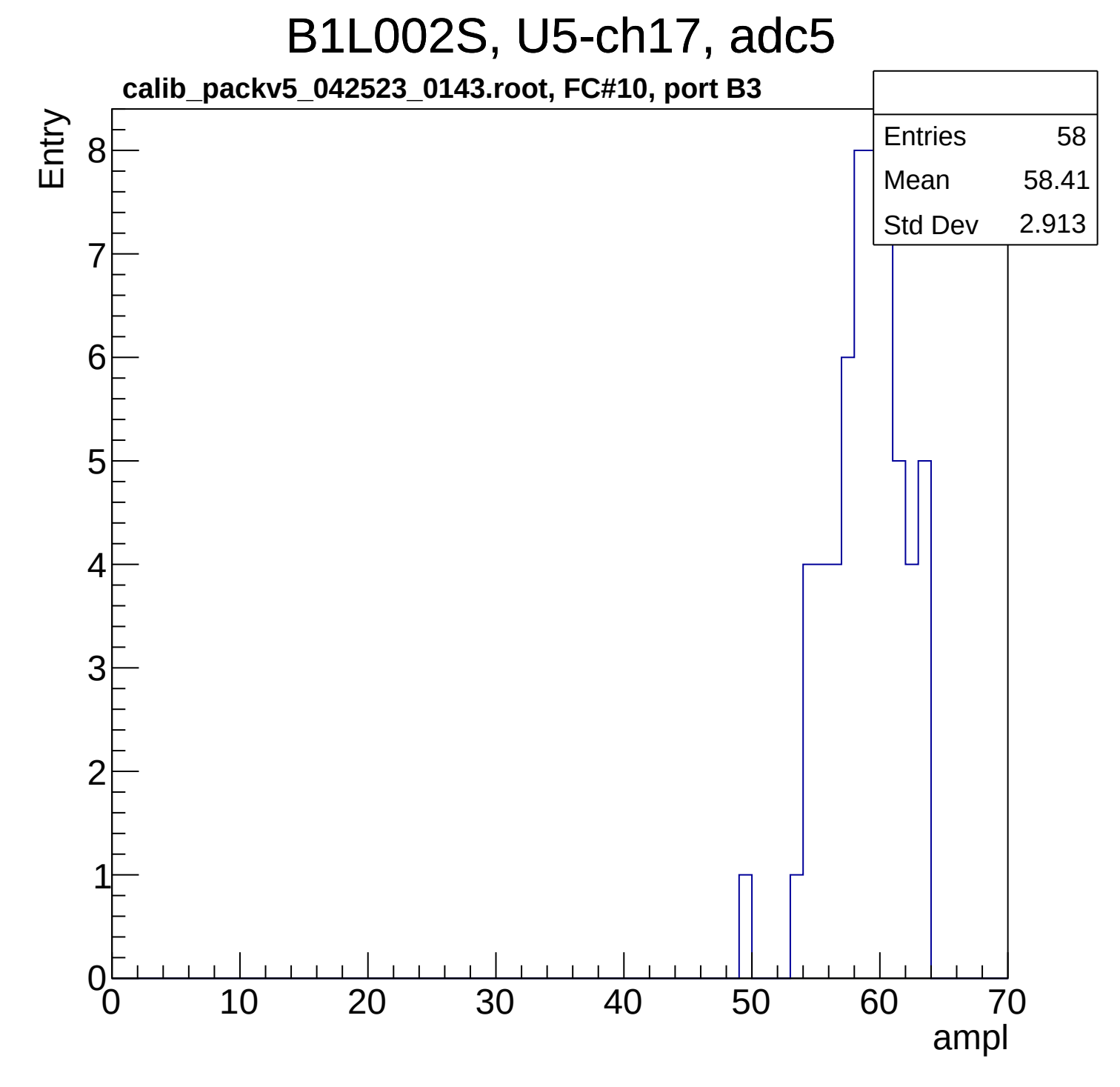
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.41
Std Dev 2.913
Entry
ampl
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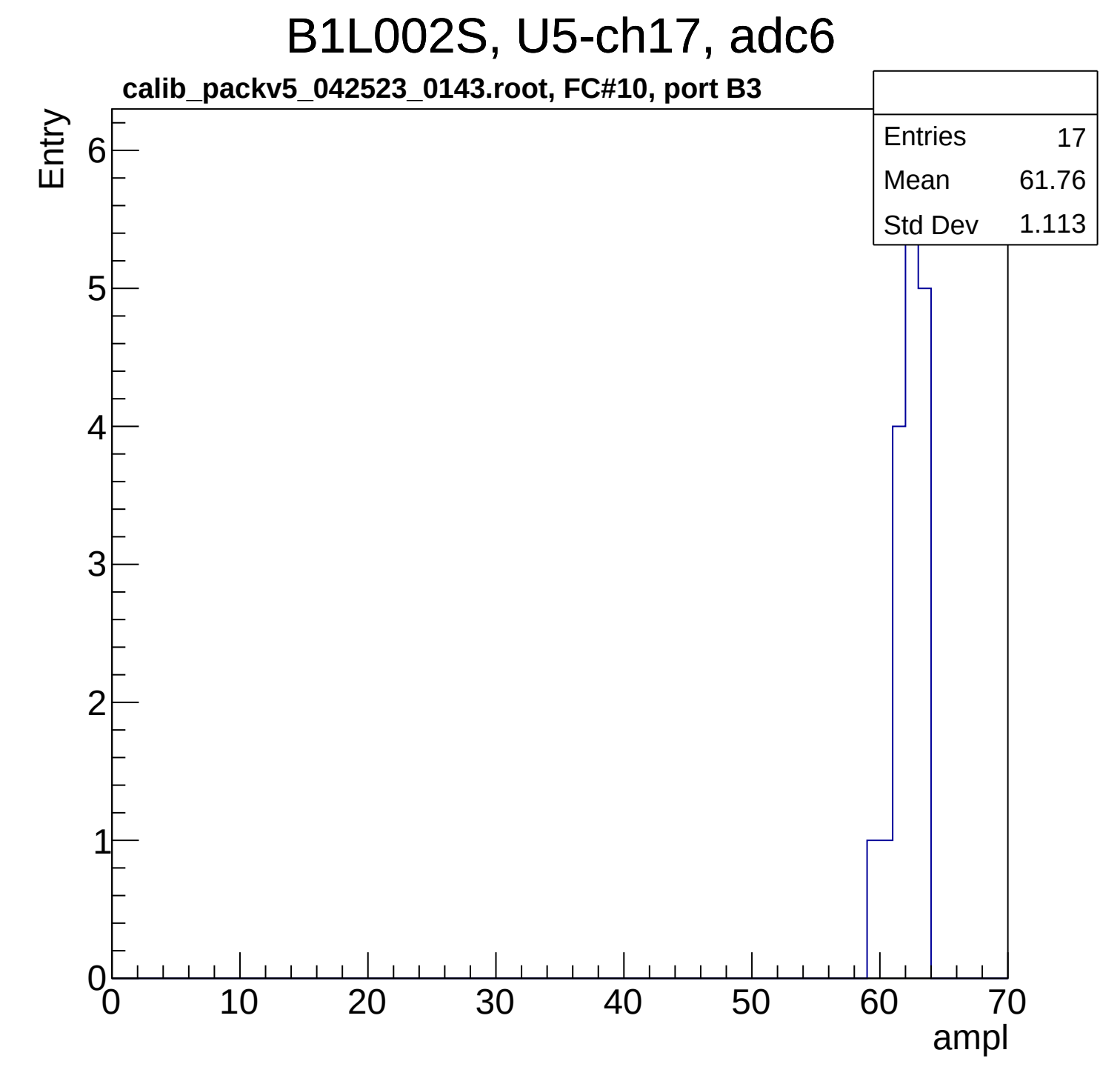
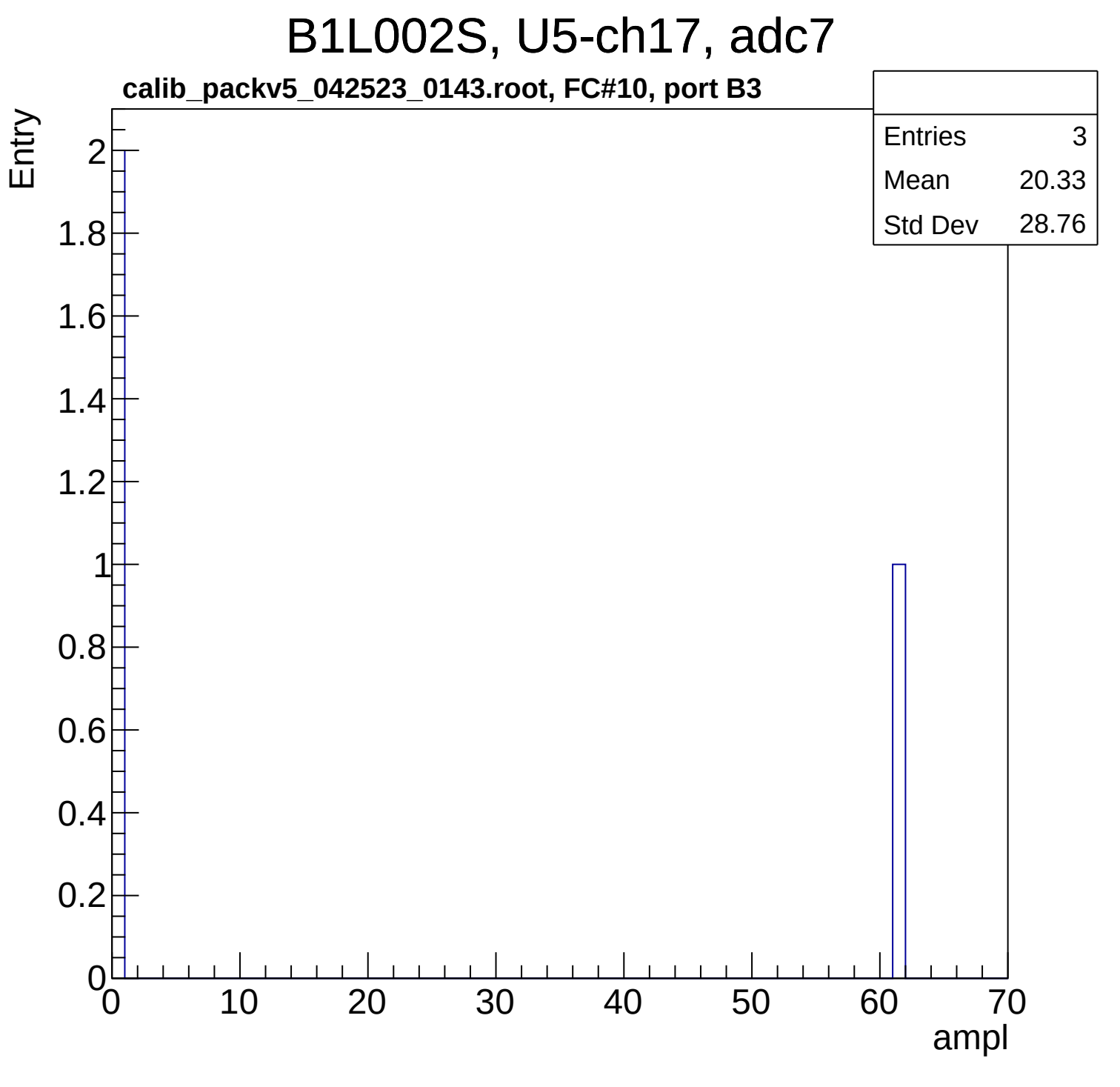
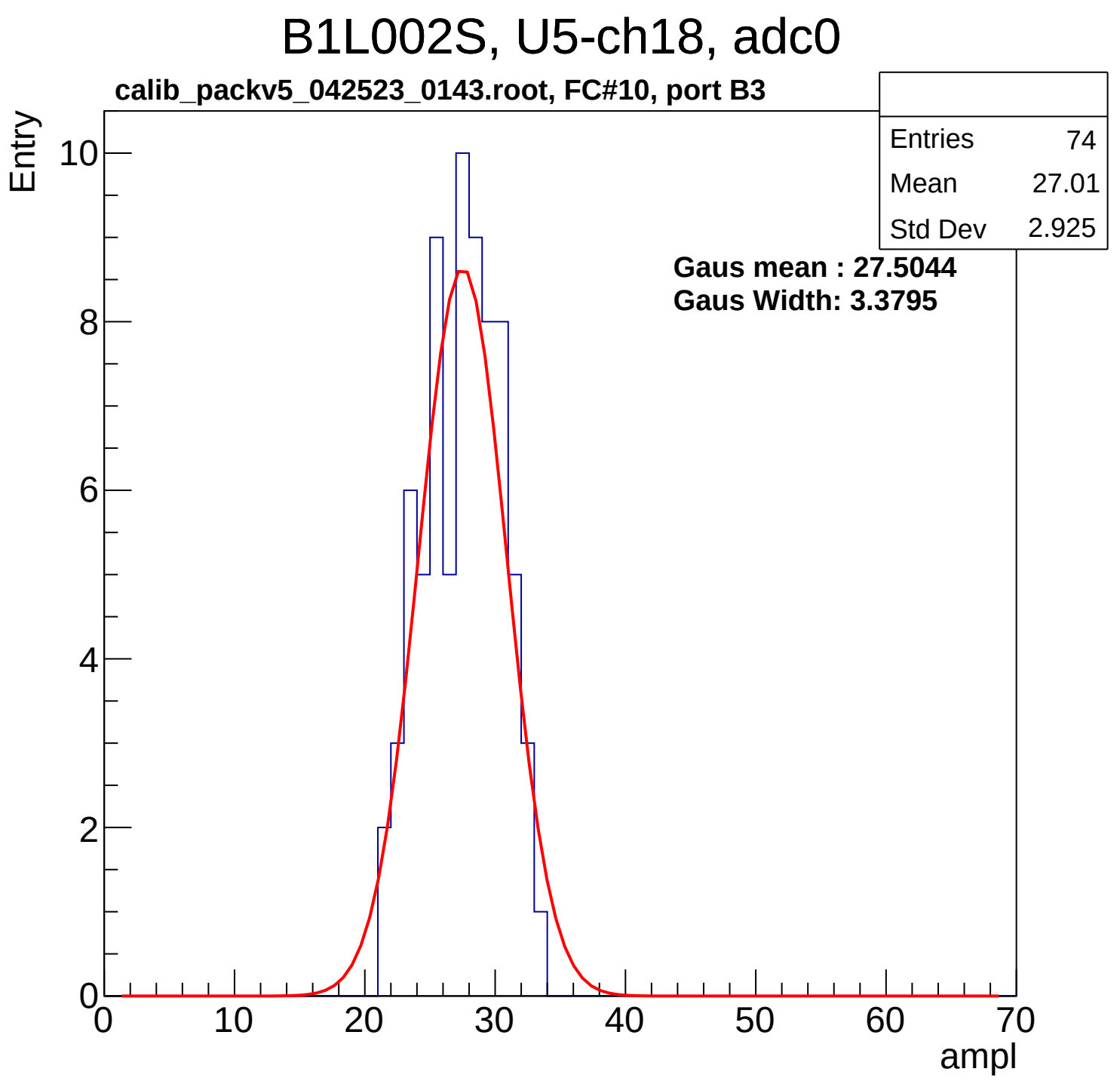
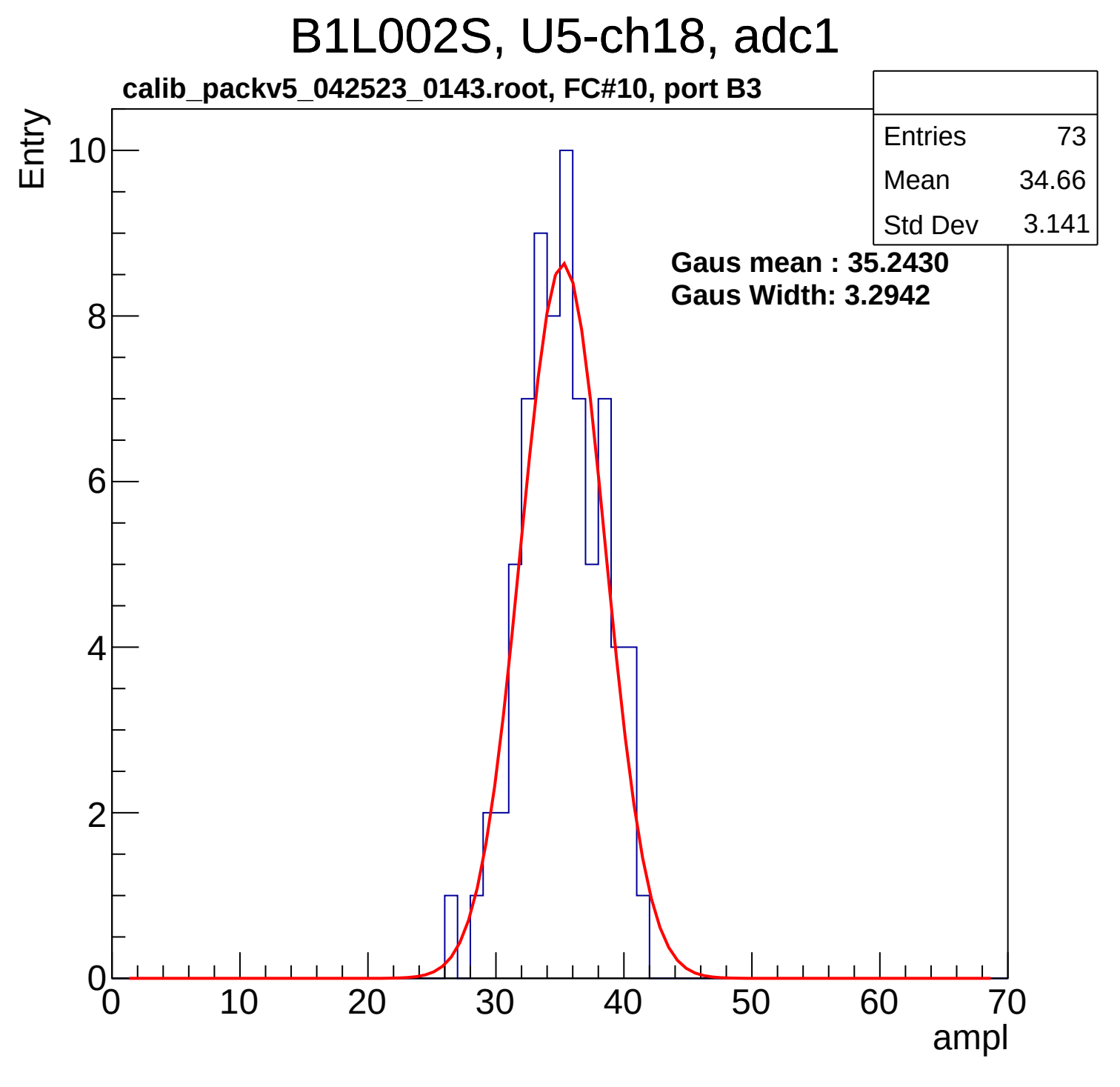
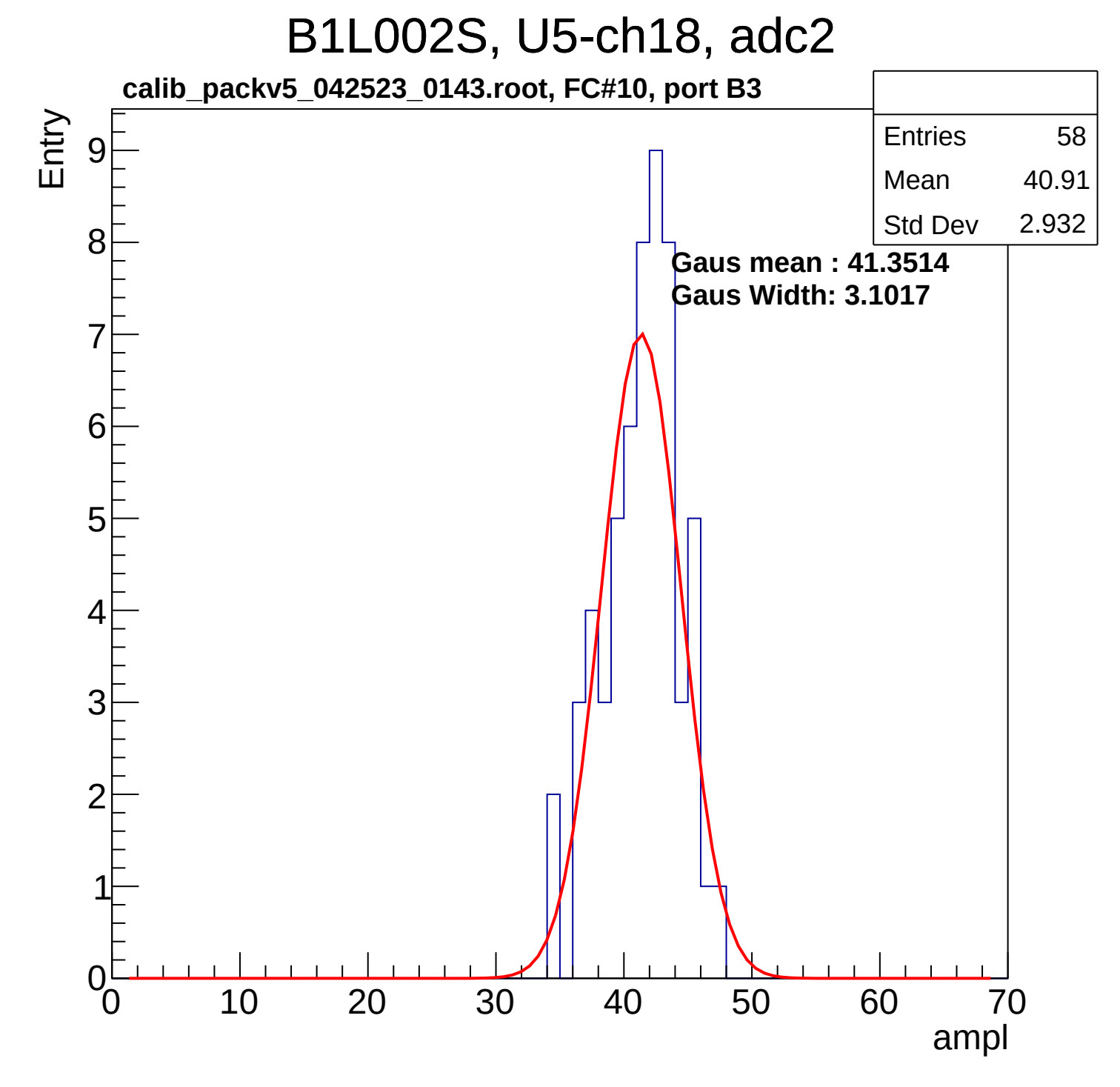
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

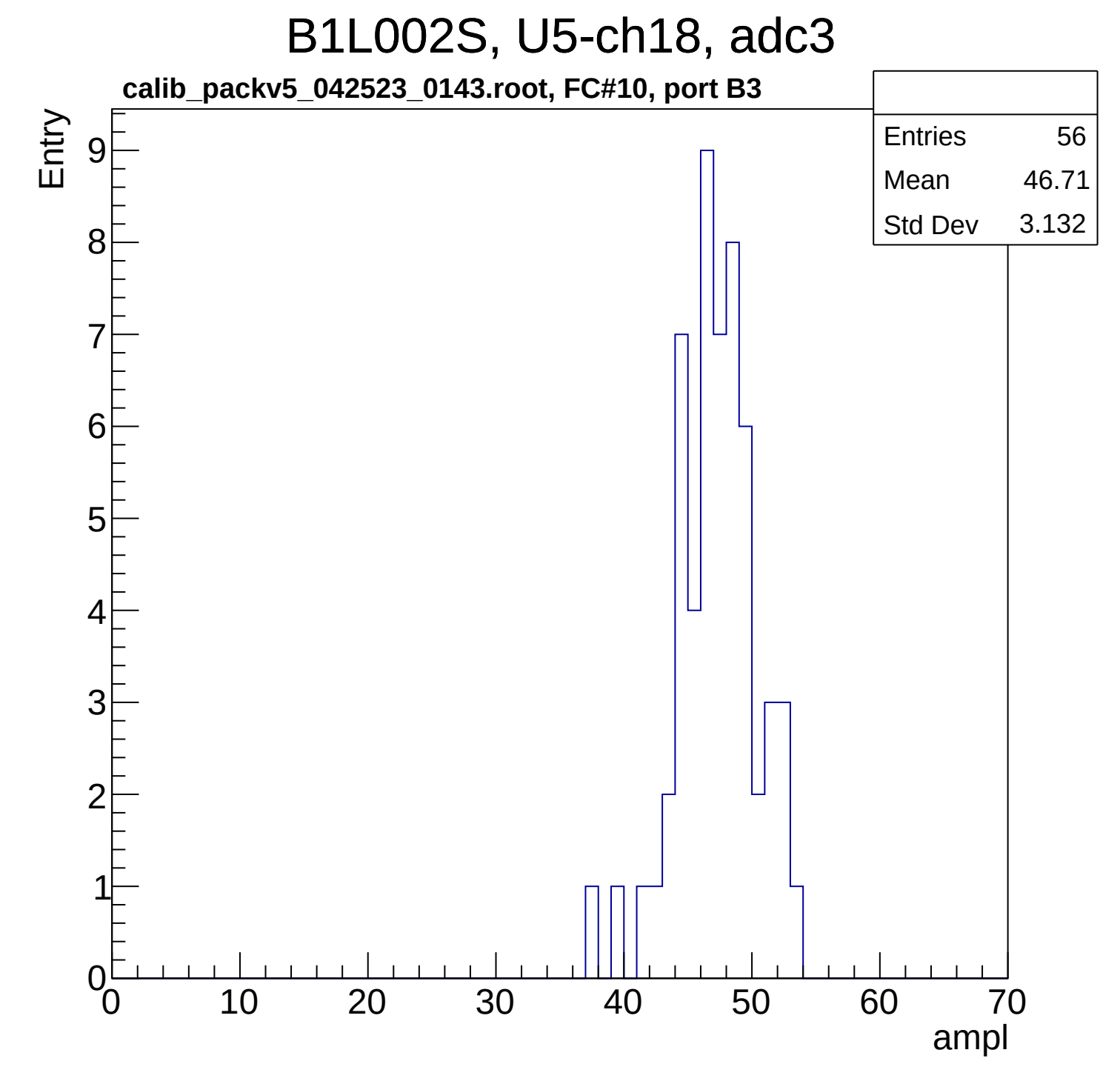

B1L002S, U5-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.76
Std Dev 1.113
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.01
Std Dev 2.925
Gaus mean : 27.5044
Gaus Width: 3.3795
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.66
Std Dev 3.141
Gaus mean : 35.2430
Gaus Width: 3.2942
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.91
Std Dev 2.932
Gaus mean : 41.3514
Gaus Width: 3.1017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.71
Std Dev 3.132
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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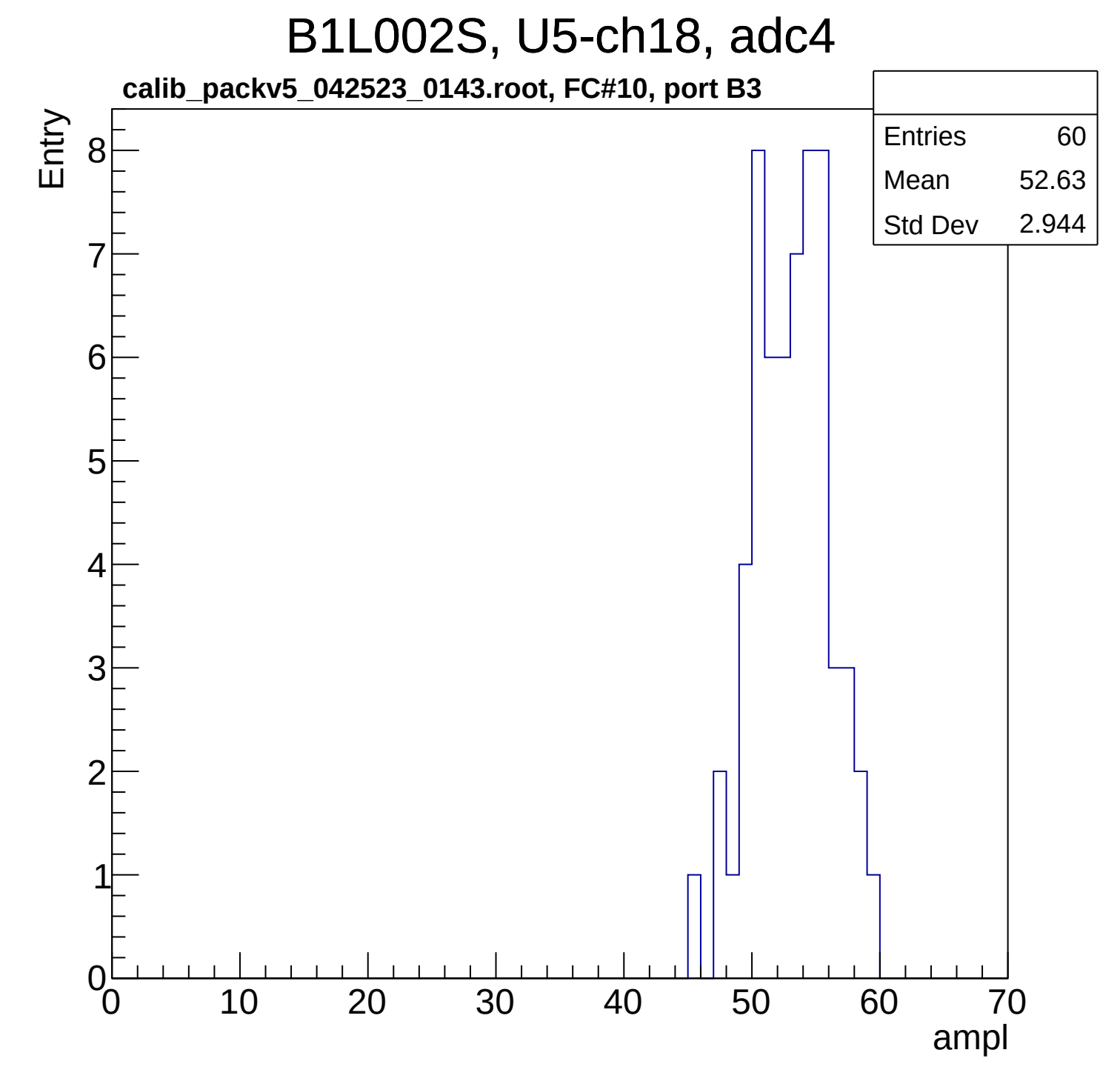
B1L002S, U5-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.63
Std Dev 2.944
Entry
ampl
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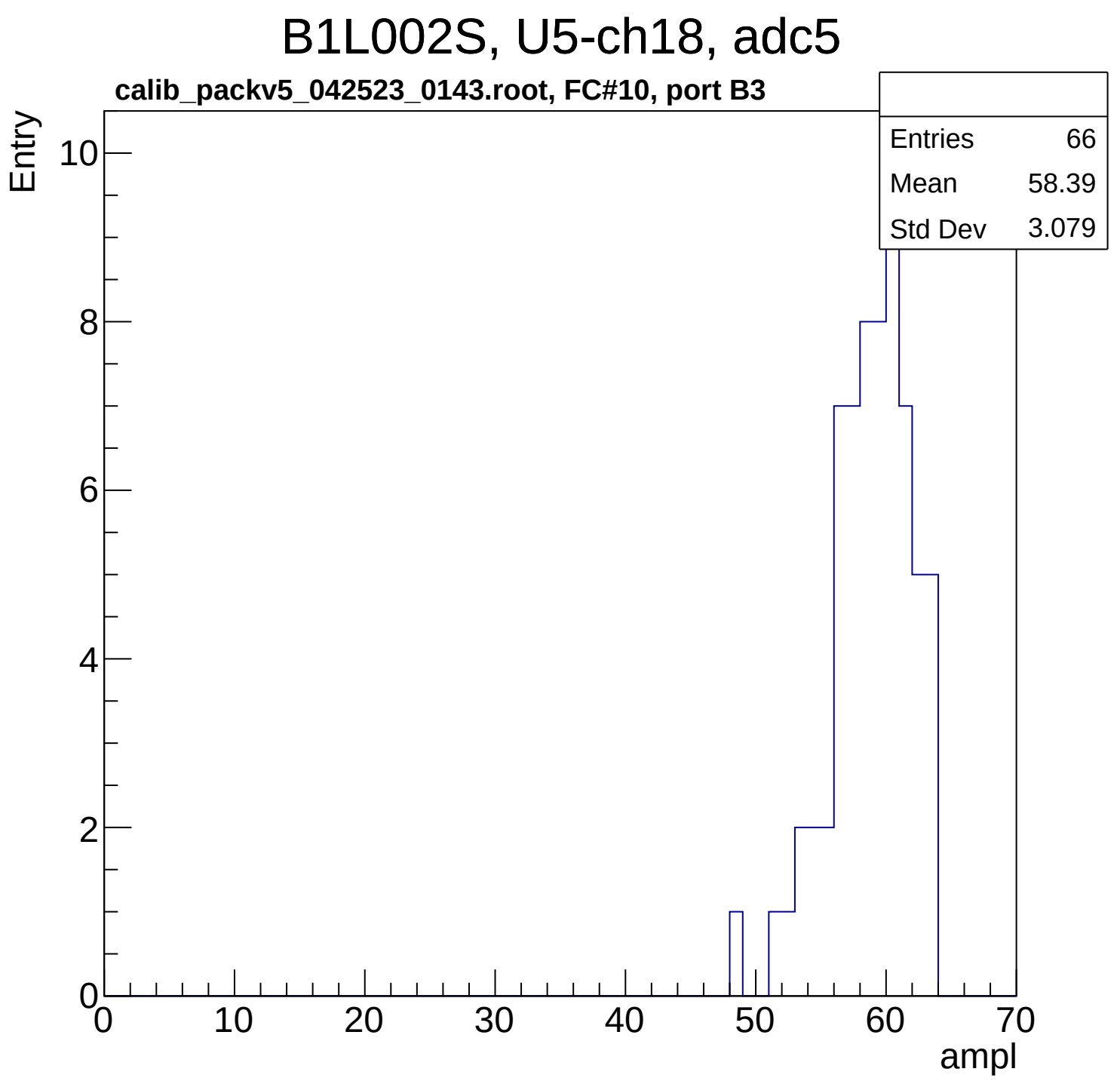
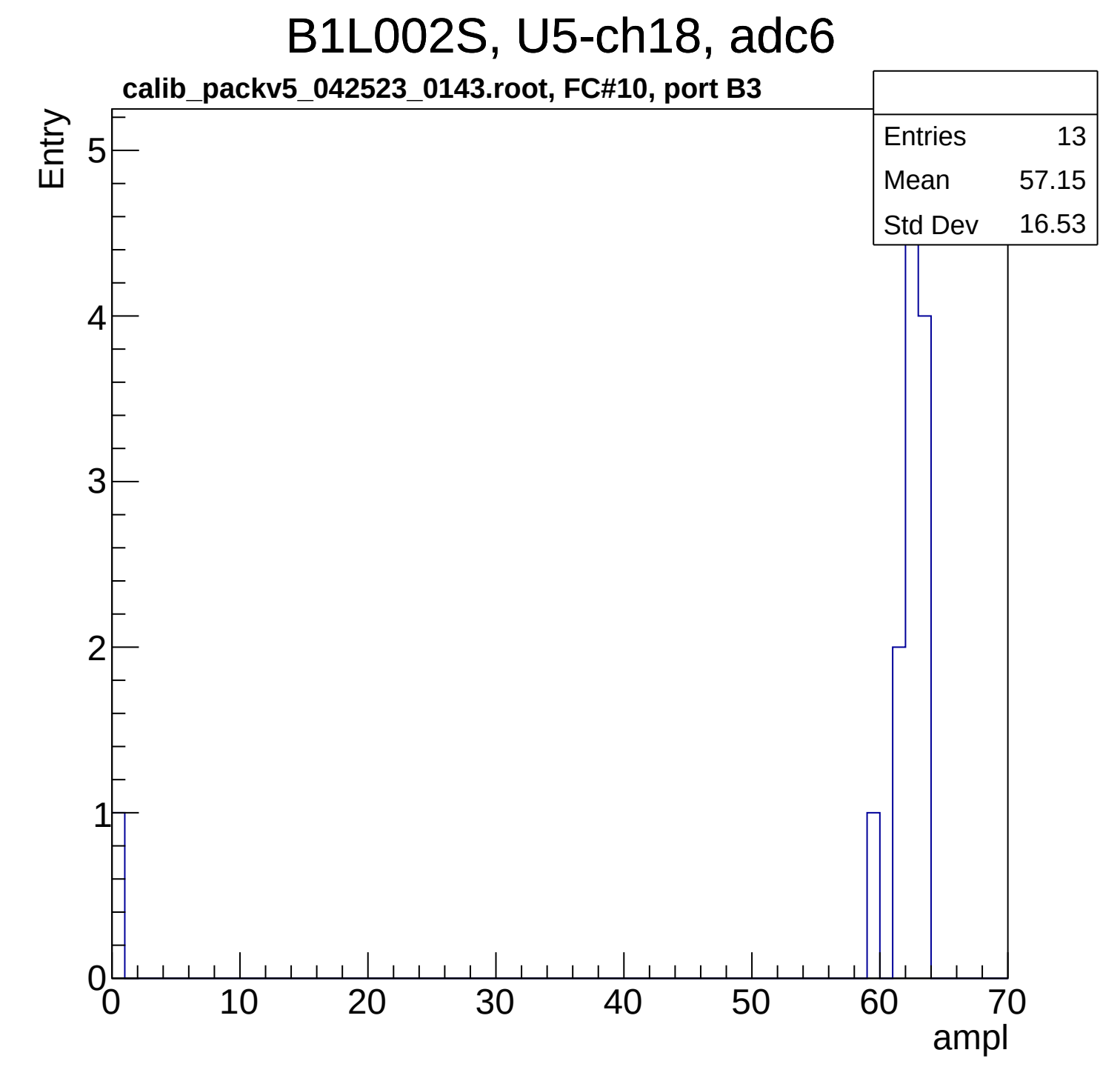
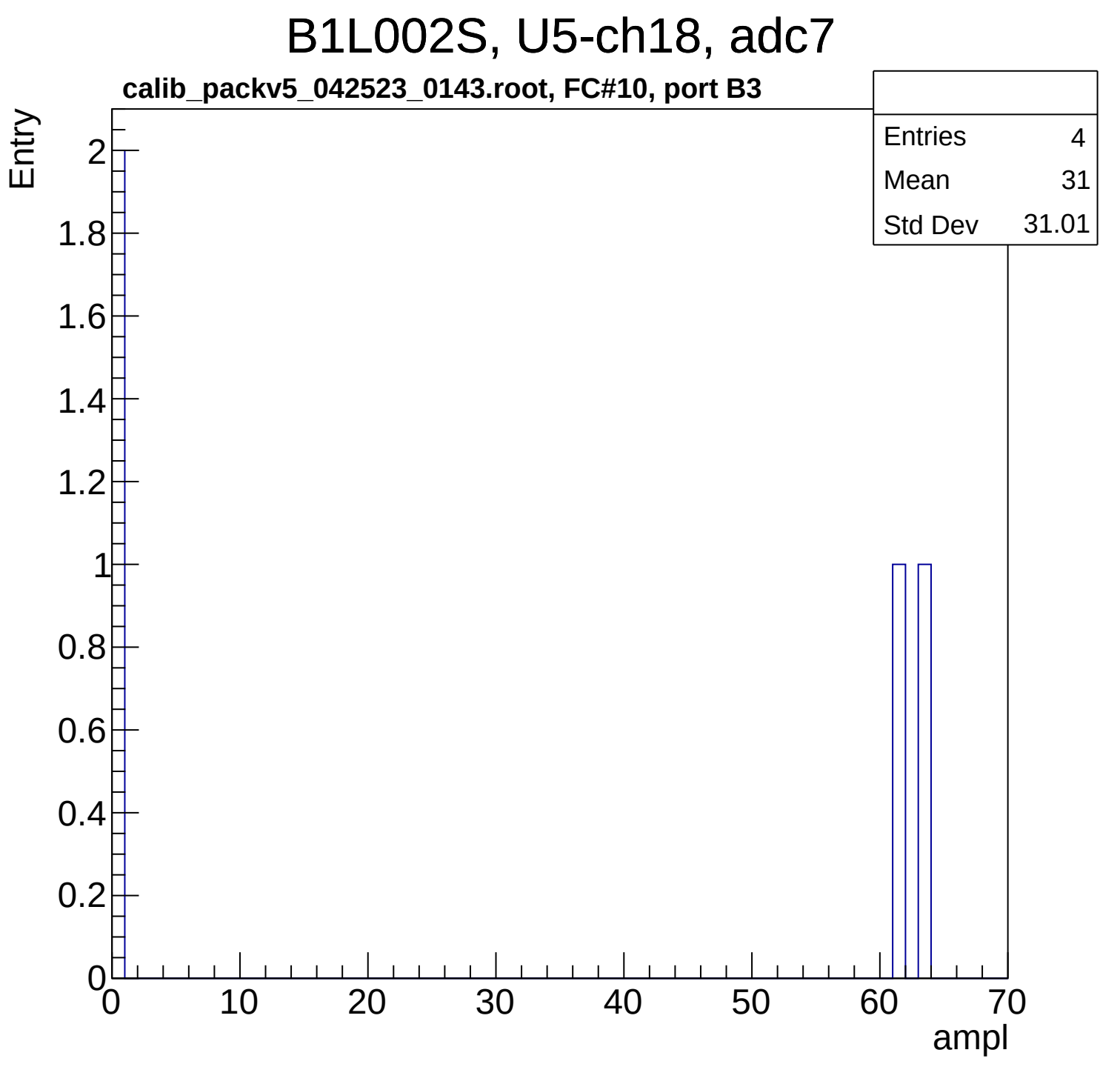
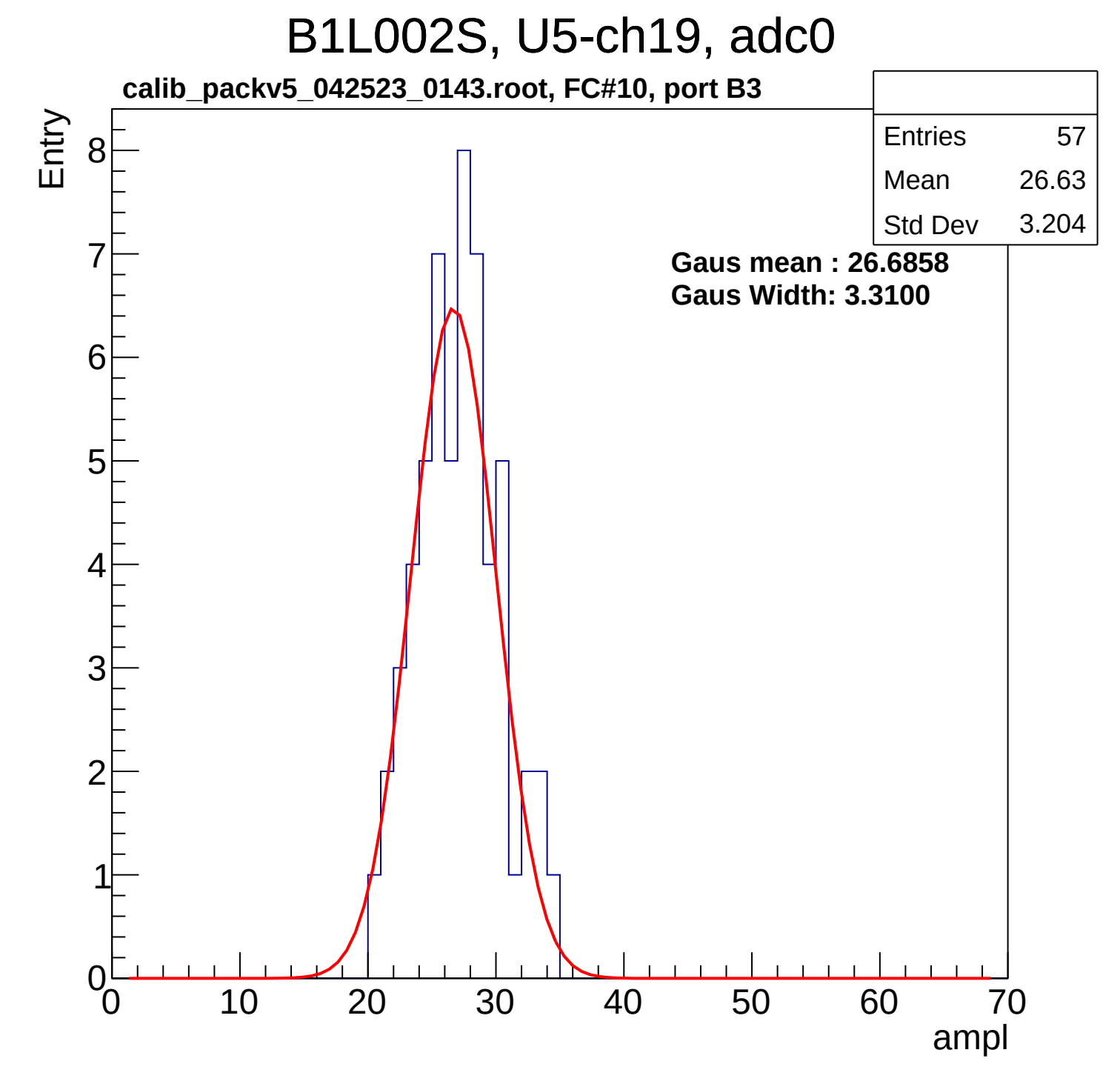
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.39
Std Dev 3.079
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.15
Std Dev 16.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.63
Std Dev 3.204
Gaus mean : 26.6858
Gaus Width: 3.3100
Entry
ampl
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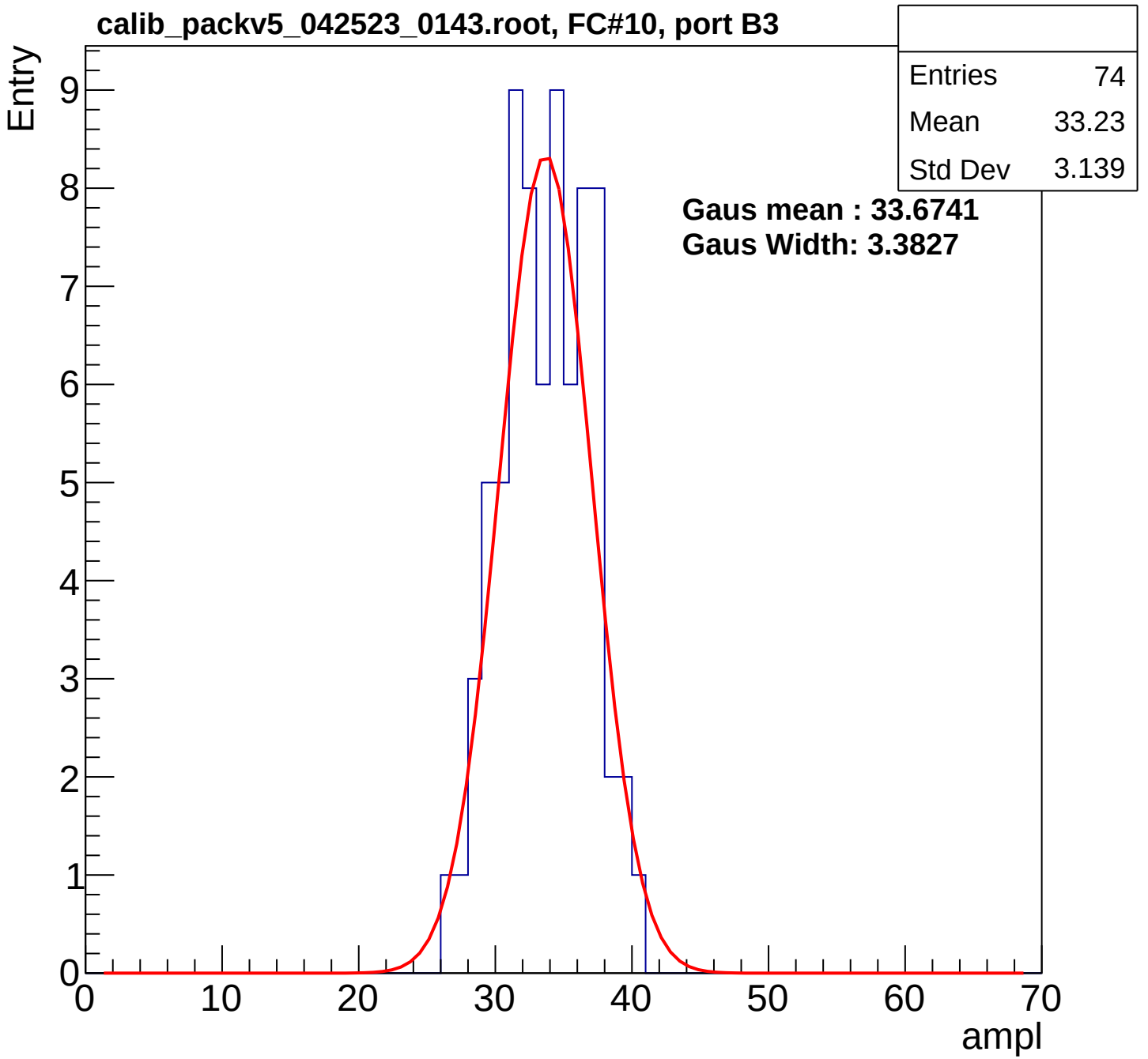
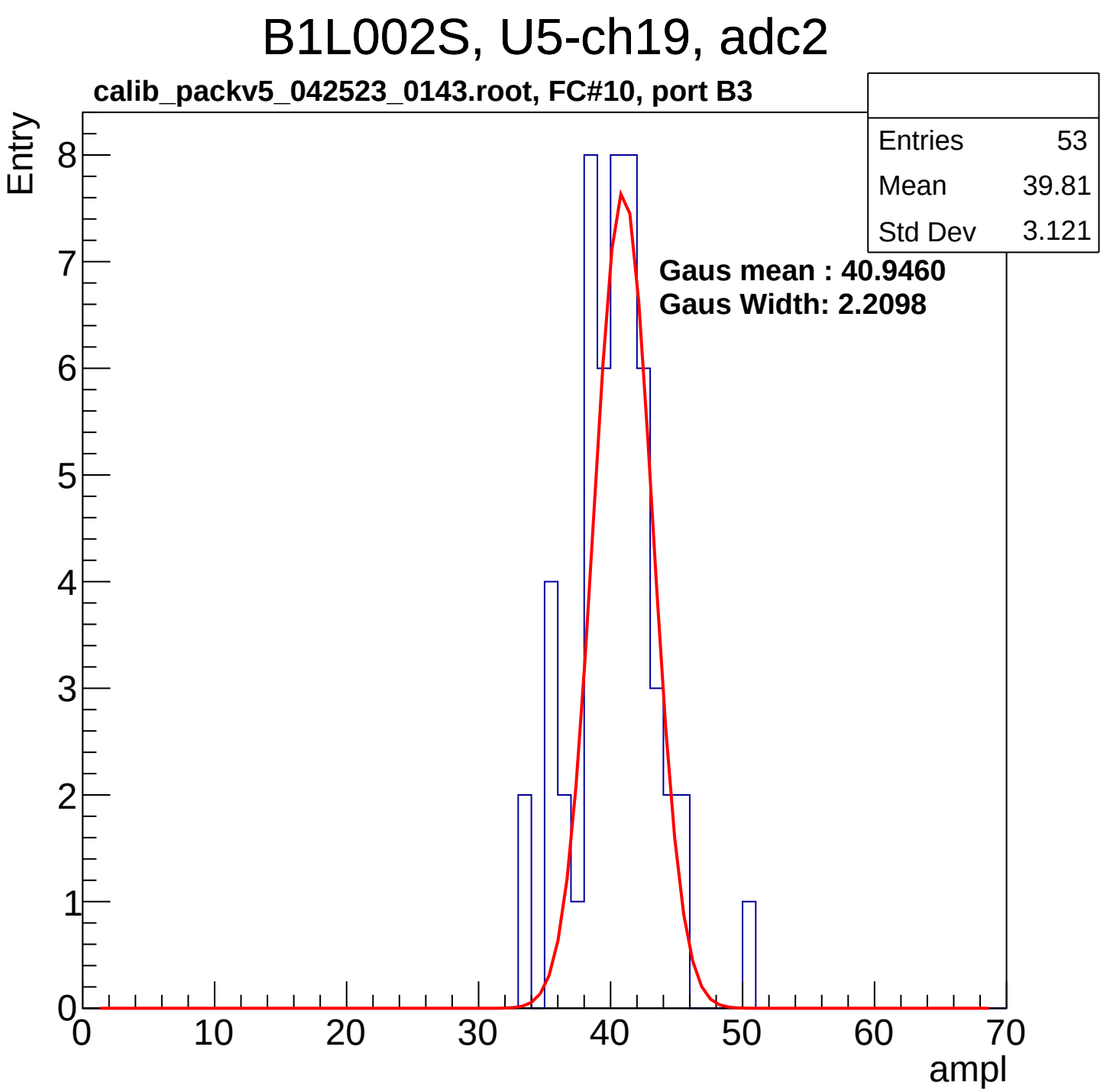
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.23
Std Dev 3.139
Gaus mean : 33.6741
Gaus Width: 3.3827
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 39.81
Std Dev 3.121
Gaus mean : 40.9460
Gaus Width: 2.2098
Entry
ampl
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1
2
3
4
5
6
7
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0
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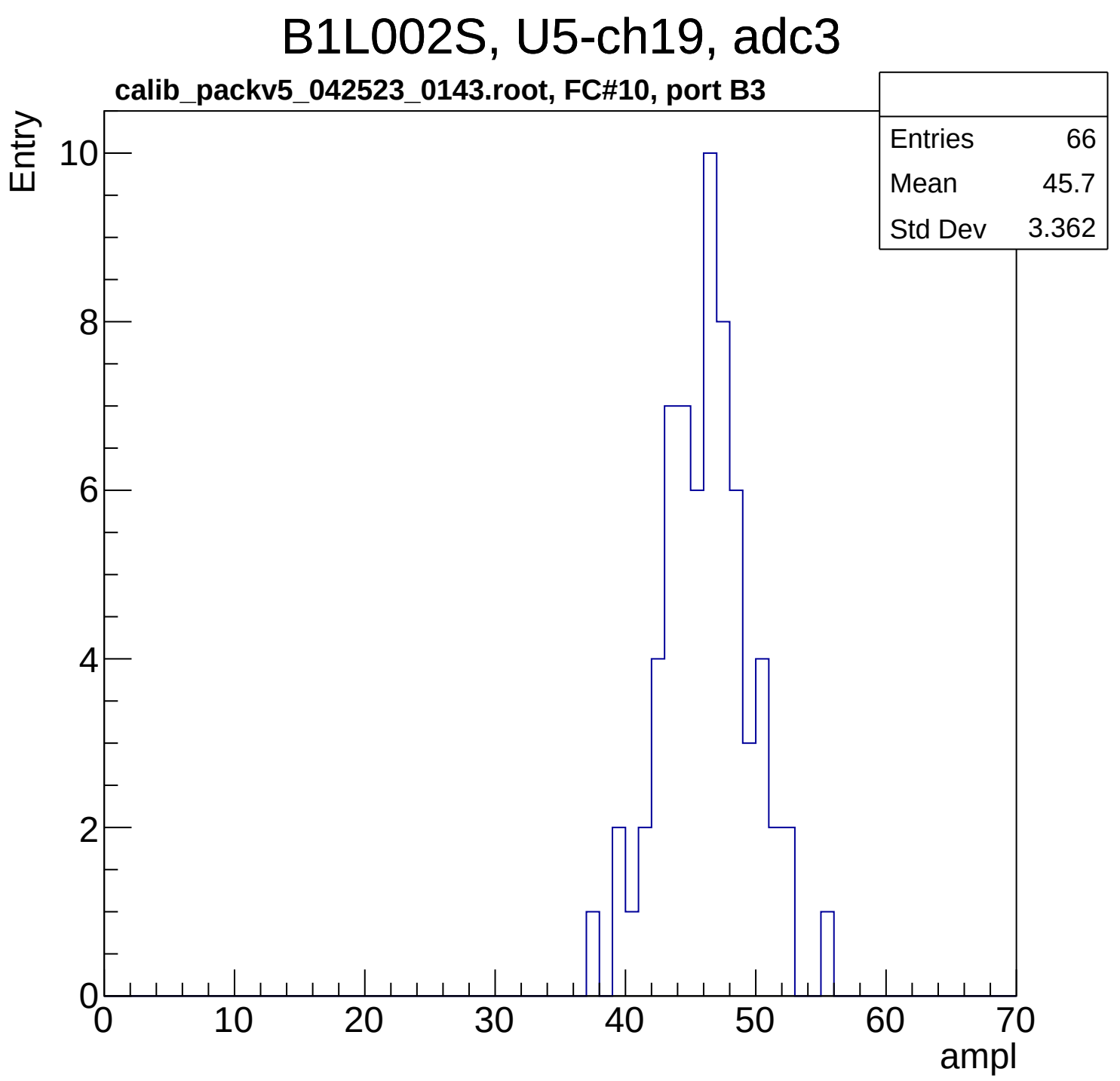
B1L002S, U5-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.7
Std Dev 3.362
Entry
ampl
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2
4
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0
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40
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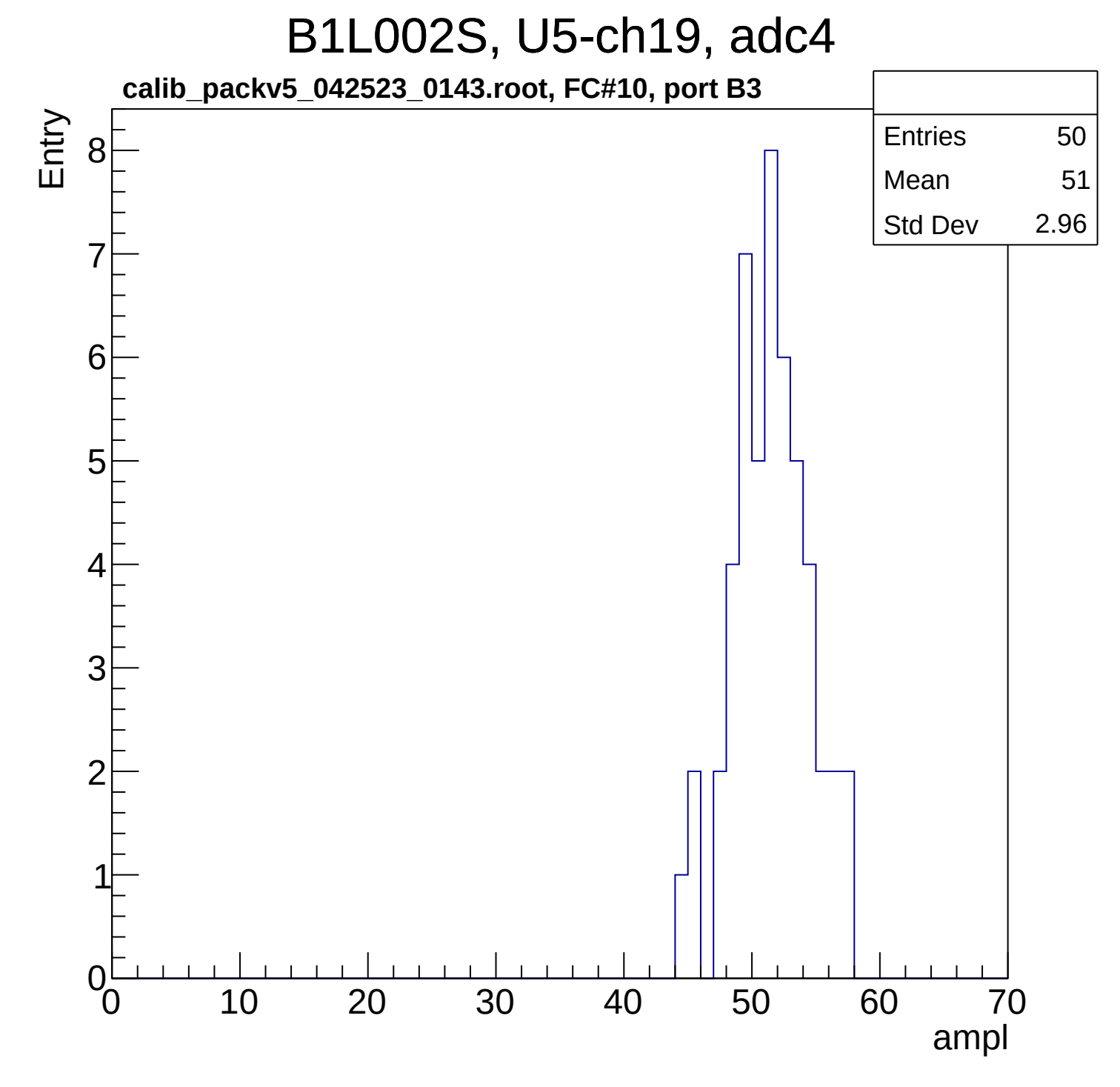
B1L002S, U5-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51
Std Dev 2.96
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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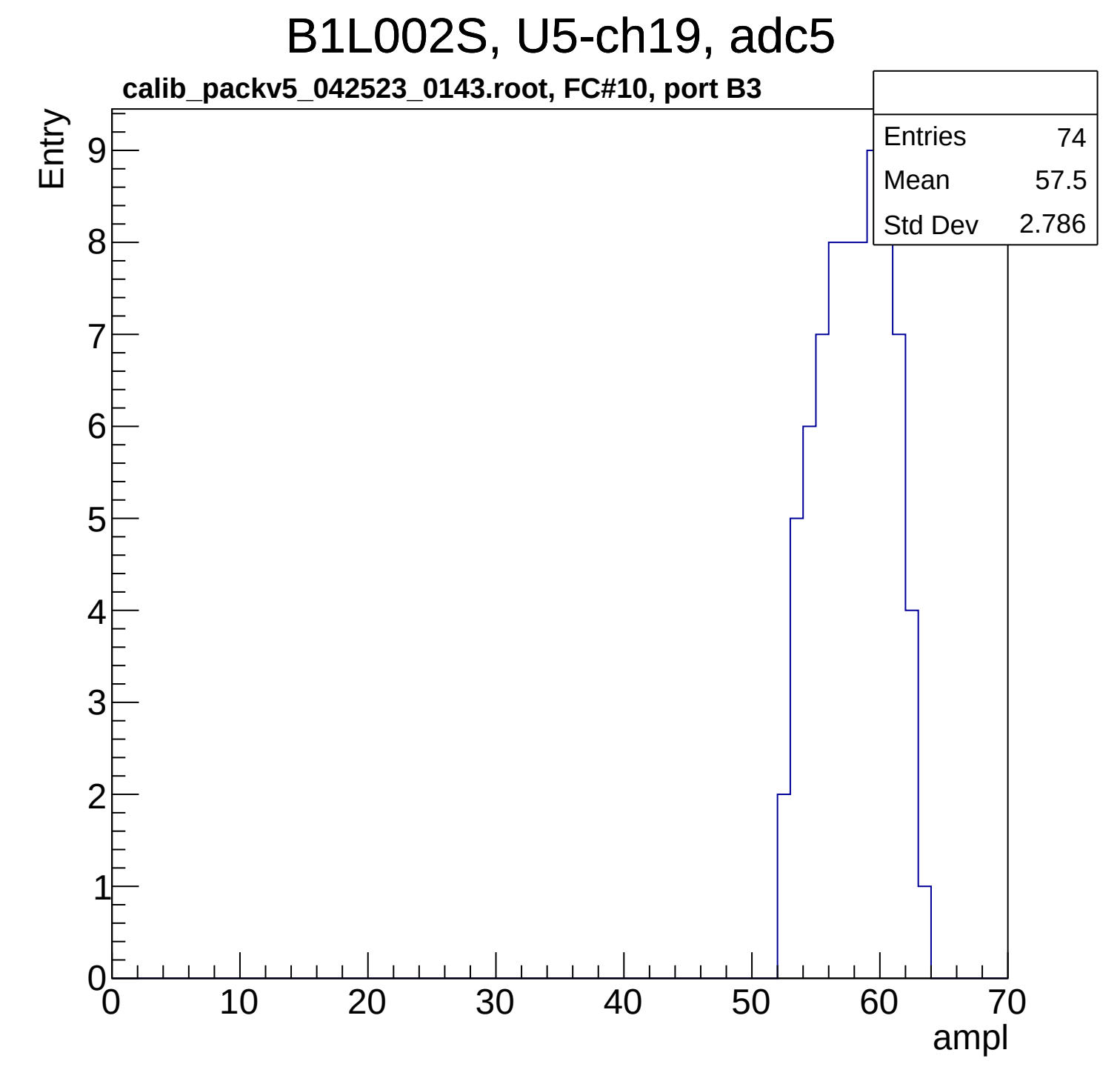
B1L002S, U5-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 57.5
Std Dev 2.786
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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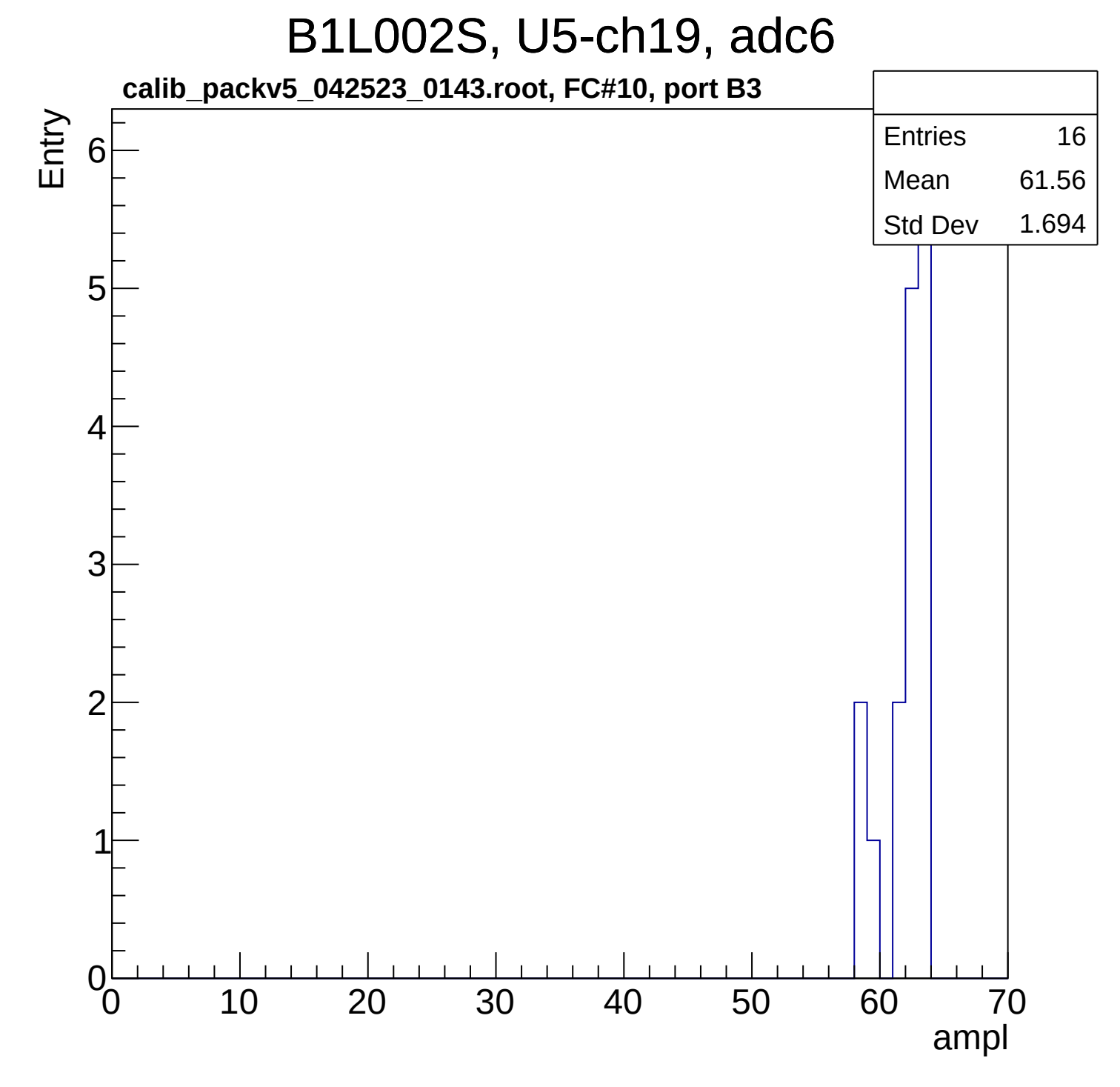
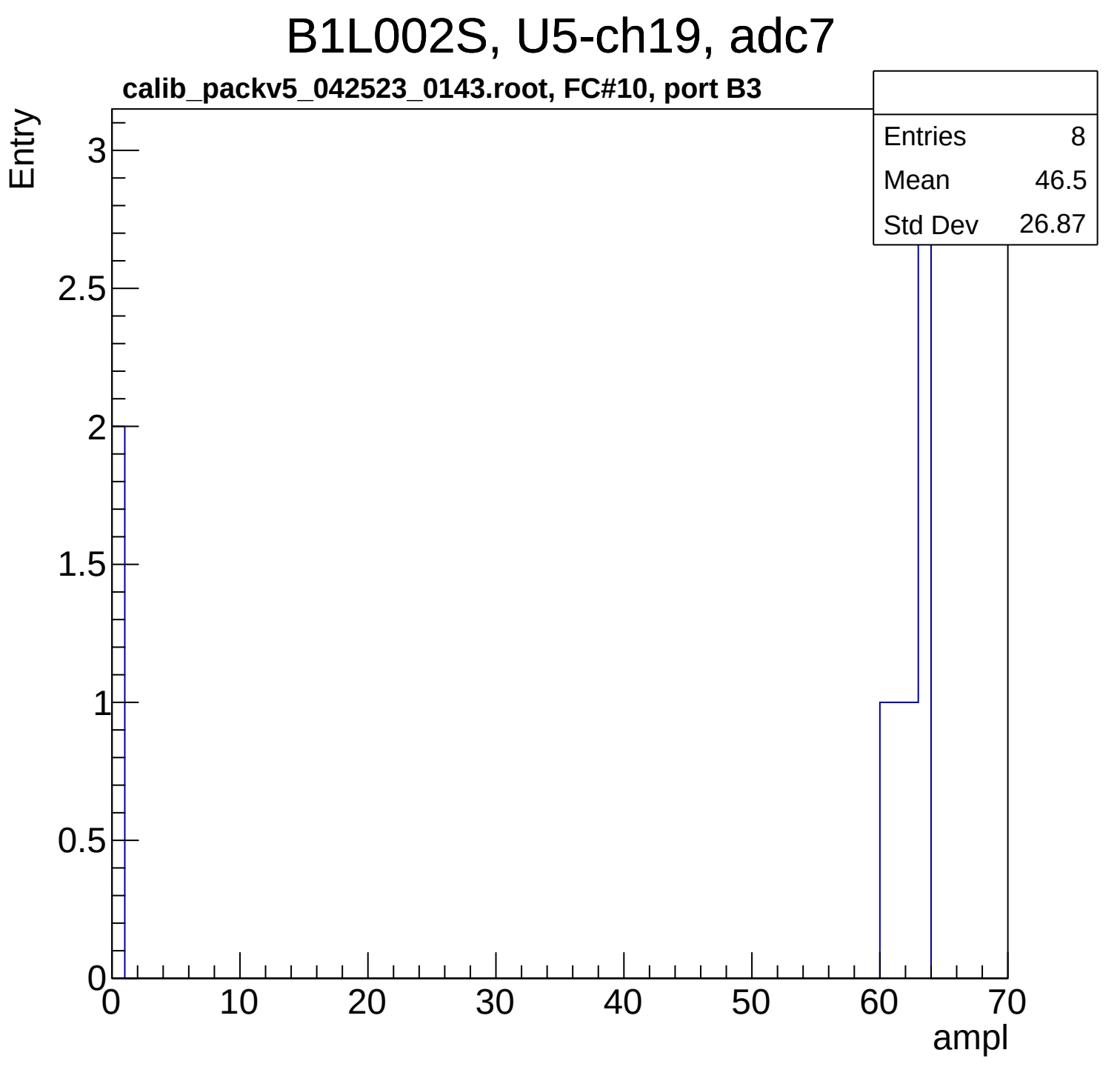

B1L002S, U5-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.56
Std Dev 1.694
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 46.5
Std Dev 26.87
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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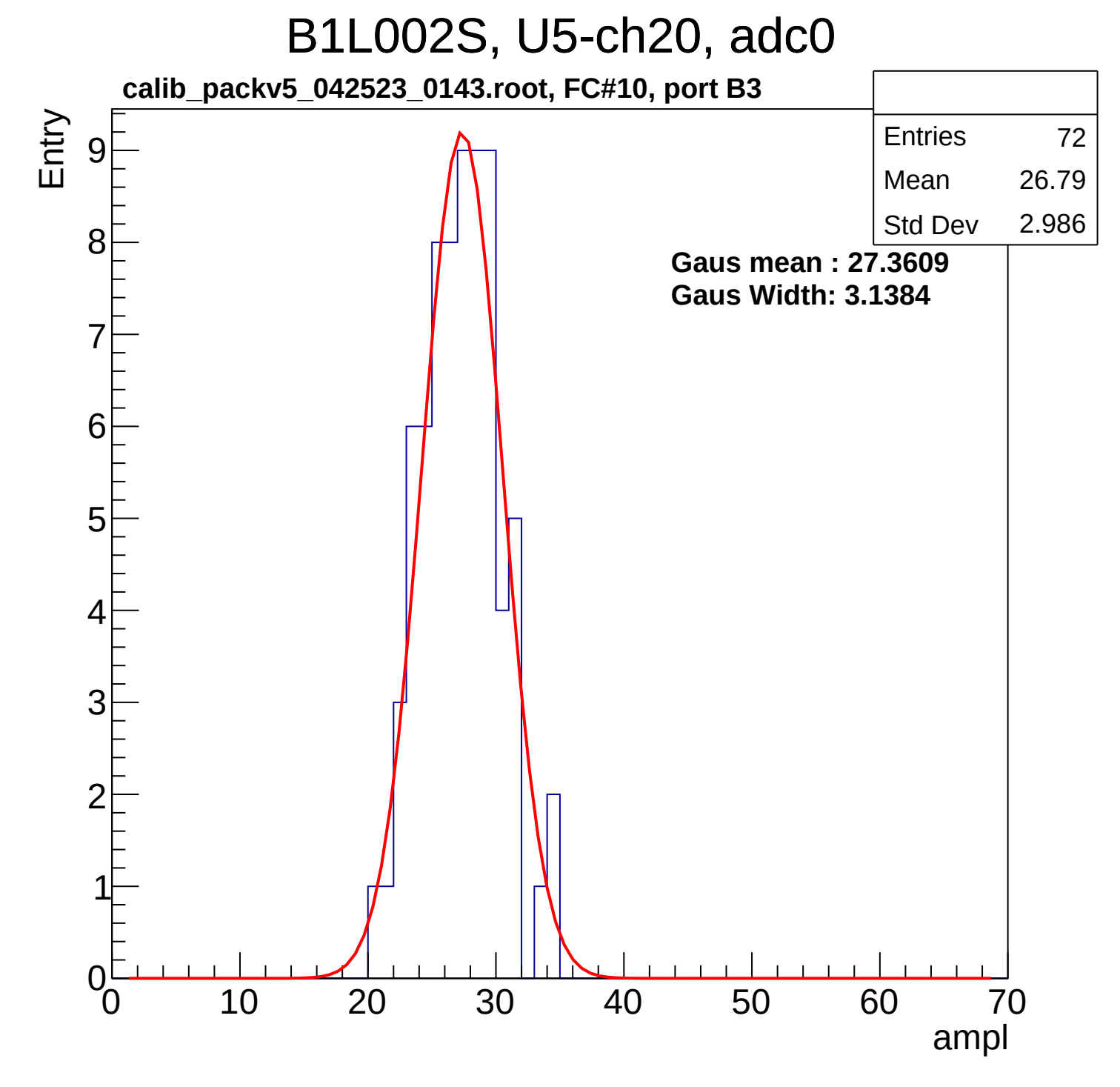
B1L002S, U5-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.79
Std Dev 2.986
Gaus mean : 27.3609
Gaus Width: 3.1384
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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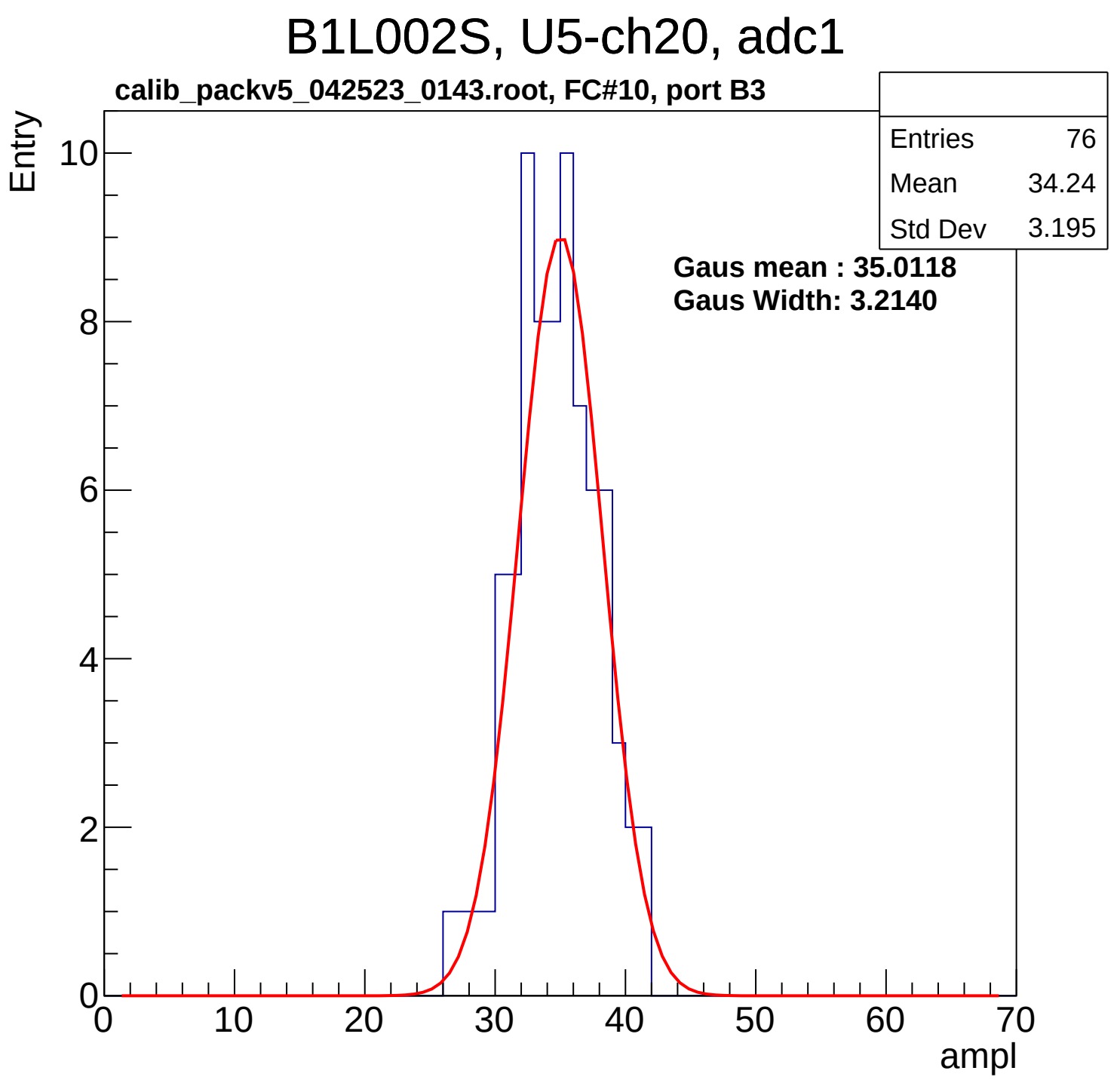
B1L002S, U5-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.24
Std Dev 3.195
Gaus mean : 35.0118
Gaus Width: 3.2140
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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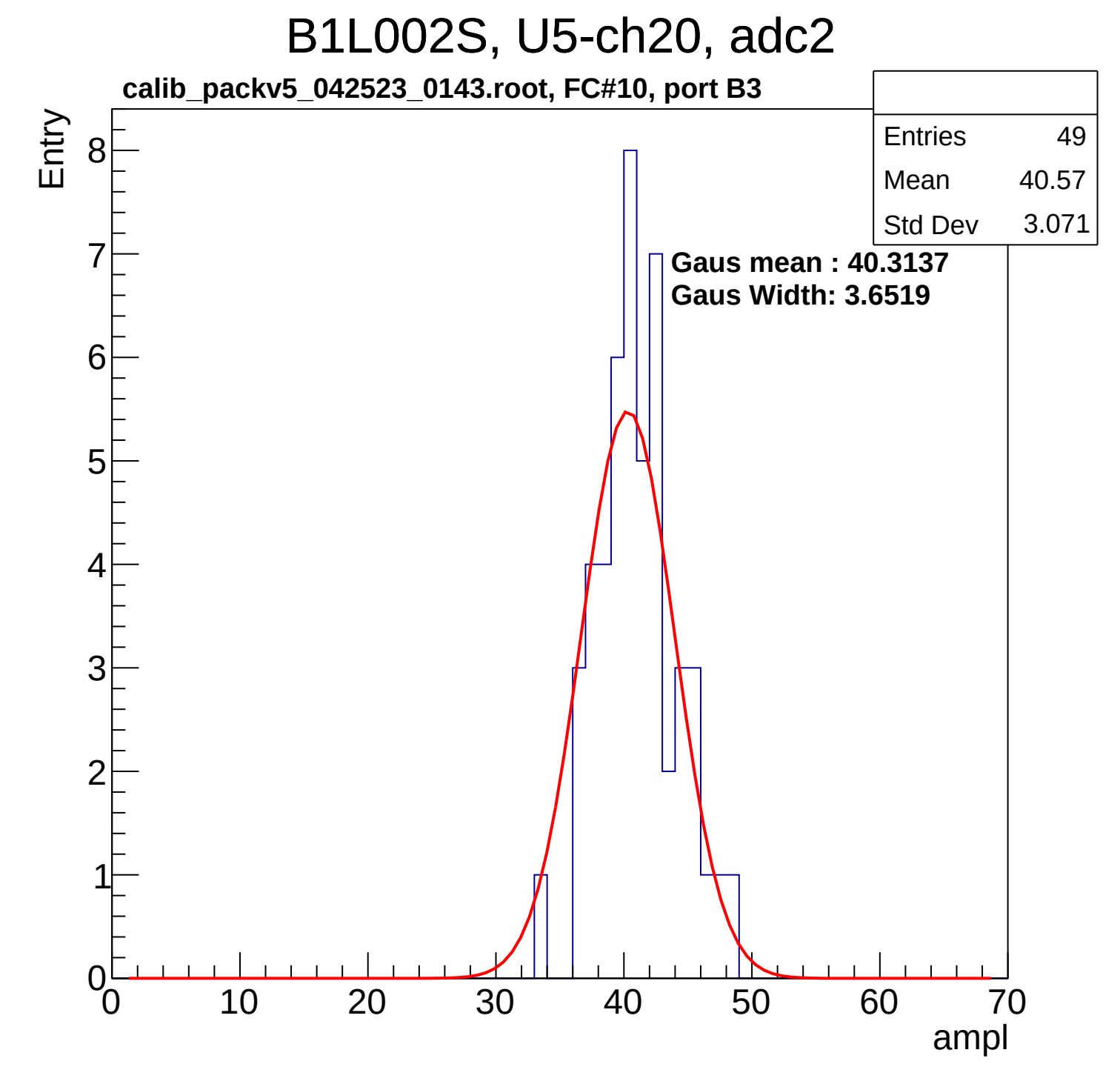
B1L002S, U5-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.57
Std Dev 3.071
Gaus mean : 40.3137
Gaus Width: 3.6519
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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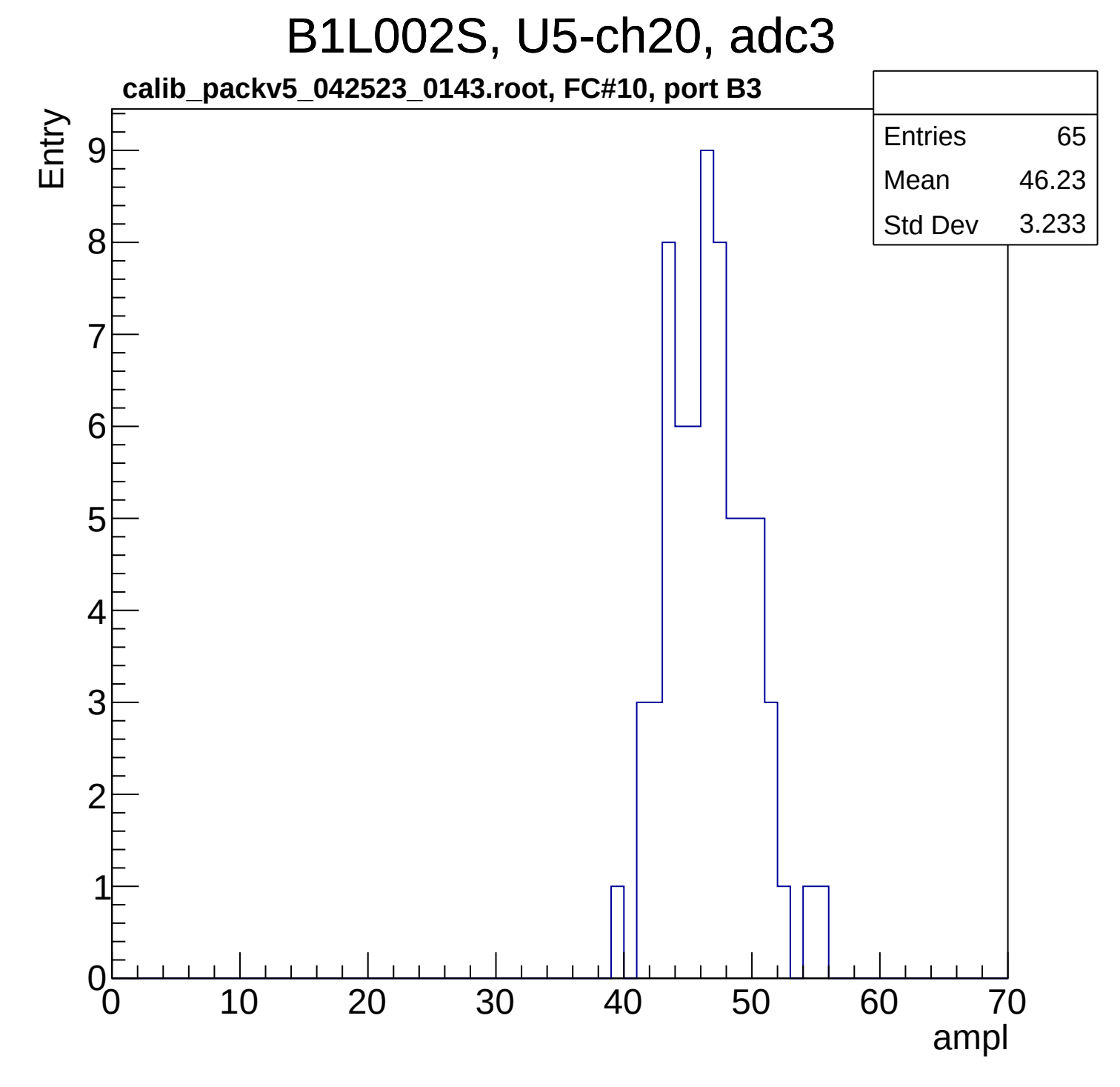
B1L002S, U5-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.23
Std Dev 3.233
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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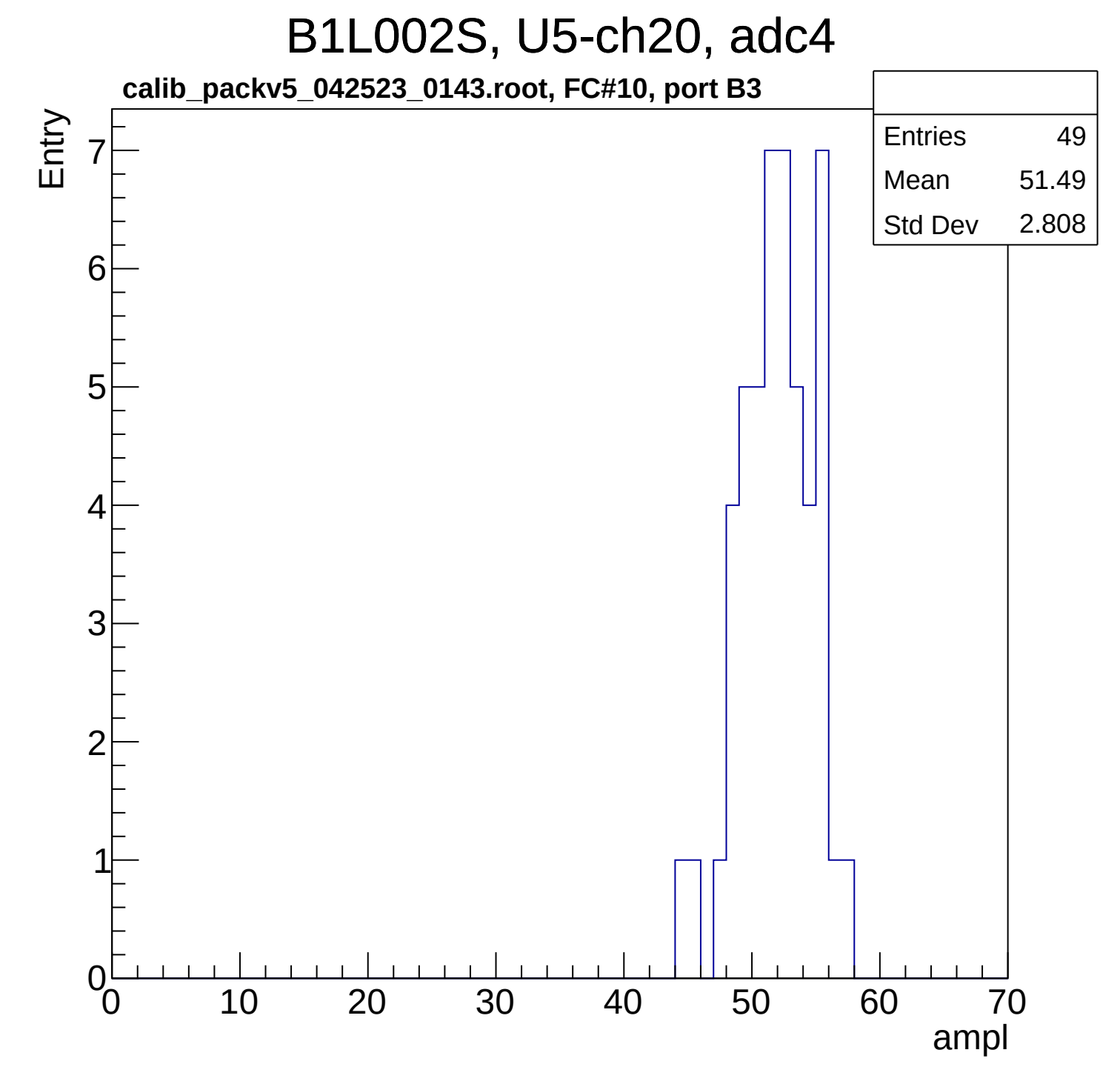
B1L002S, U5-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.49
Std Dev 2.808
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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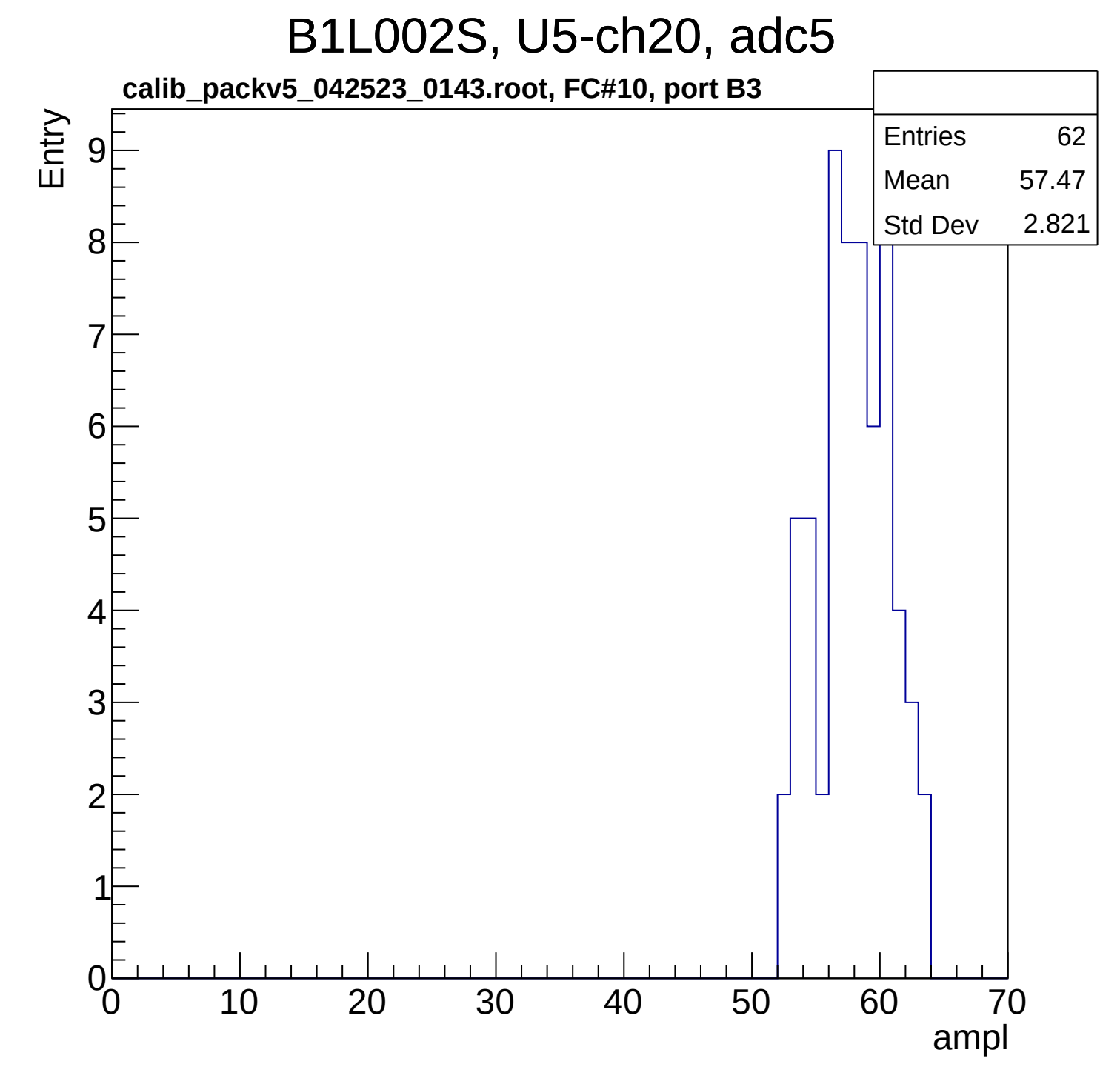
B1L002S, U5-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.47
Std Dev 2.821
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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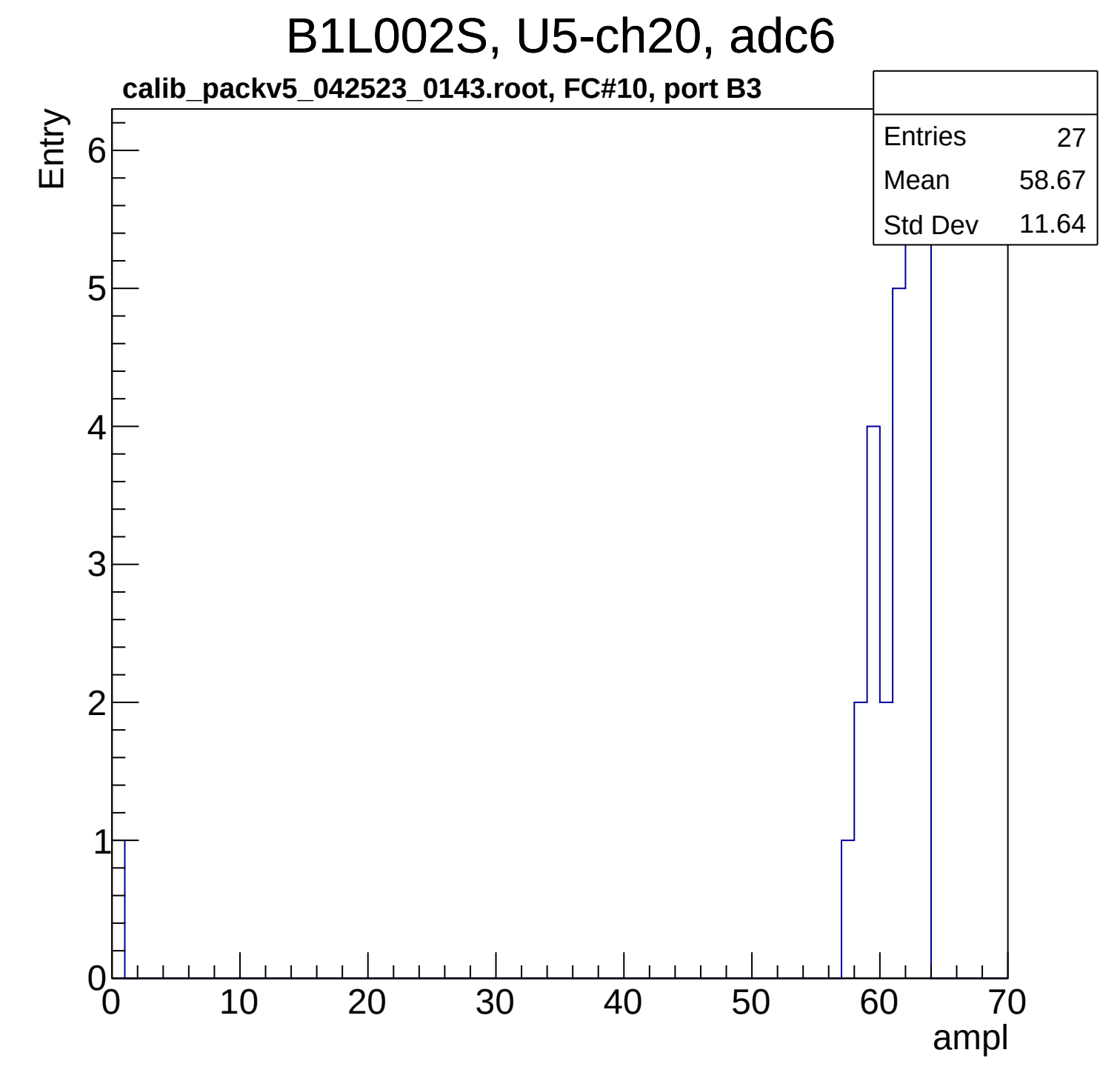
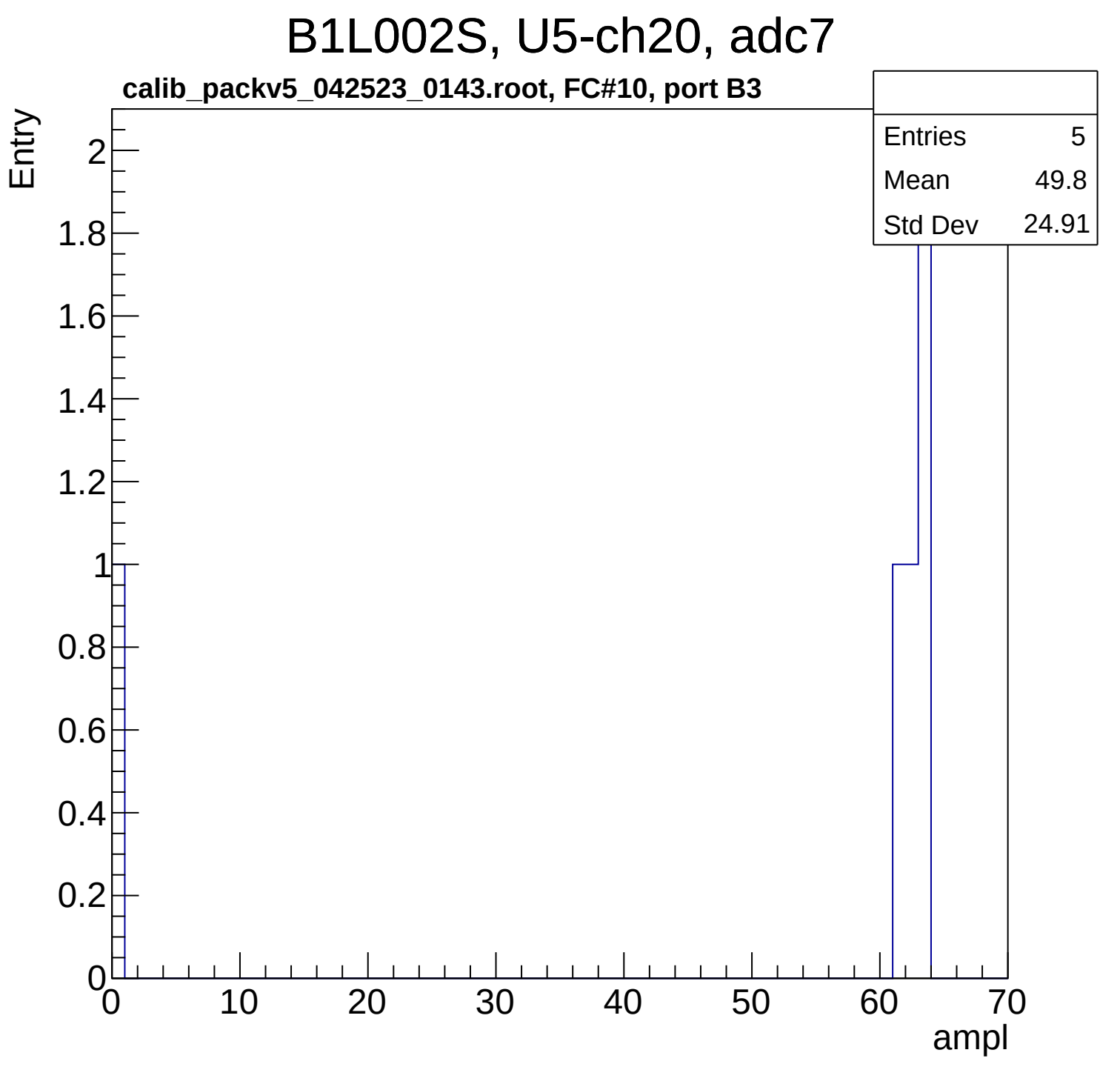

B1L002S, U5-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.67
Std Dev 11.64
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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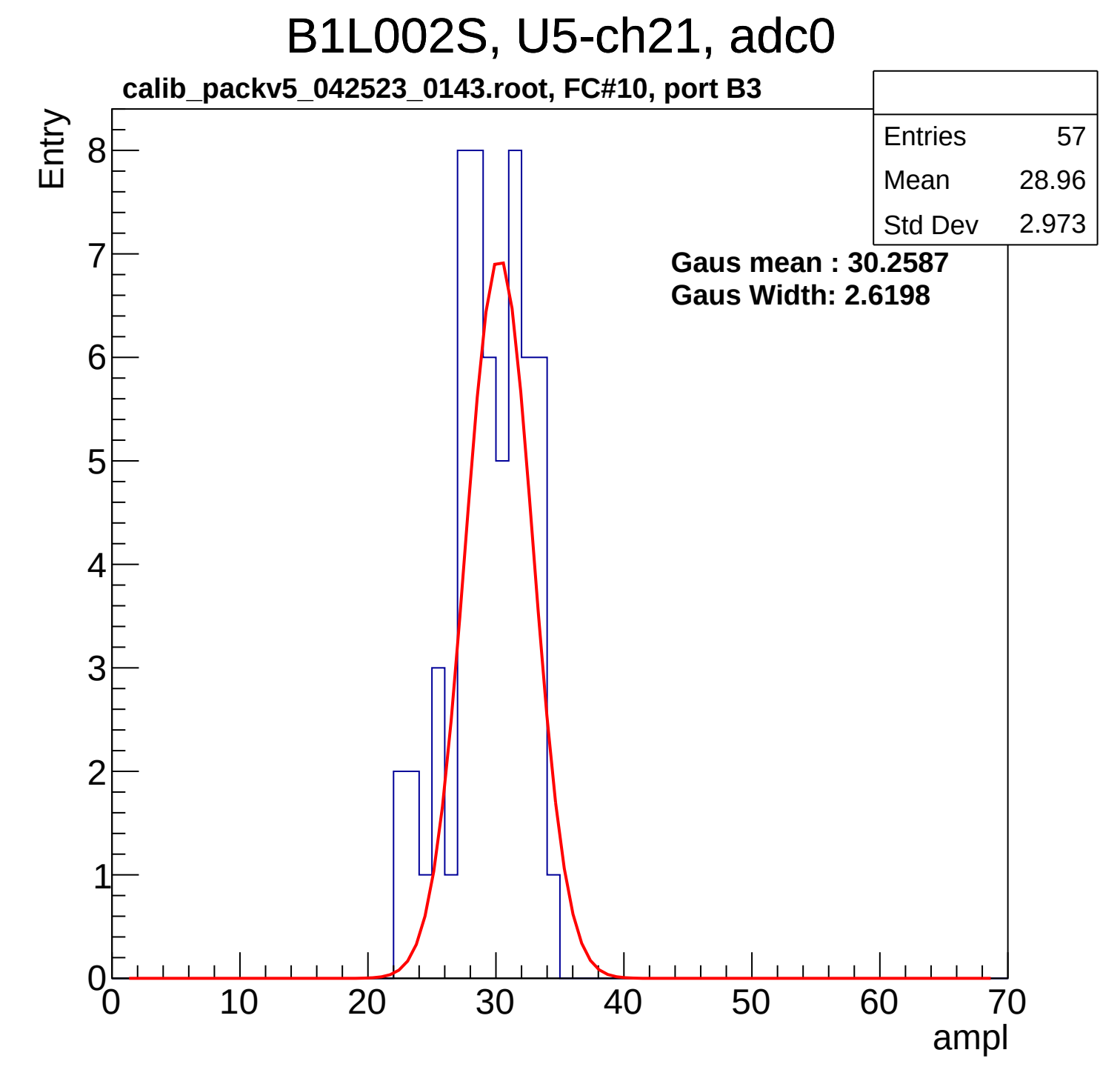
B1L002S, U5-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.96
Std Dev 2.973
Gaus mean : 30.2587
Gaus Width: 2.6198
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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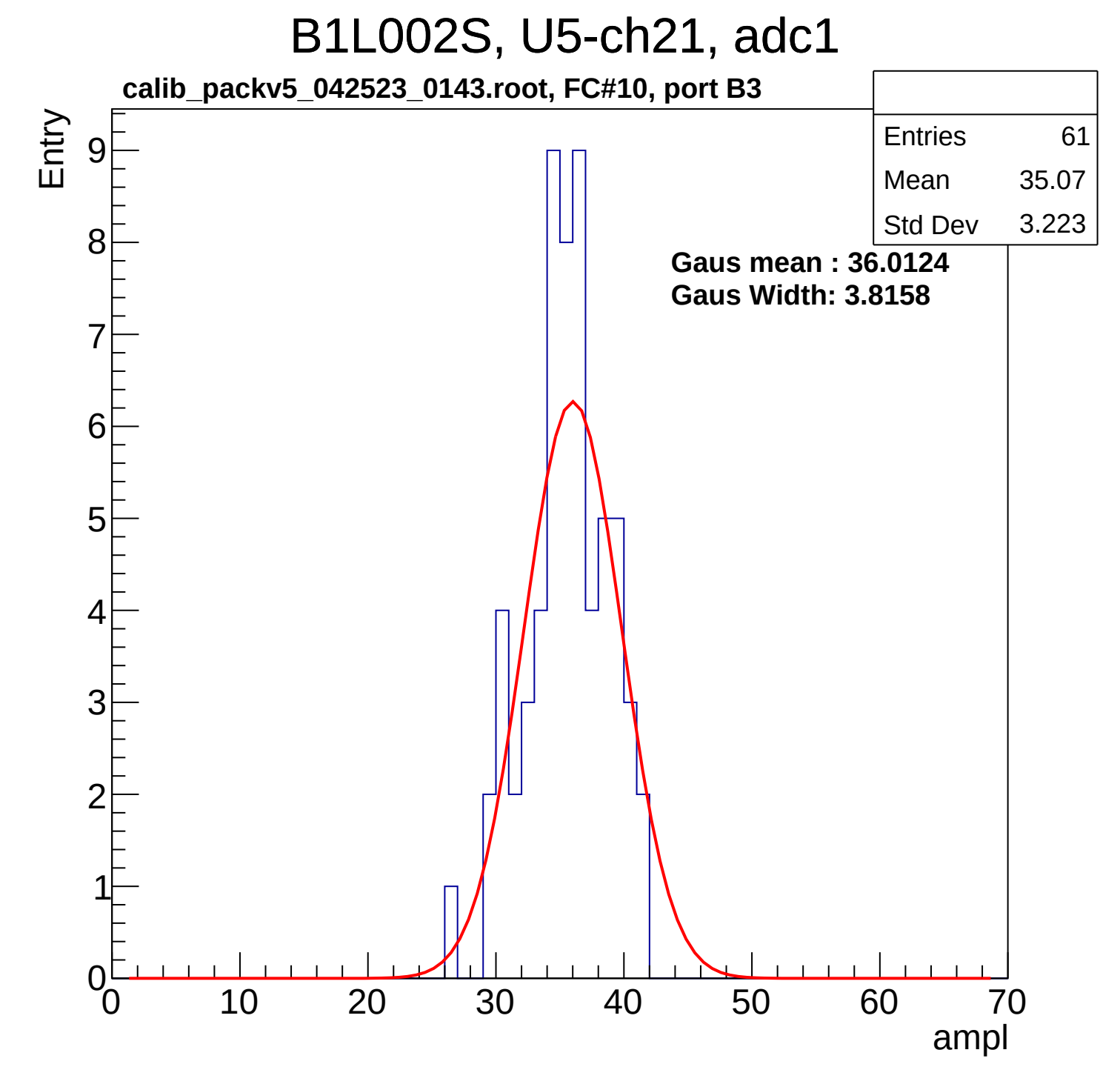
B1L002S, U5-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.07
Std Dev 3.223
Gaus mean : 36.0124
Gaus Width: 3.8158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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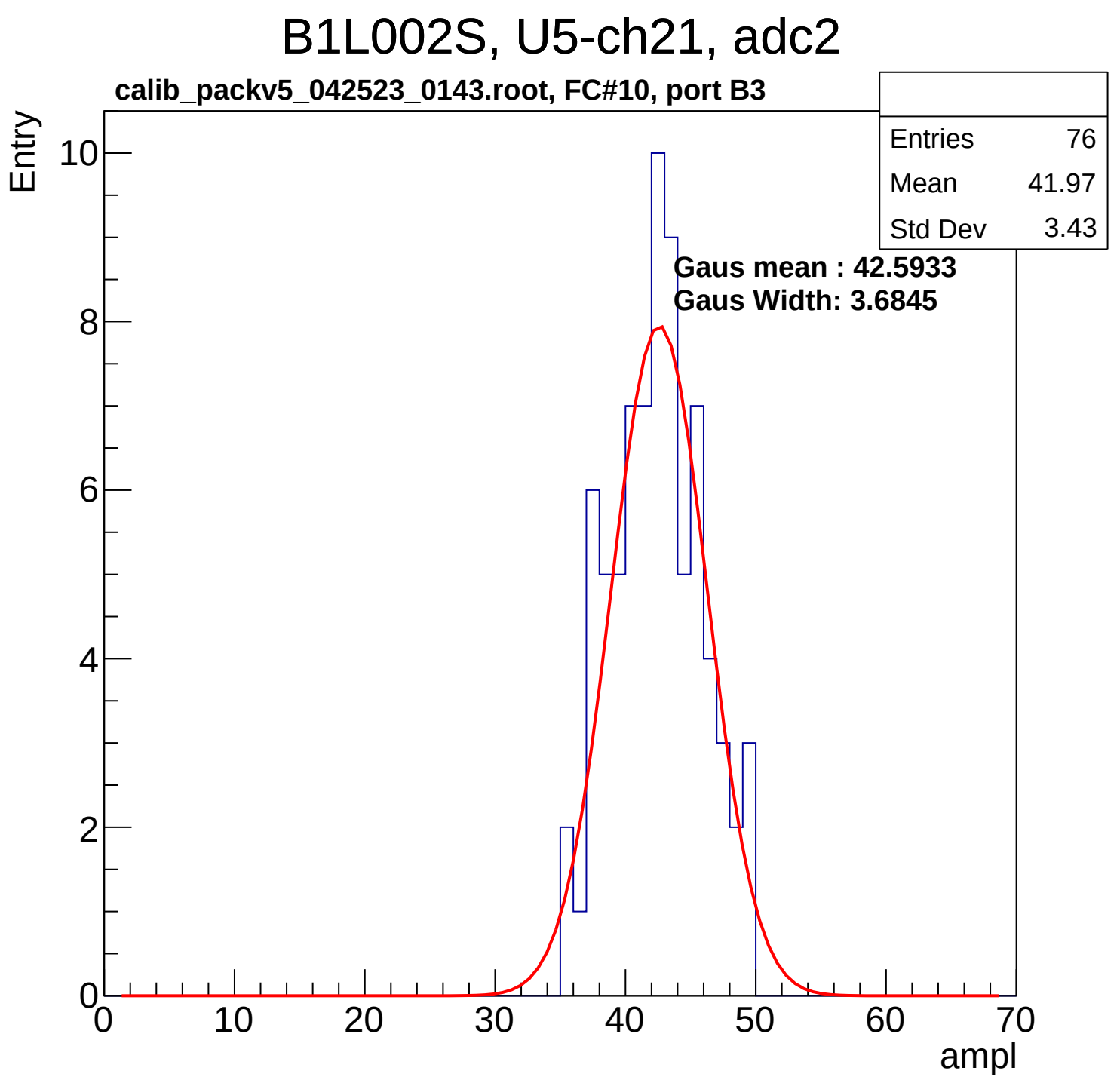
B1L002S, U5-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 41.97
Std Dev 3.43
Gaus mean : 42.5933
Gaus Width: 3.6845
Entry
ampl
0
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4
6
8
10
0
10
20
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40
50
60
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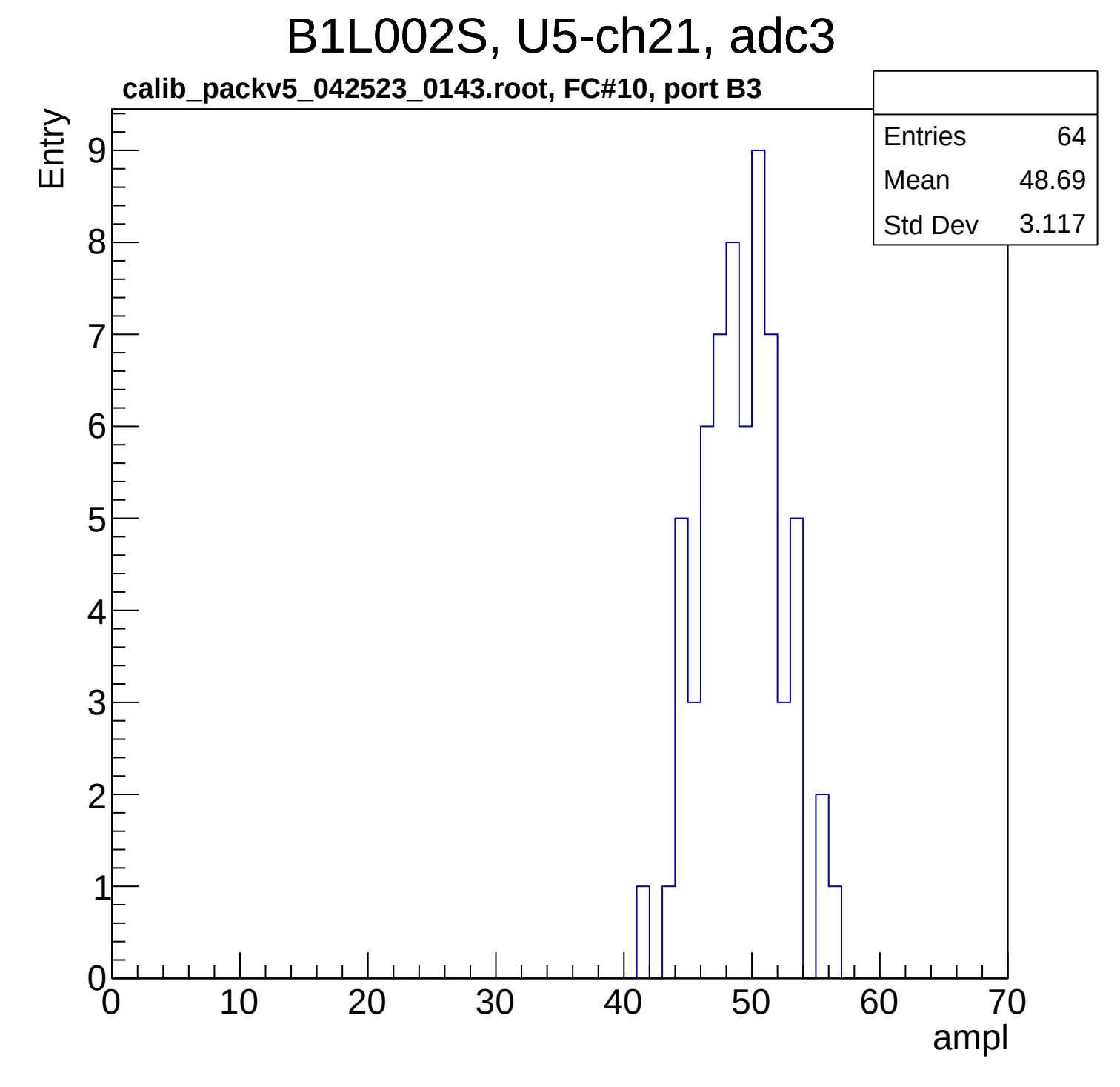
B1L002S, U5-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.69
Std Dev 3.117
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
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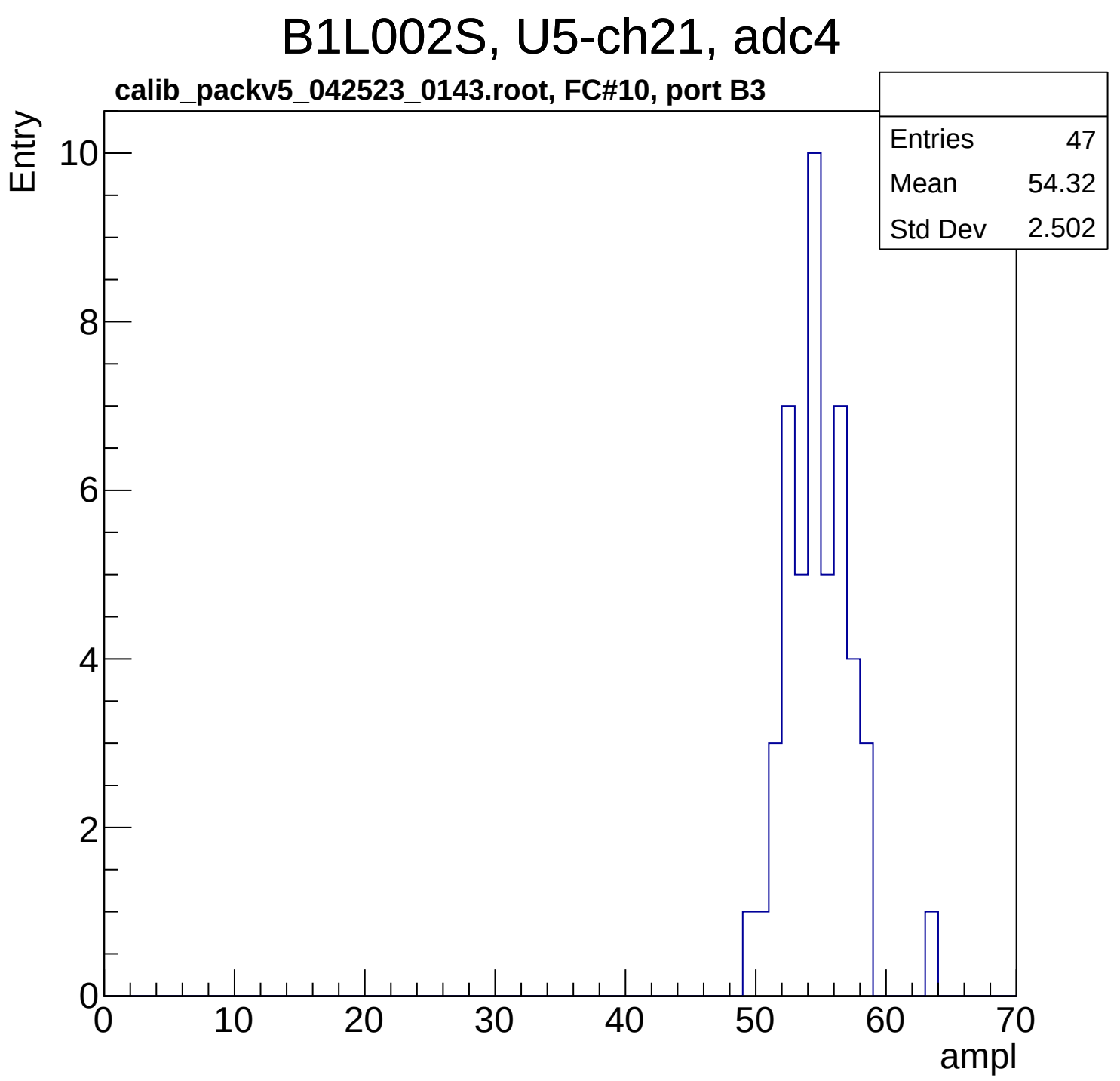
60
70


B1L002S, U5-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 54.32
Std Dev 2.502
Entry
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2
4
6
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10
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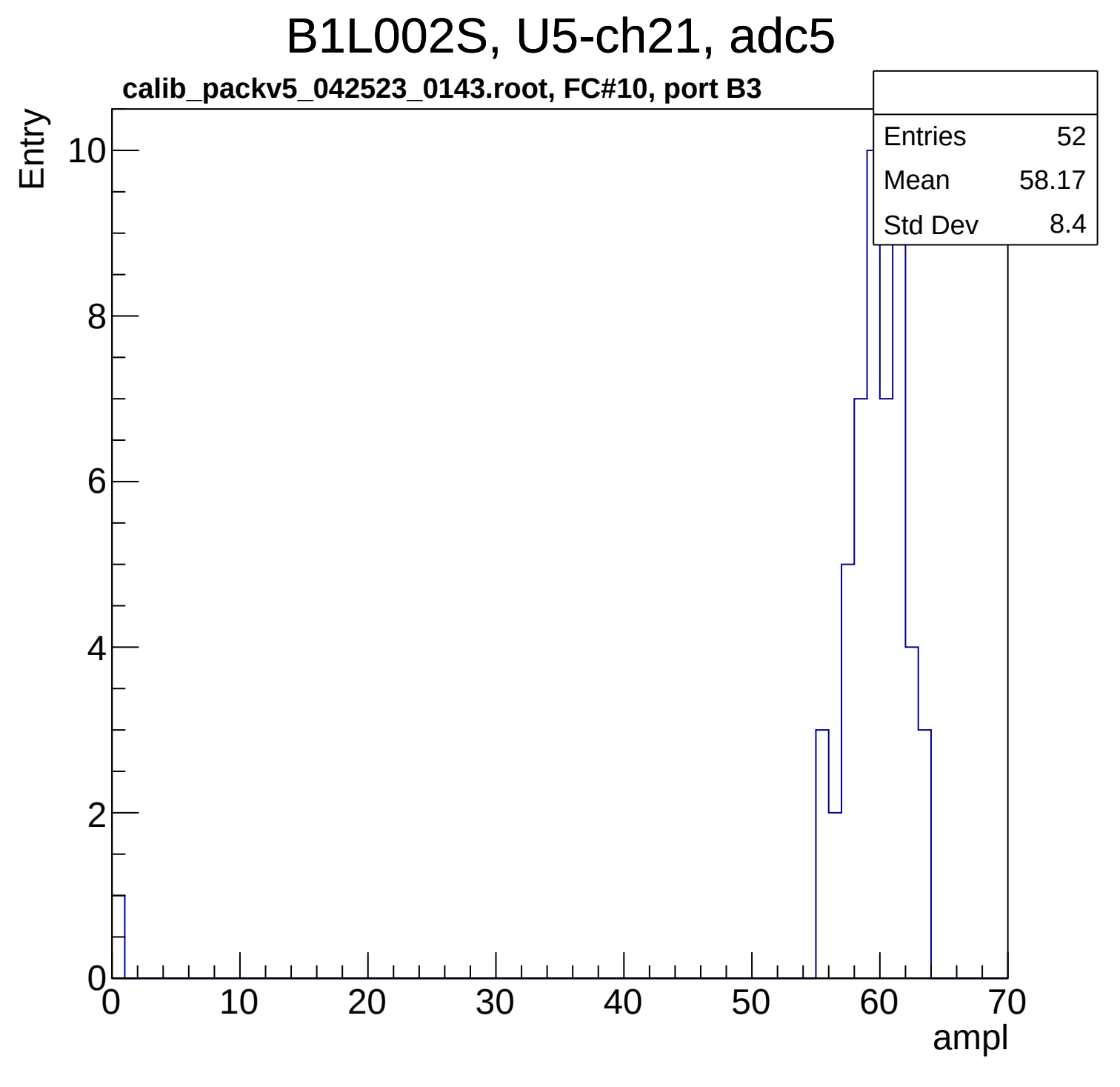
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.17
Std Dev 8.4
Entry
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10
20
30
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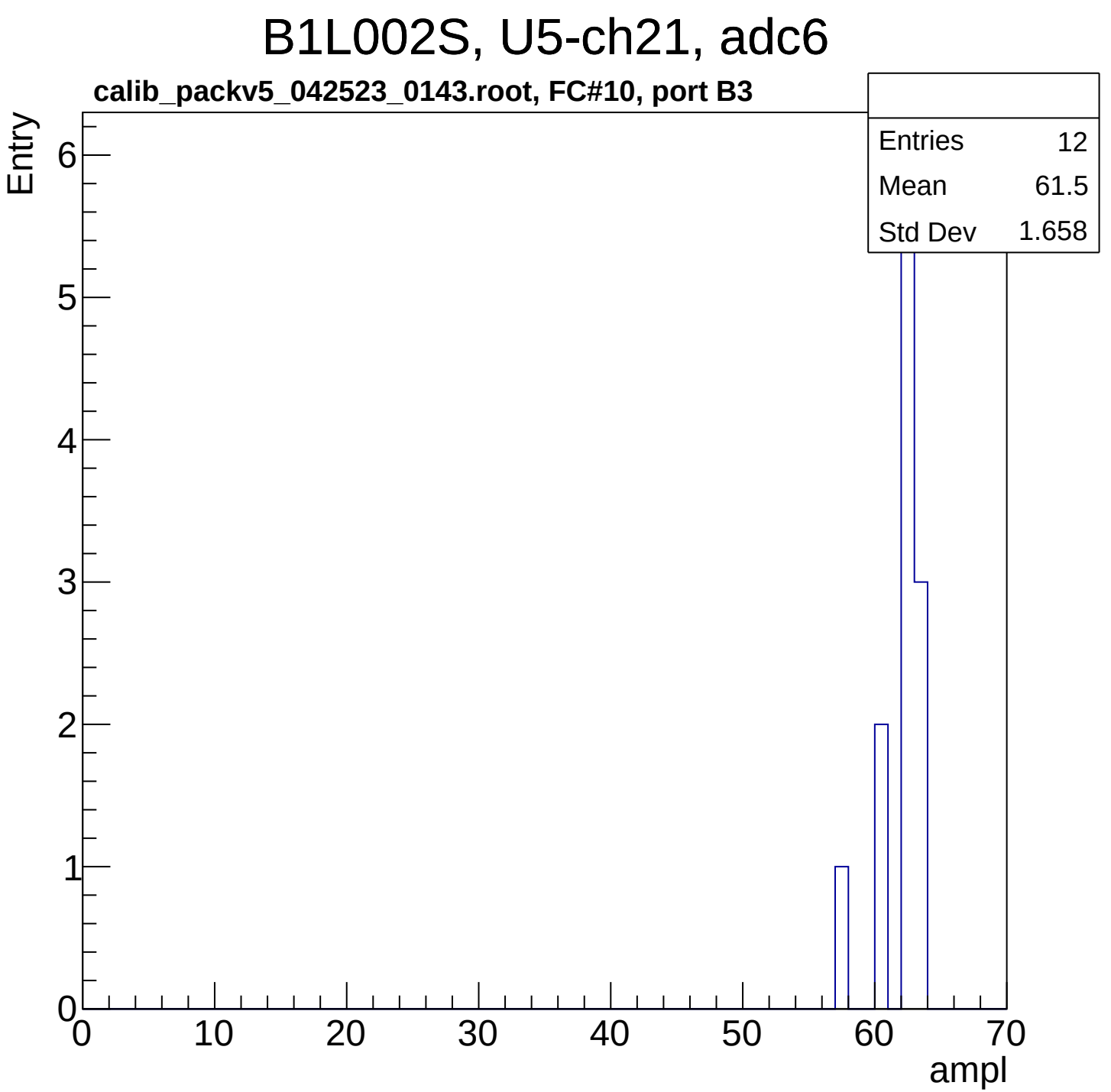
50
60
70
ampl


B1L002S, U5-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.5
Std Dev 1.658
Entry
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2
3
4
5
6
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10
20
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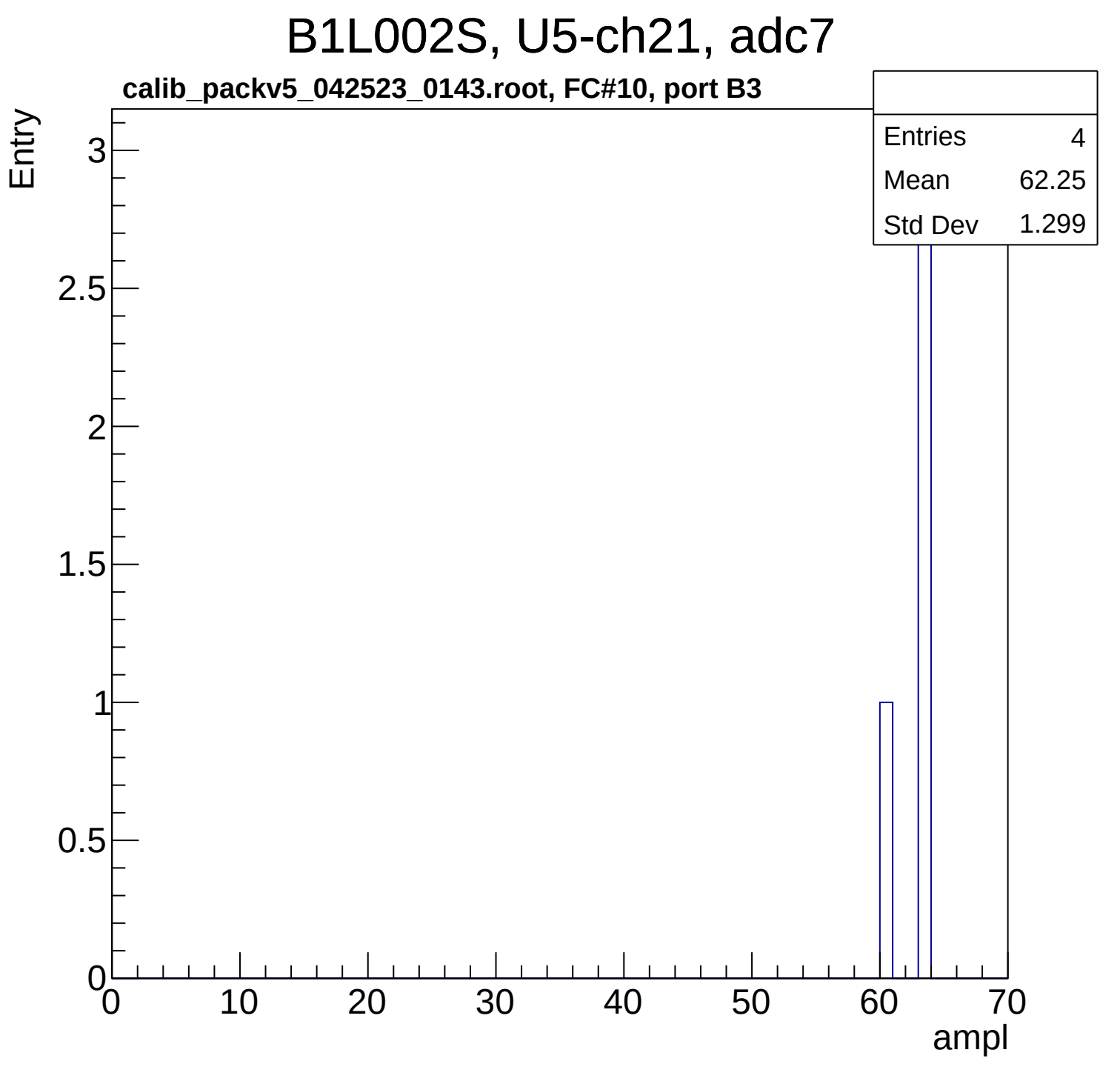
50
60
70
ampl


B1L002S, U5-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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1
1.5
2
2.5
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0
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20
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60
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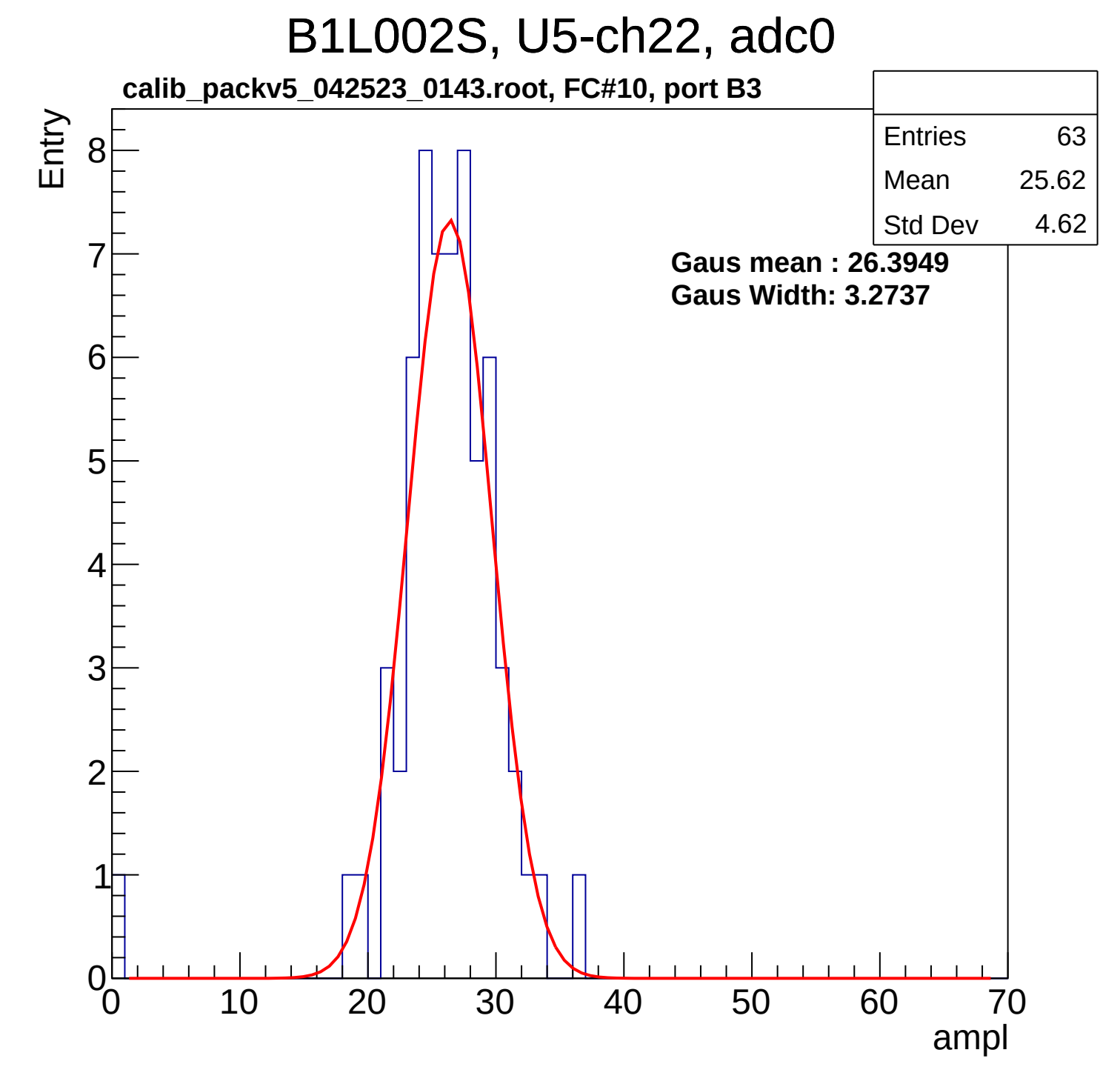
B1L002S, U5-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 25.62
Std Dev 4.62
Gaus mean : 26.3949
Gaus Width: 3.2737
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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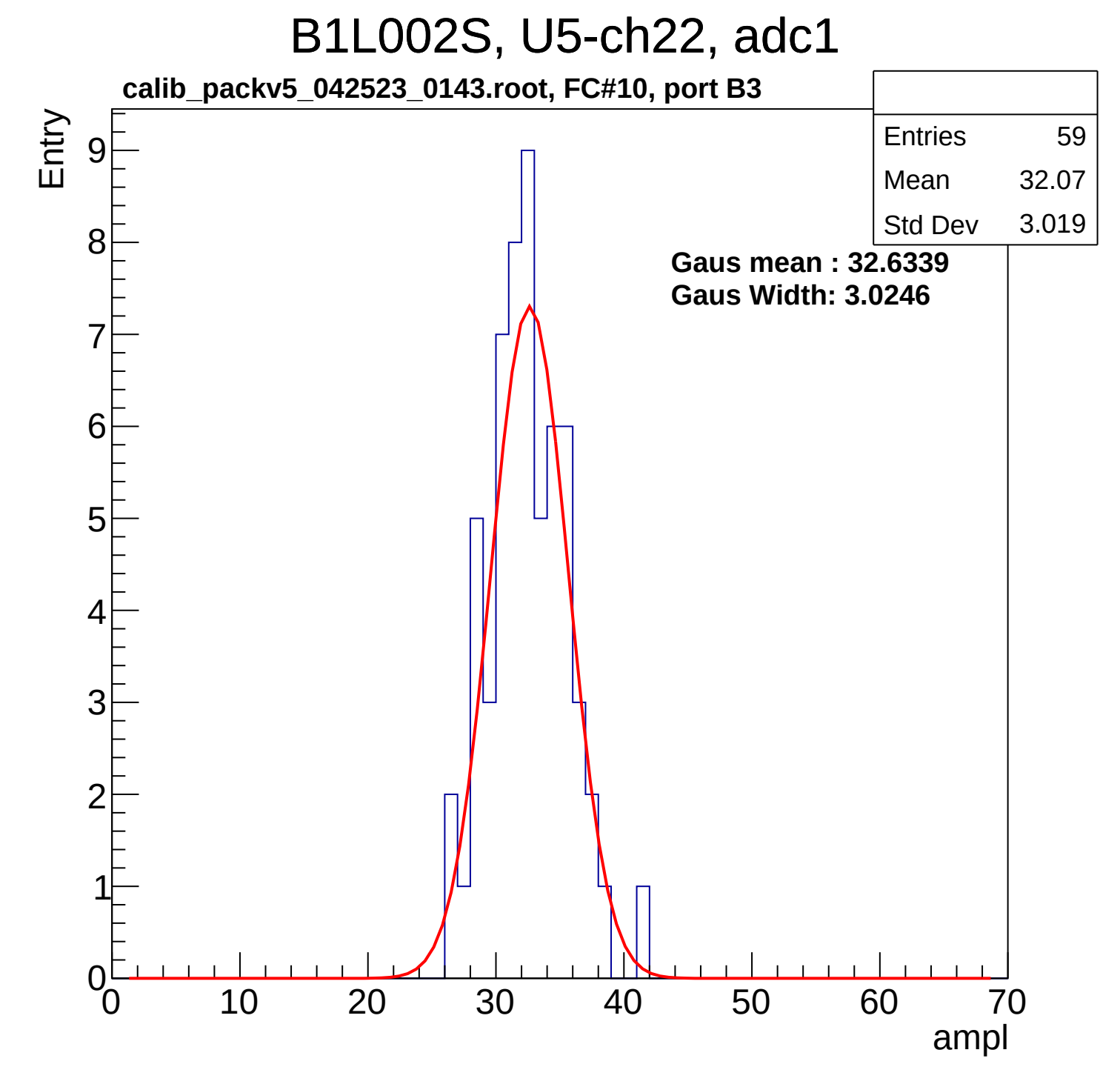
B1L002S, U5-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 32.07
Std Dev 3.019
Gaus mean : 32.6339
Gaus Width: 3.0246
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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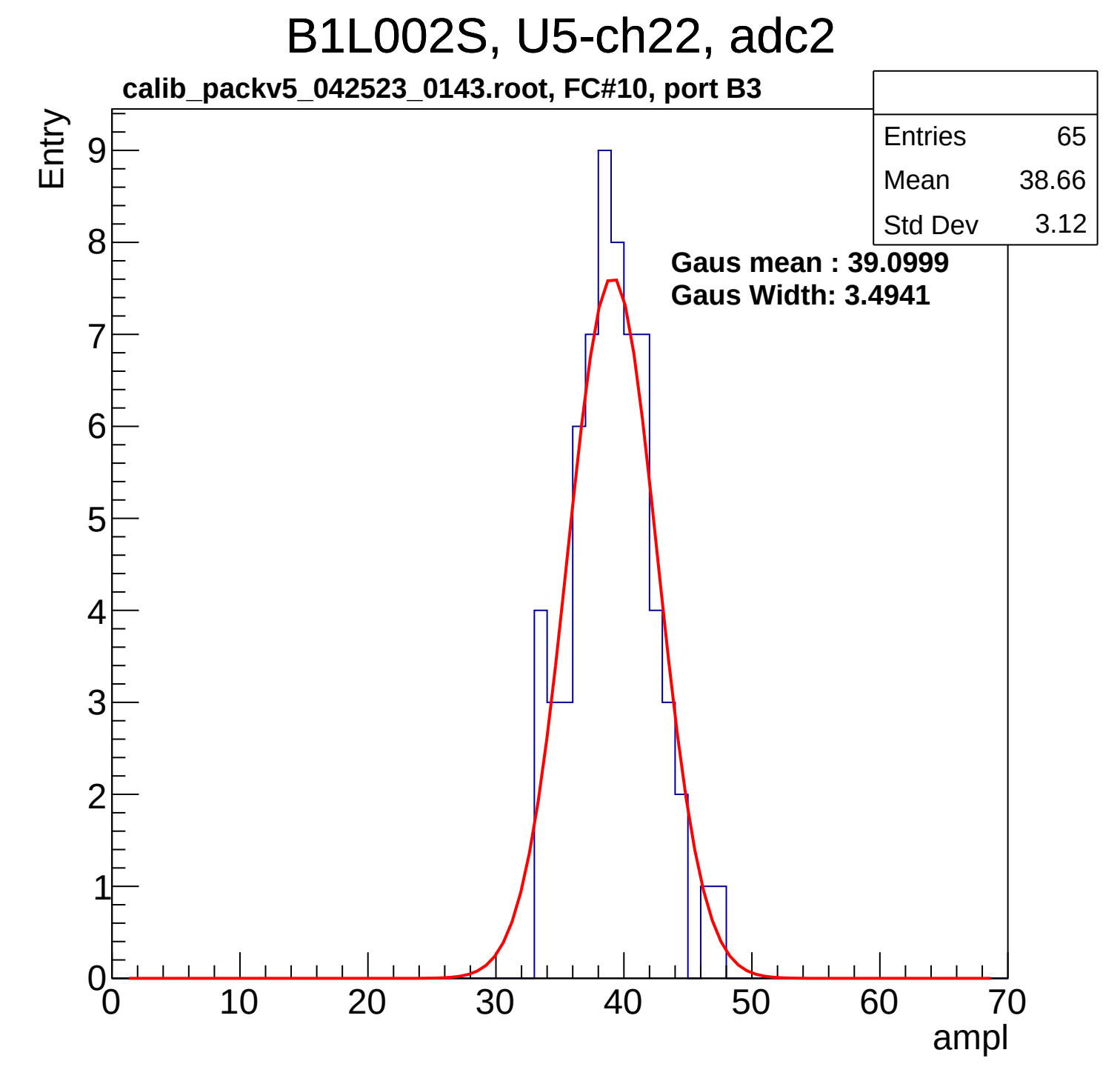
B1L002S, U5-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 38.66
Std Dev 3.12
Gaus mean : 39.0999
Gaus Width: 3.4941
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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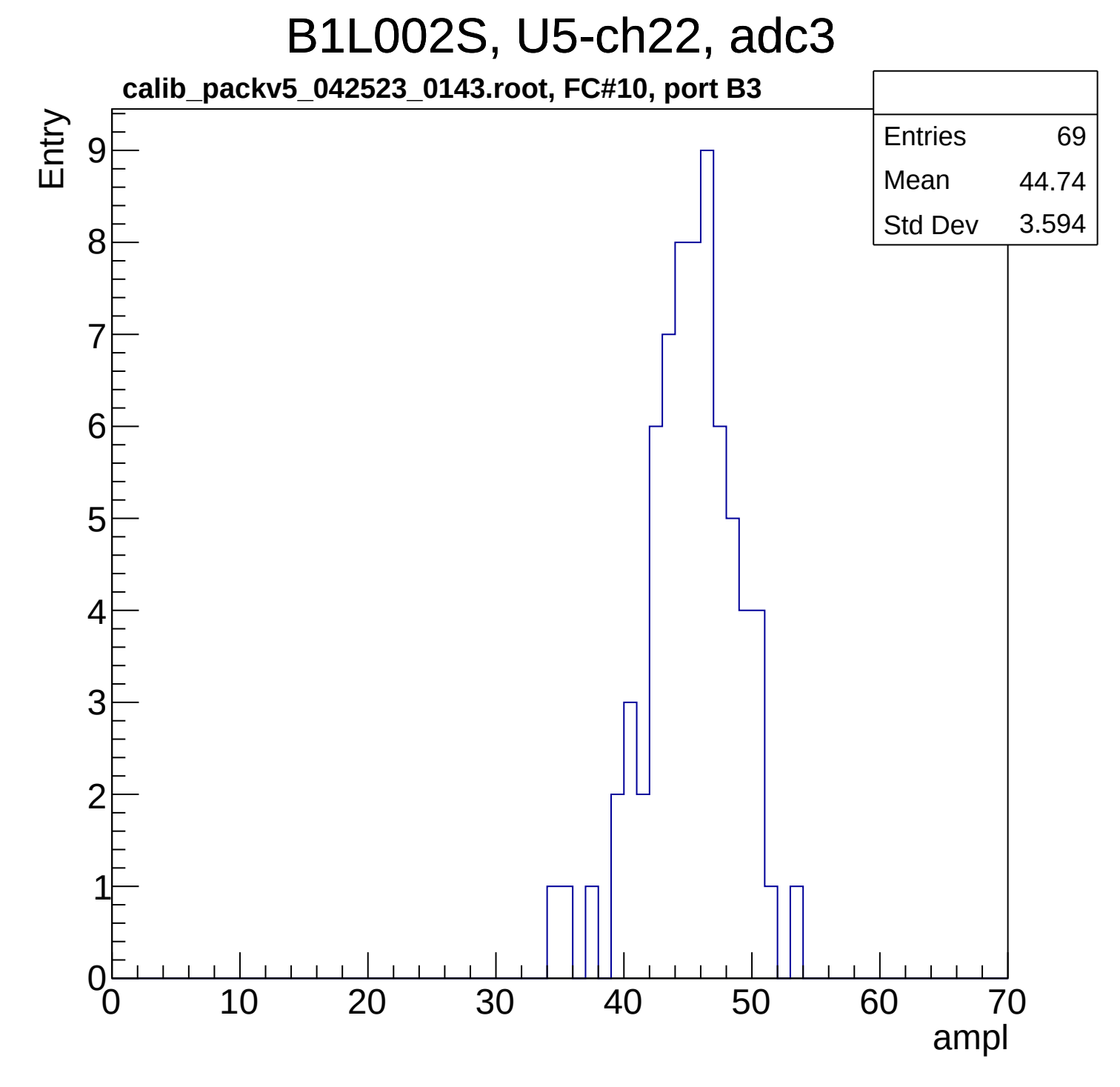
B1L002S, U5-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 44.74
Std Dev 3.594
Entry
ampl
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6
7
8
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0
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60
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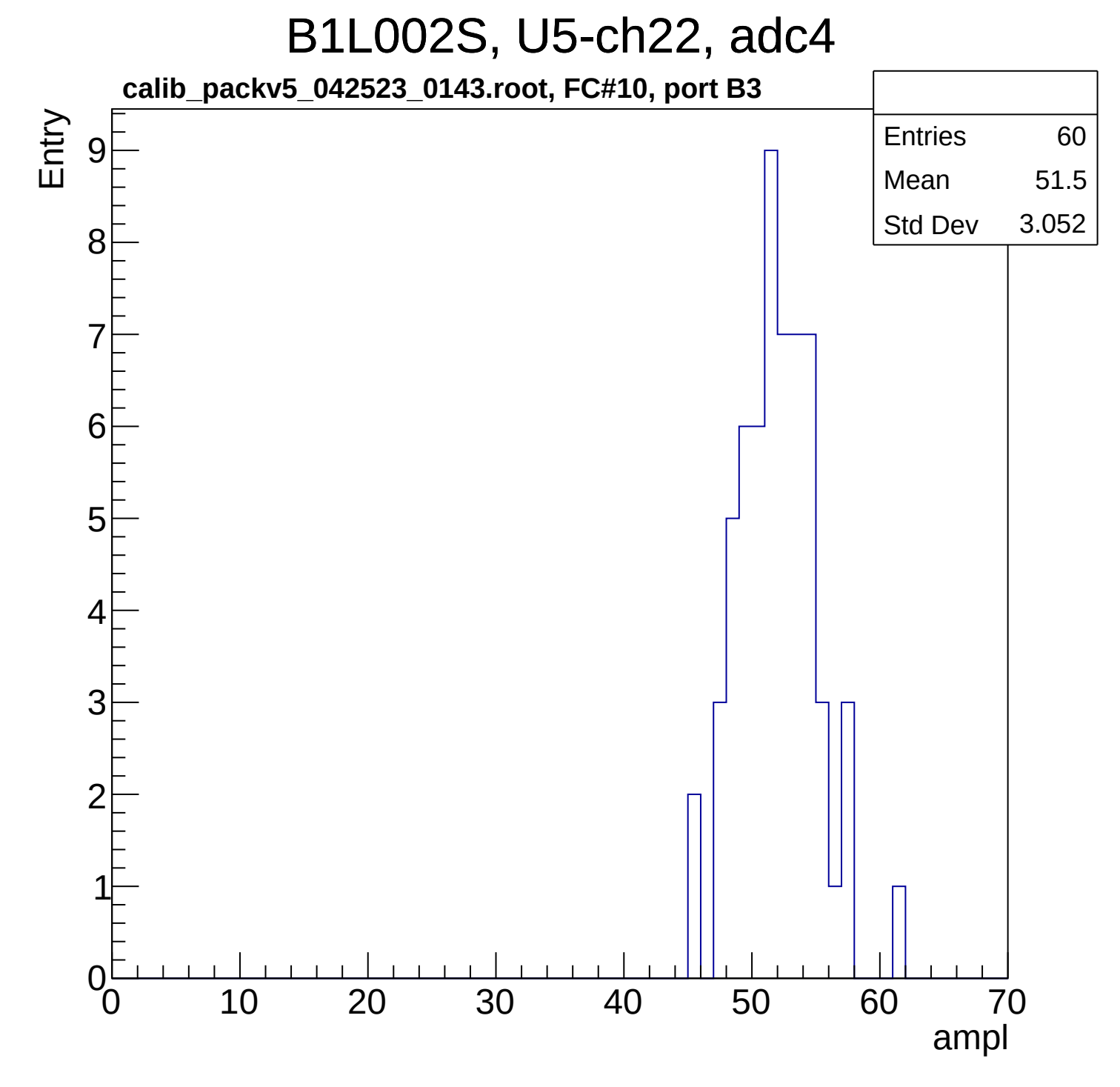
B1L002S, U5-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.5
Std Dev 3.052
Entry
ampl
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3
4
5
6
7
8
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0
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60
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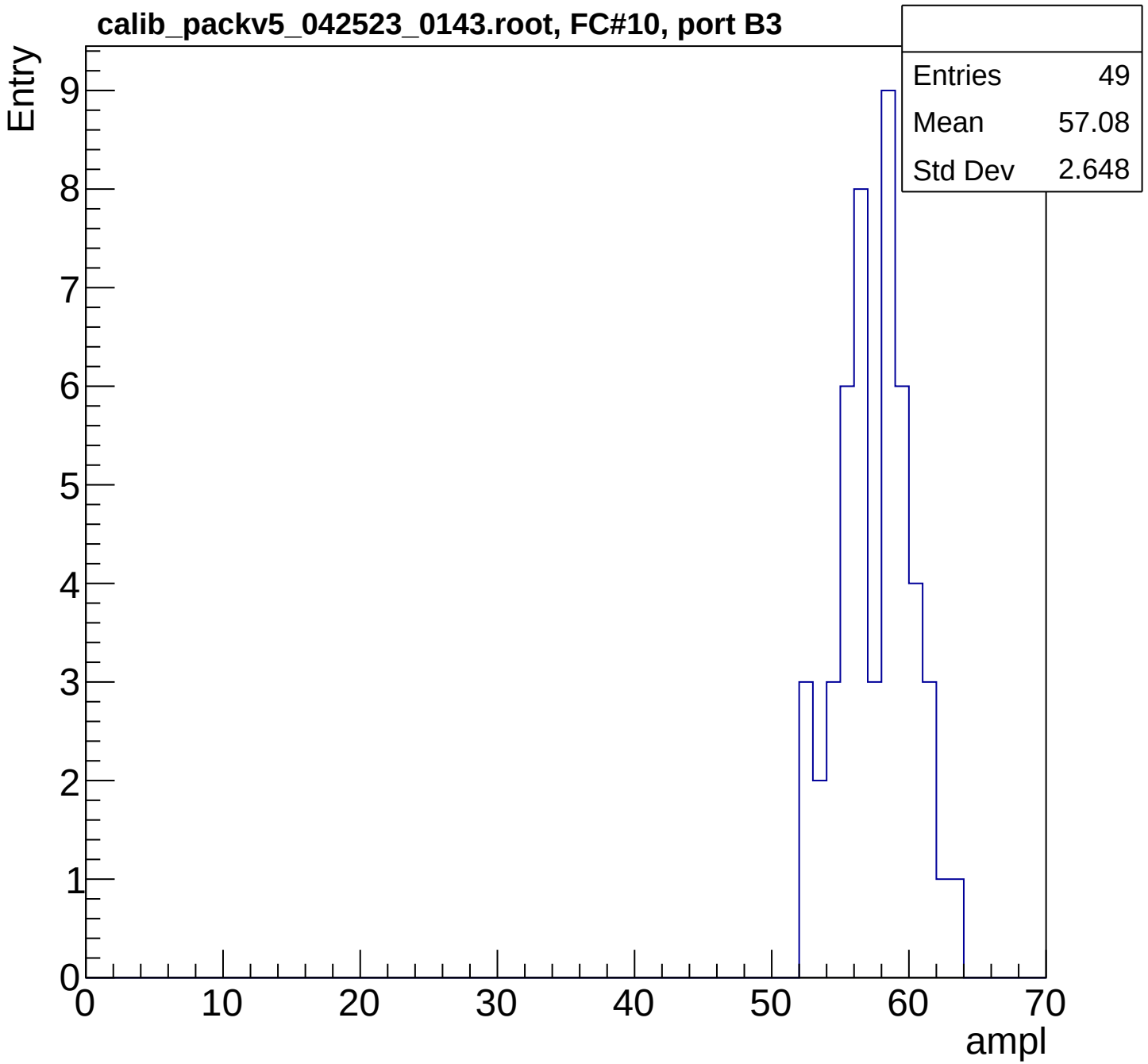
B1L002S, U5-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.08
Std Dev 2.648
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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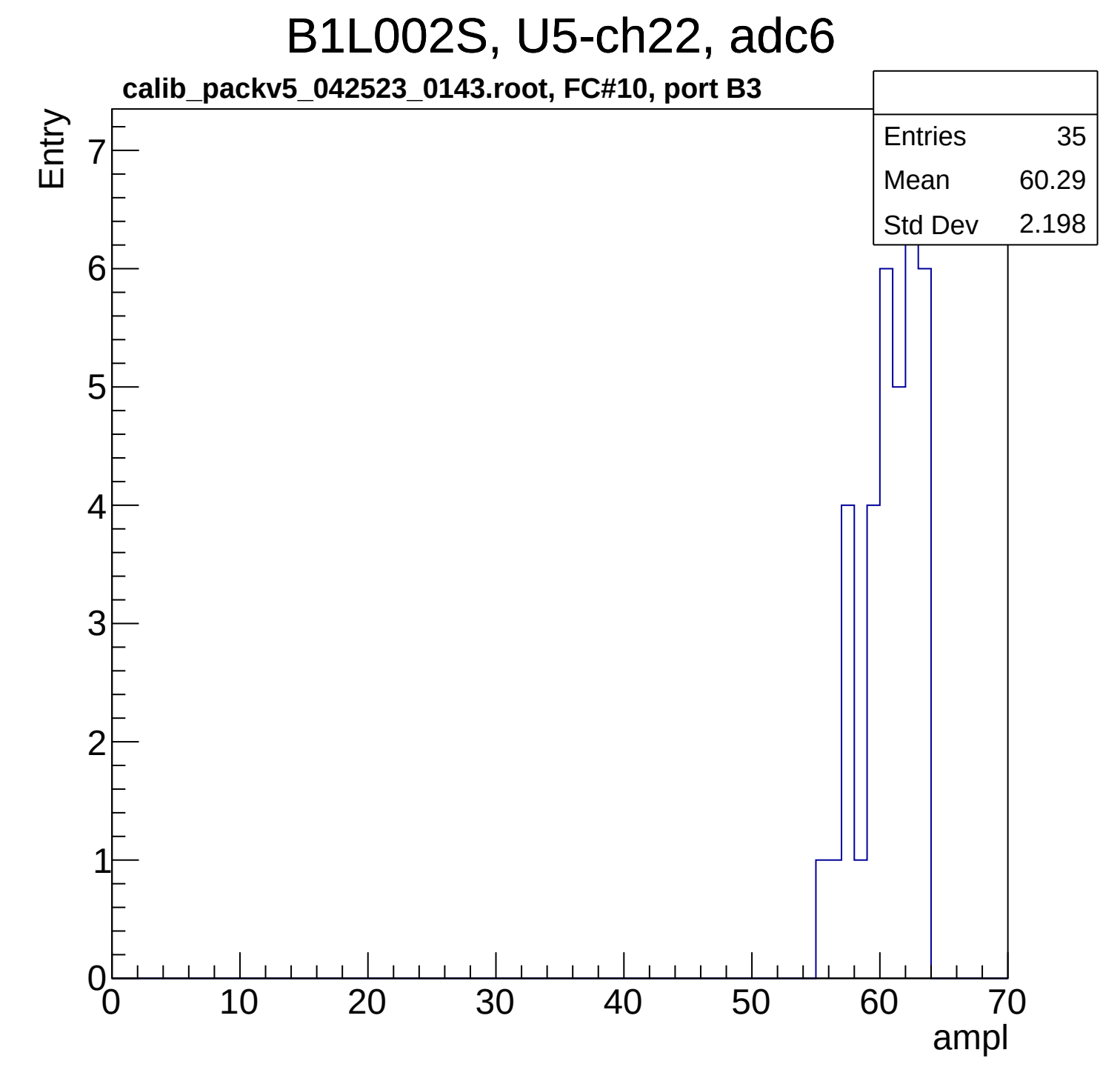
B1L002S, U5-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.29
Std Dev 2.198
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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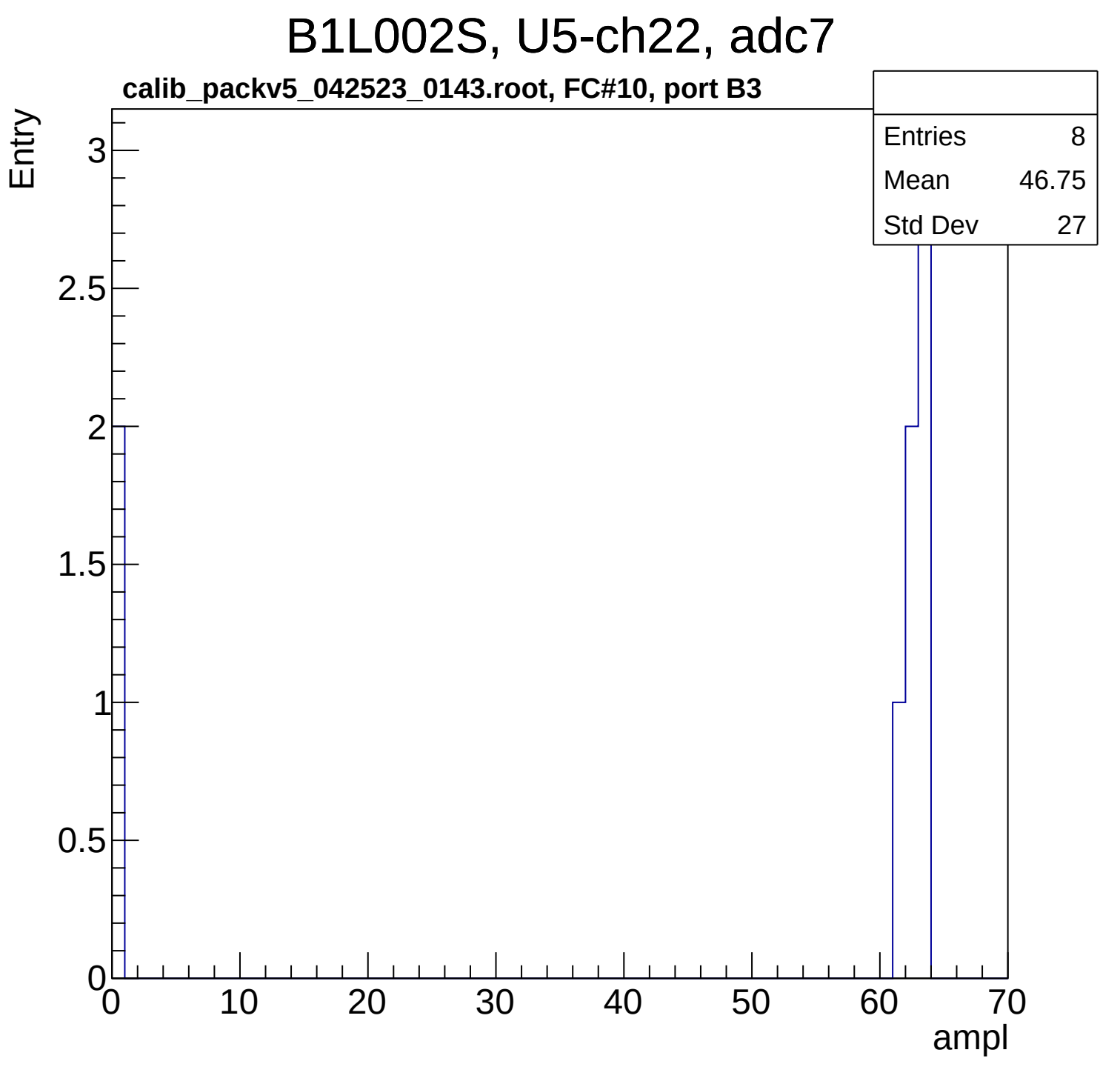
B1L002S, U5-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 46.75
Std Dev 27
Entry
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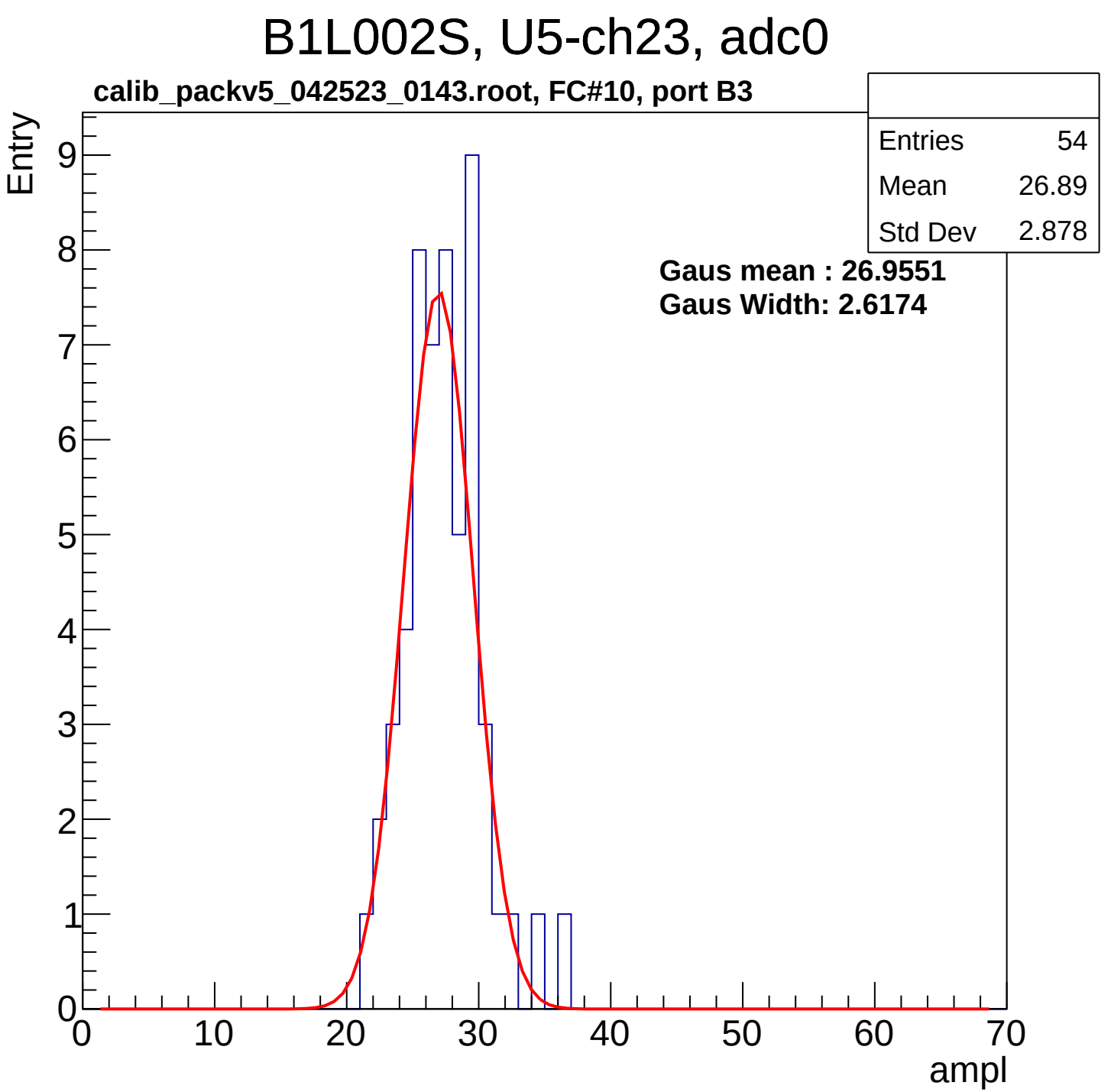
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 26.89
Std Dev 2.878
Gaus mean : 26.9551
Gaus Width: 2.6174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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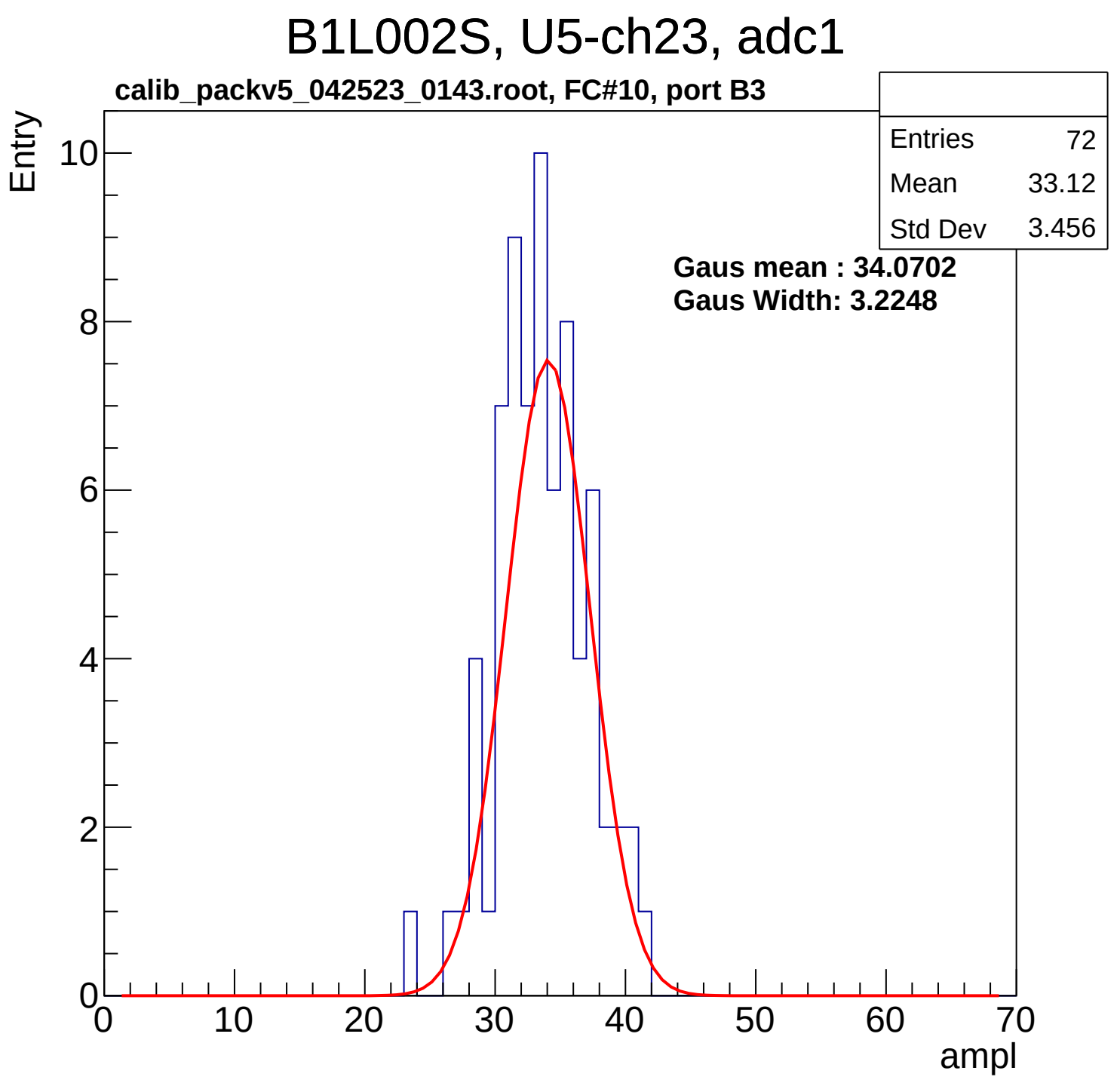
B1L002S, U5-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.12
Std Dev 3.456
Gaus mean : 34.0702
Gaus Width: 3.2248
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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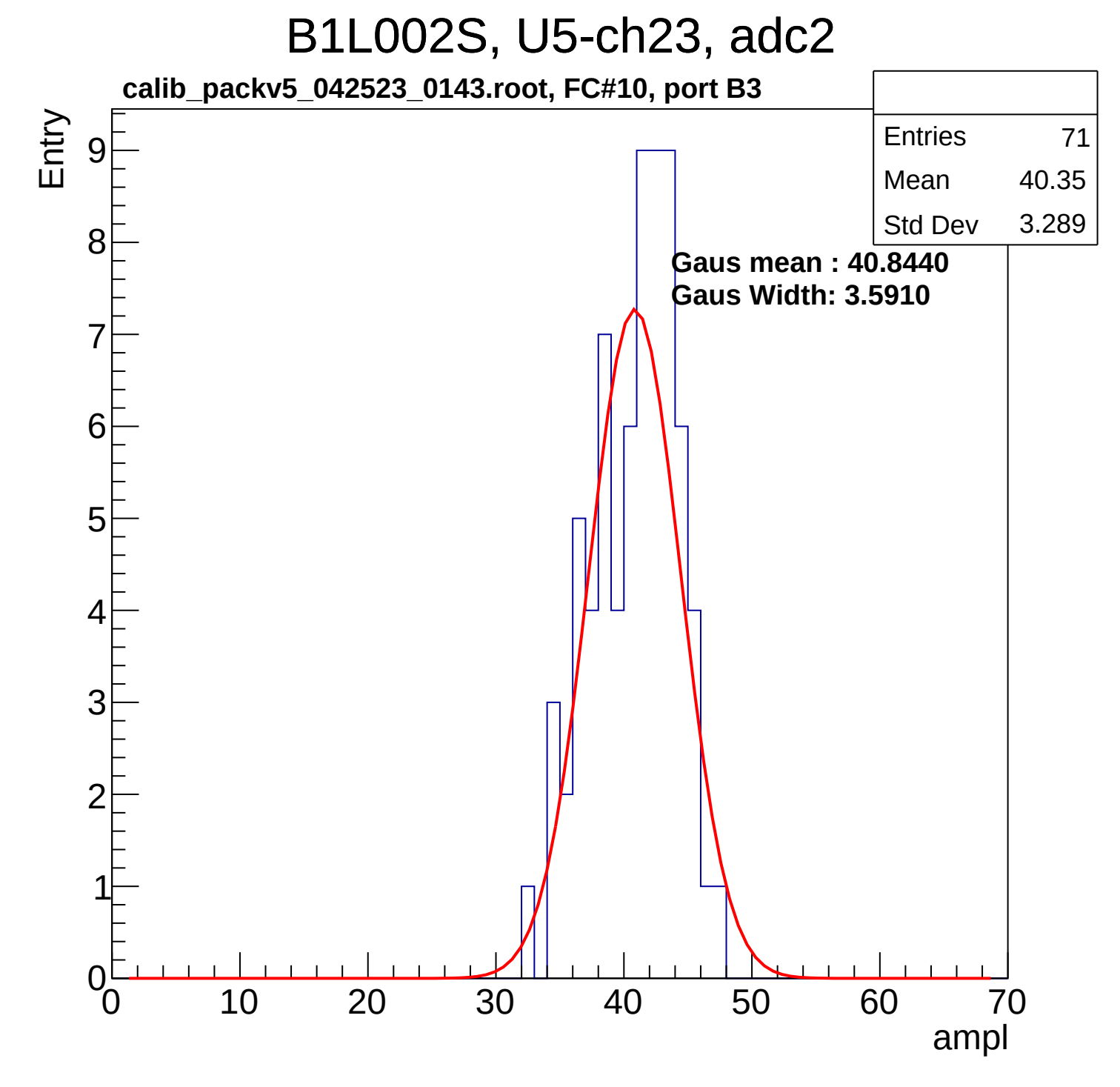
B1L002S, U5-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.35
Std Dev 3.289
Gaus mean : 40.8440
Gaus Width: 3.5910
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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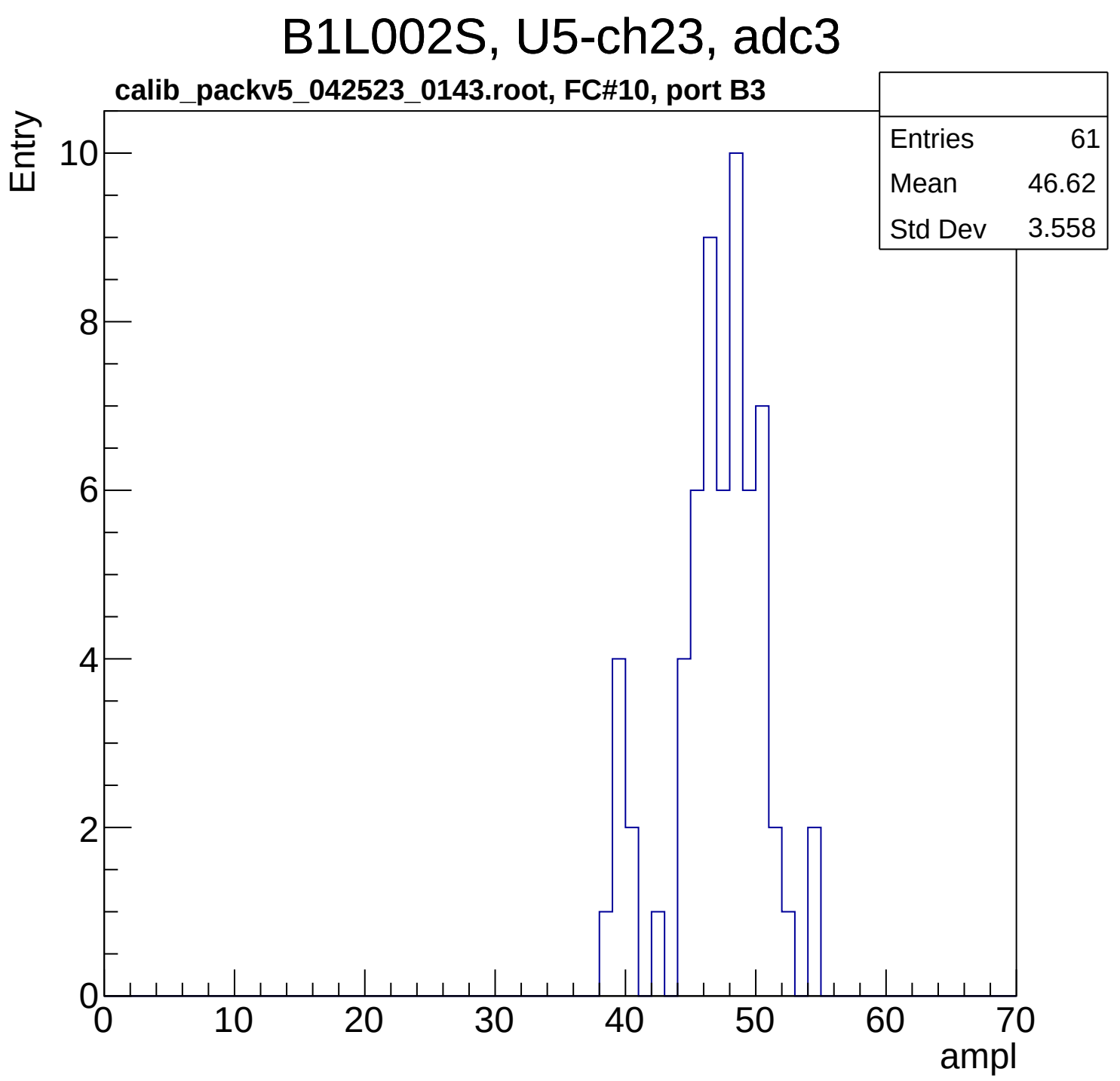
B1L002S, U5-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.62
Std Dev 3.558
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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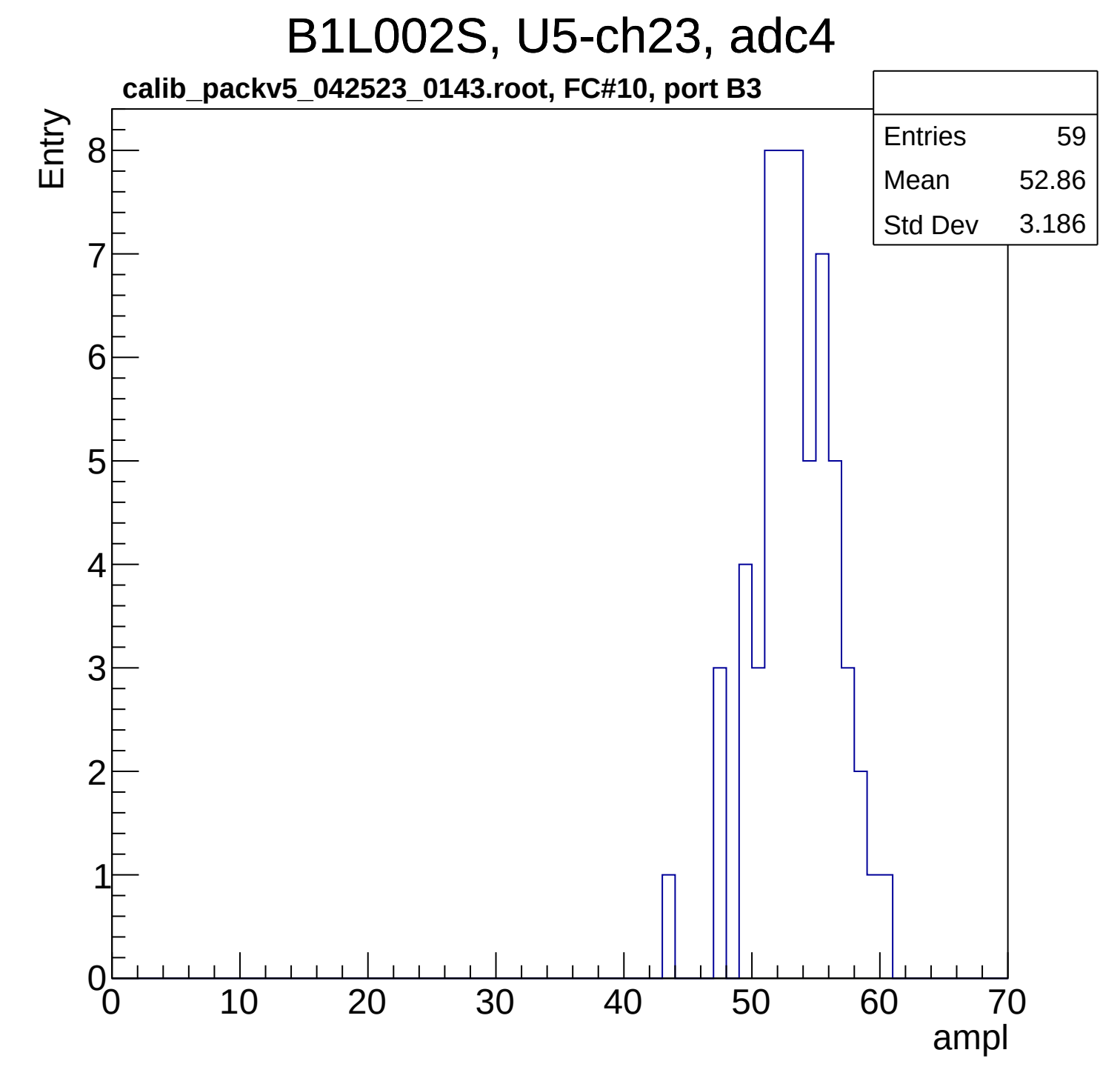
B1L002S, U5-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.86
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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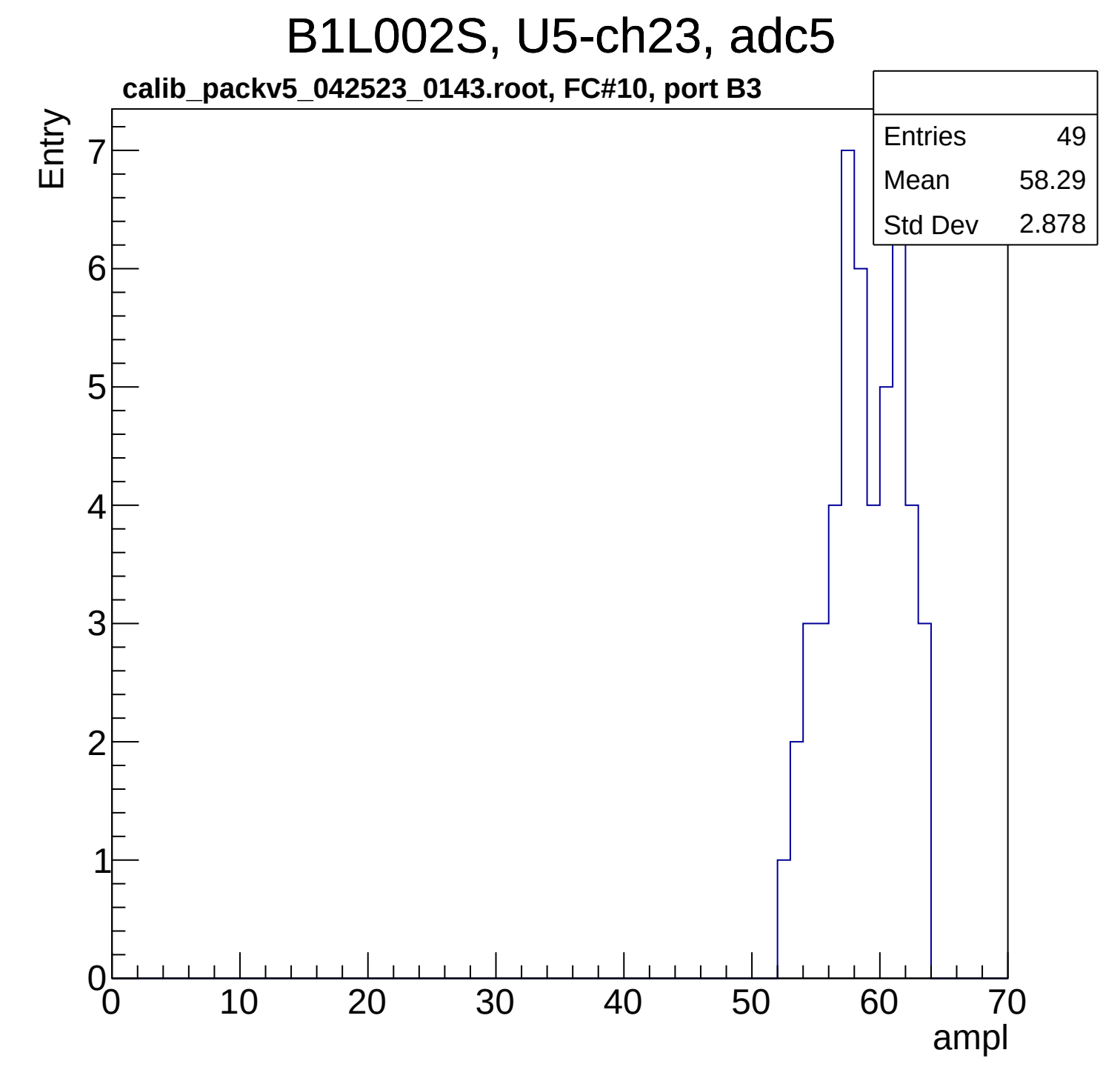
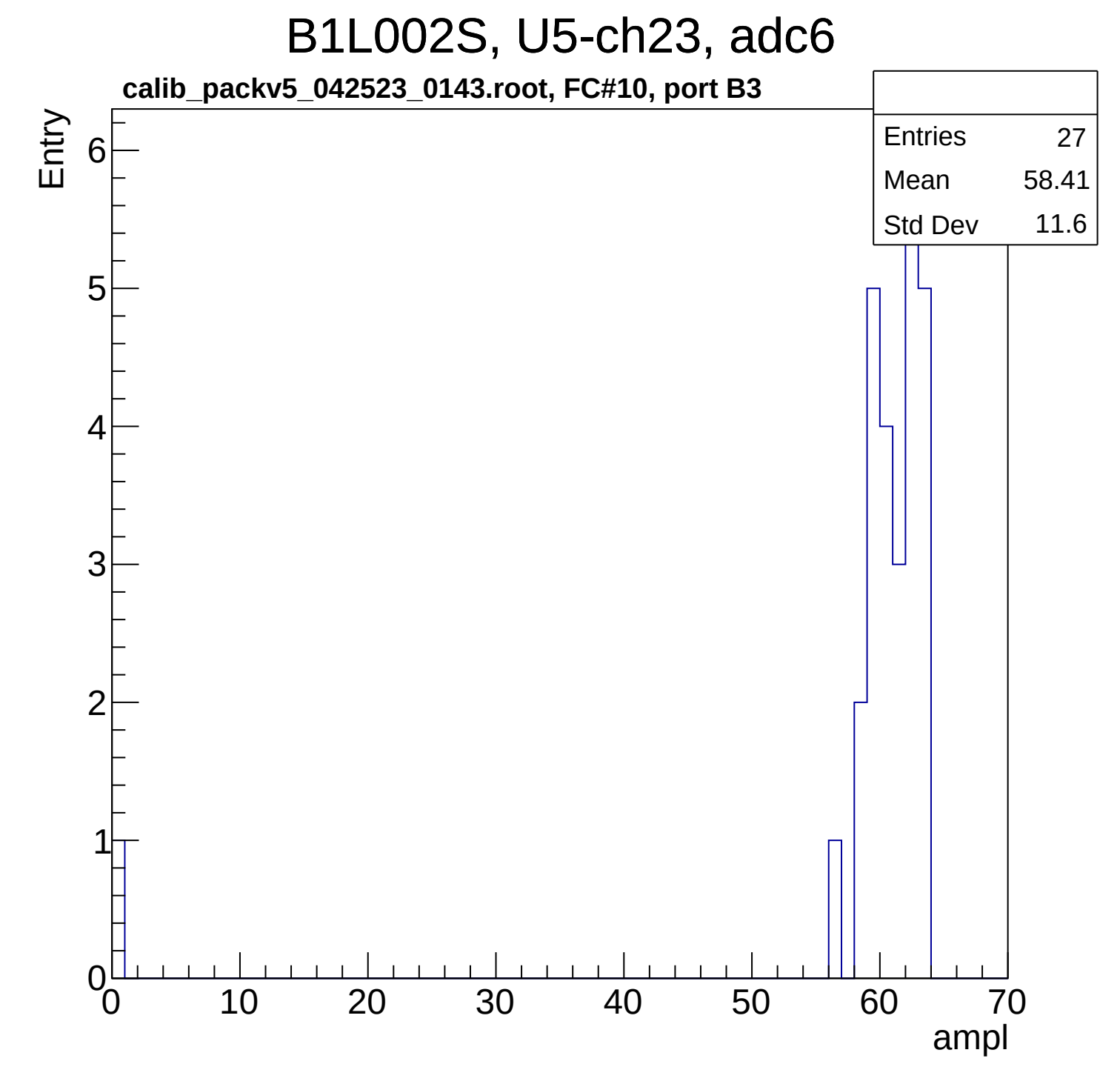
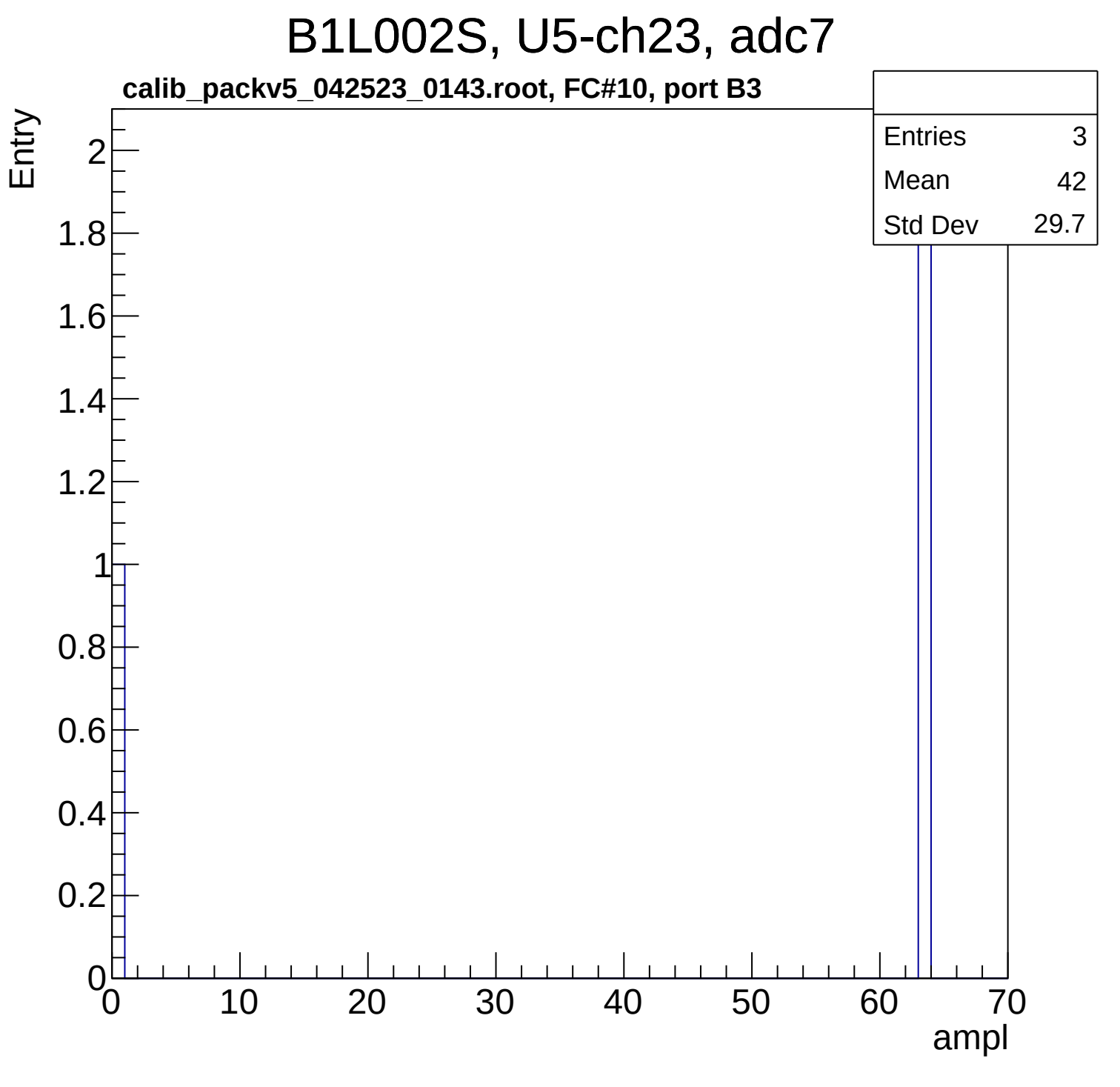

B1L002S, U5-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.29
Std Dev 2.878
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.41
Std Dev 11.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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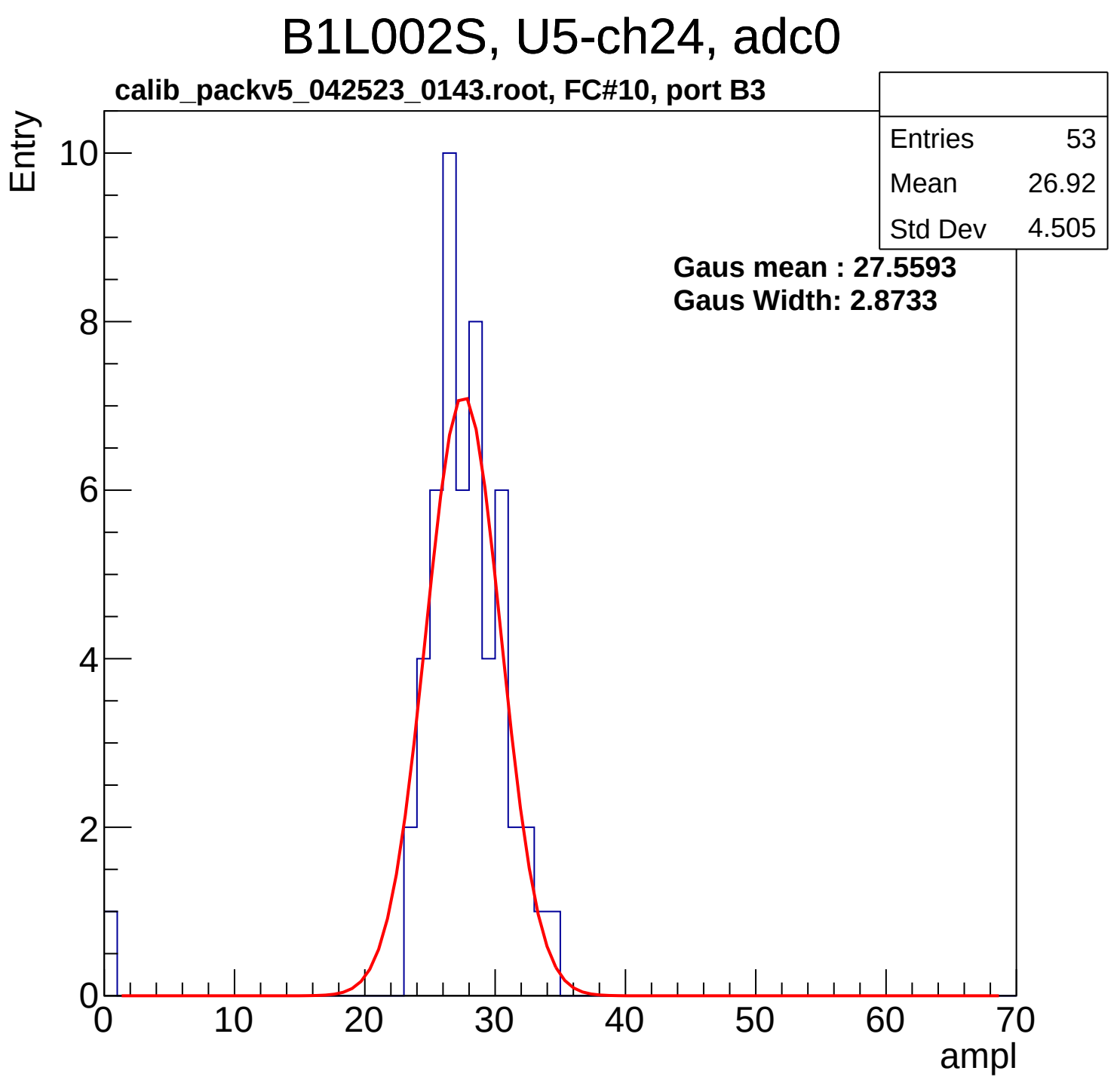
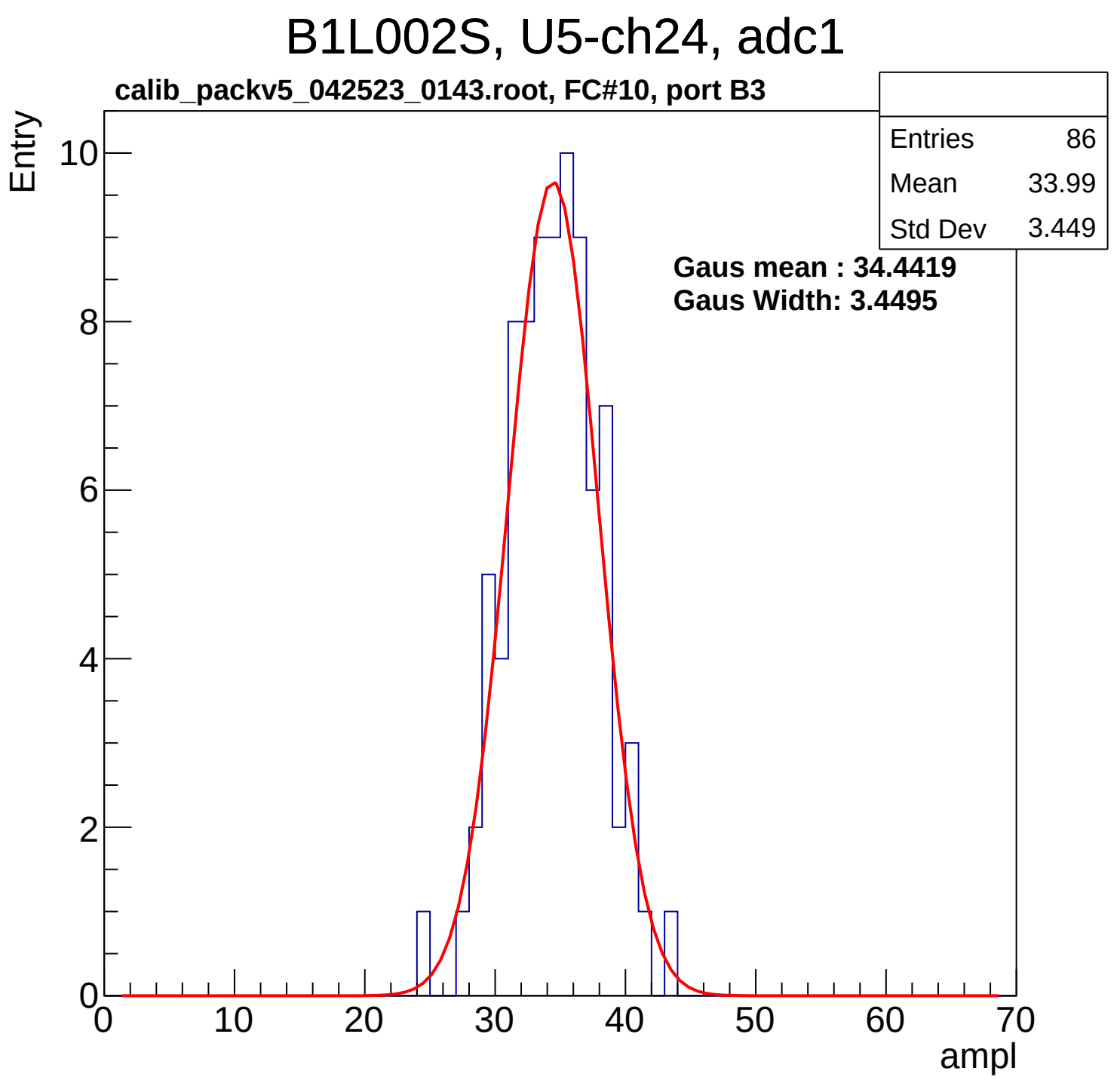
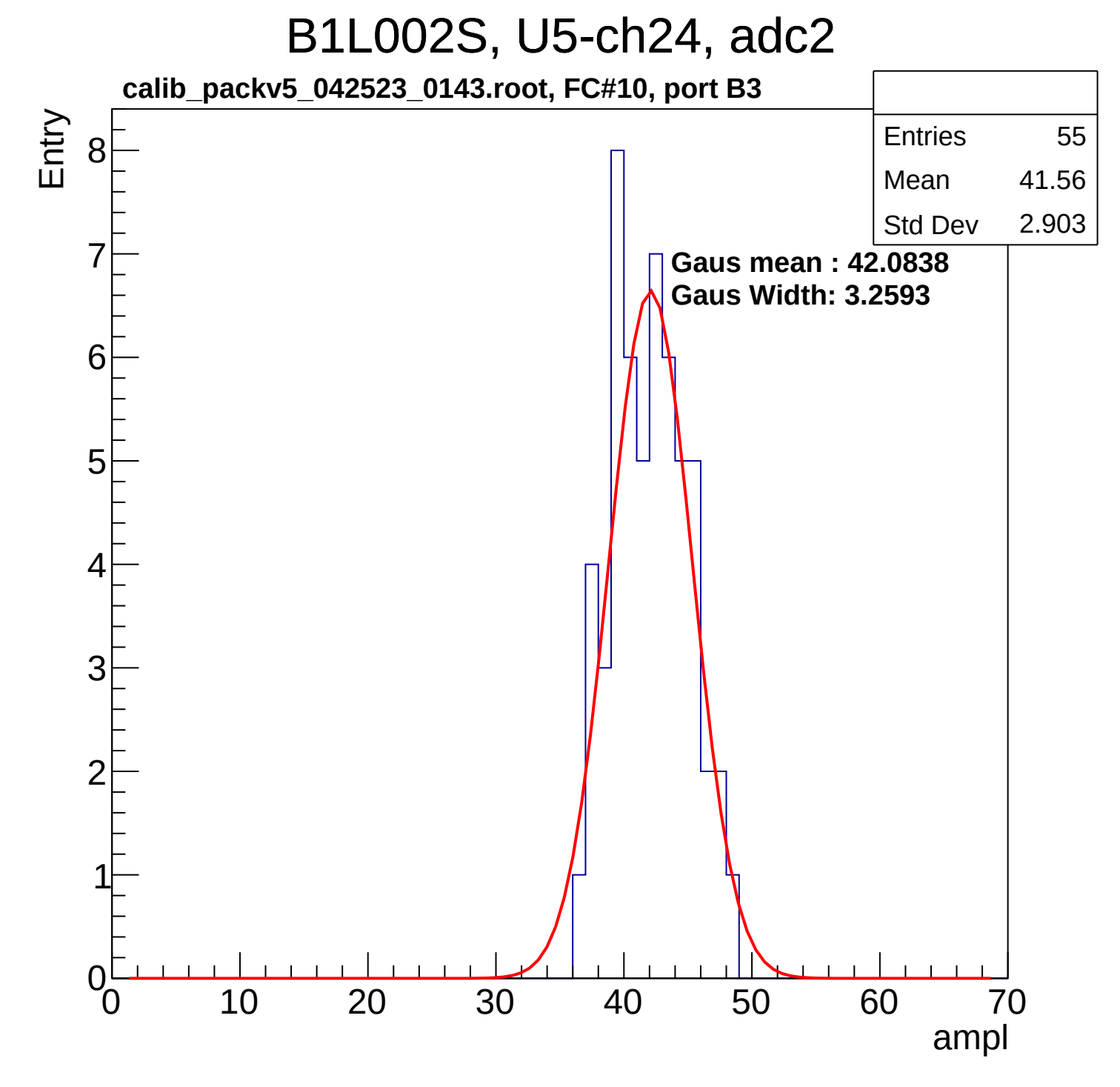
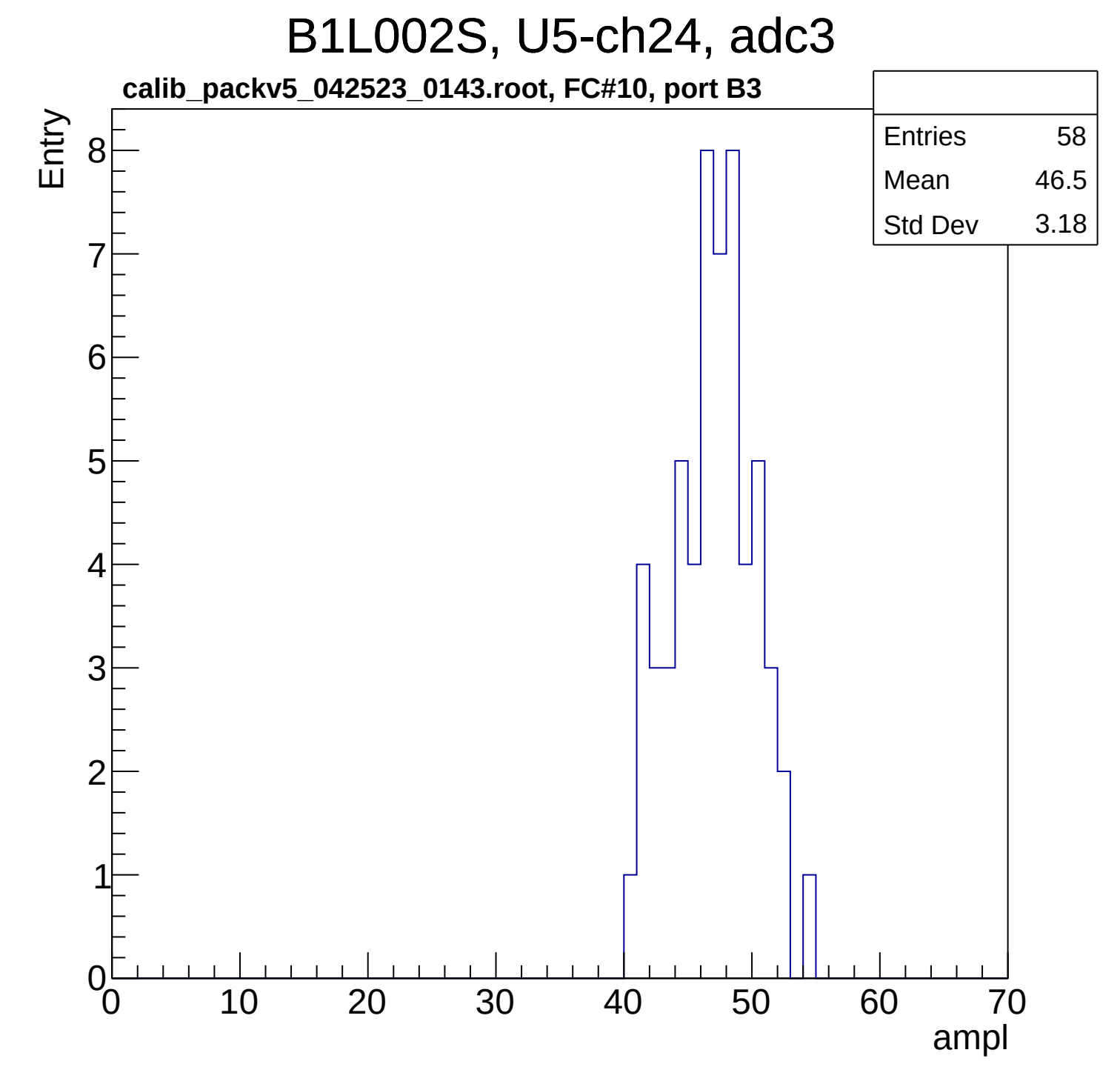
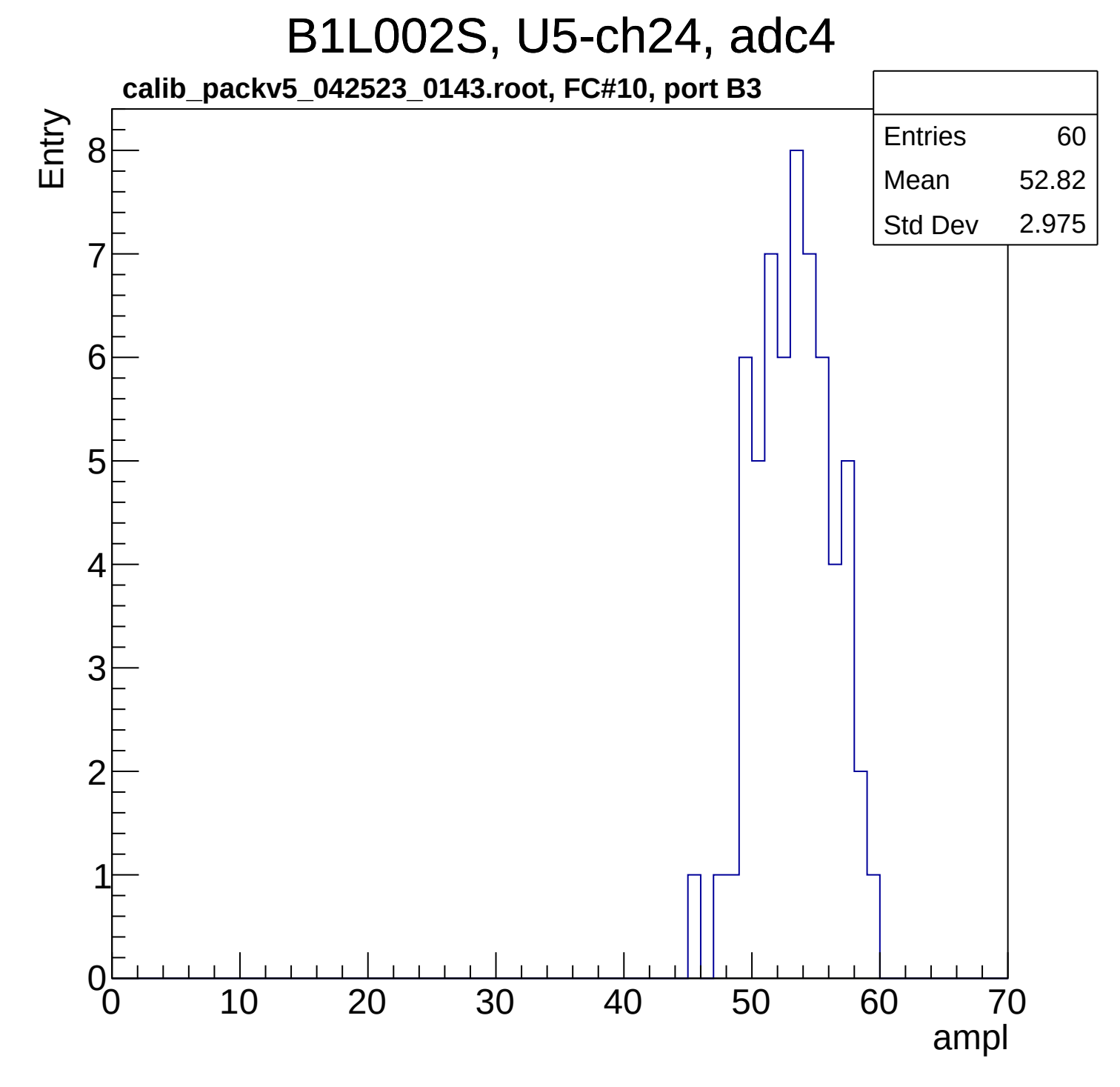

B1L002S, U5-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 26.92
Std Dev 4.505
Gaus mean : 27.5593
Gaus Width: 2.8733
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 33.99
Std Dev 3.449
Gaus mean : 34.4419
Gaus Width: 3.4495
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.56
Std Dev 2.903
Gaus mean : 42.0838
Gaus Width: 3.2593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.5
Std Dev 3.18
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.82
Std Dev 2.975
Entry
ampl
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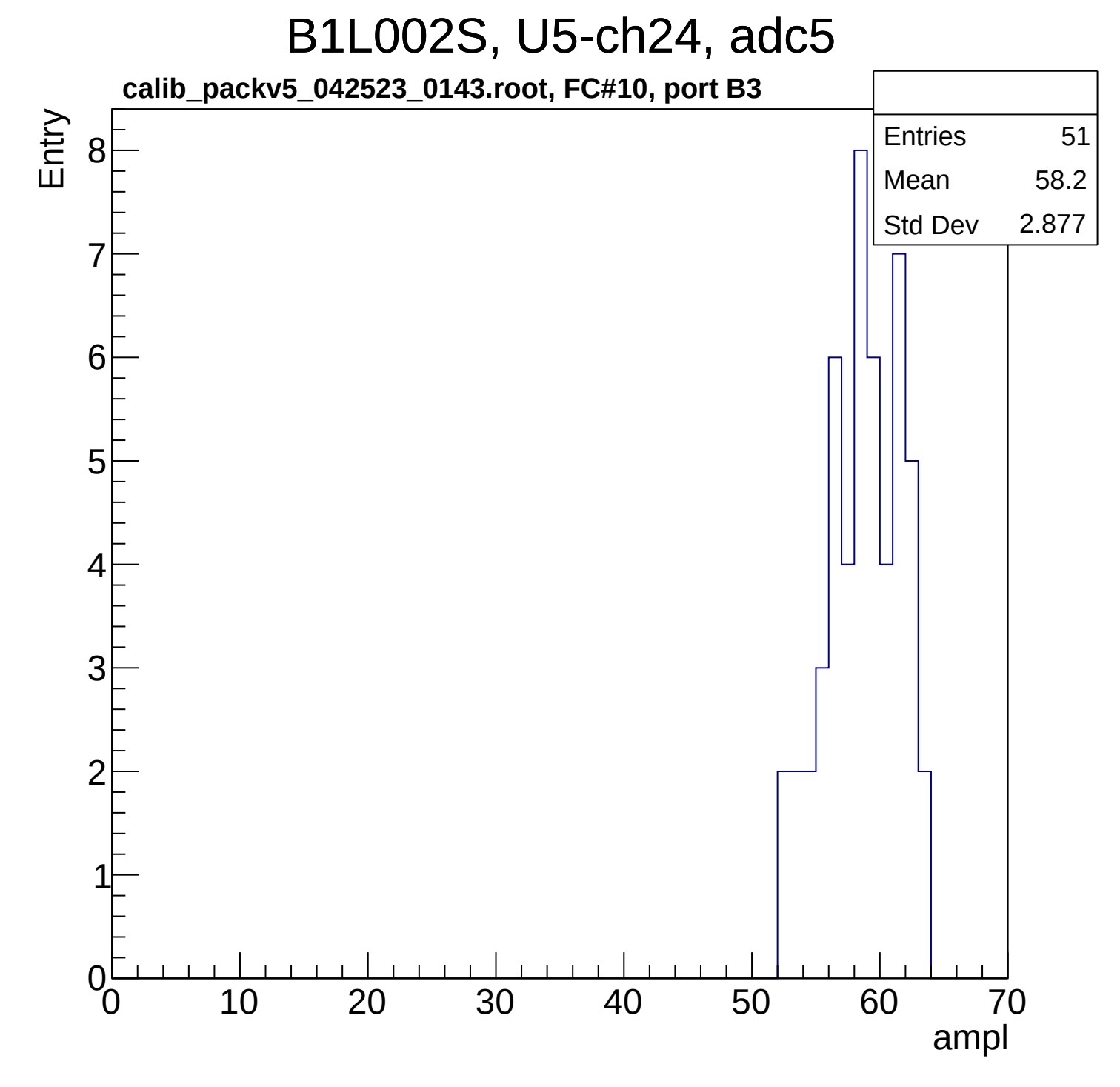
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.2
Std Dev 2.877
Entry
ampl
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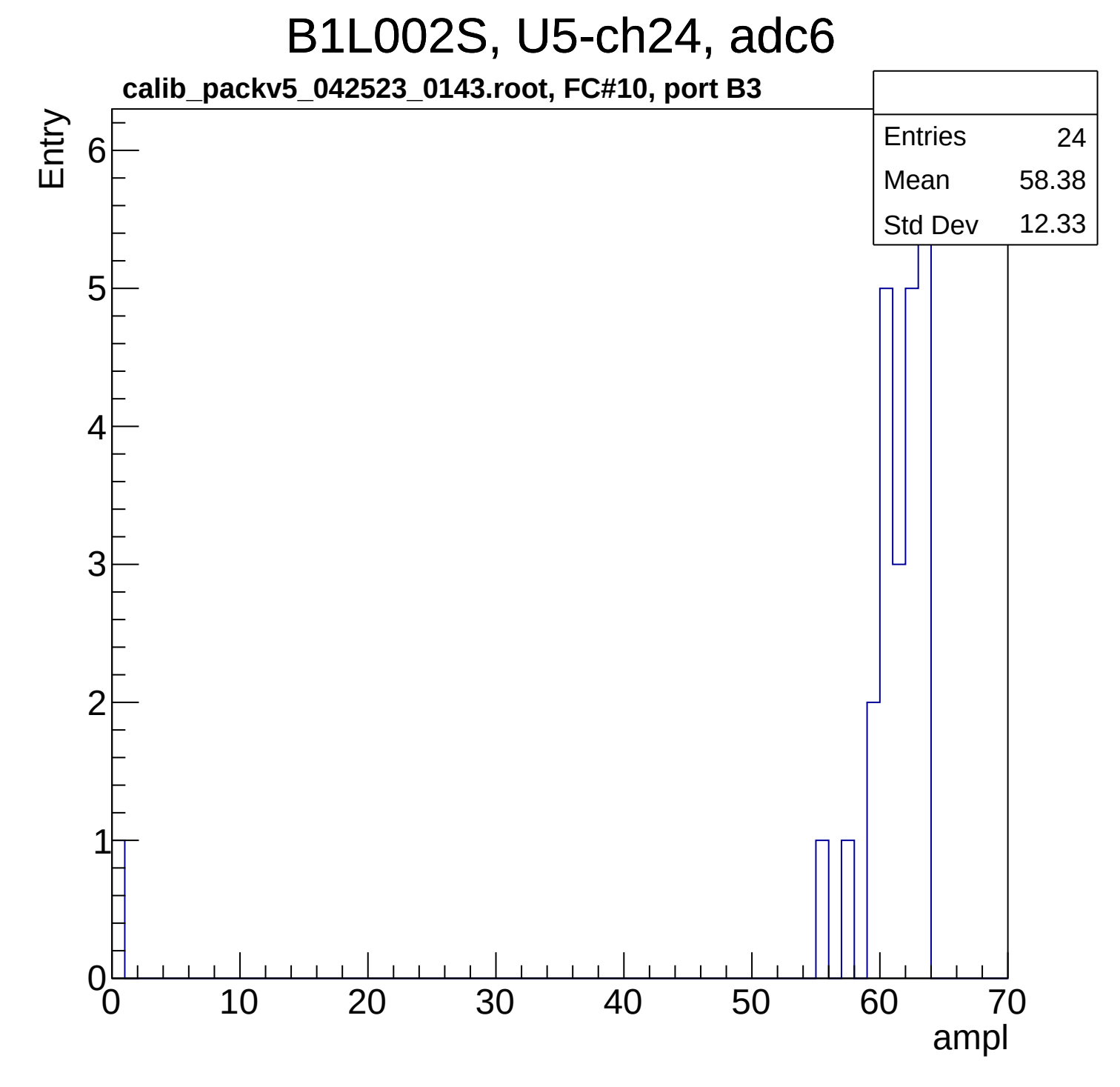
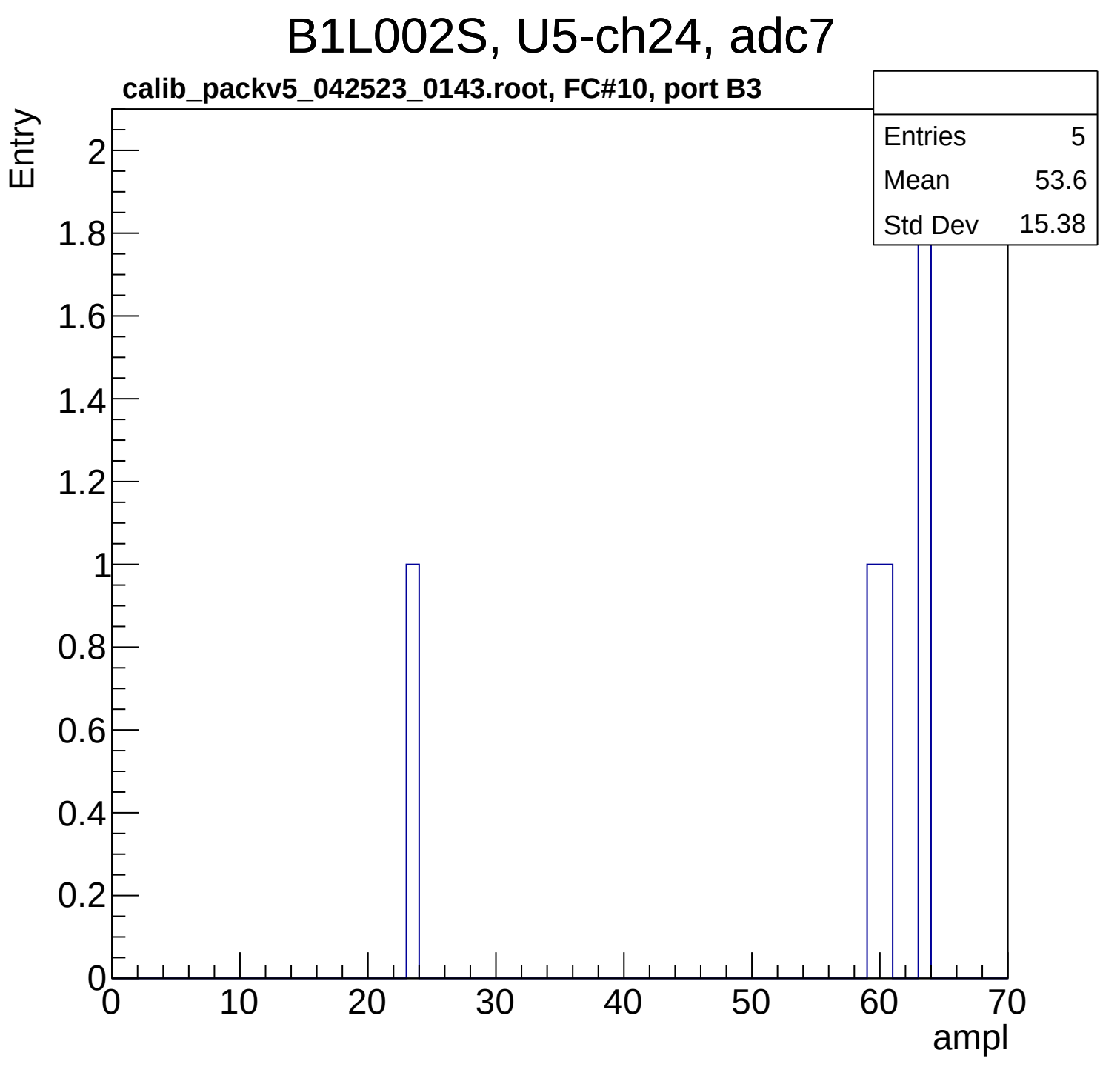
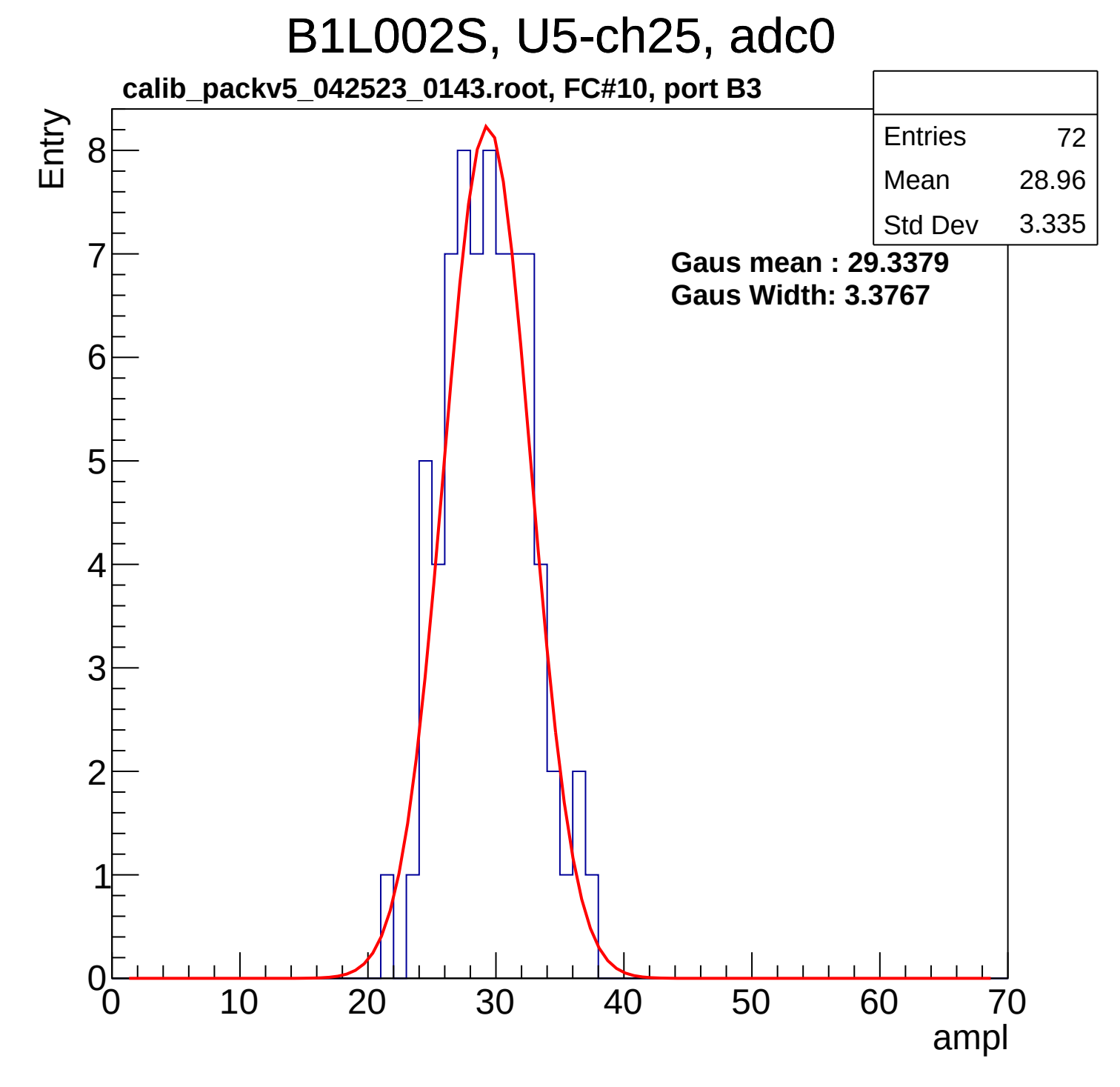
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.38
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U5-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 53.6
Std Dev 15.38
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.96
Std Dev 3.335
Gaus mean : 29.3379
Gaus Width: 3.3767
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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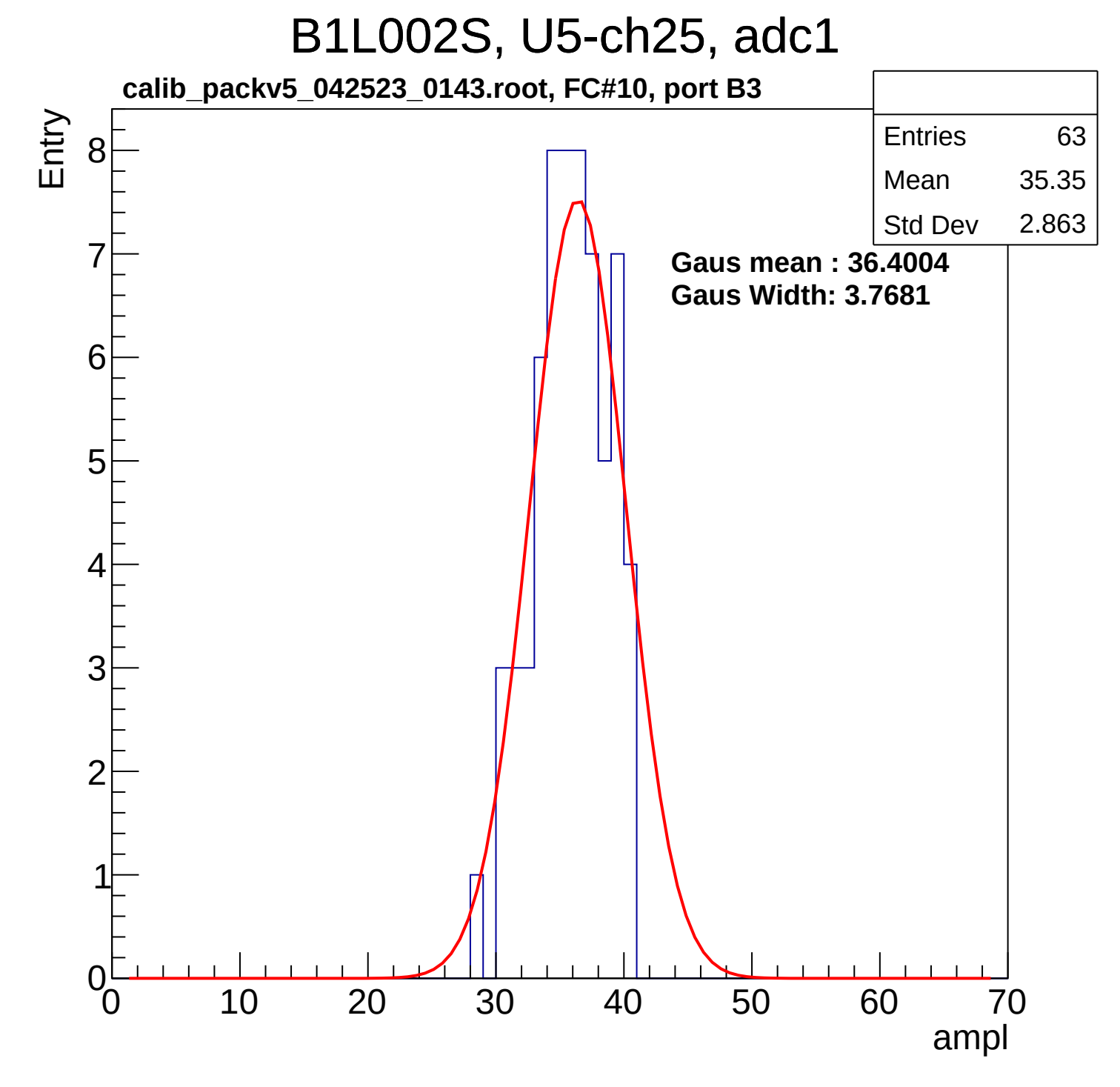
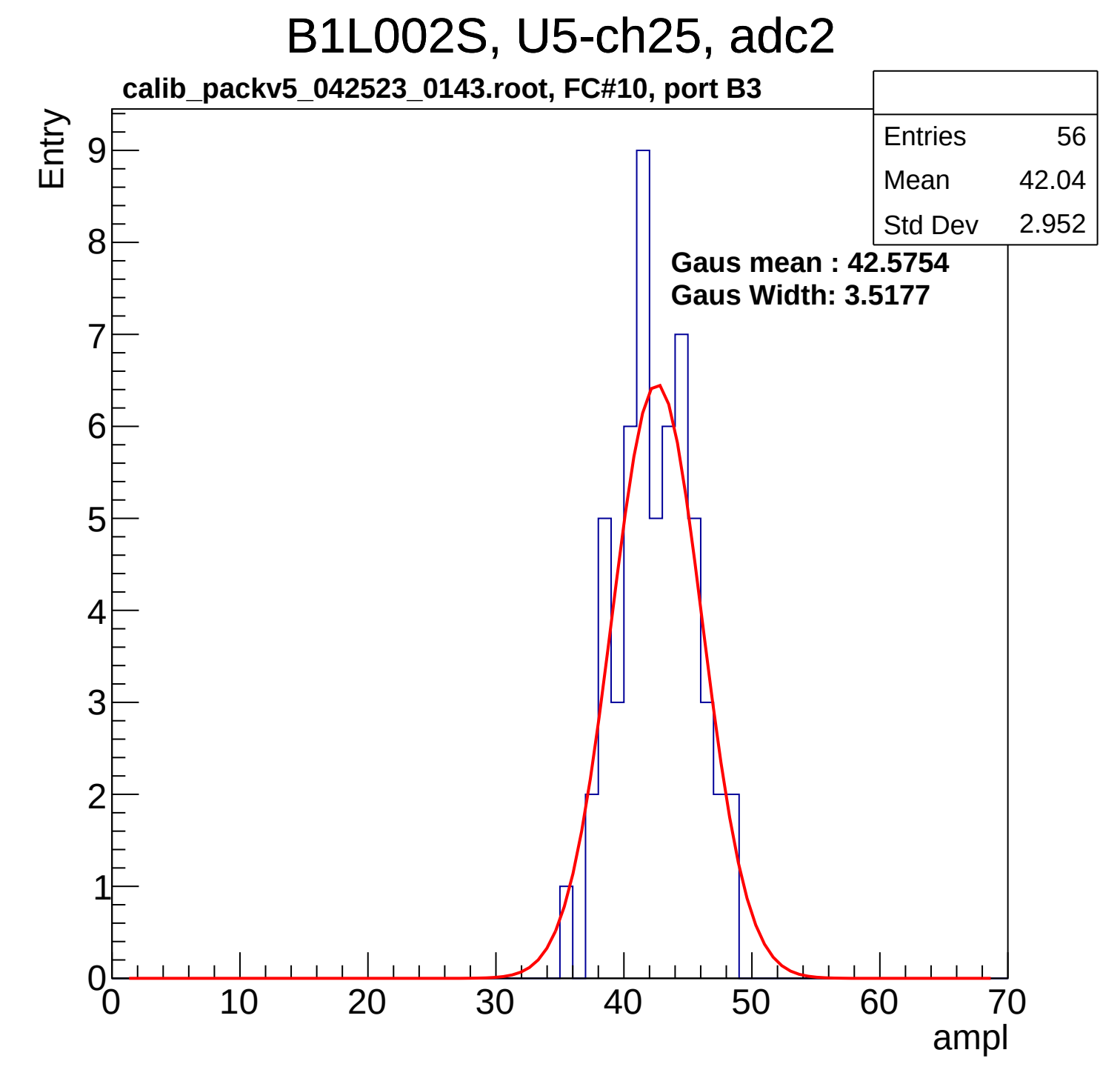

B1L002S, U5-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.35
Std Dev 2.863
Gaus mean : 36.4004
Gaus Width: 3.7681
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.04
Std Dev 2.952
Gaus mean : 42.5754
Gaus Width: 3.5177
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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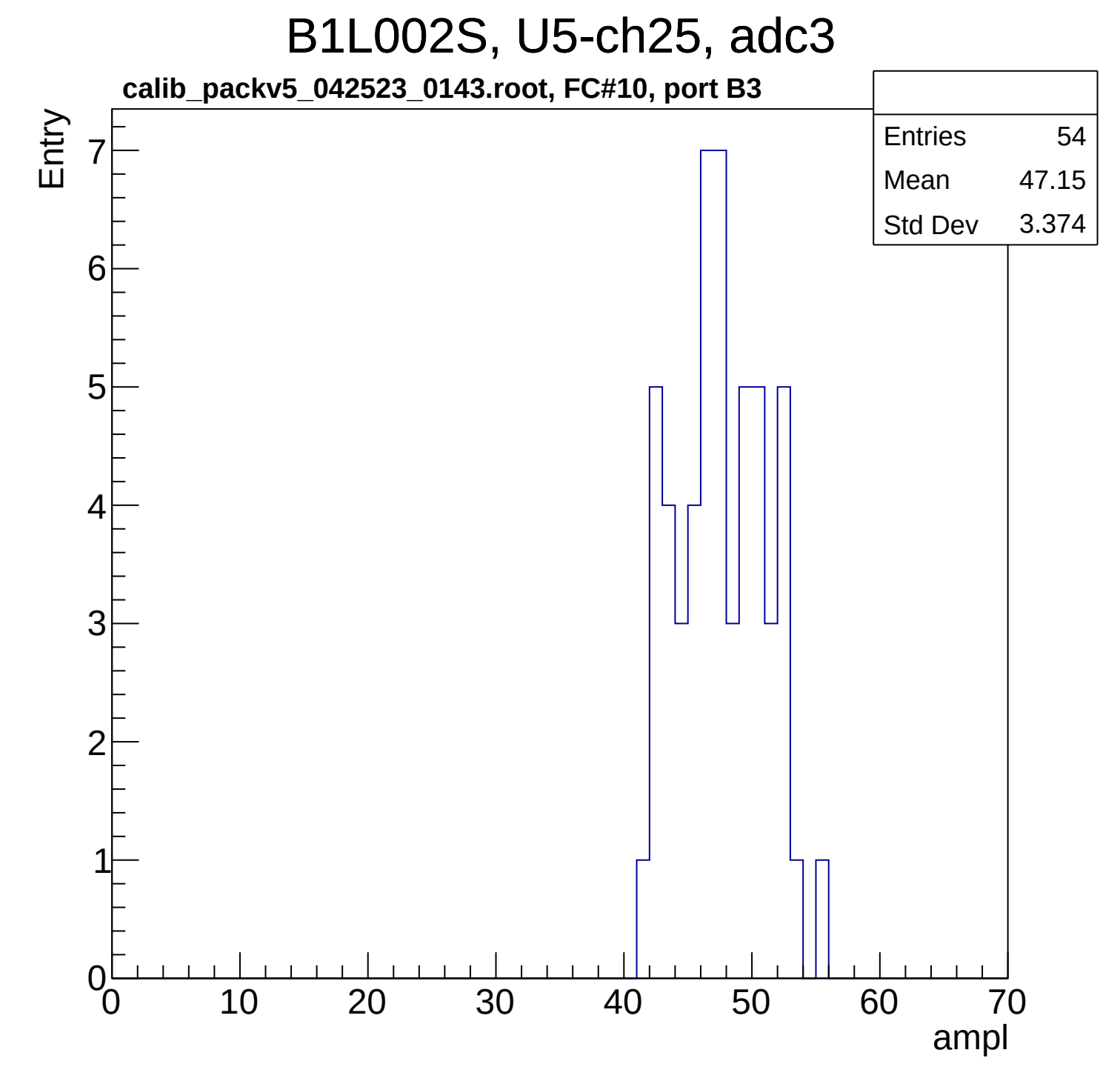
B1L002S, U5-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.15
Std Dev 3.374
Entry
ampl
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1
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3
4
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6
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0
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20
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40
50
60
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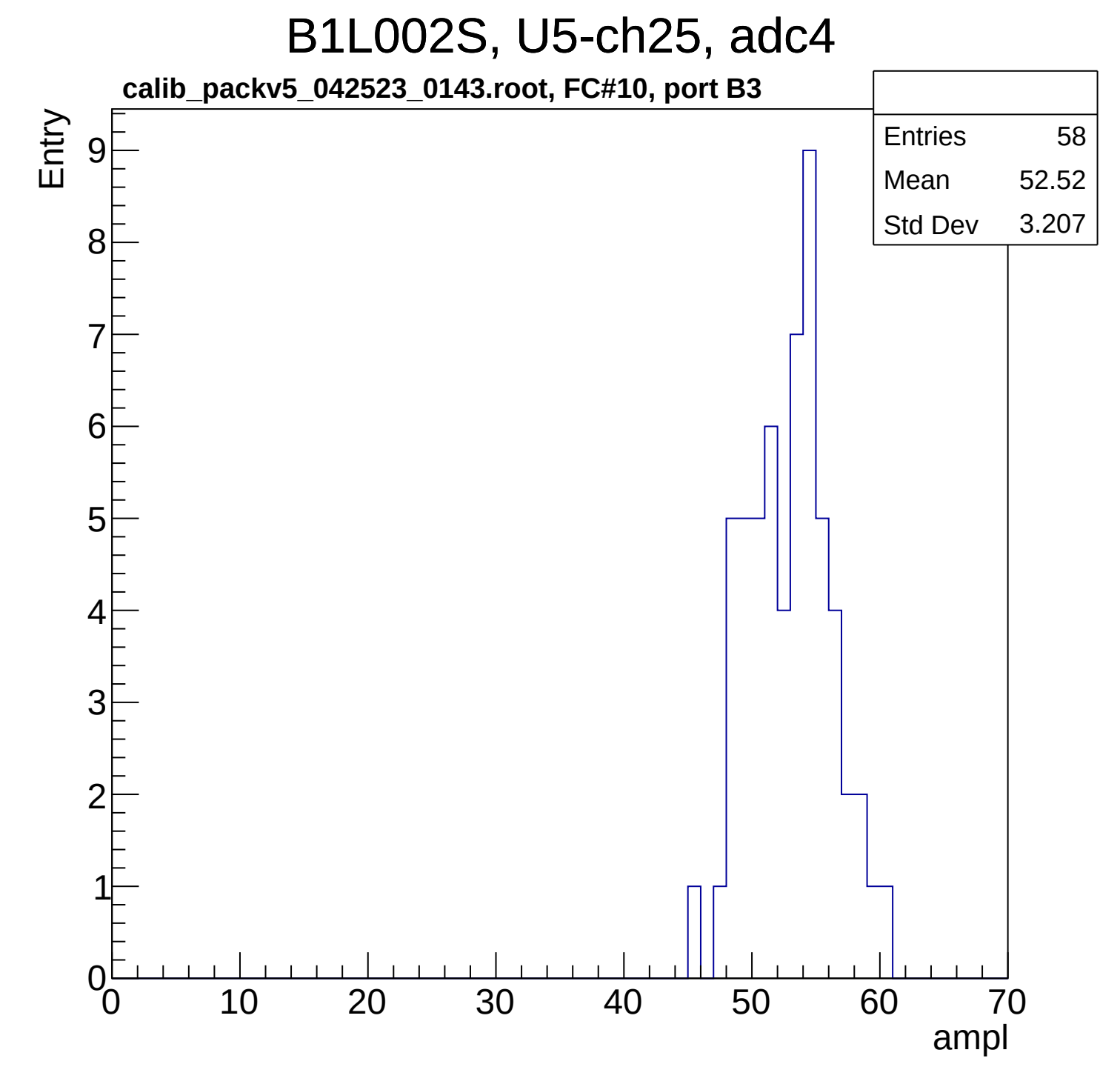
B1L002S, U5-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.52
Std Dev 3.207
Entry
ampl
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5
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8
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0
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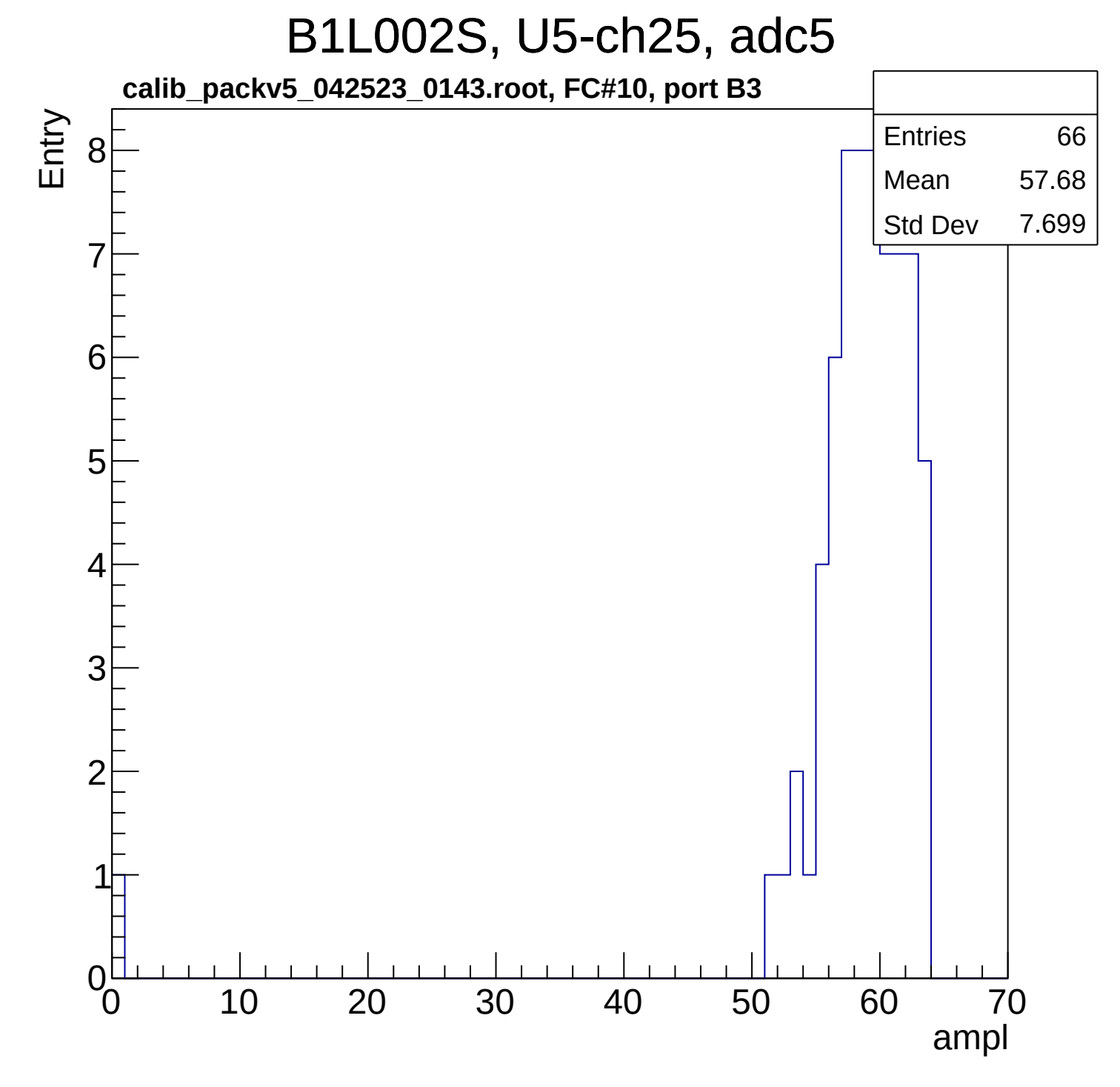
B1L002S, U5-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.68
Std Dev 7.699
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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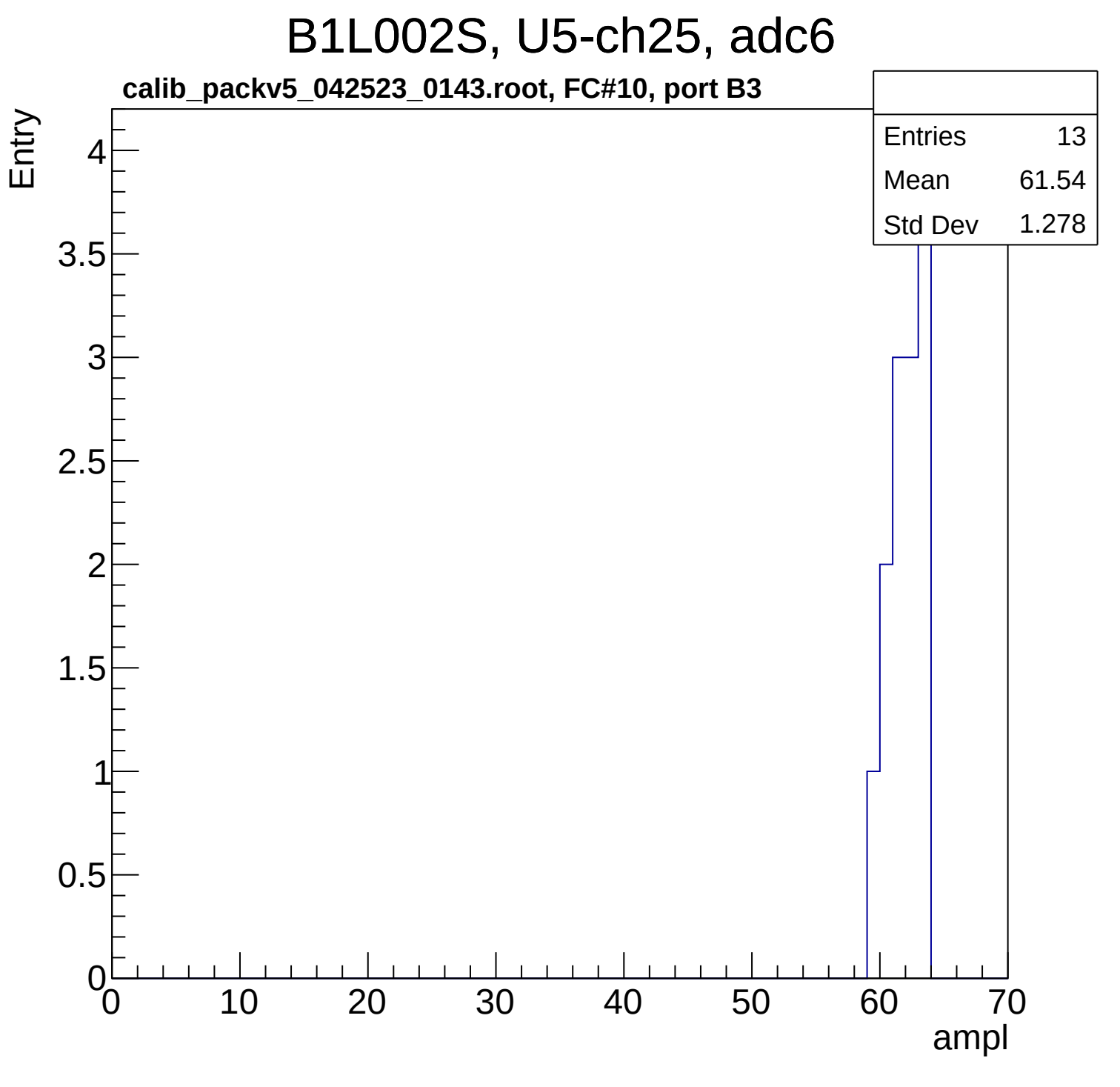
40
50
60
70


B1L002S, U5-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.54
Std Dev 1.278
Entry
ampl
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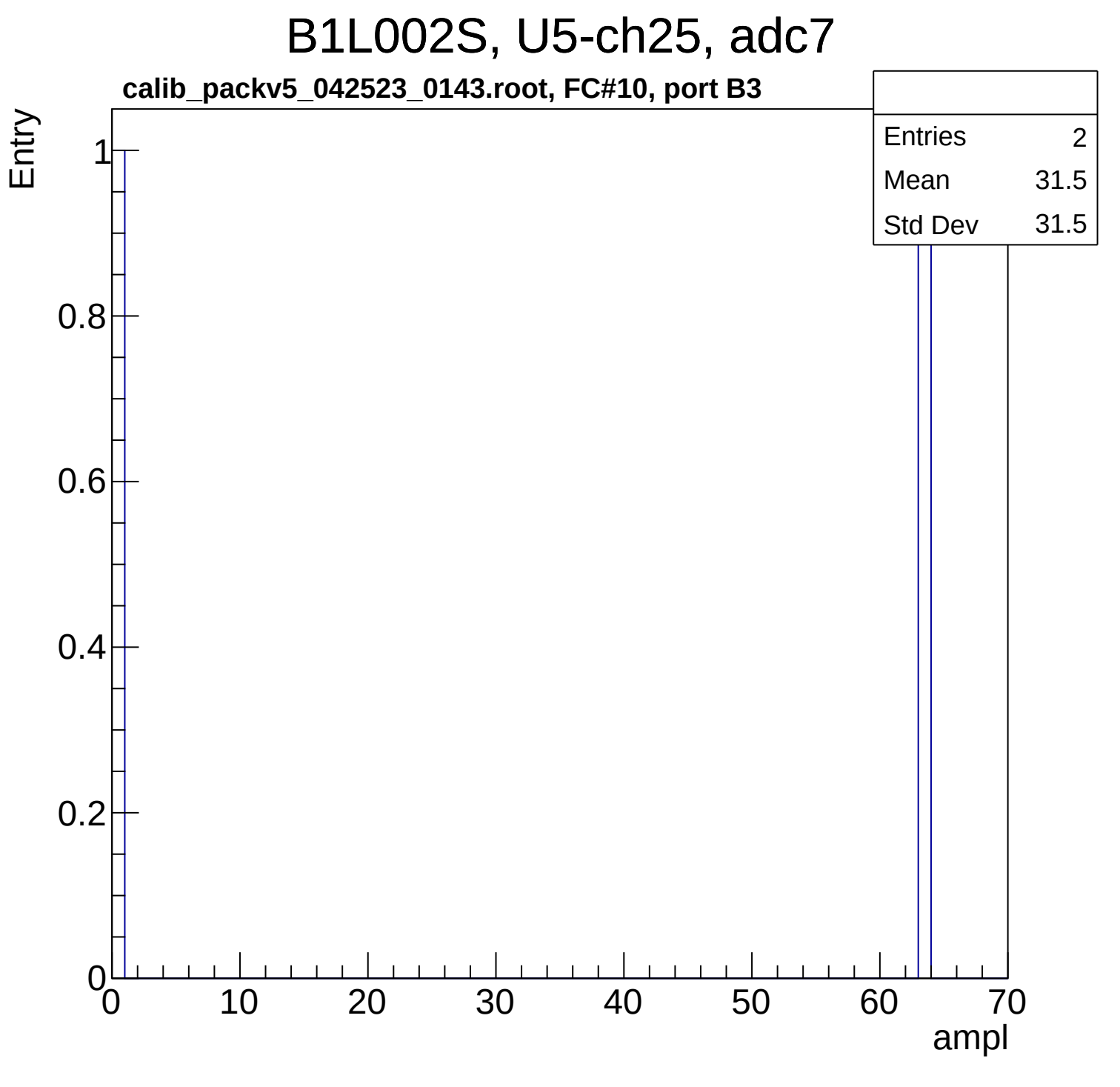
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U5-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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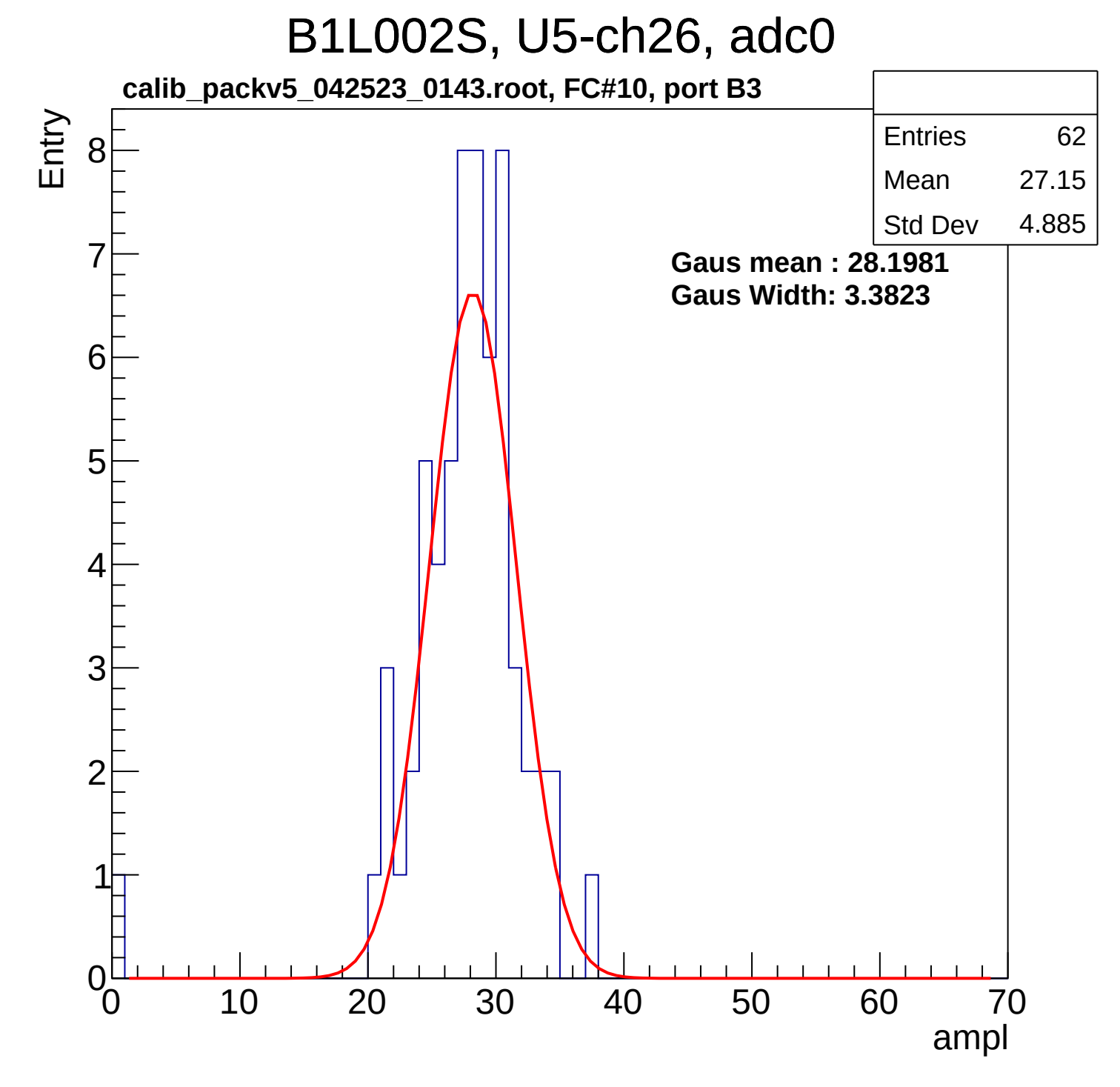
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.15
Std Dev 4.885
Gaus mean : 28.1981
Gaus Width: 3.3823
Entry
ampl
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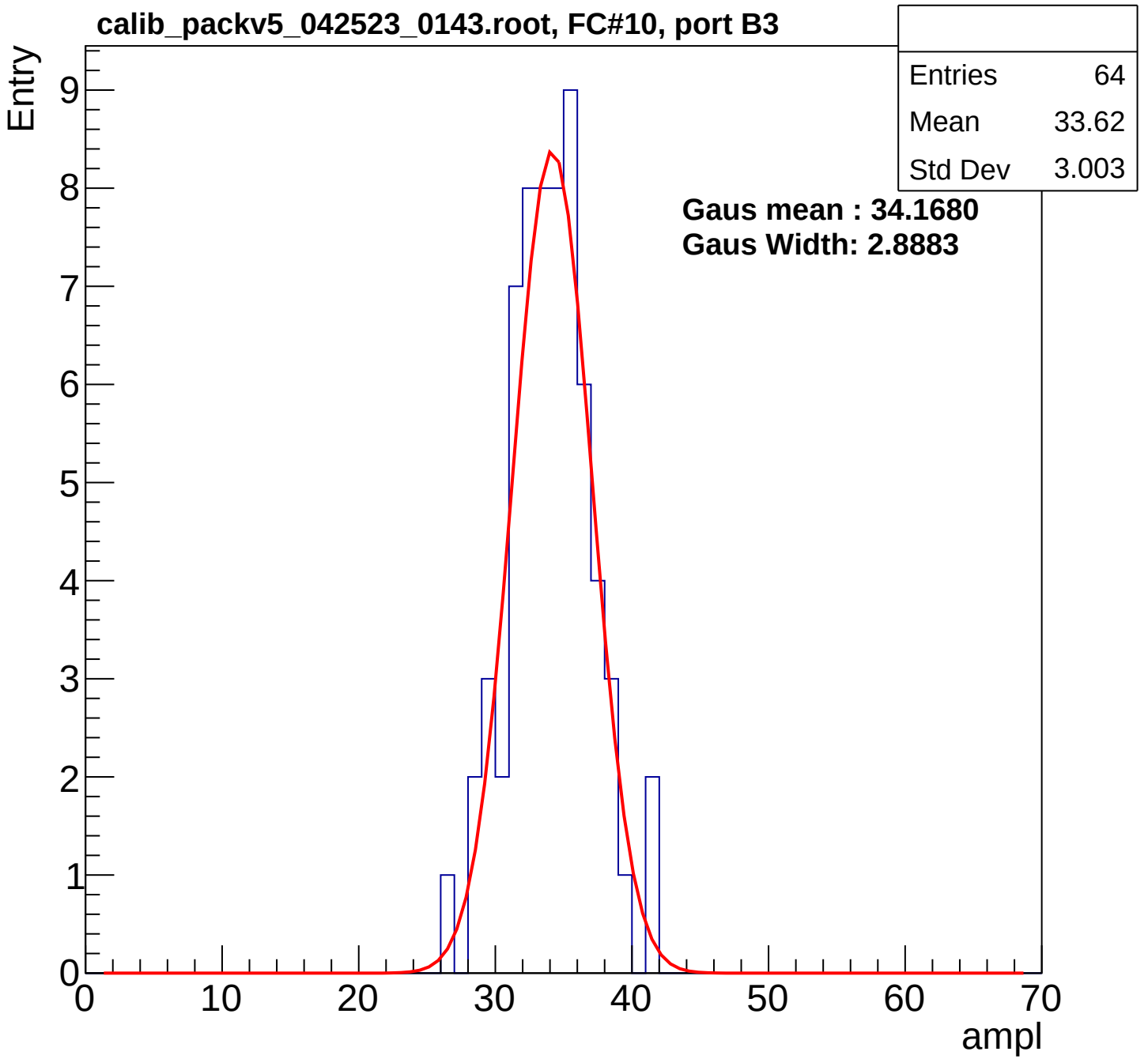
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 33.62
Std Dev 3.003
Gaus mean : 34.1680
Gaus Width: 2.8883
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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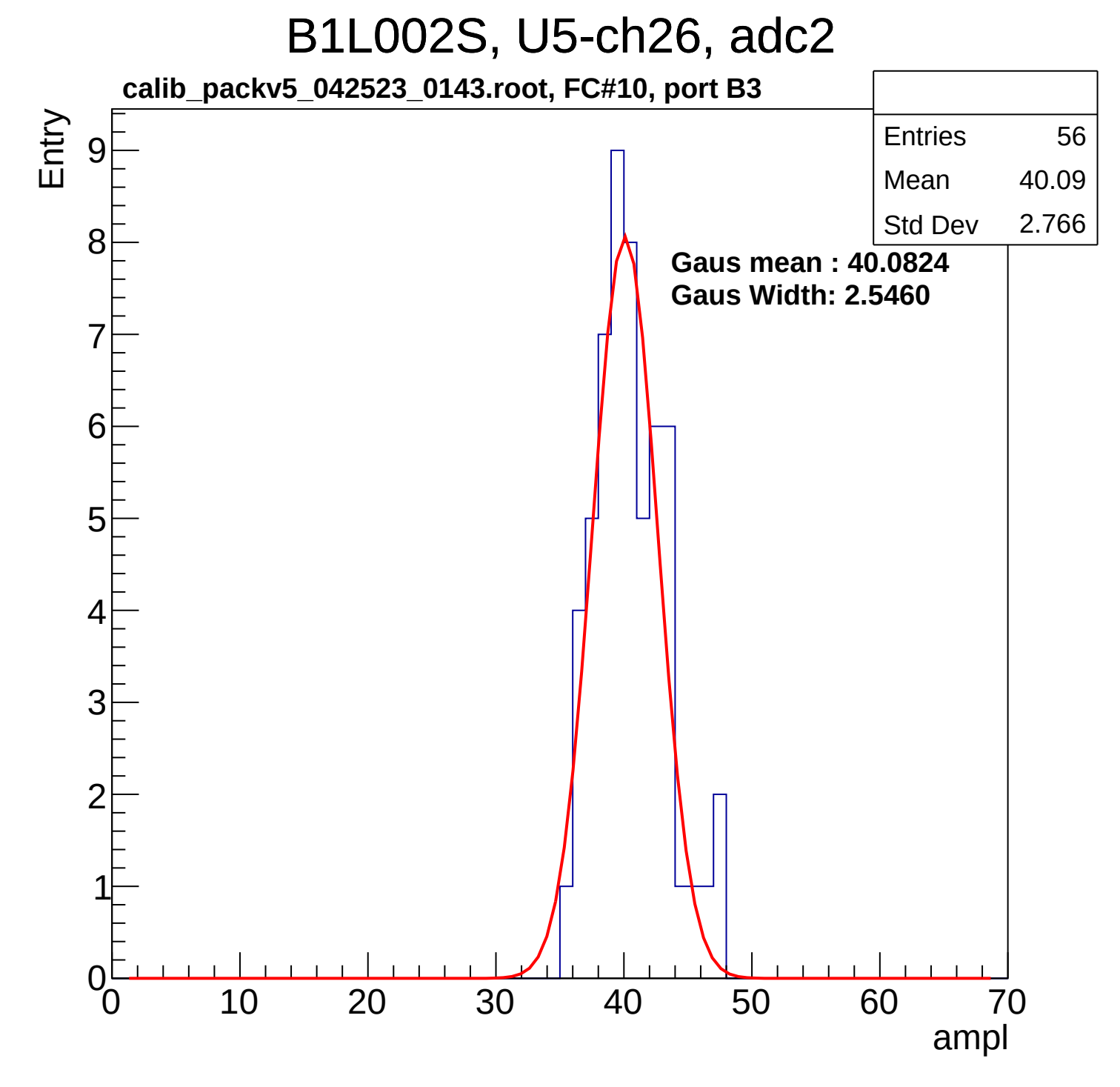
B1L002S, U5-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.09
Std Dev 2.766
Gaus mean : 40.0824
Gaus Width: 2.5460
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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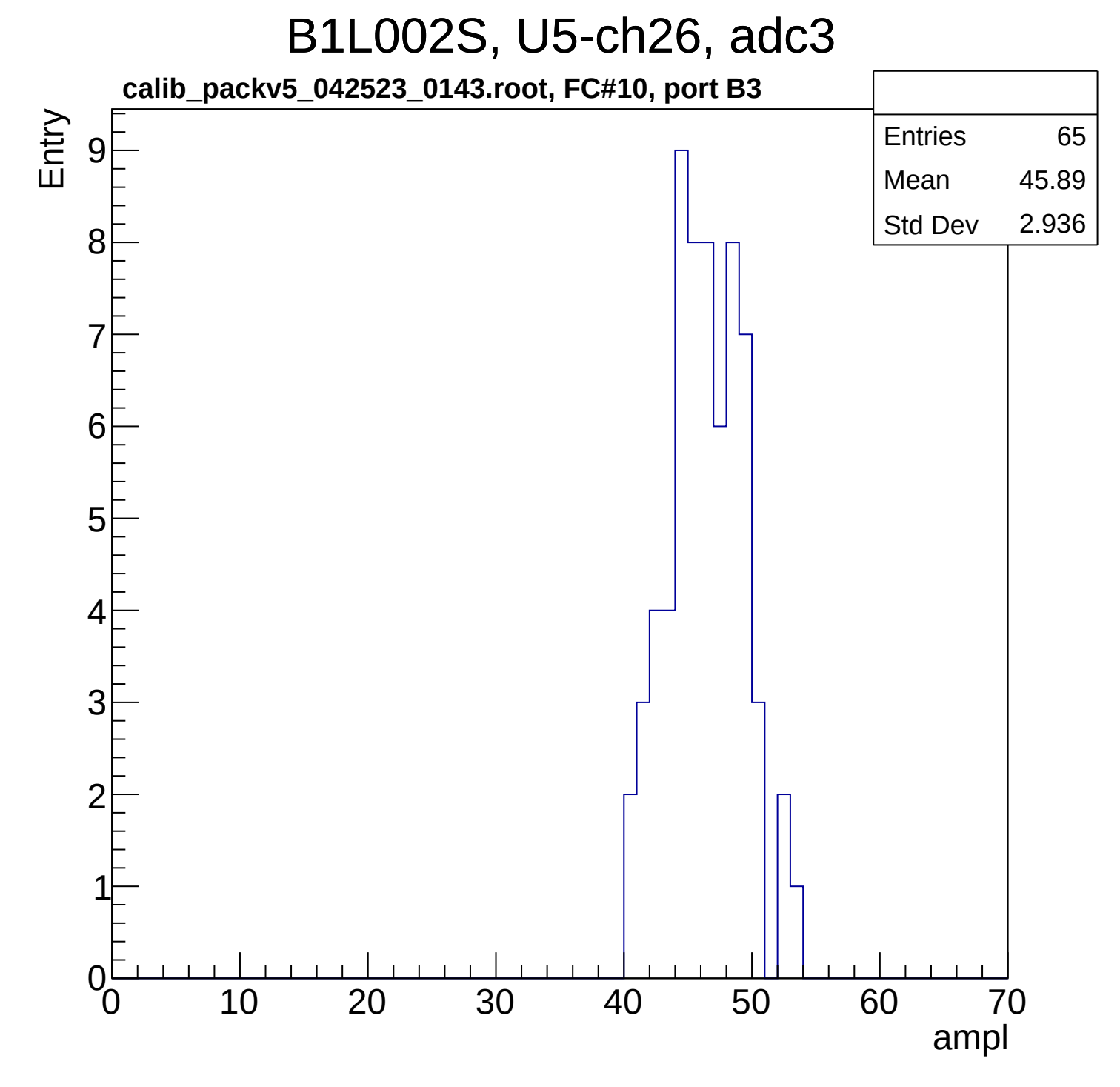
B1L002S, U5-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.89
Std Dev 2.936
Entry
ampl
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4
5
6
7
8
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0
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20
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40
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60
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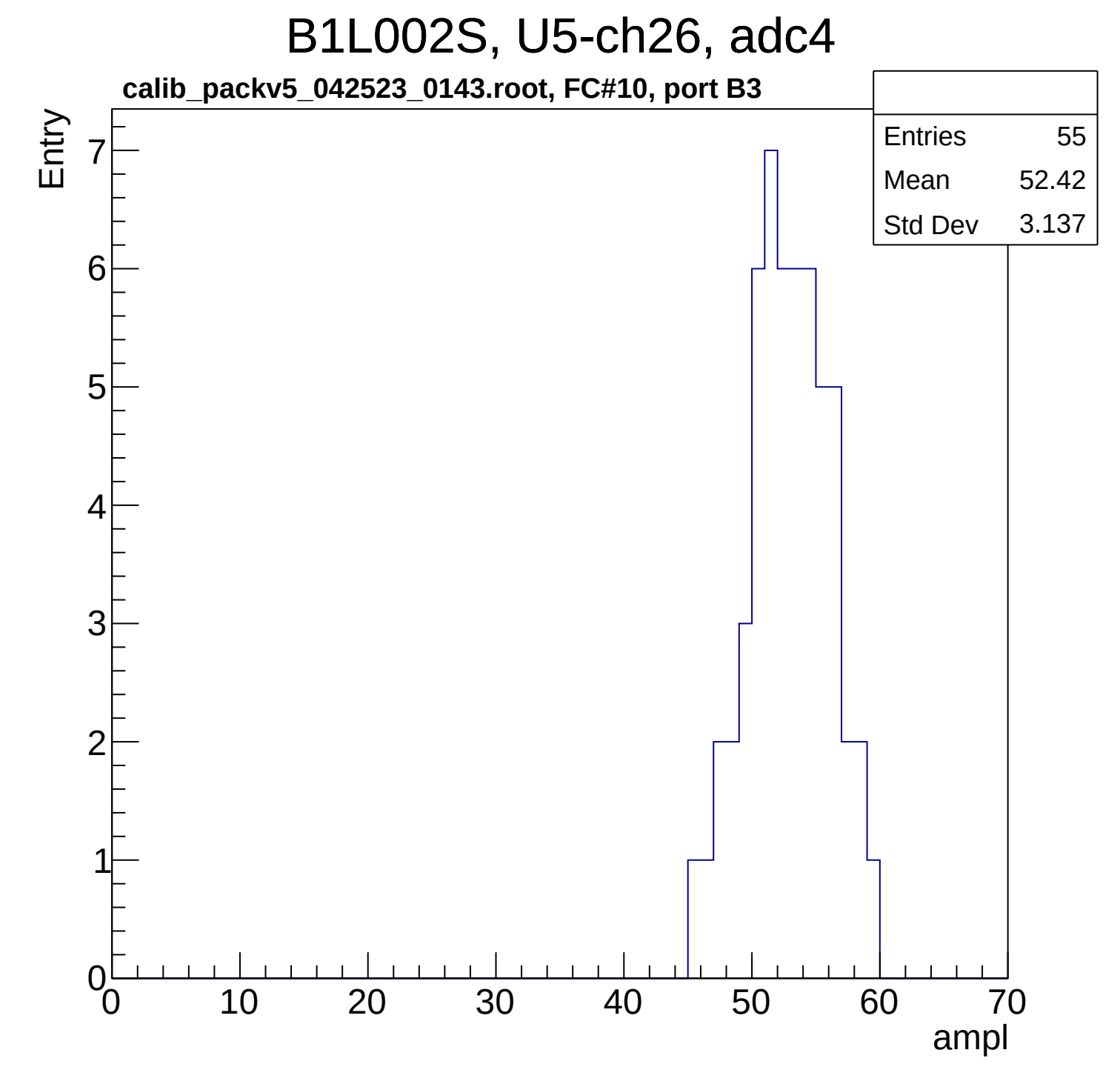
B1L002S, U5-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.42
Std Dev 3.137
Entry
ampl
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2
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4
5
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7
0
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20
30
40
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60
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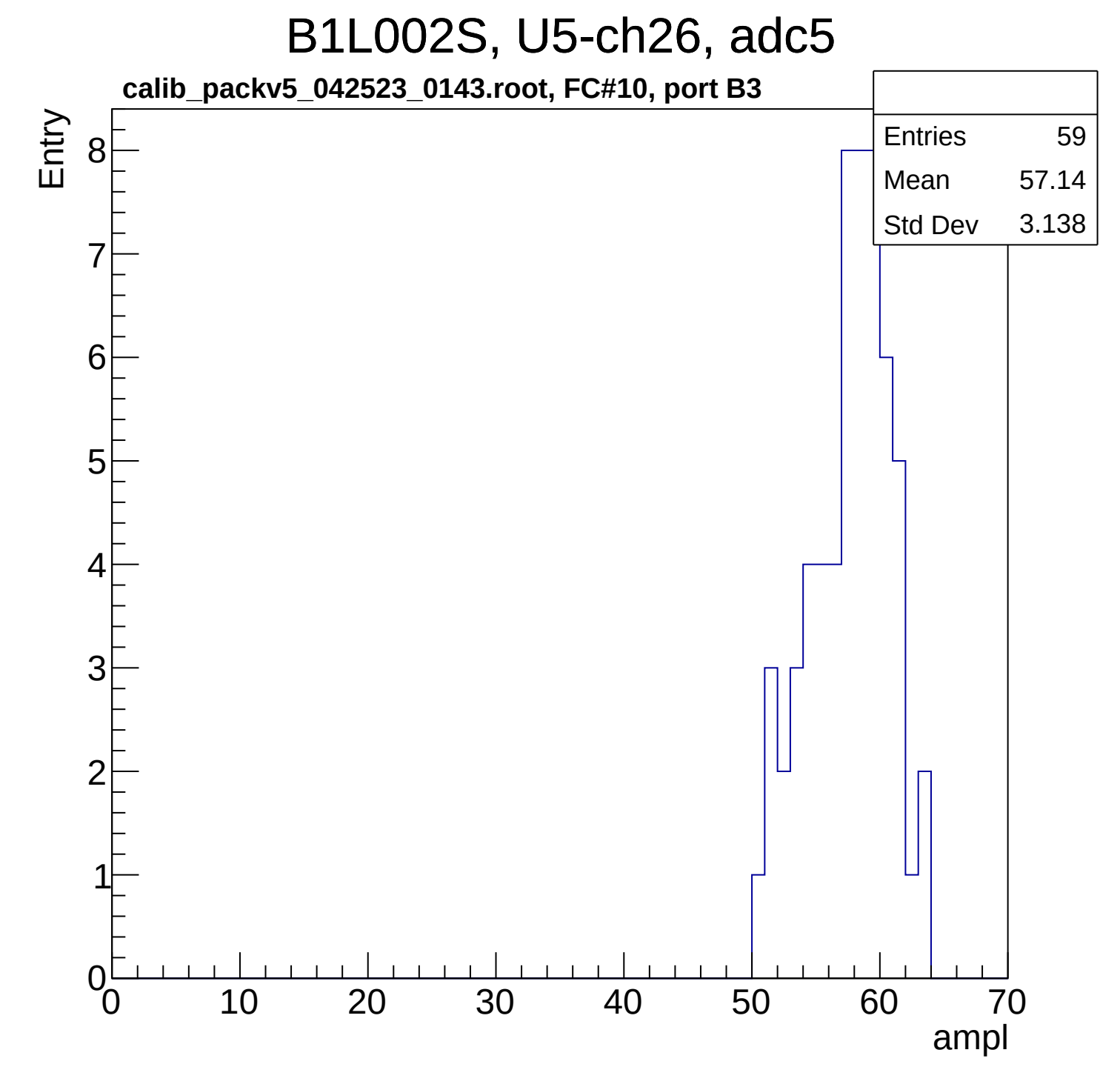
B1L002S, U5-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.14
Std Dev 3.138
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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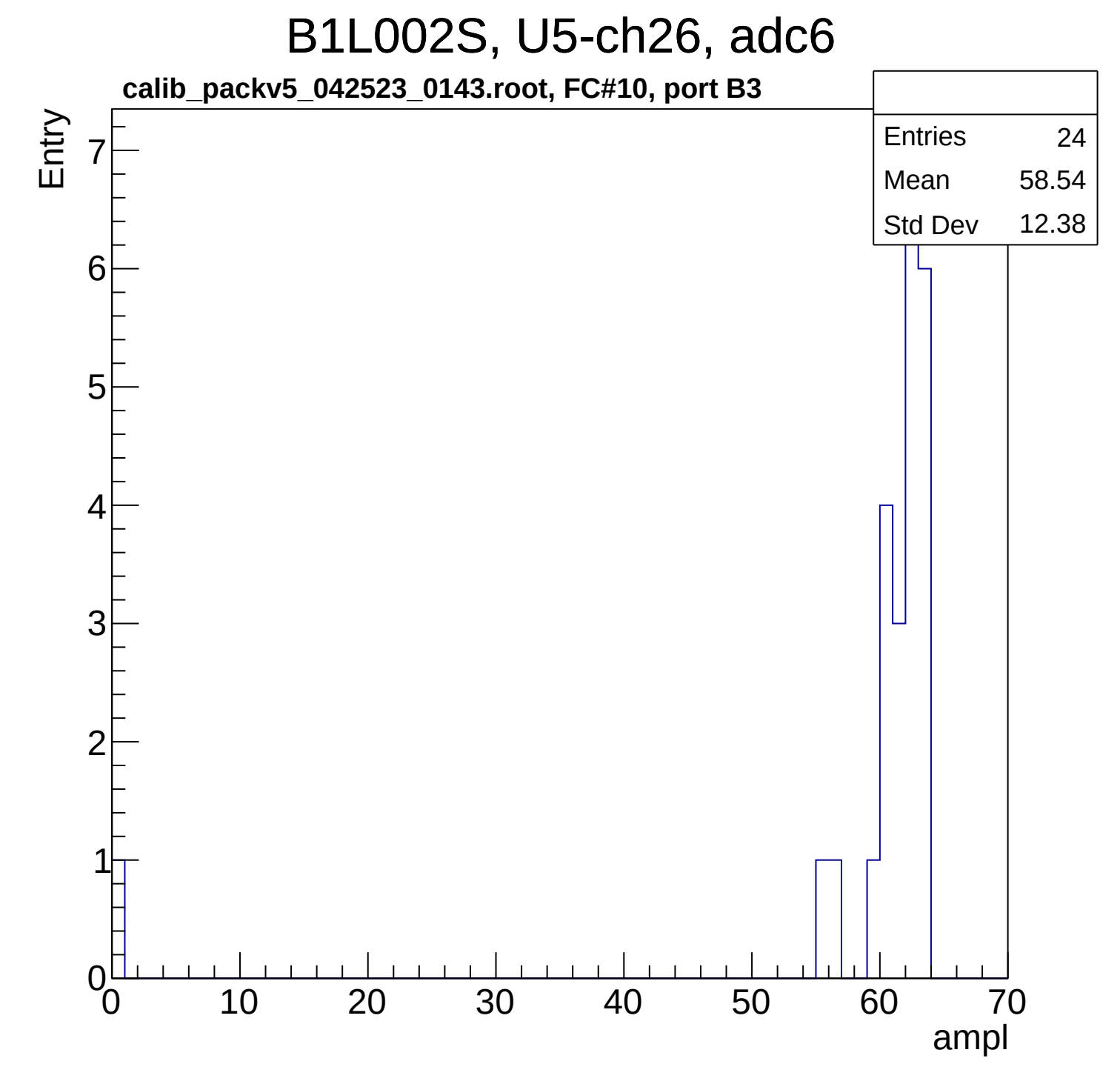
B1L002S, U5-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.54
Std Dev 12.38
Entry
ampl
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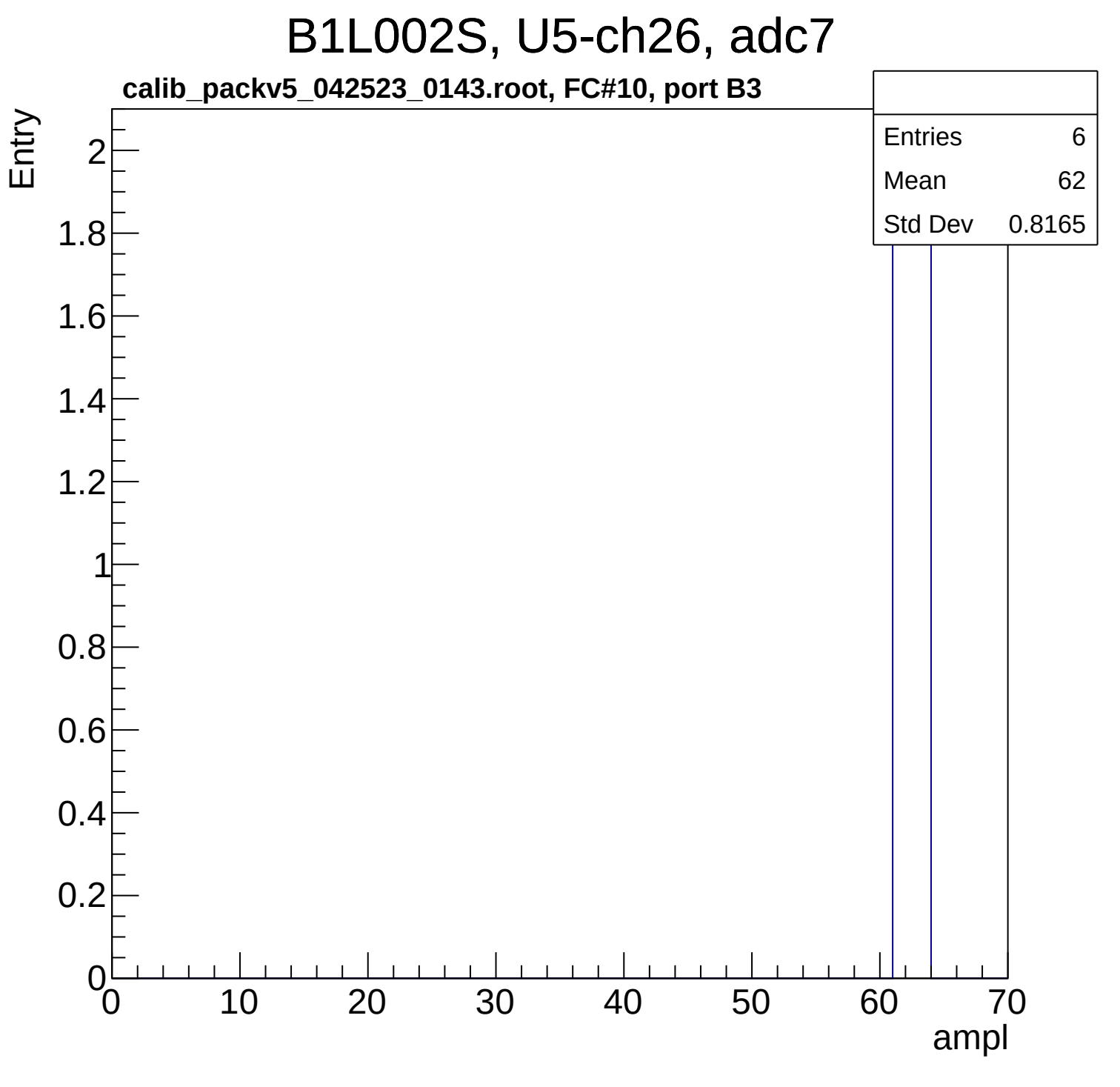
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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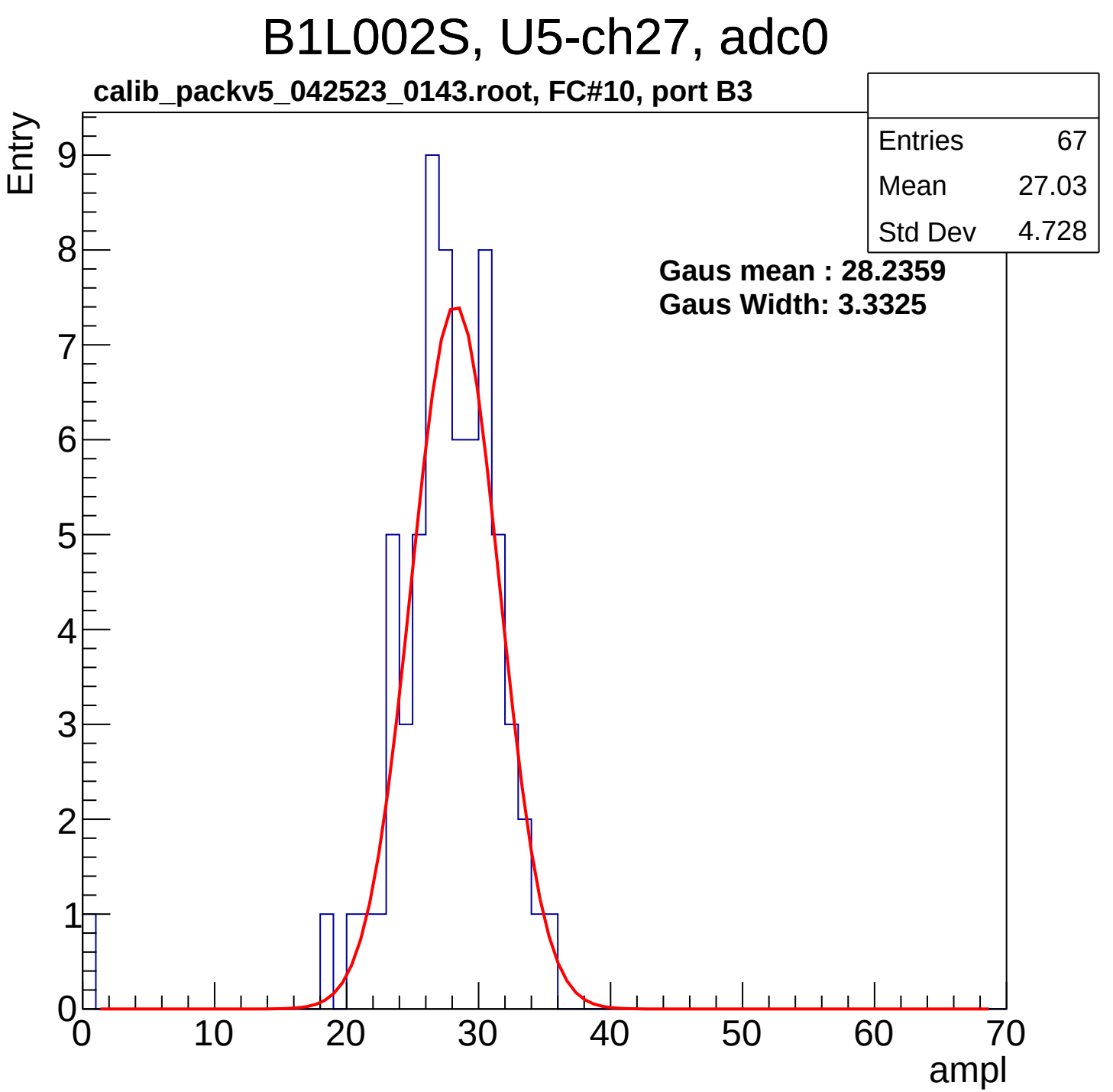
B1L002S, U5-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.03
Std Dev 4.728
Gaus mean : 28.2359
Gaus Width: 3.3325
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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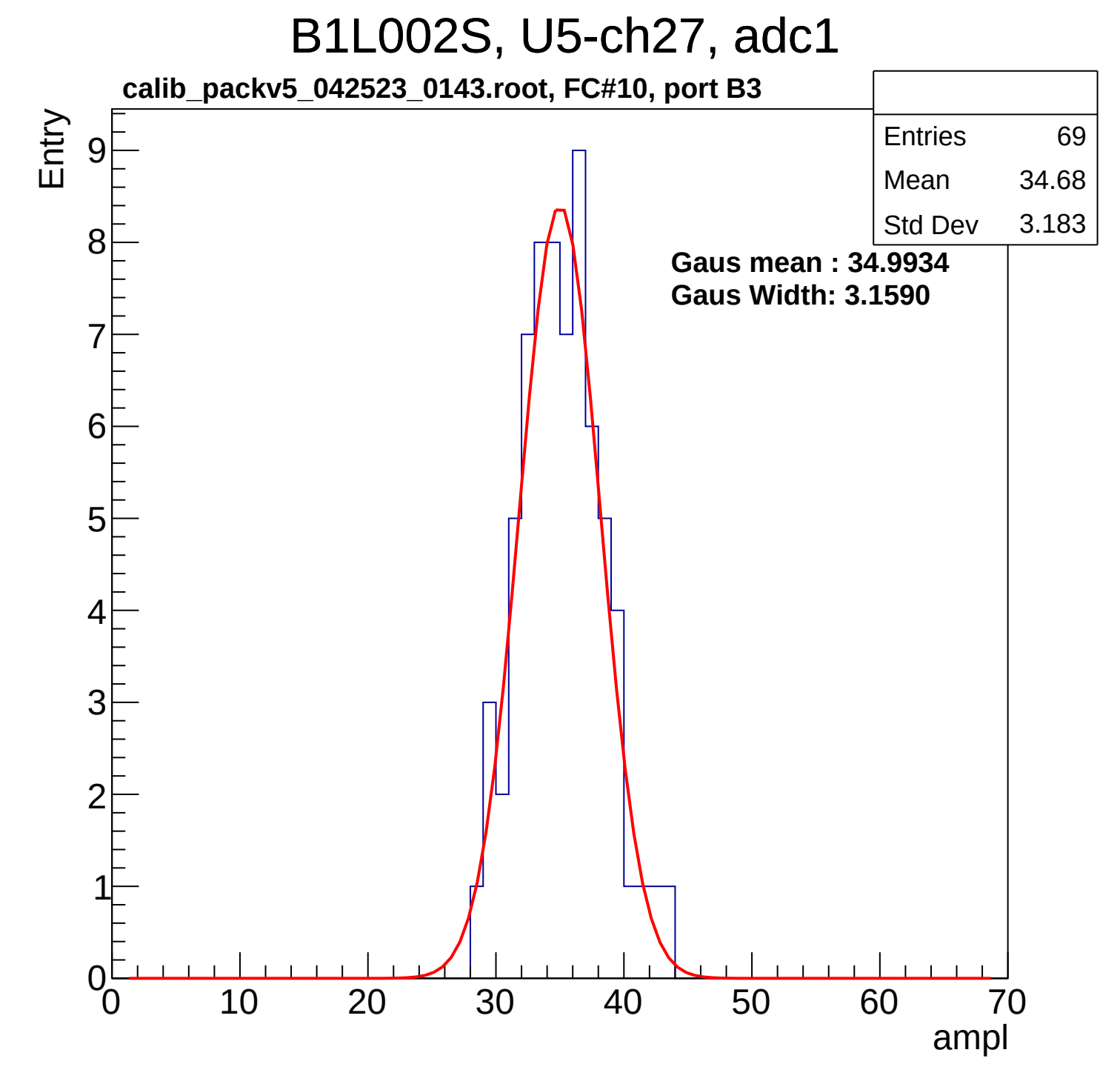
B1L002S, U5-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.68
Std Dev 3.183
Gaus mean : 34.9934
Gaus Width: 3.1590
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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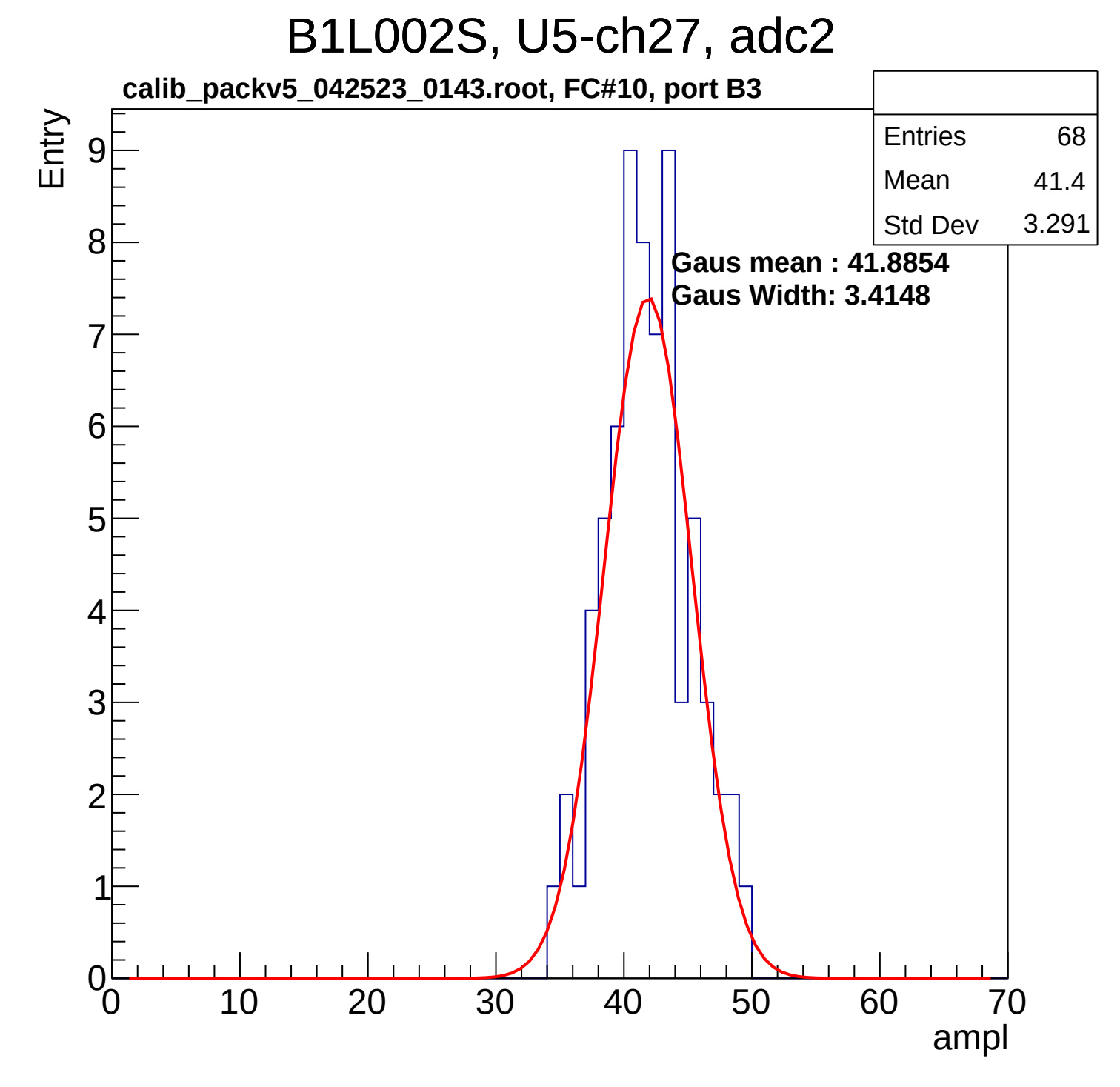
B1L002S, U5-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.4
Std Dev 3.291
Gaus mean : 41.8854
Gaus Width: 3.4148
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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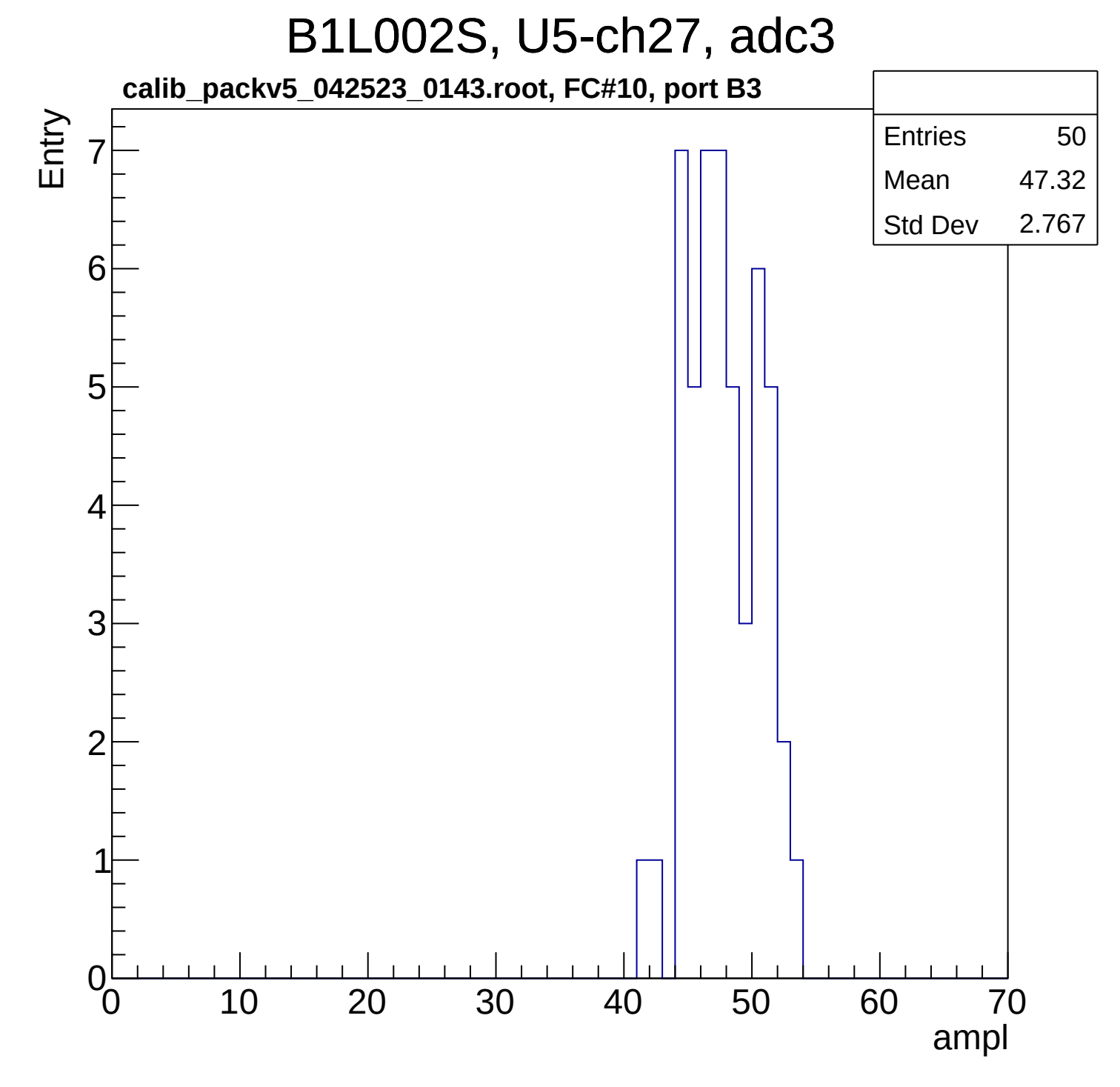
B1L002S, U5-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.32
Std Dev 2.767
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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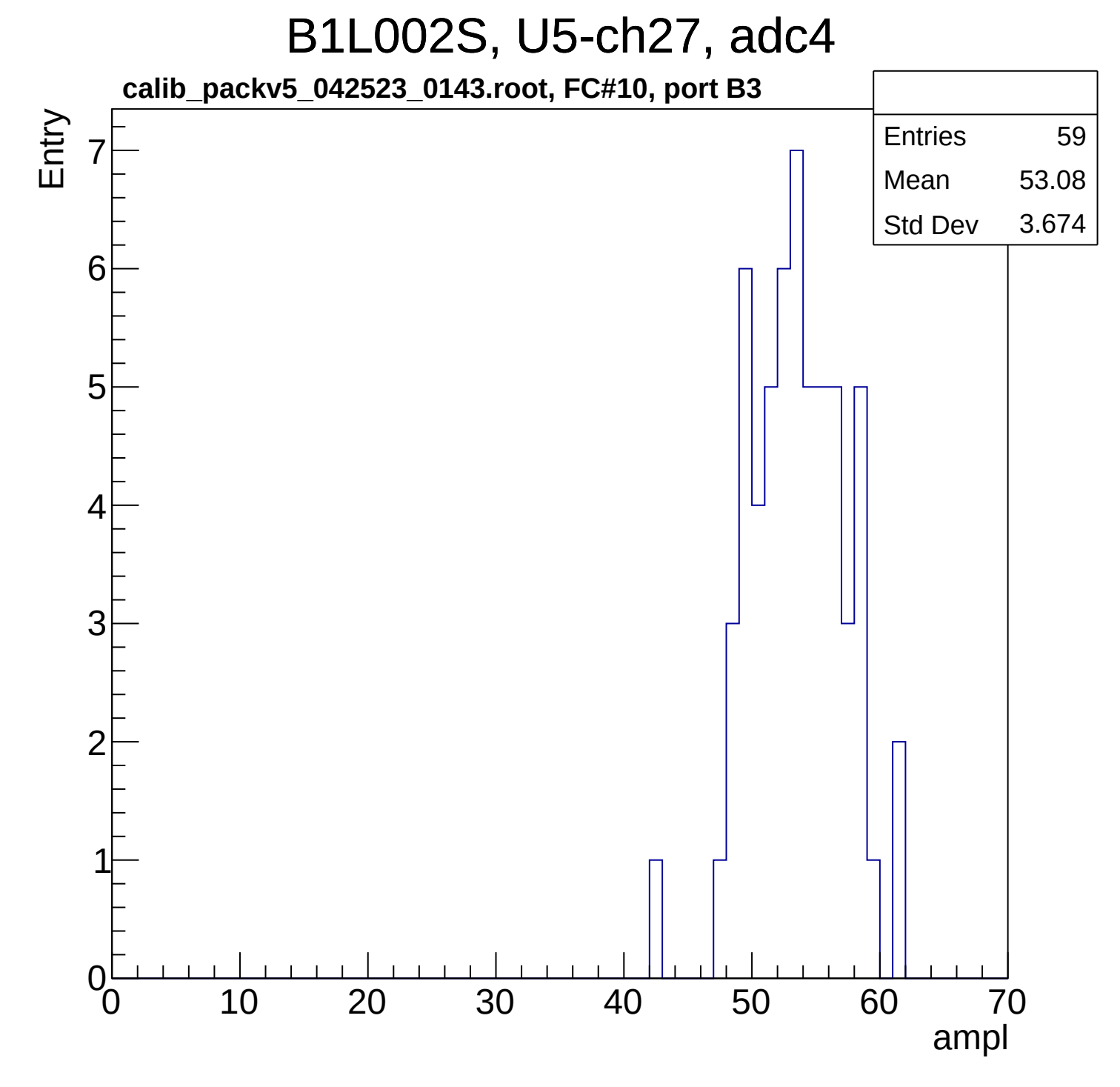
B1L002S, U5-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.08
Std Dev 3.674
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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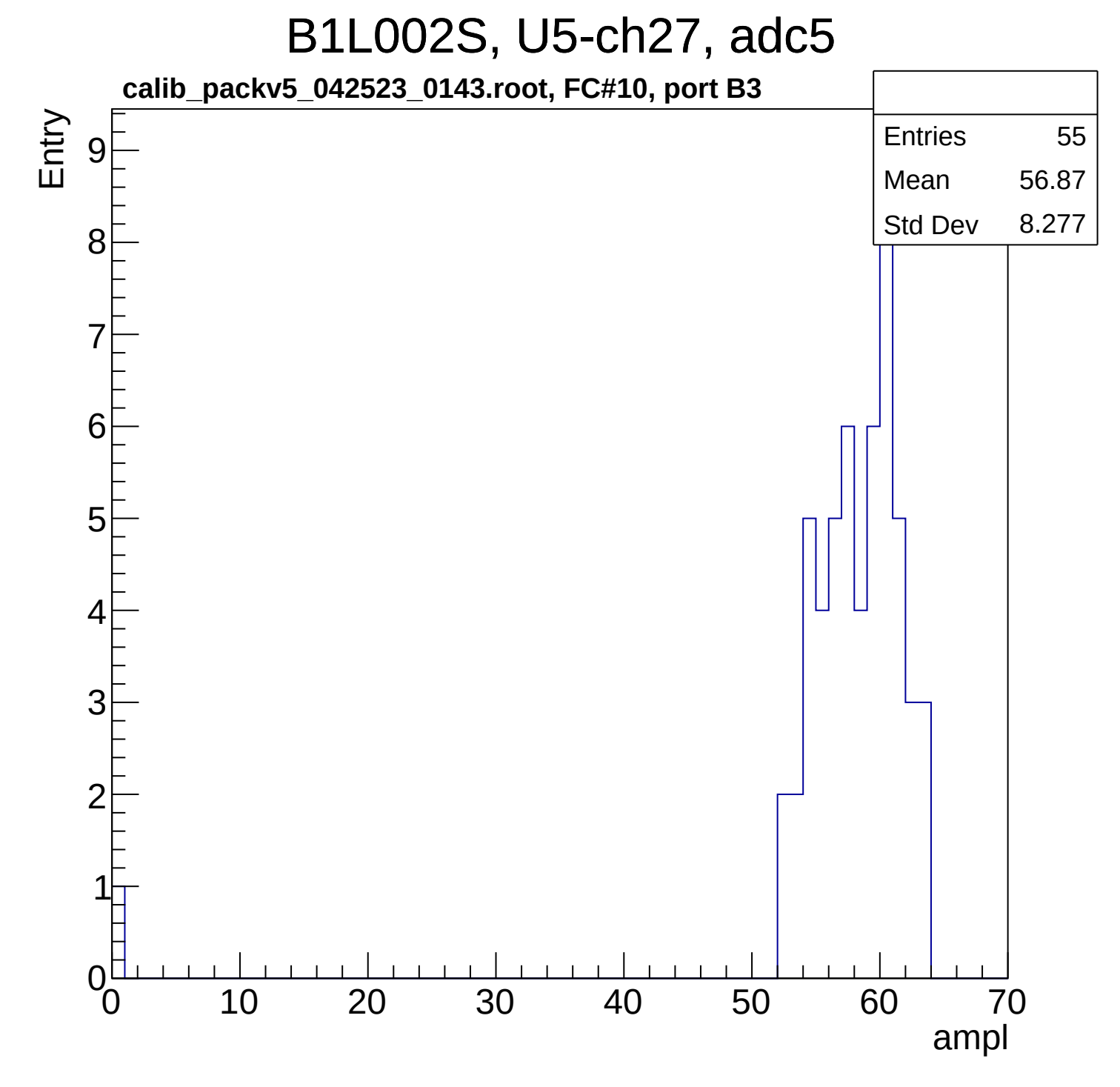
B1L002S, U5-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 56.87
Std Dev 8.277
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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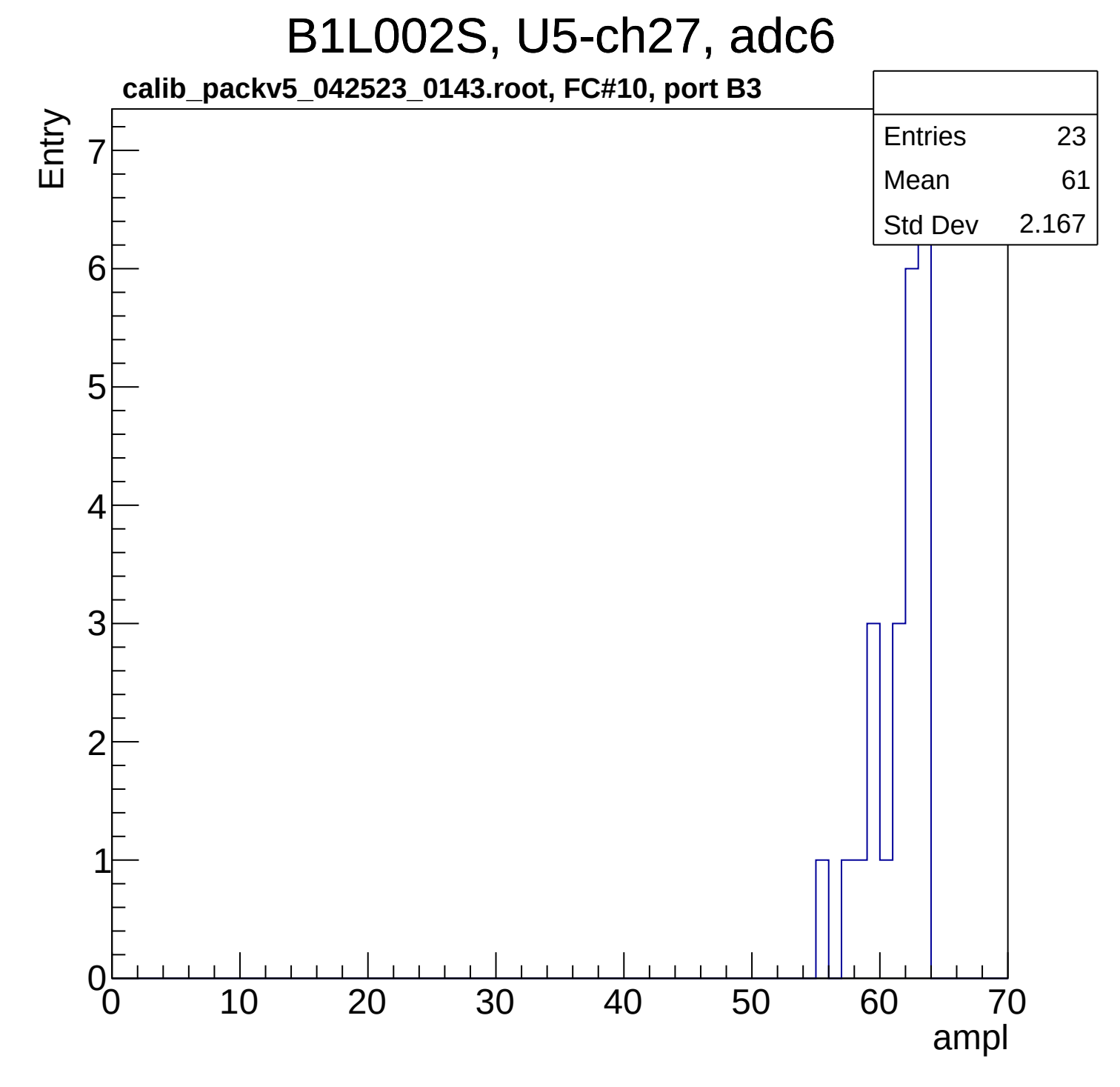
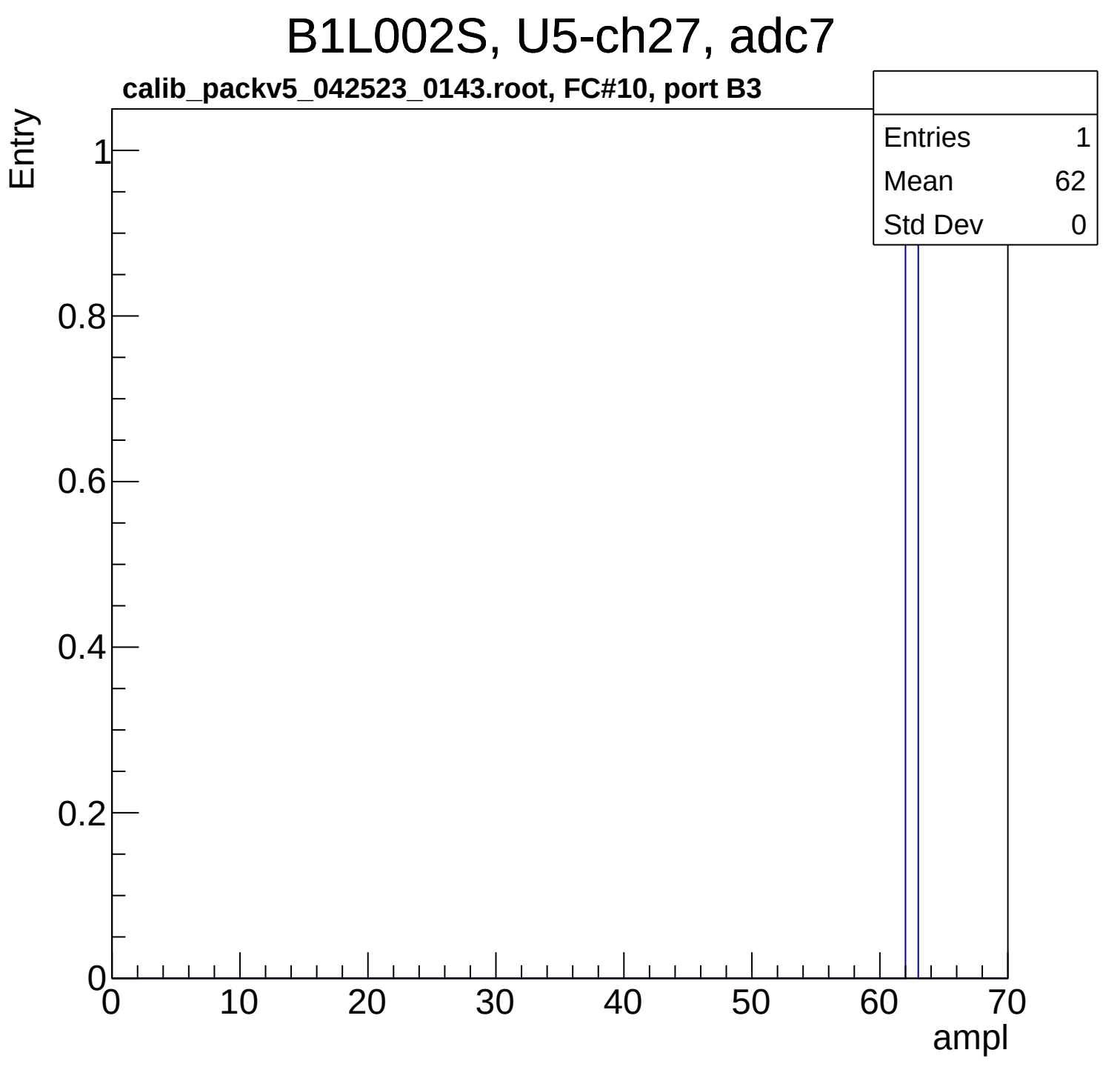

B1L002S, U5-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61
Std Dev 2.167
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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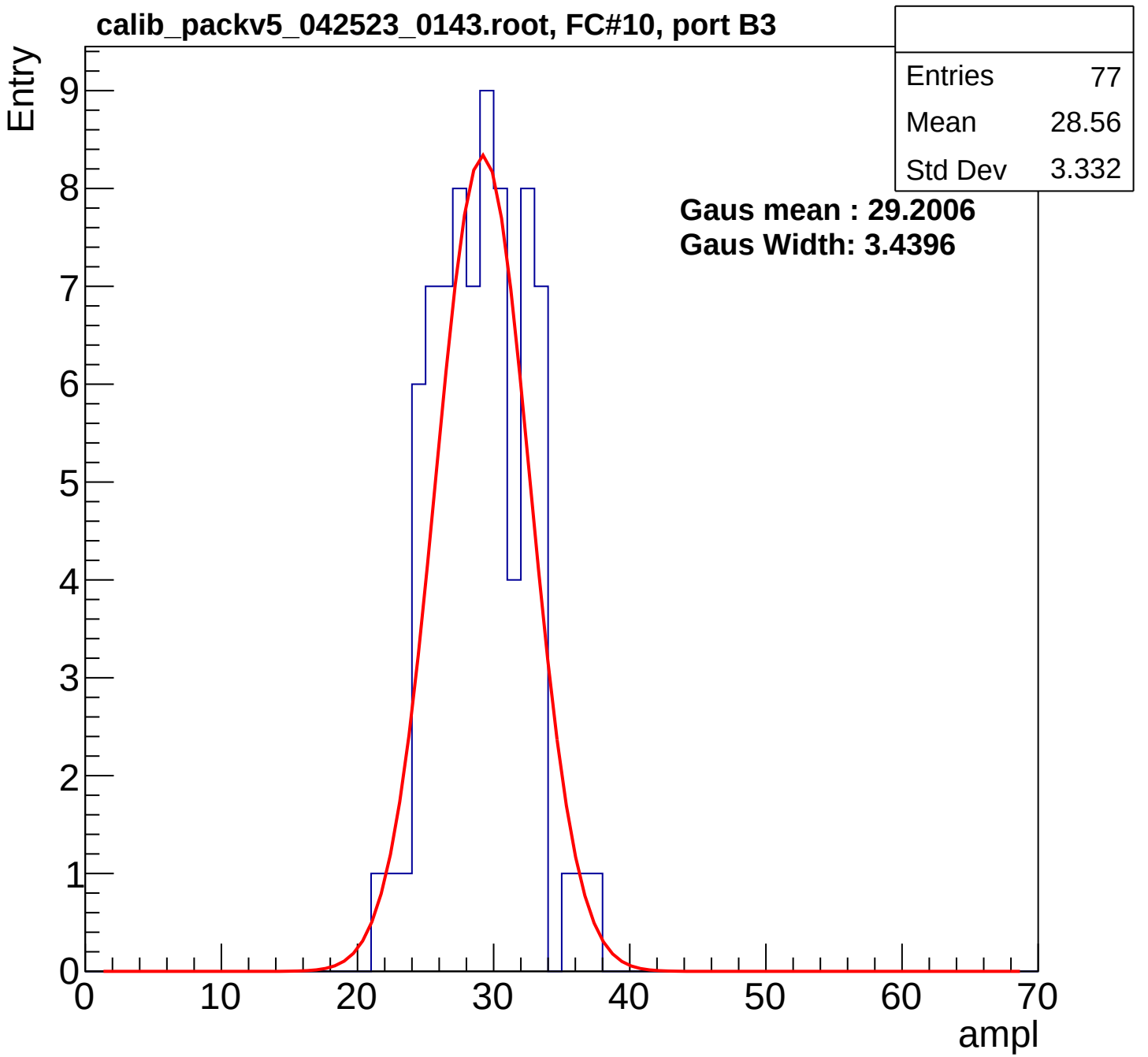
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.56
Std Dev 3.332
Gaus mean : 29.2006
Gaus Width: 3.4396
Entry
ampl
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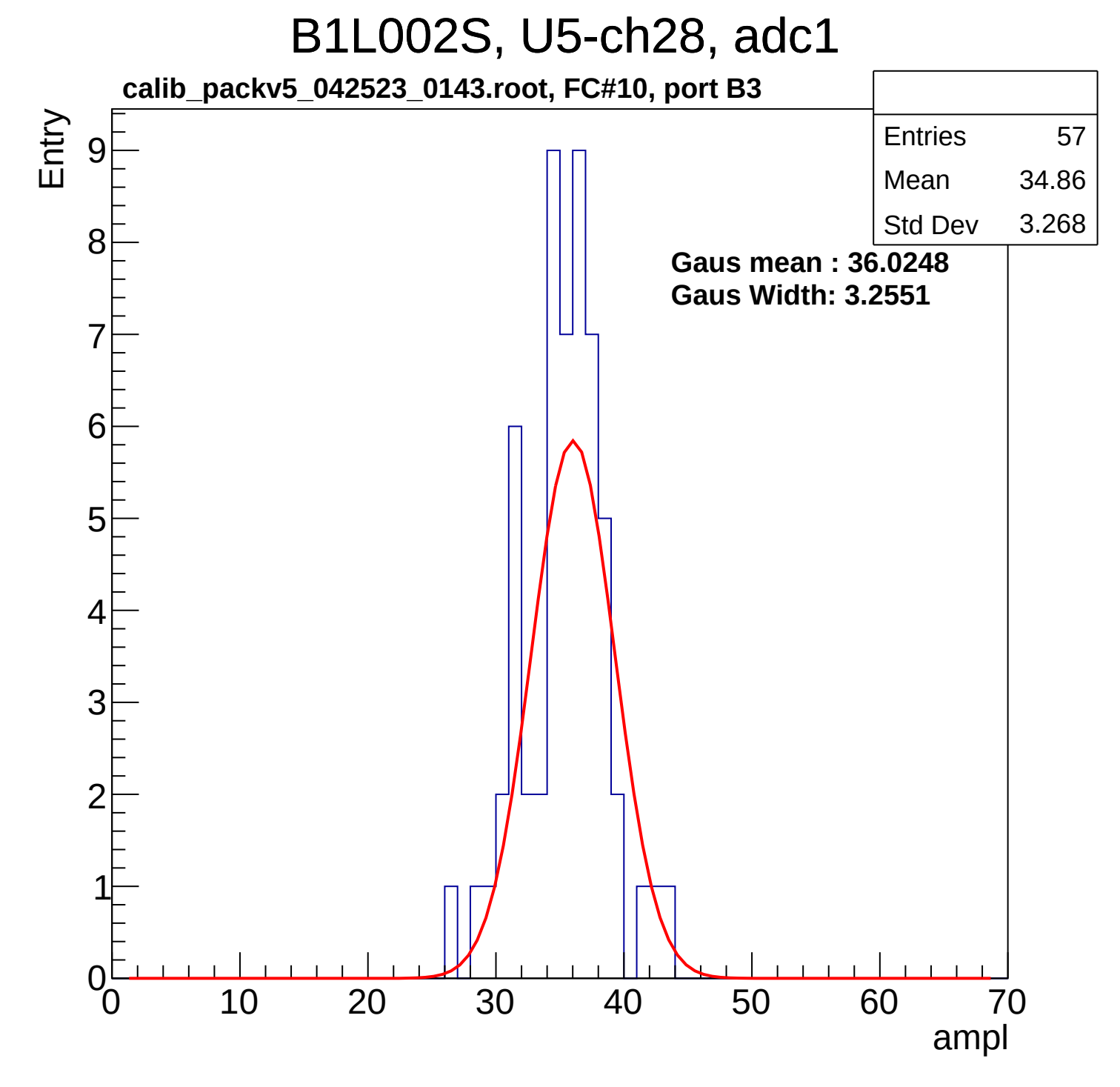
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.86
Std Dev 3.268
Gaus mean : 36.0248
Gaus Width: 3.2551
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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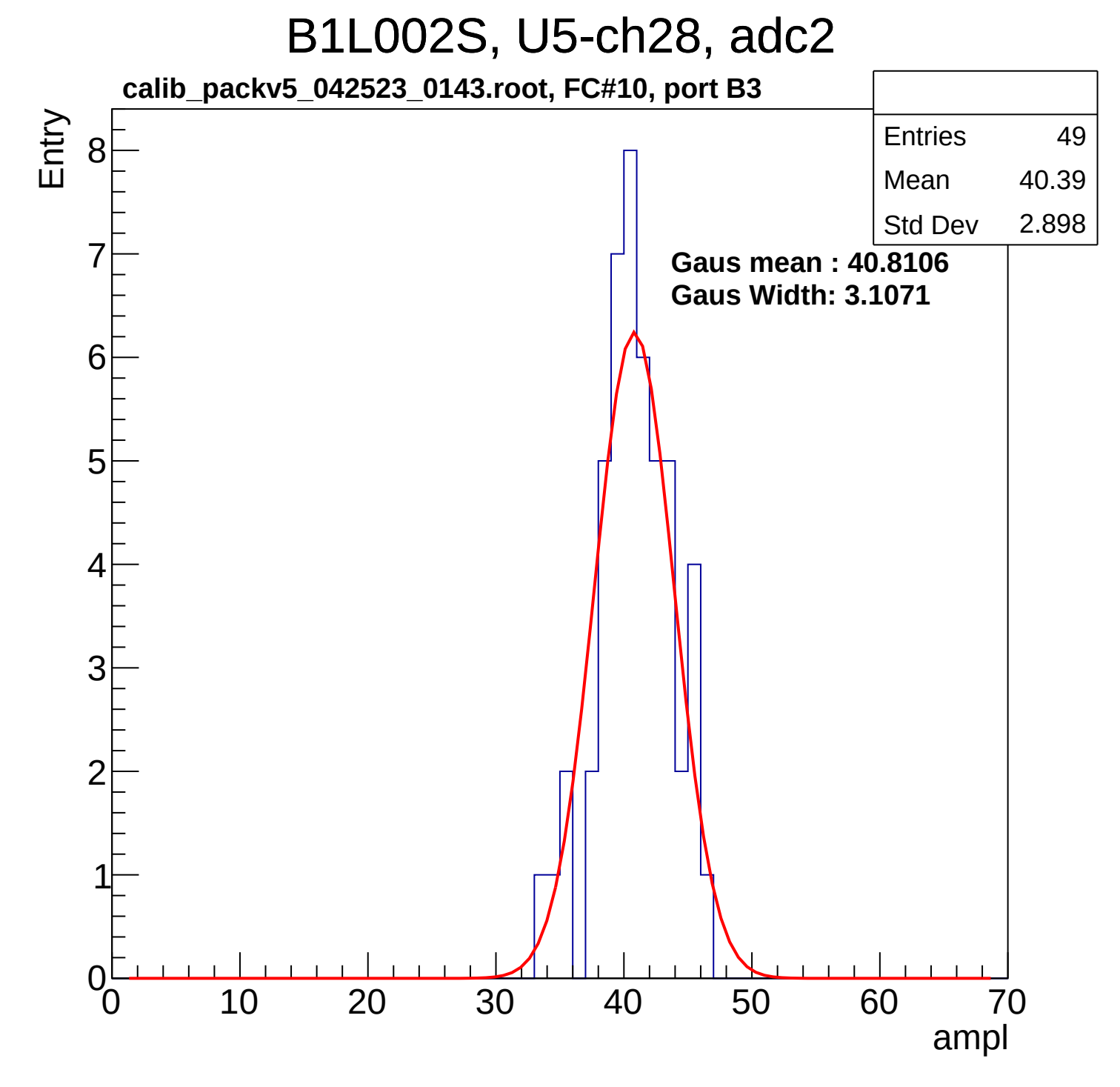
B1L002S, U5-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.39
Std Dev 2.898
Gaus mean : 40.8106
Gaus Width: 3.1071
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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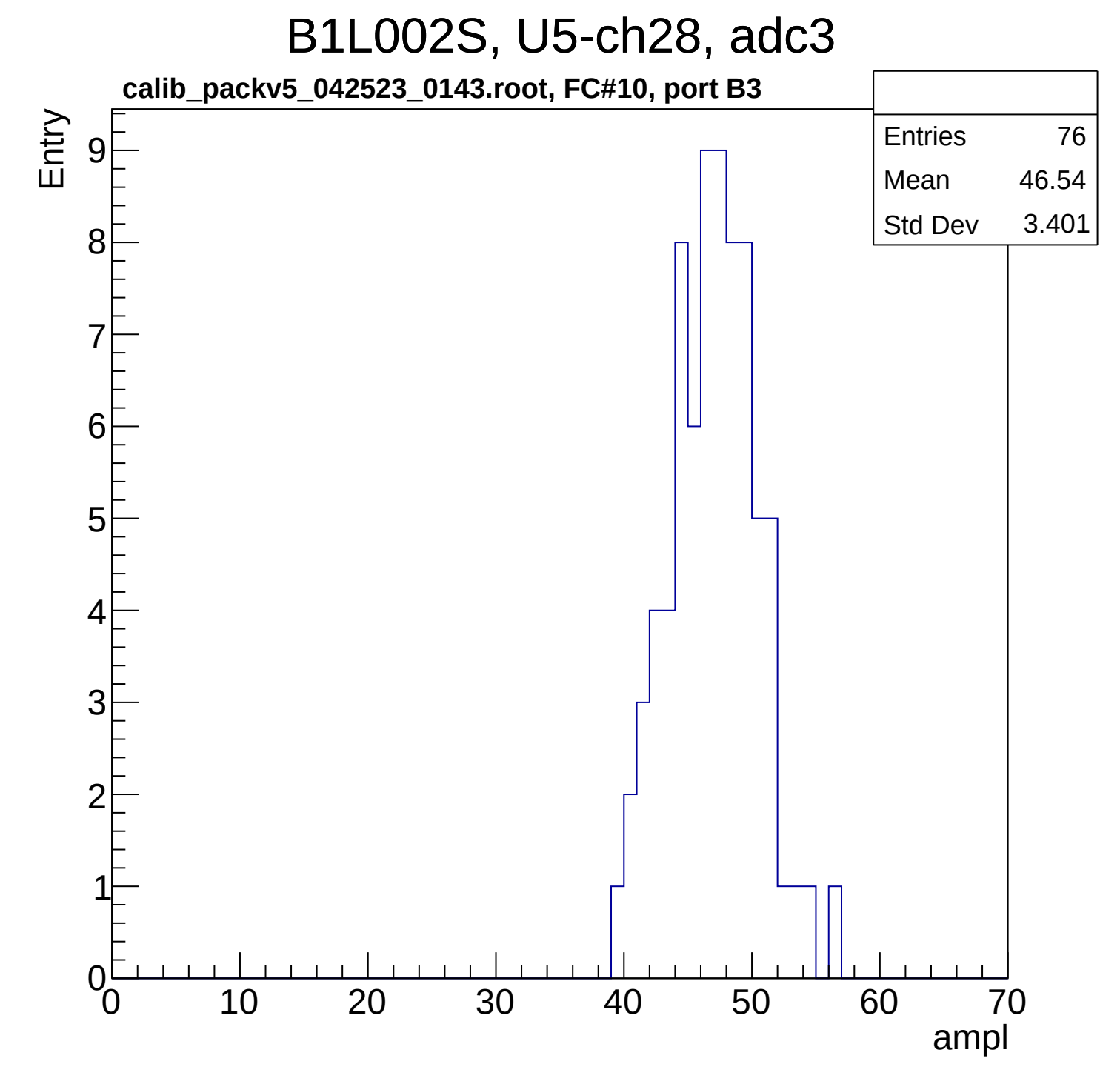
B1L002S, U5-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.54
Std Dev 3.401
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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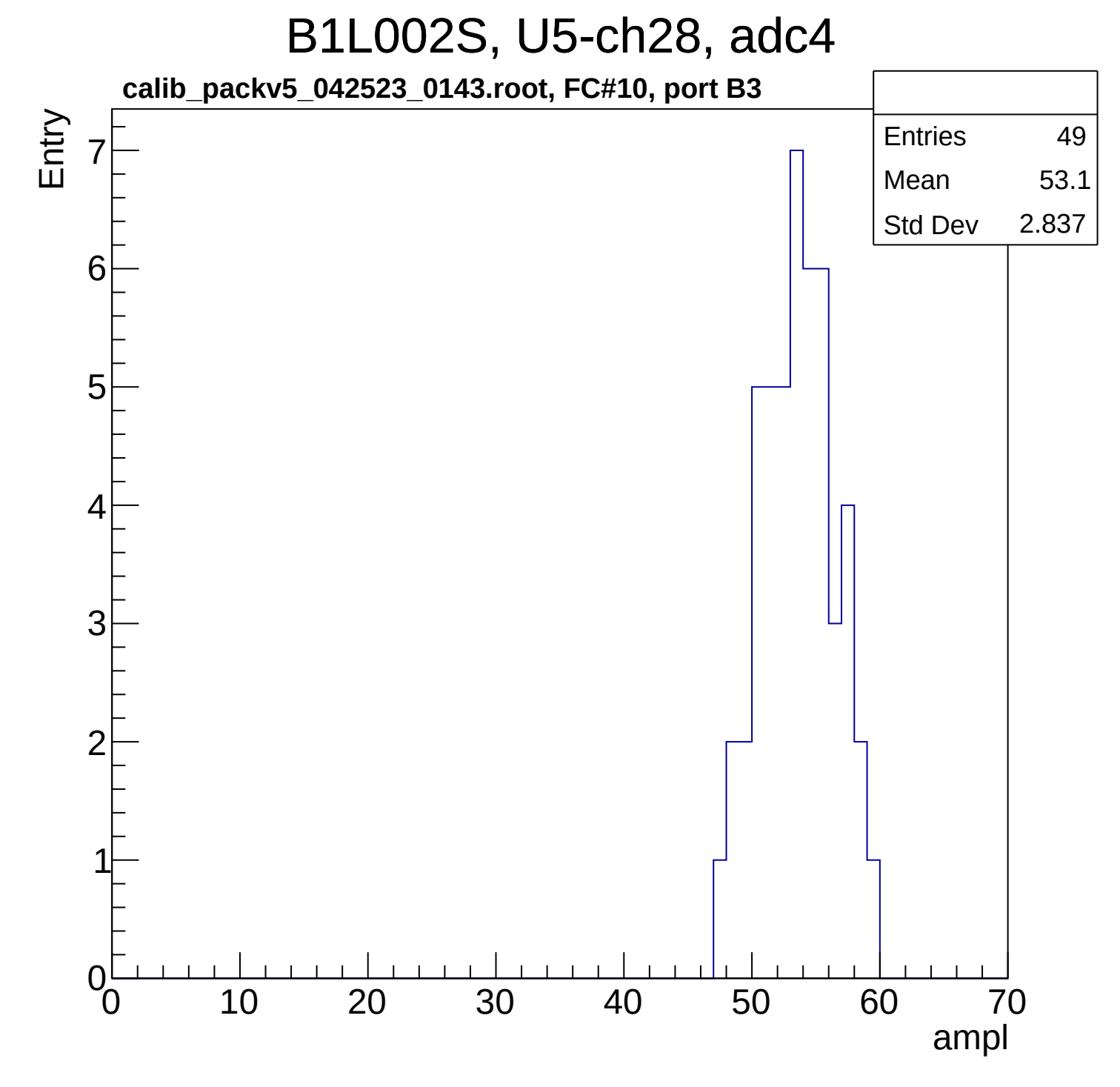
B1L002S, U5-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.1
Std Dev 2.837
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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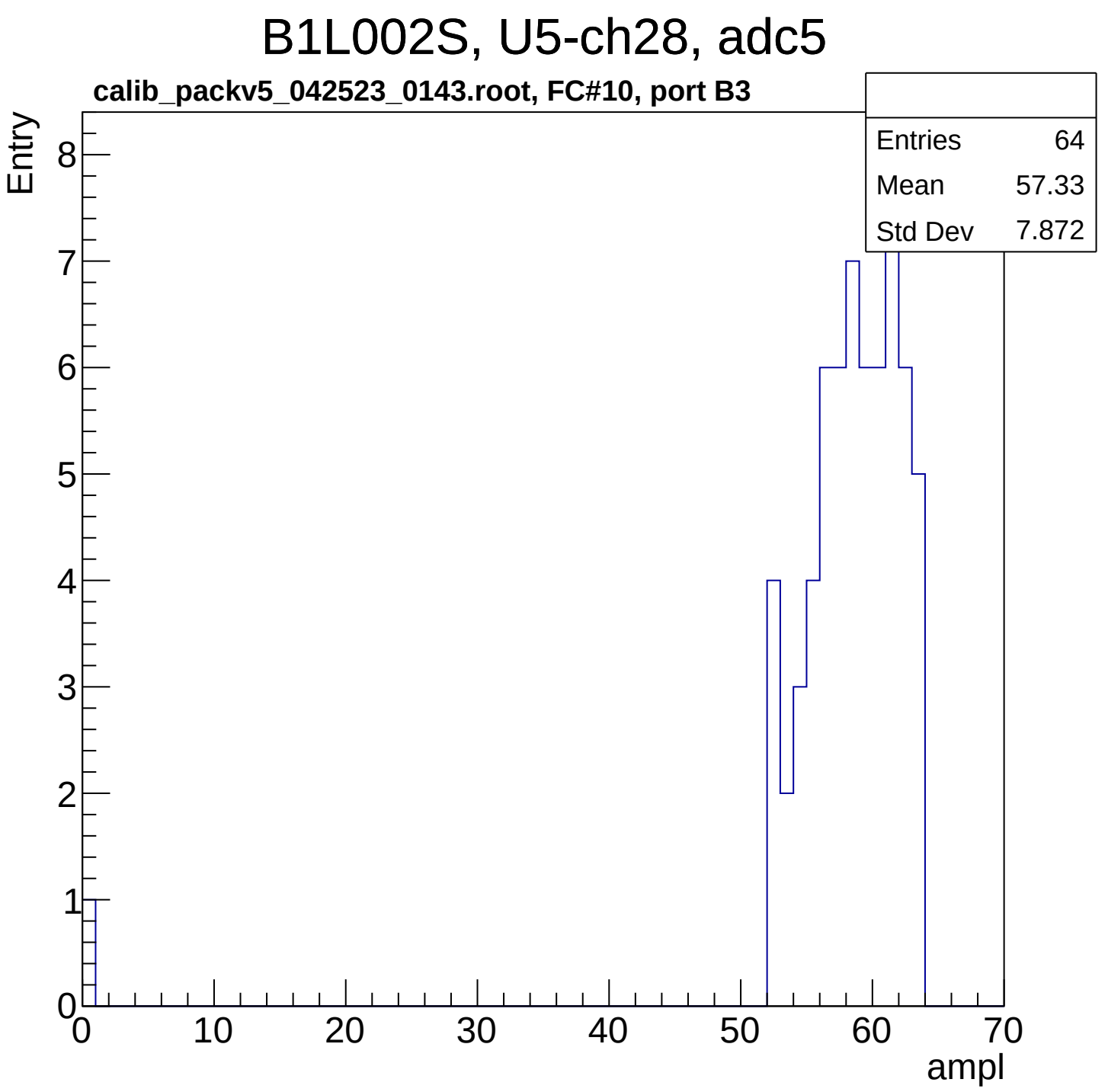
B1L002S, U5-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.33
Std Dev 7.872
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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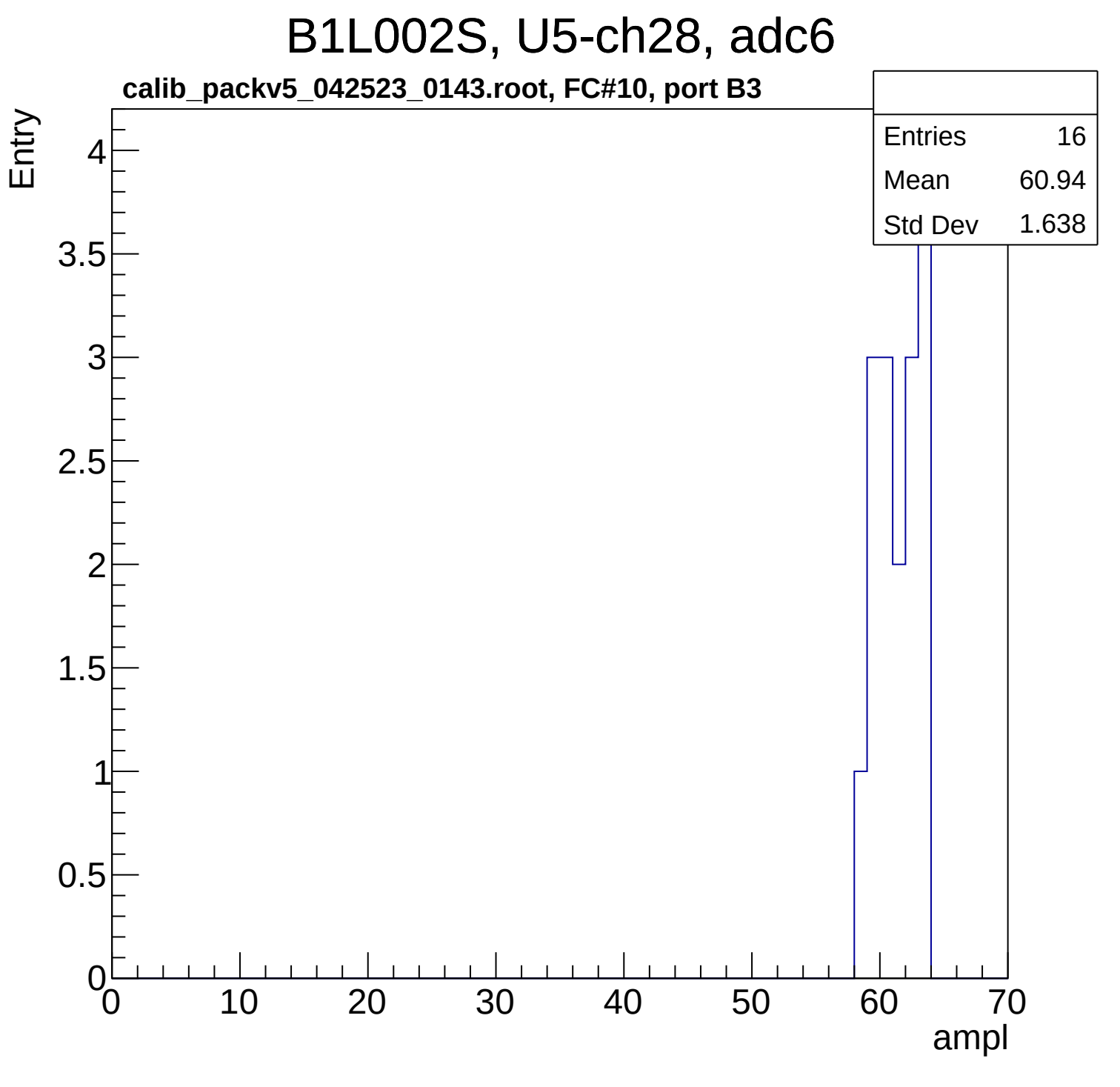
B1L002S, U5-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 60.94
Std Dev 1.638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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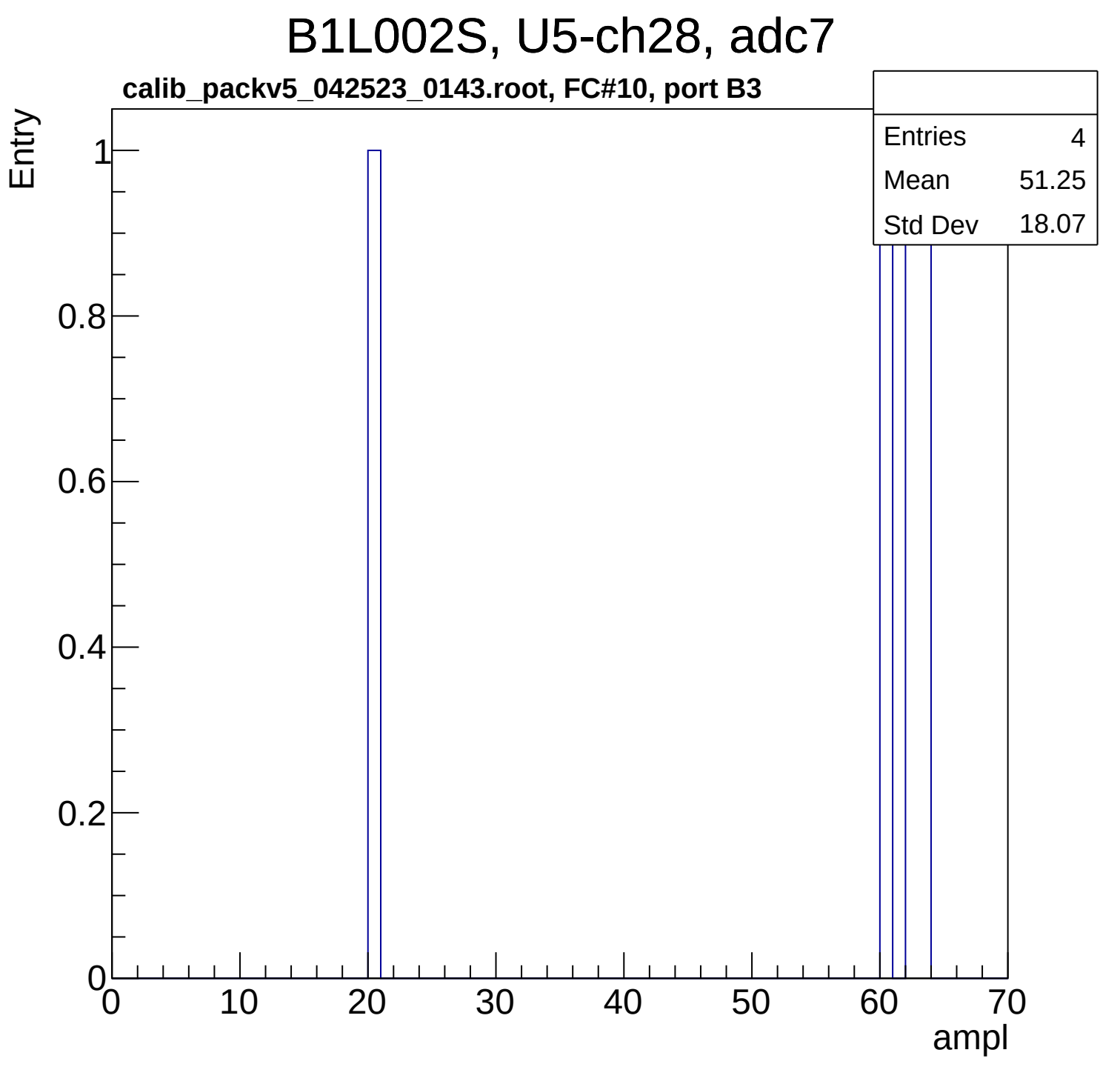
B1L002S, U5-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.25
Std Dev 18.07
Entry
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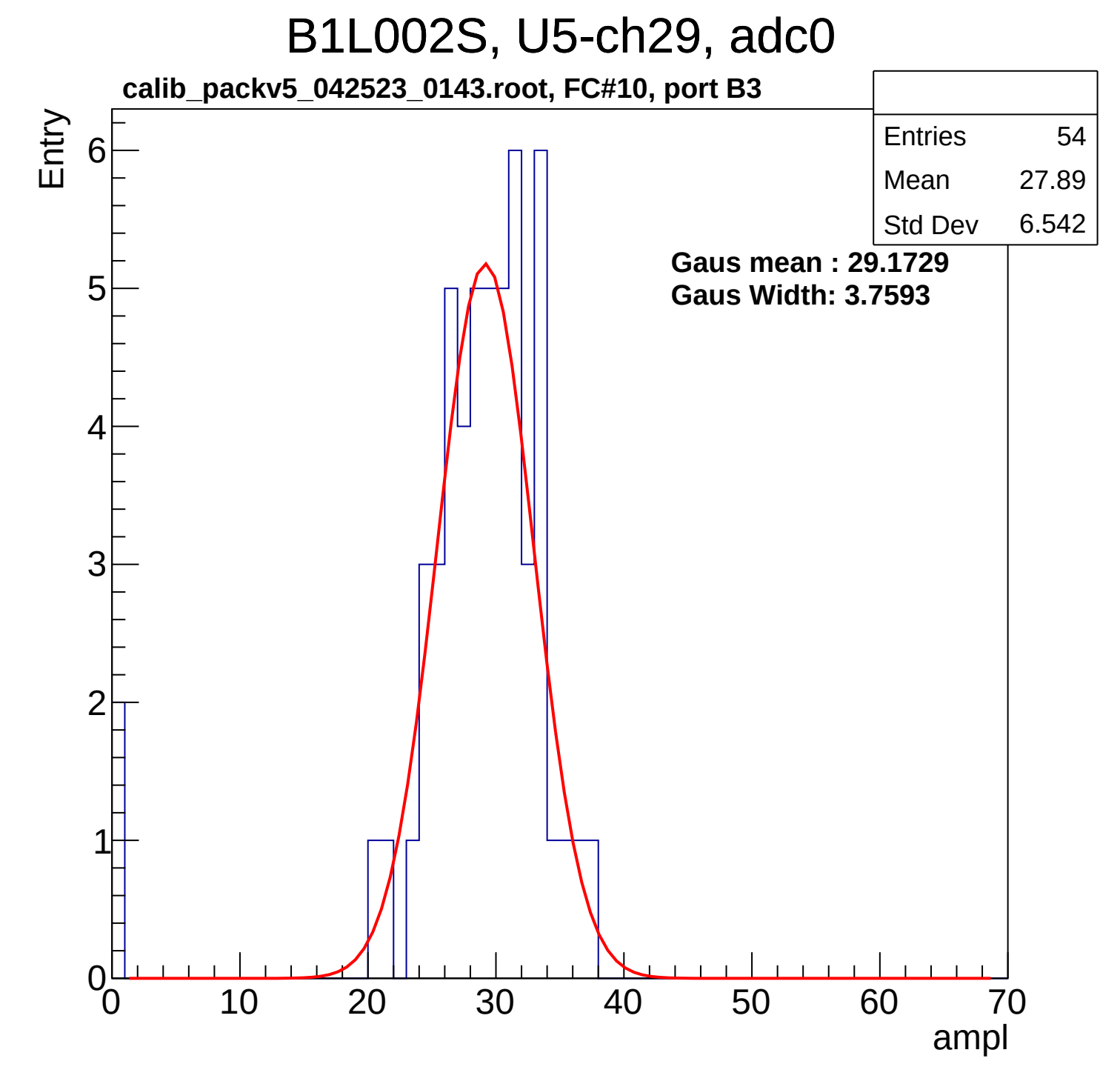
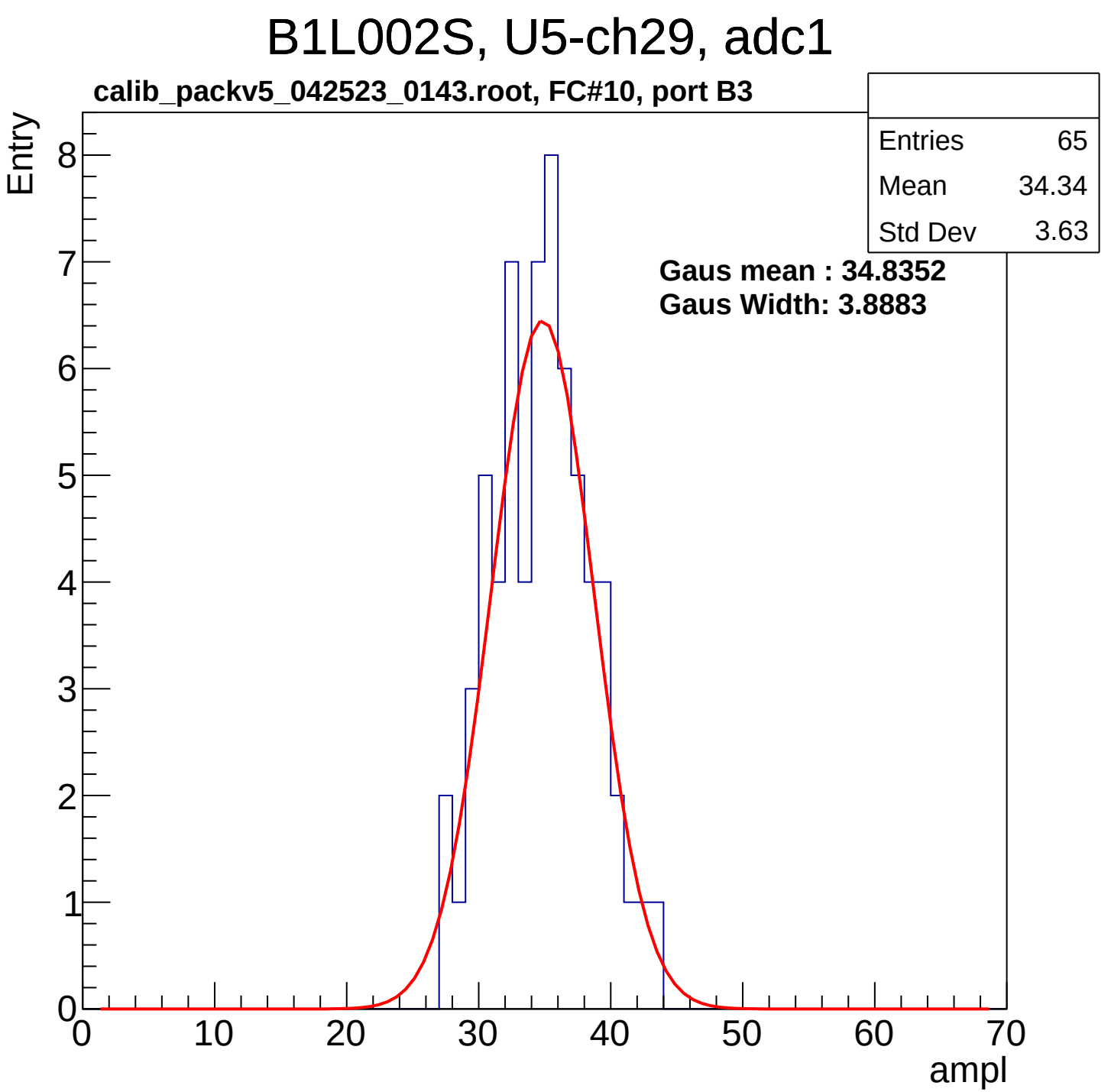
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.89
Std Dev 6.542
Gaus mean : 29.1729
Gaus Width: 3.7593
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U5-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.34
Std Dev 3.63
Gaus mean : 34.8352
Gaus Width: 3.8883
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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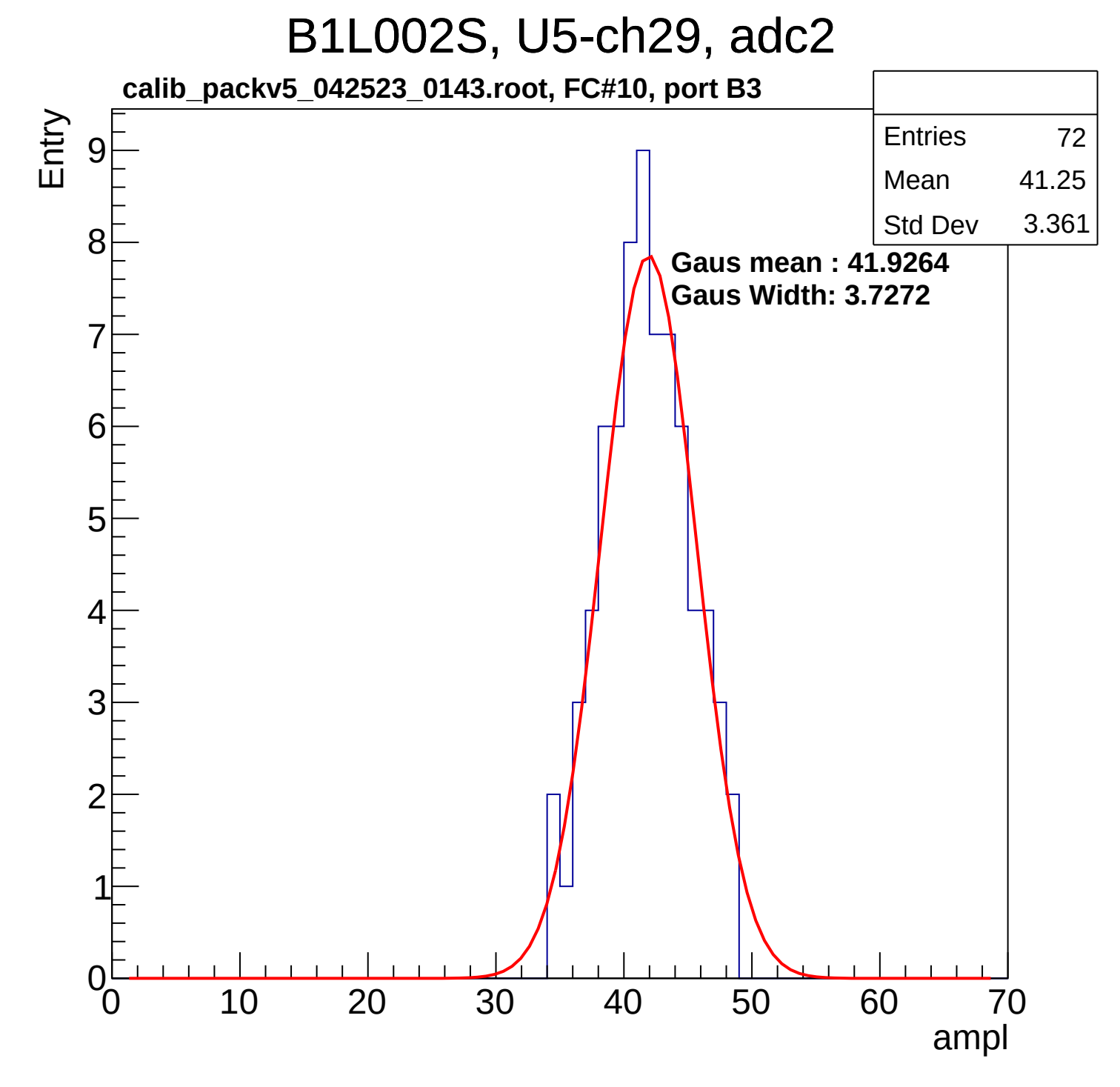
B1L002S, U5-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.25
Std Dev 3.361
Gaus mean : 41.9264
Gaus Width: 3.7272
Entry
ampl
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10
20
30
40
50
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1
2
3
4
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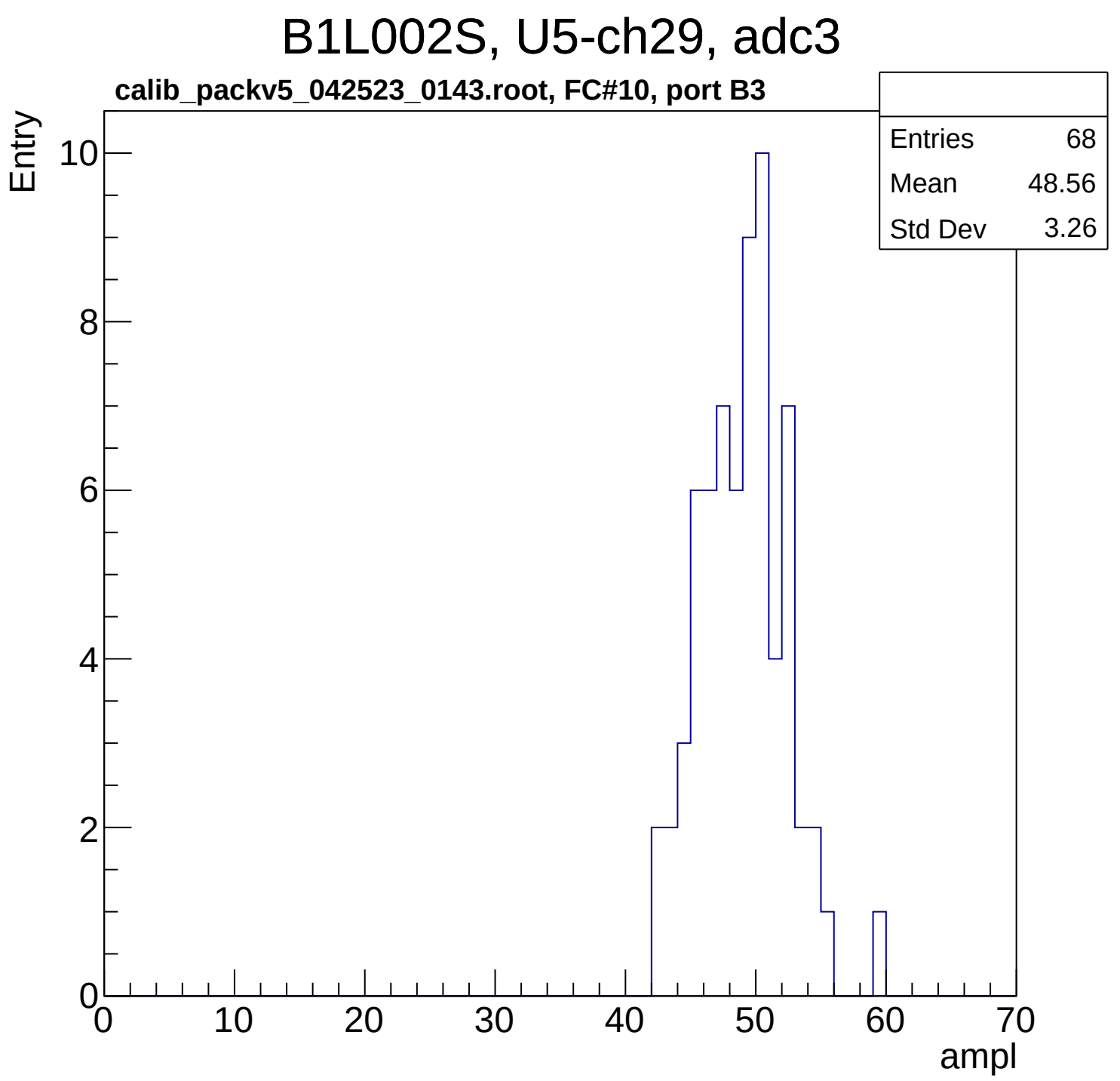
6
7
8
9


B1L002S, U5-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.56
Std Dev 3.26
Entry
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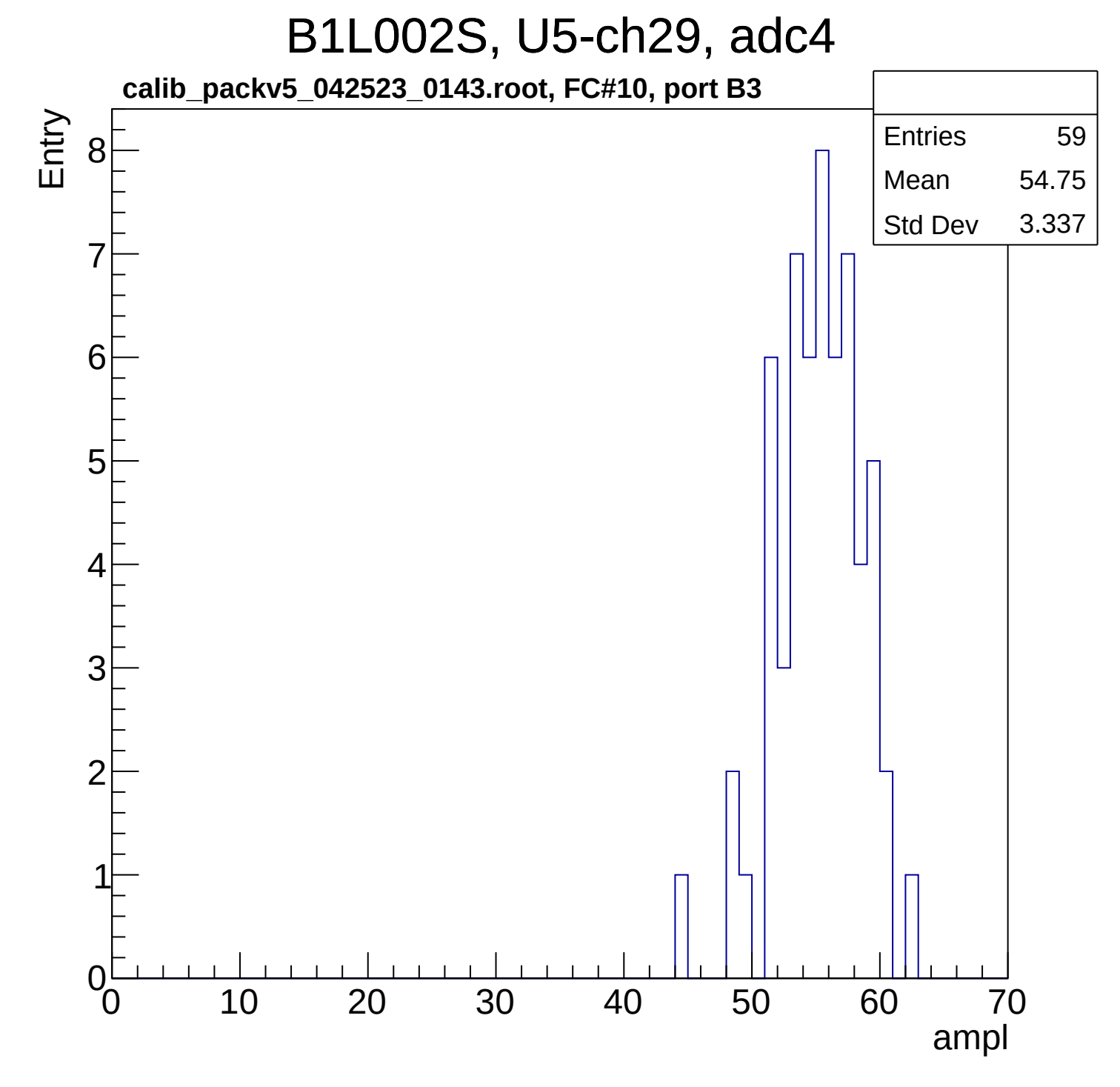
50
60
70
ampl


B1L002S, U5-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.75
Std Dev 3.337
Entry
ampl
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0
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40
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60
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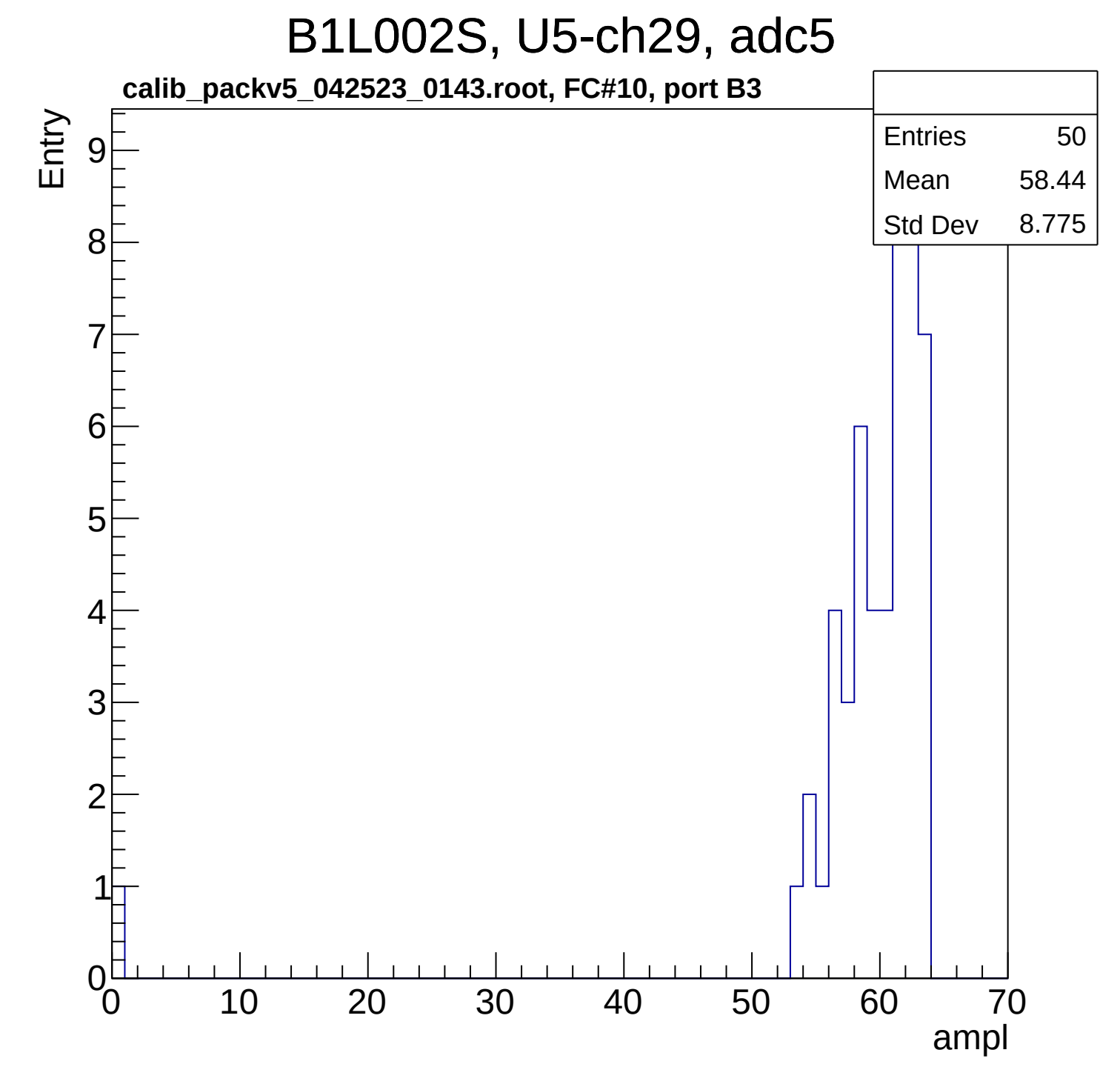
B1L002S, U5-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.44
Std Dev 8.775
Entry
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7
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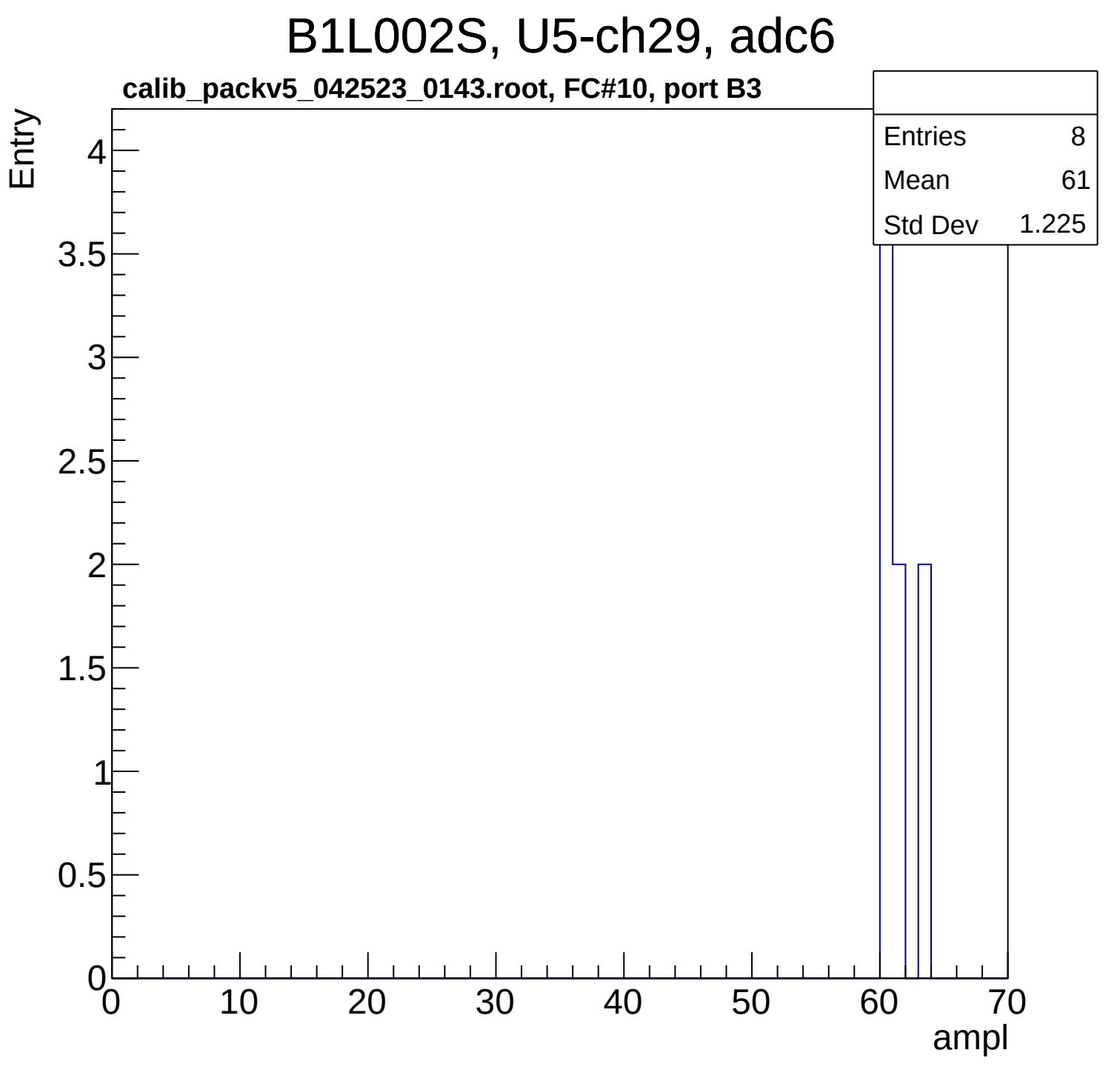
50
60
70
ampl


B1L002S, U5-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61
Std Dev 1.225
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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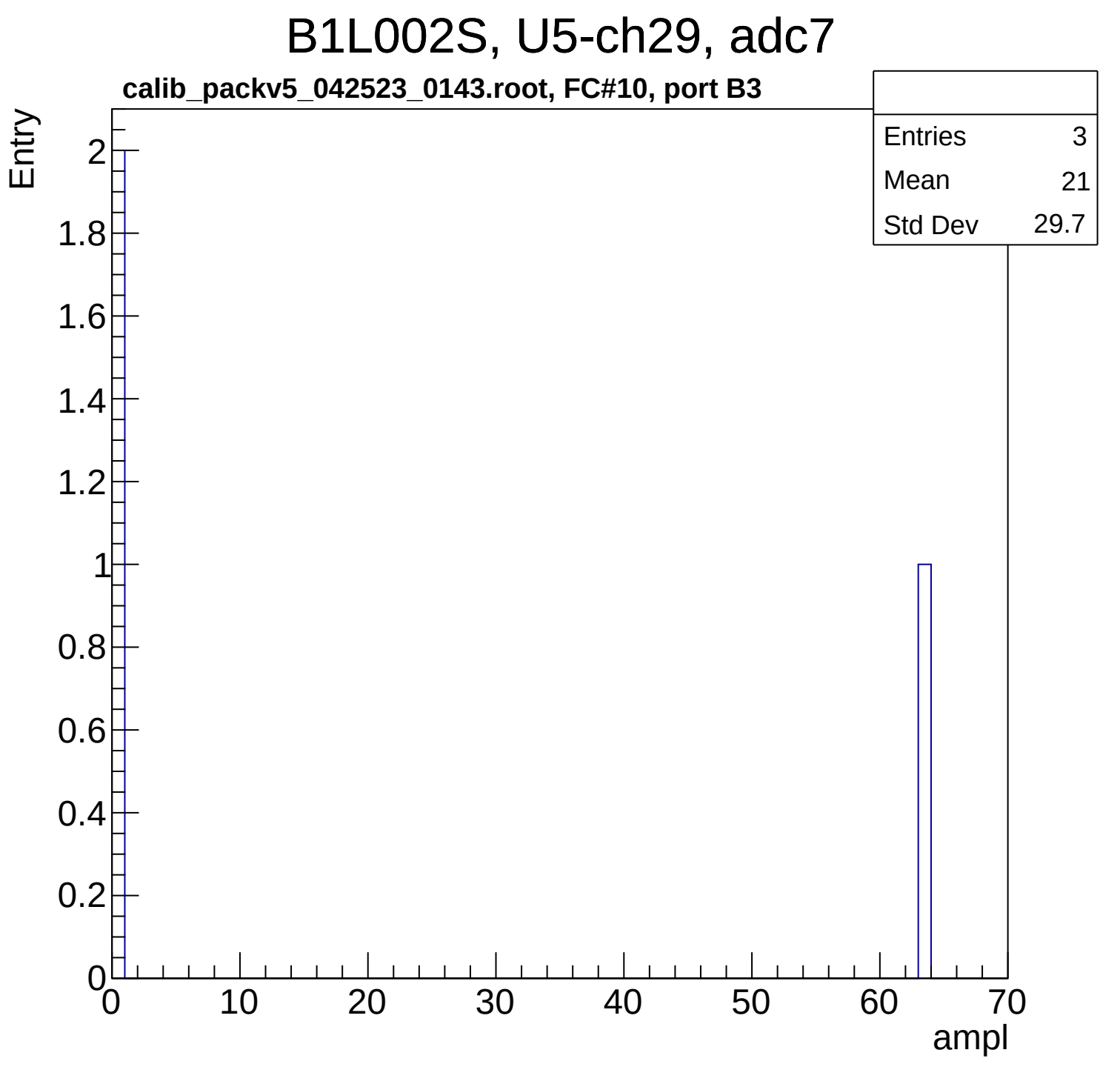
B1L002S, U5-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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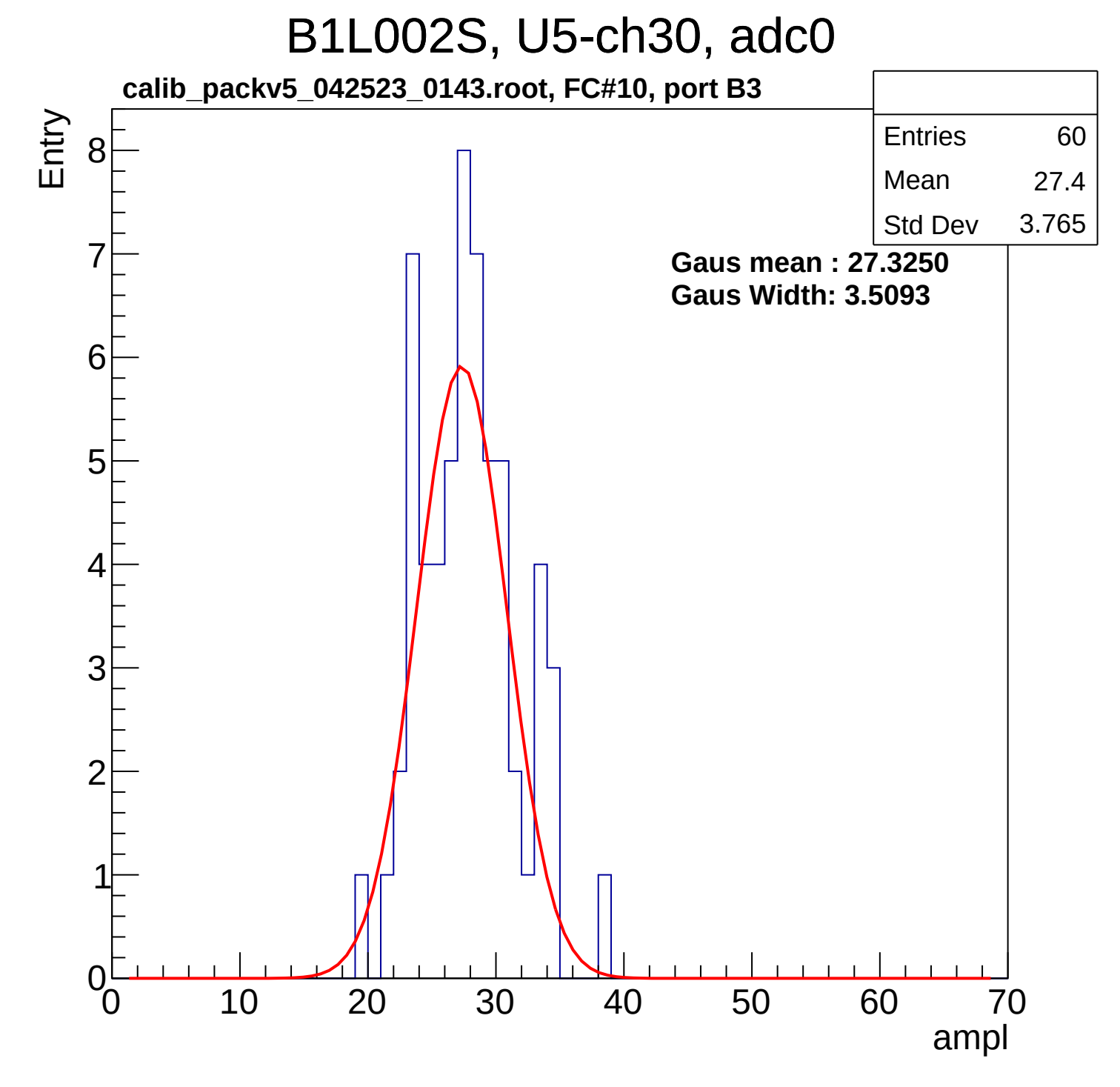
B1L002S, U5-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.4
Std Dev 3.765
Gaus mean : 27.3250
Gaus Width: 3.5093
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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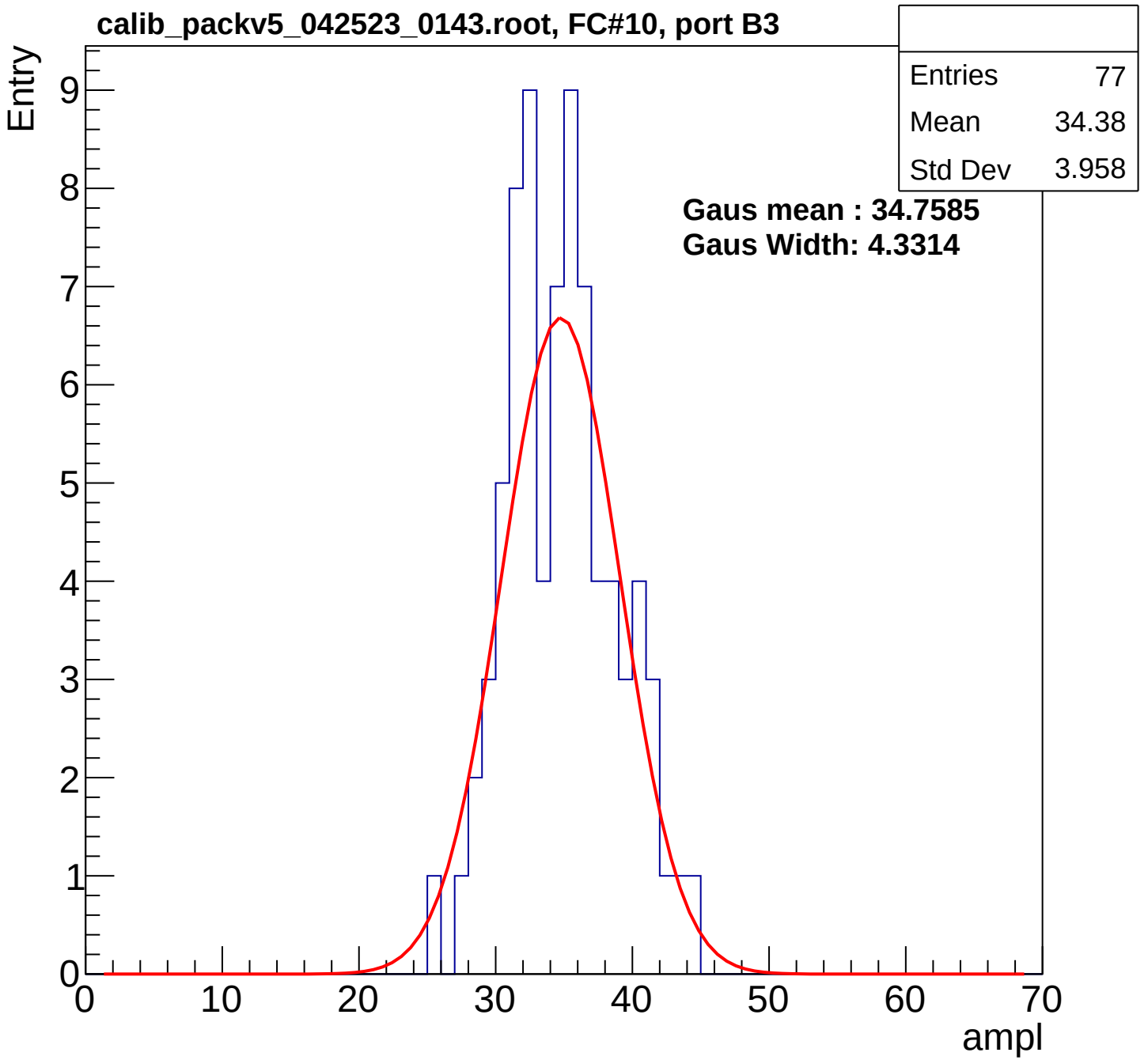
60
70


B1L002S, U5-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.38
Std Dev 3.958
Gaus mean : 34.7585
Gaus Width: 4.3314
Entry
ampl
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2
3
4
5
6
7
8
9
0
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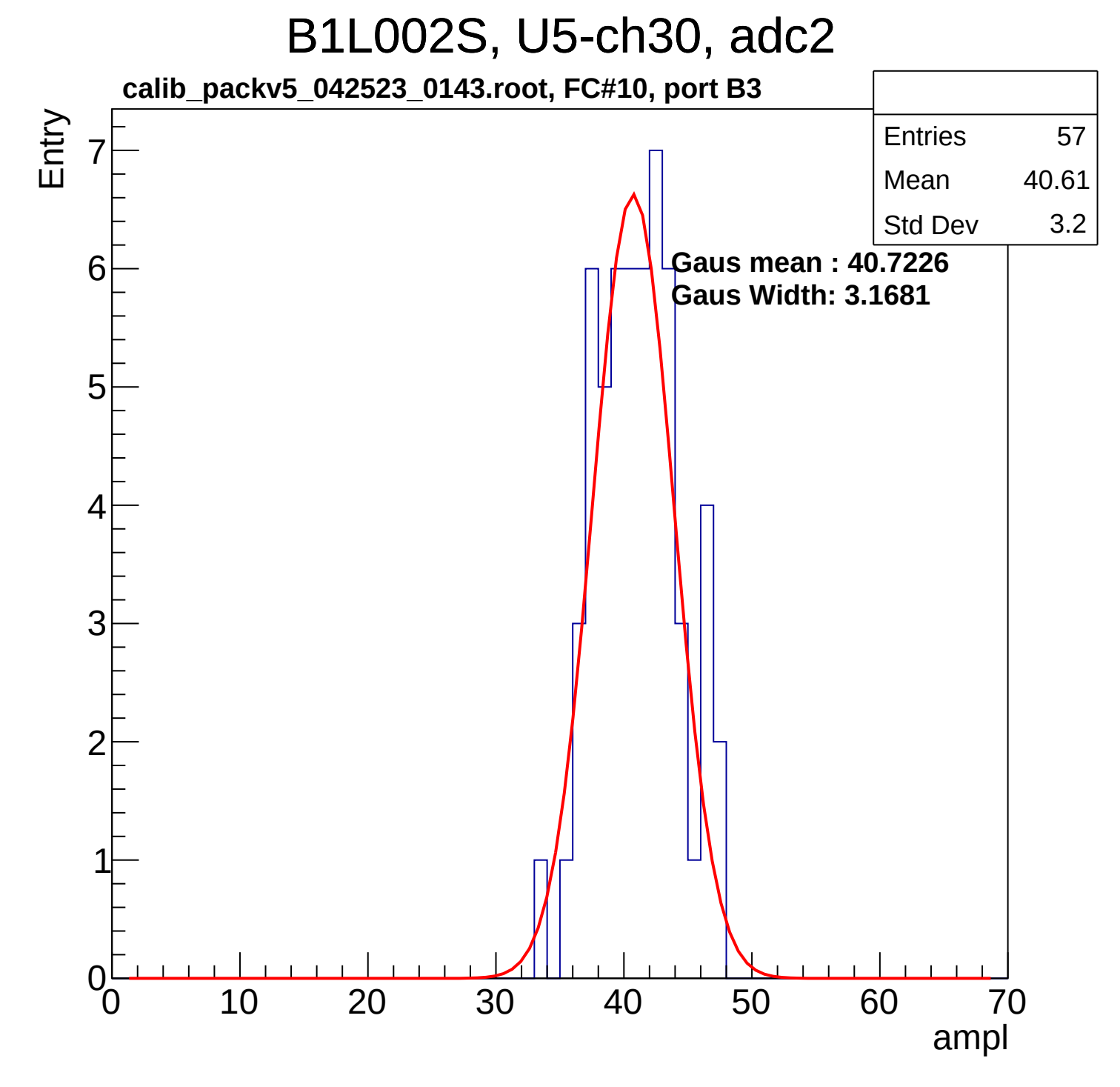
60
70


B1L002S, U5-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.61
Std Dev 3.2
Gaus mean : 40.7226
Gaus Width: 3.1681
Entry
ampl
0
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2
3
4
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6
7
0
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40
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60
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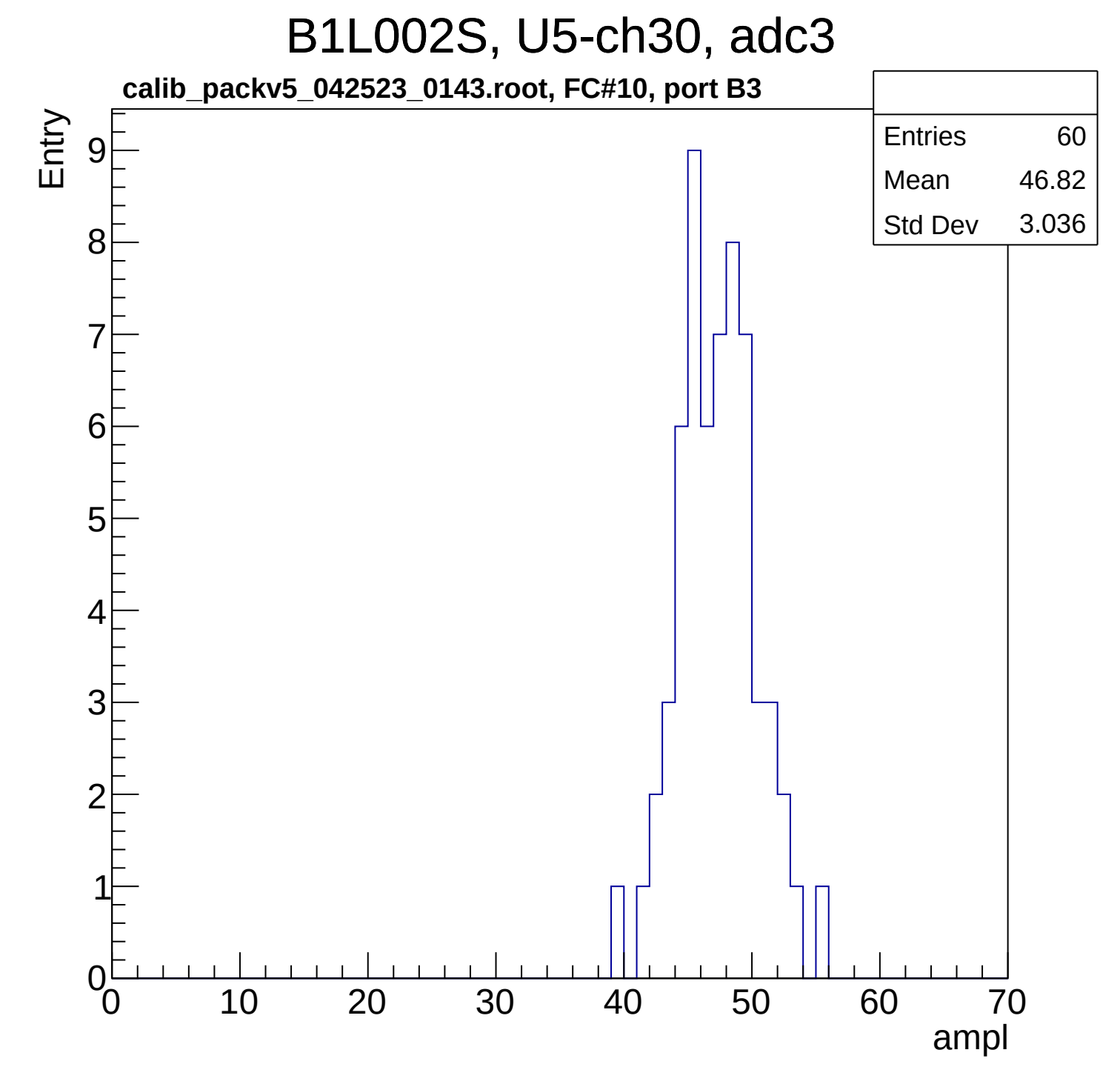
B1L002S, U5-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.82
Std Dev 3.036
Entry
ampl
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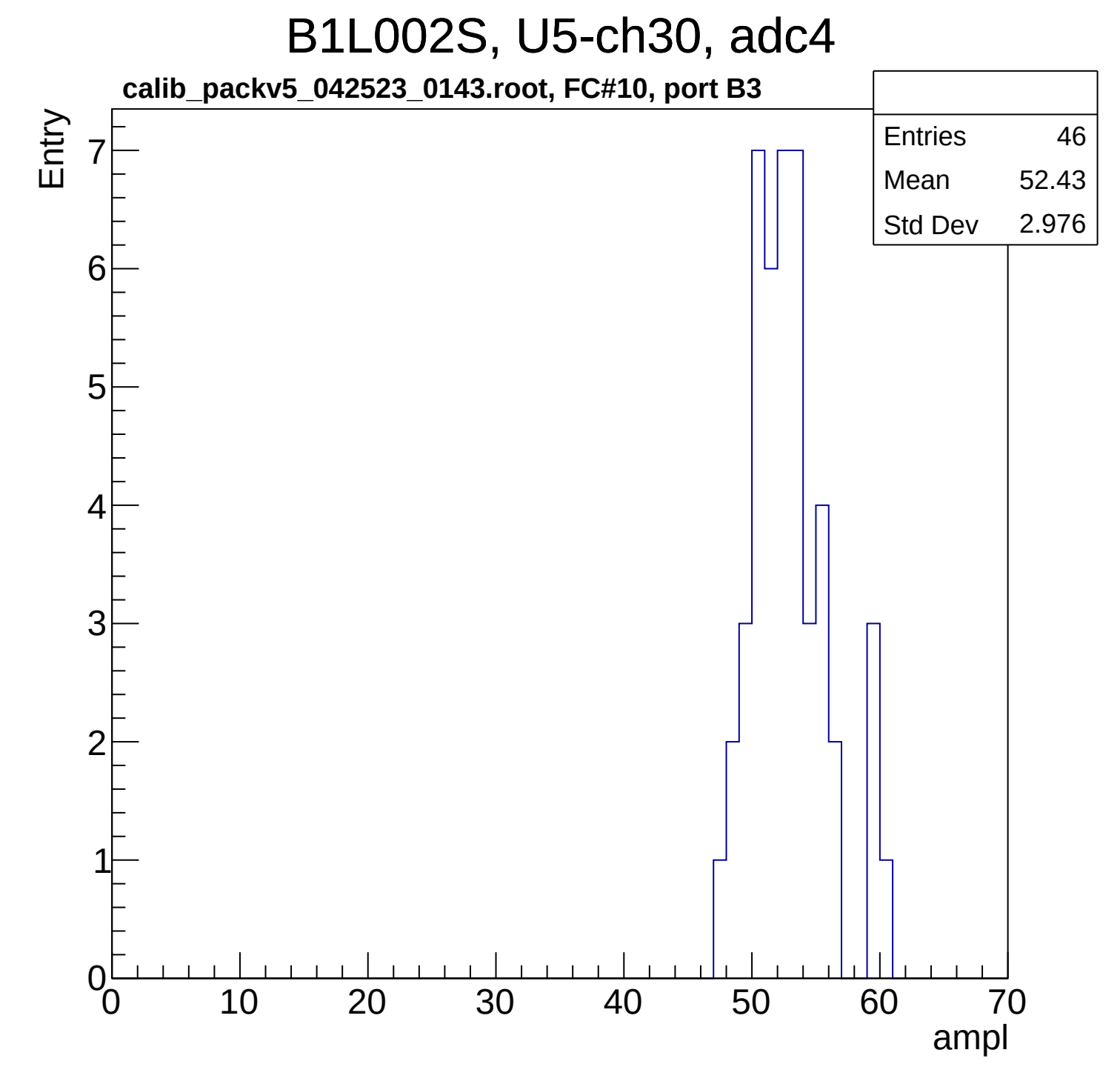
B1L002S, U5-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 52.43
Std Dev 2.976
Entry
ampl
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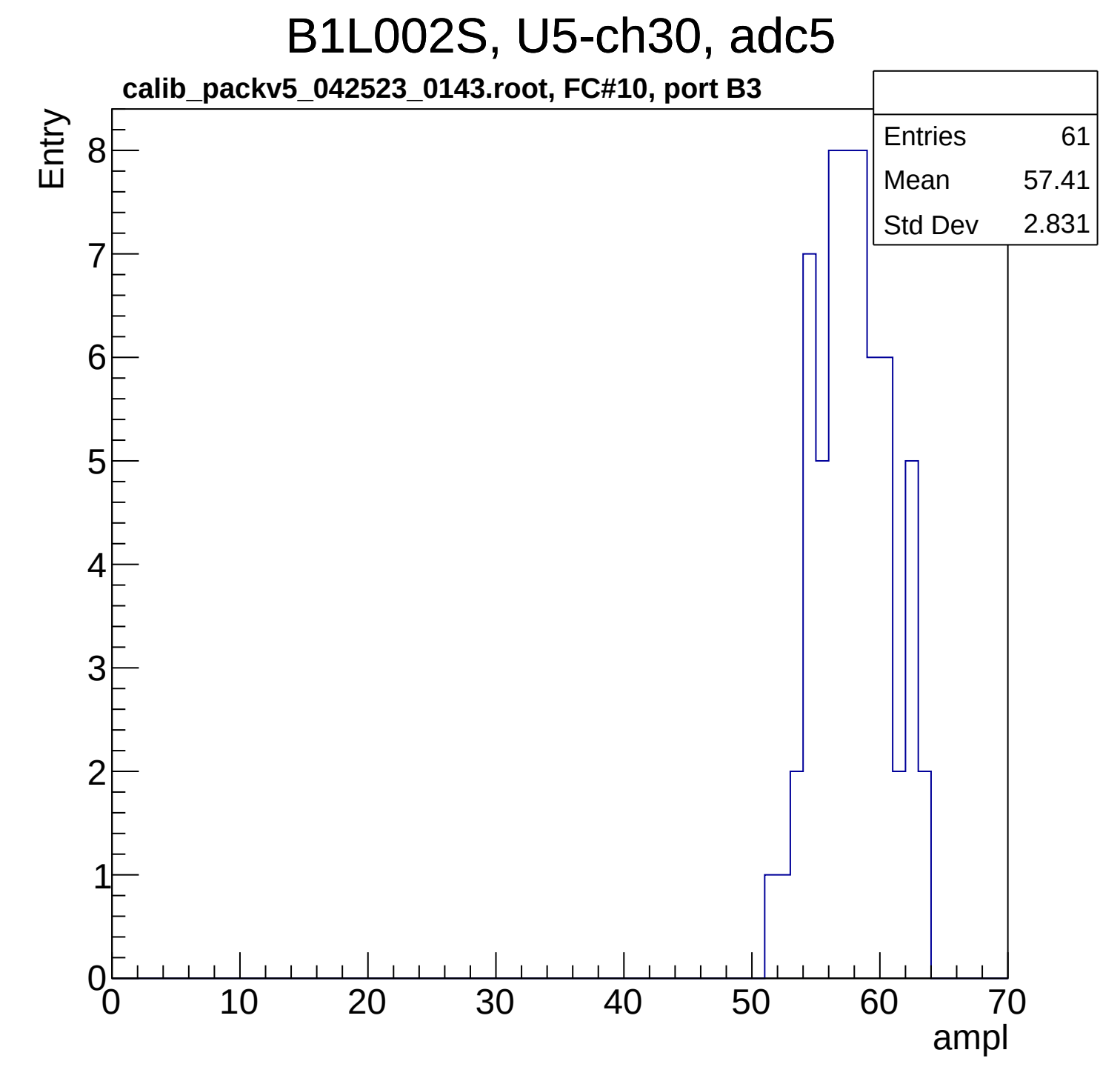
B1L002S, U5-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.41
Std Dev 2.831
Entry
ampl
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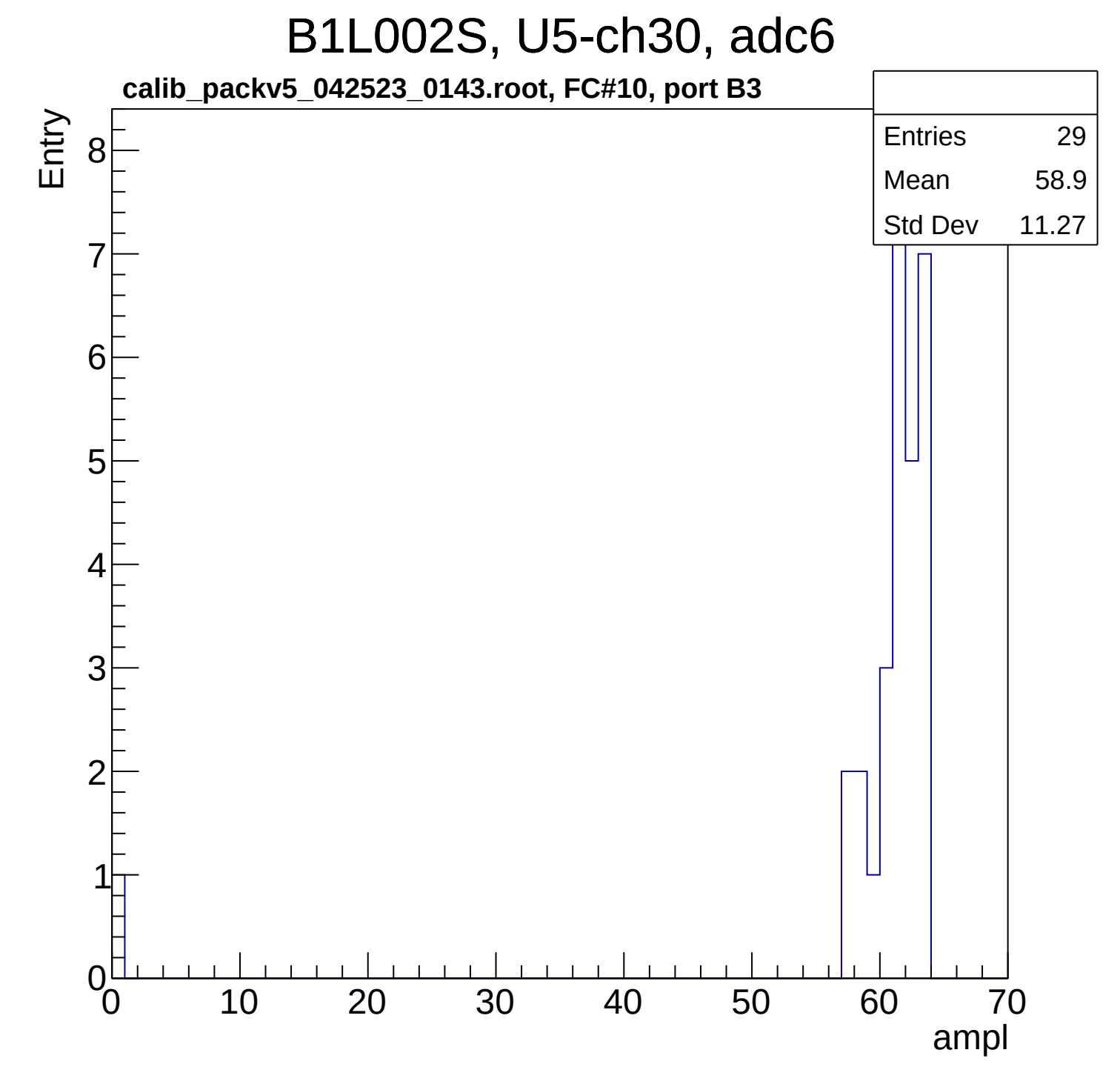
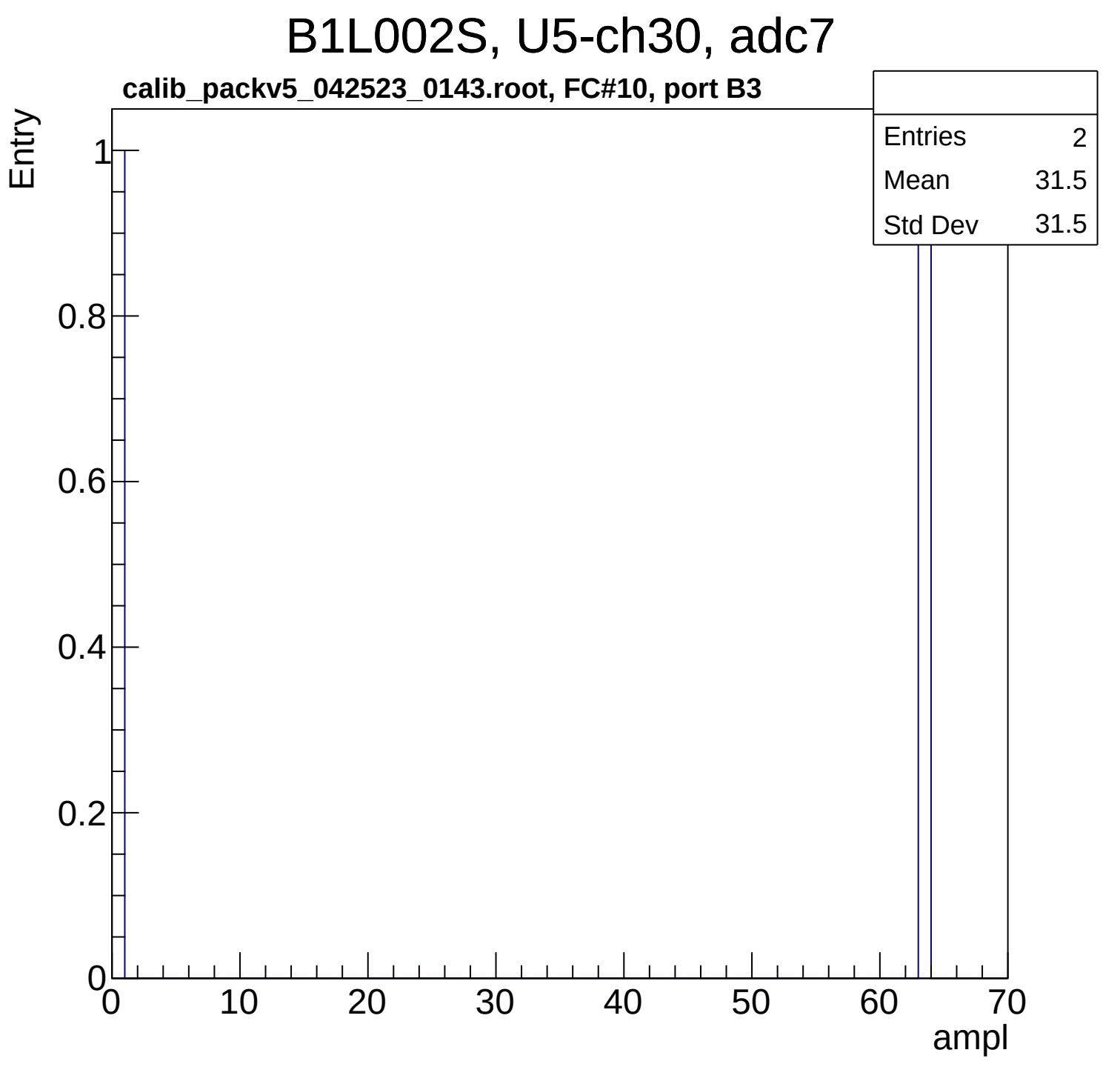

B1L002S, U5-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.9
Std Dev 11.27
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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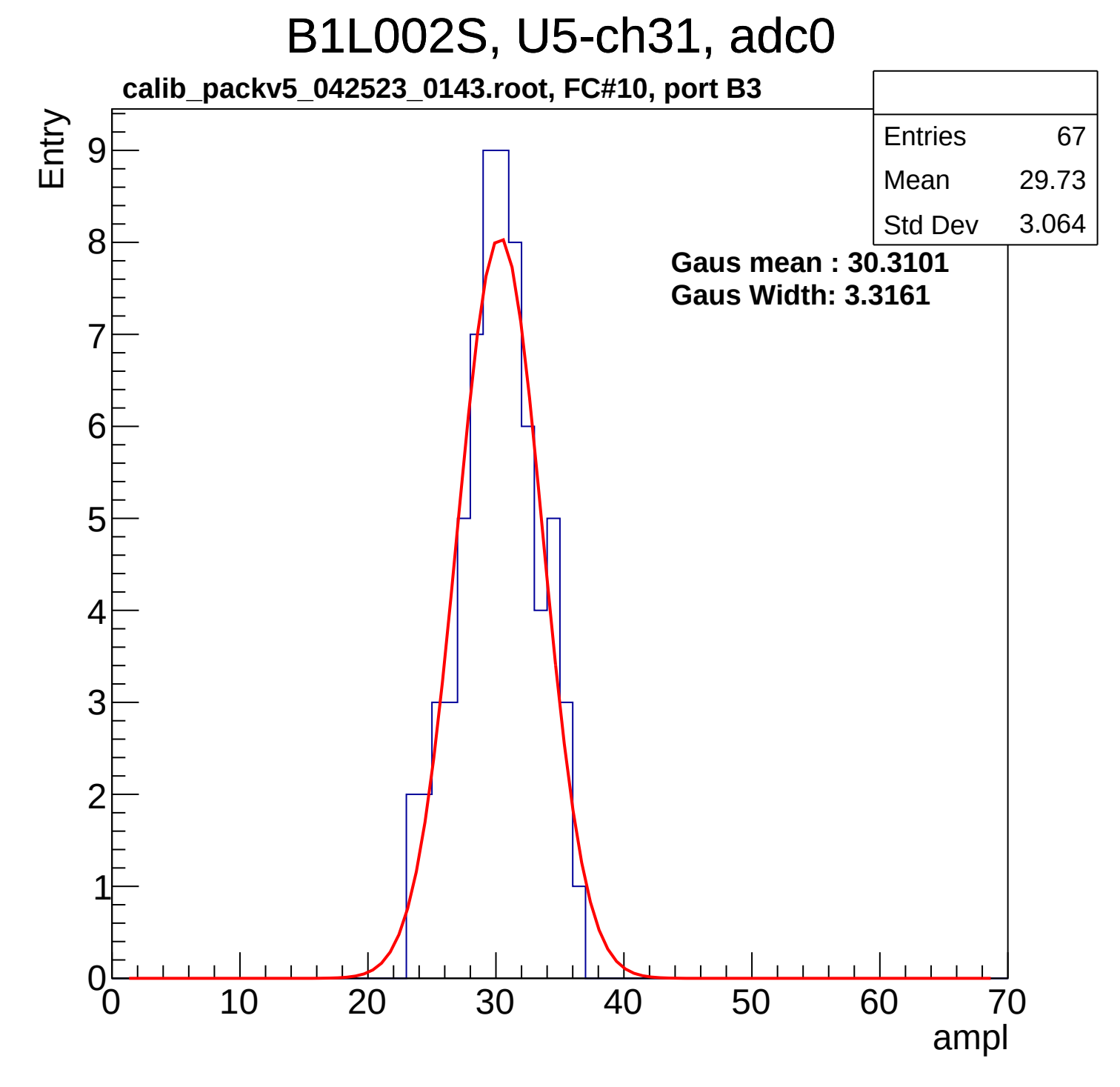
40
50
60
70
ampl


B1L002S, U5-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.73
Std Dev 3.064
Gaus mean : 30.3101
Gaus Width: 3.3161
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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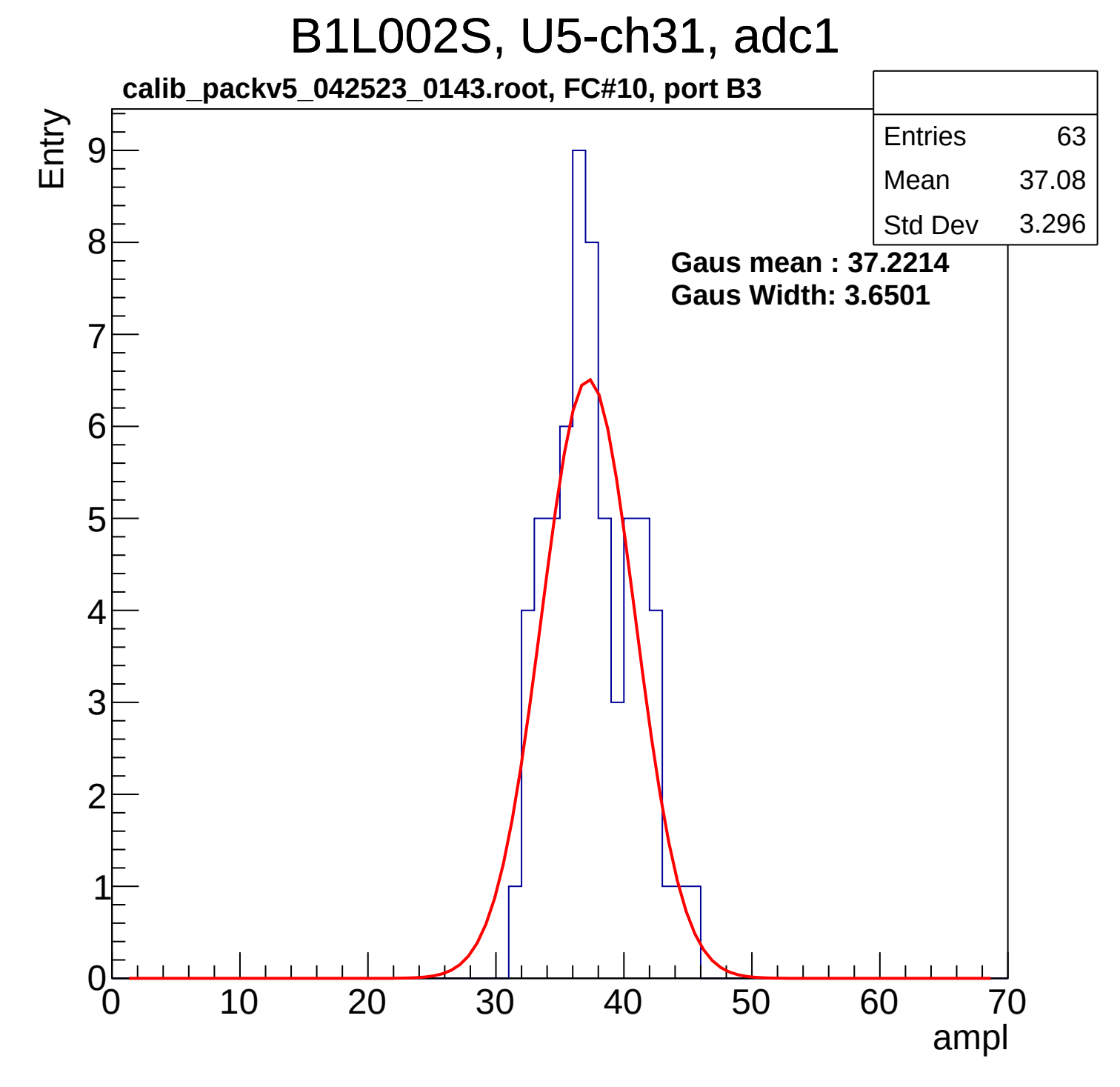
B1L002S, U5-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 37.08
Std Dev 3.296
Gaus mean : 37.2214
Gaus Width: 3.6501
Entry
ampl
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3
4
5
6
7
8
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0
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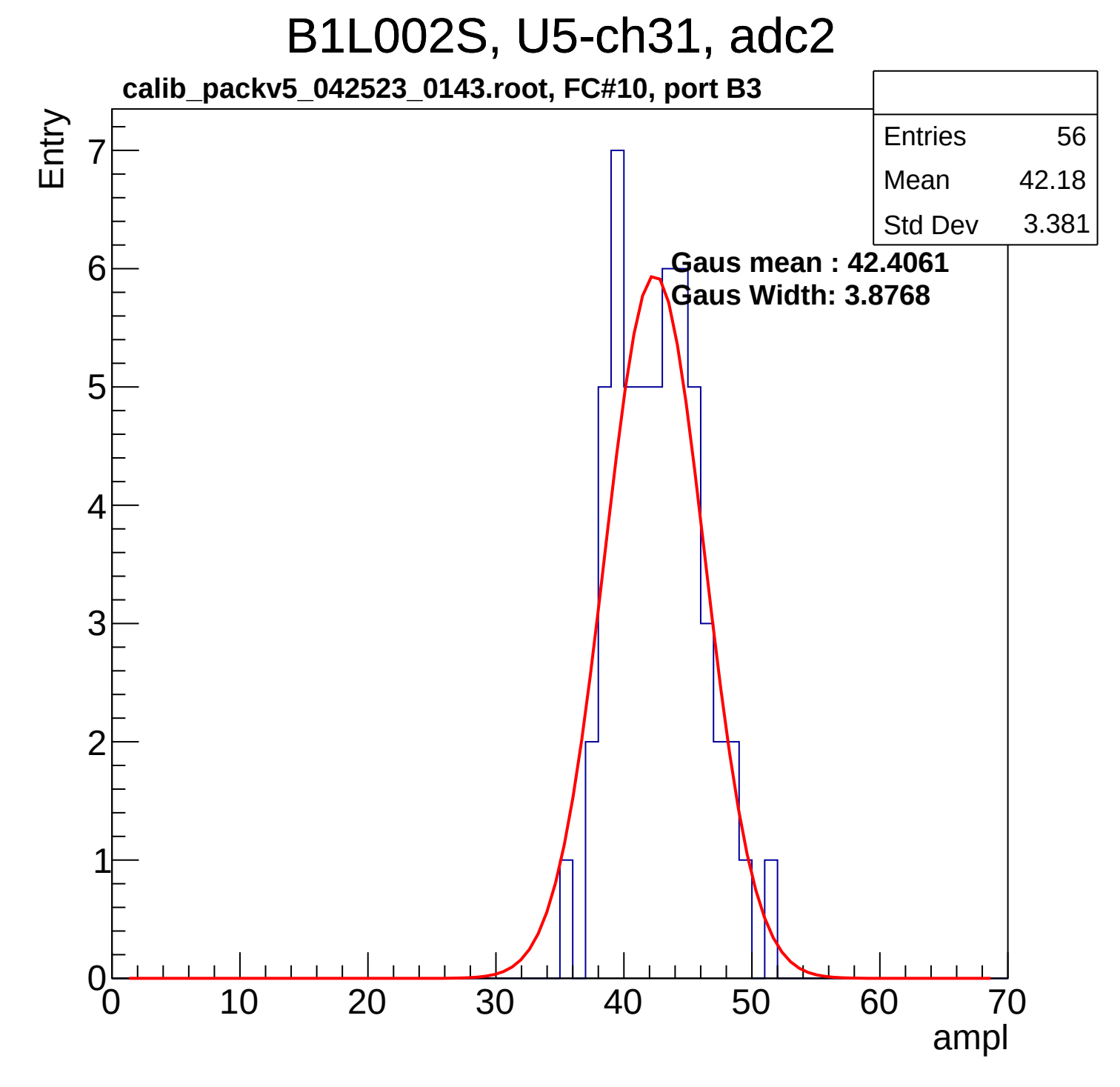
B1L002S, U5-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.18
Std Dev 3.381
Gaus mean : 42.4061
Gaus Width: 3.8768
Entry
ampl
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1
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4
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0
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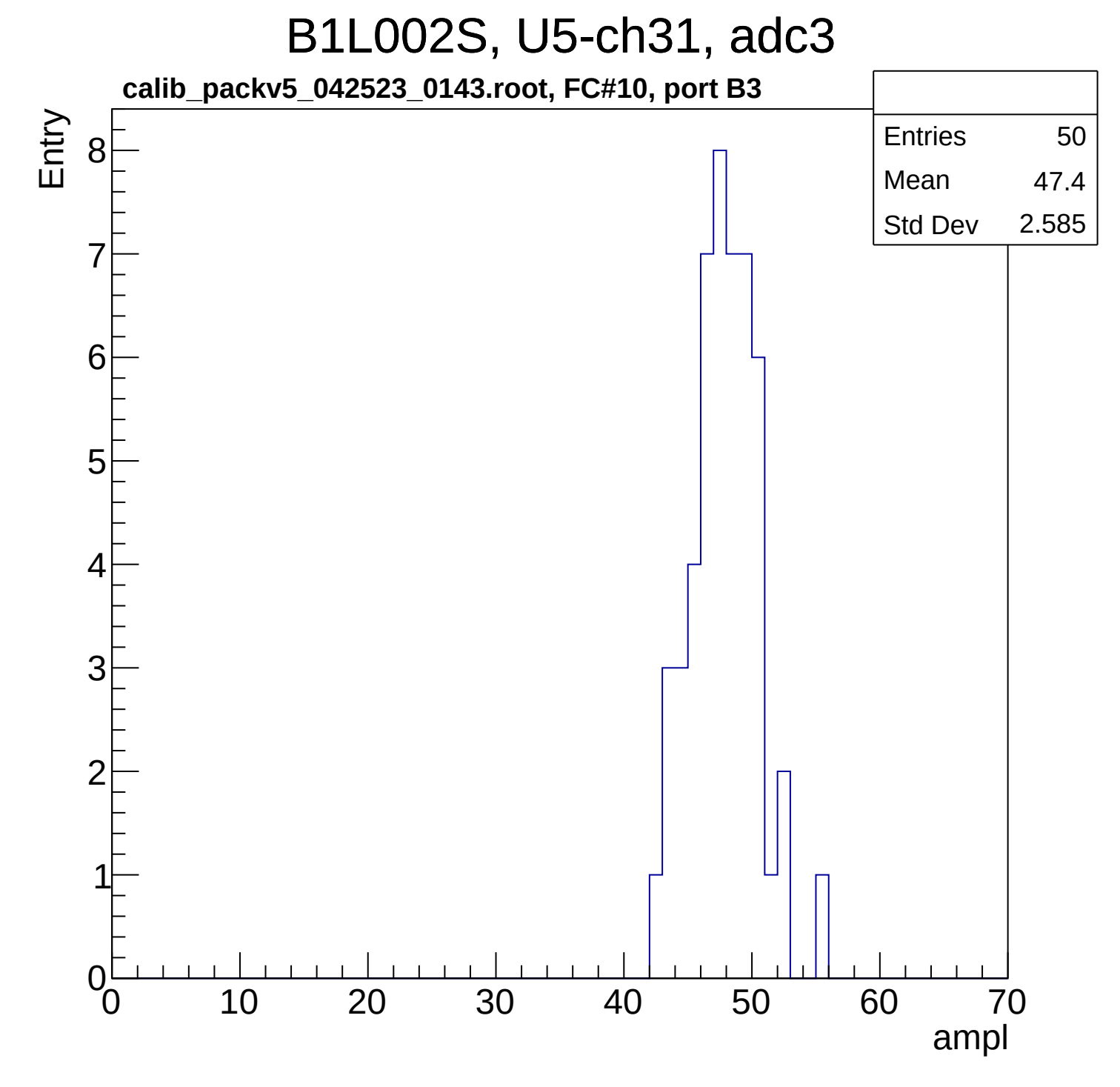
B1L002S, U5-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.4
Std Dev 2.585
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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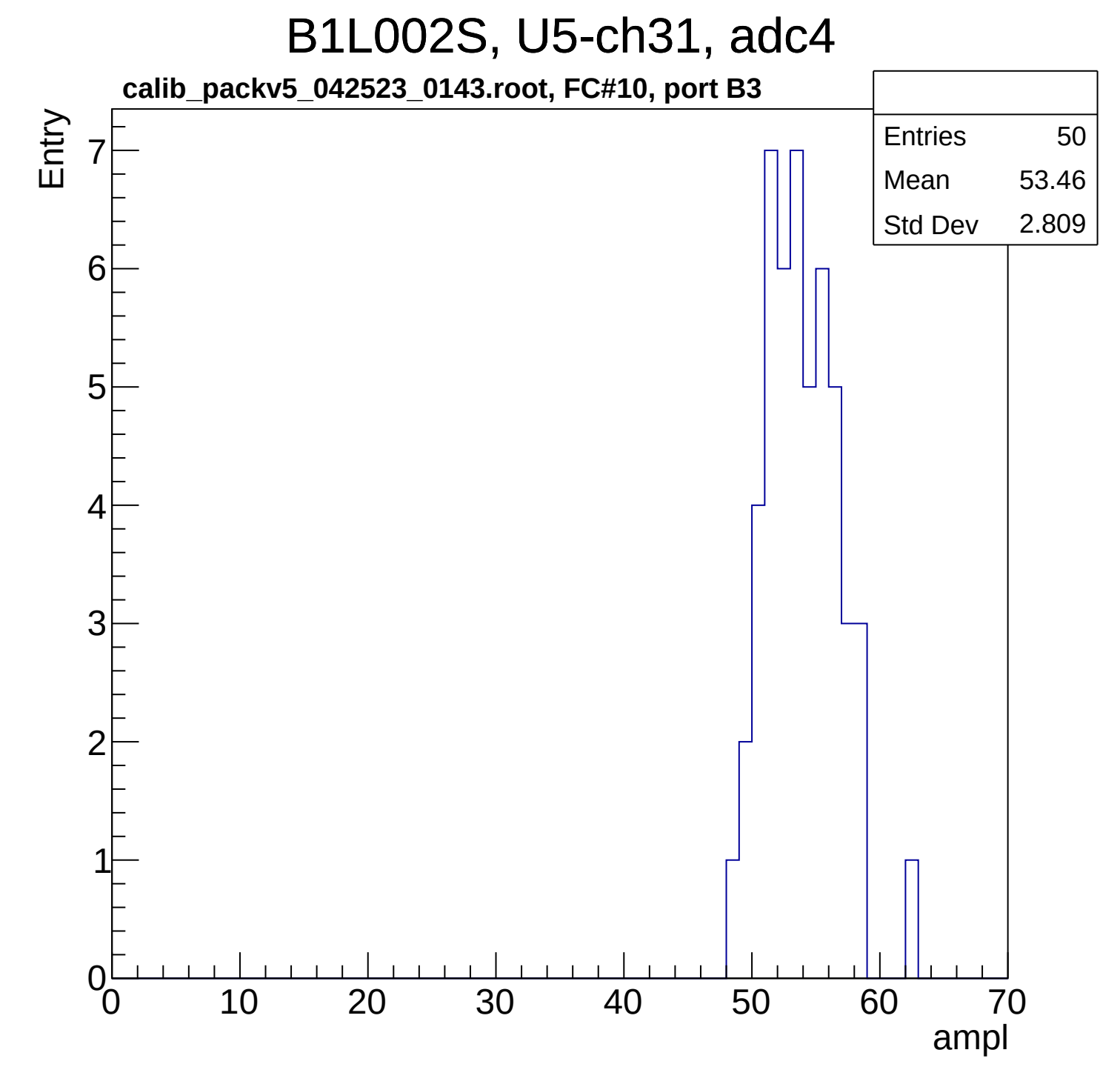
B1L002S, U5-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.46
Std Dev 2.809
Entry
ampl
0
1
2
3
4
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0
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50
60
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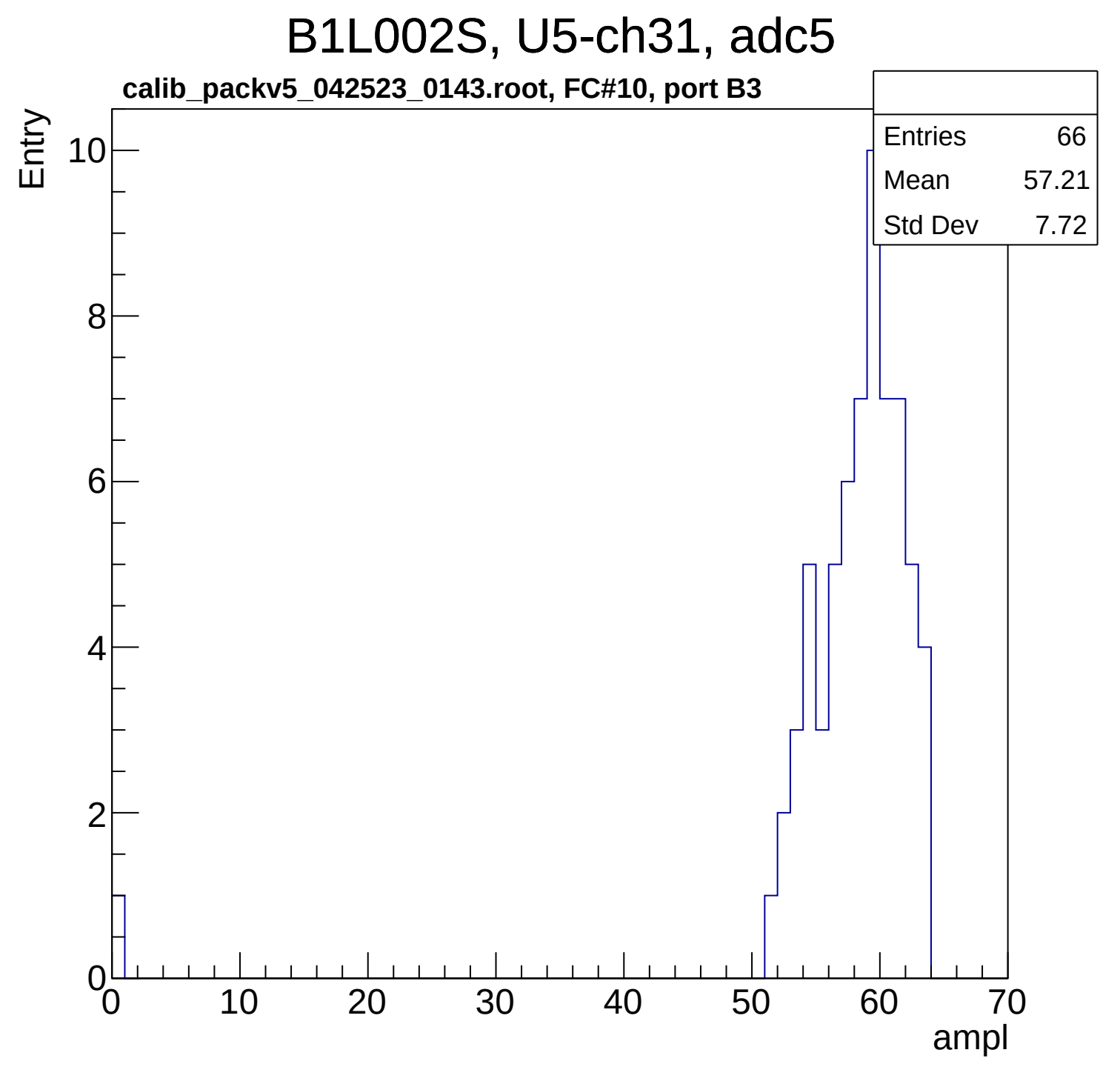
B1L002S, U5-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.21
Std Dev 7.72
Entry
0
2
4
6
8
10
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10
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30
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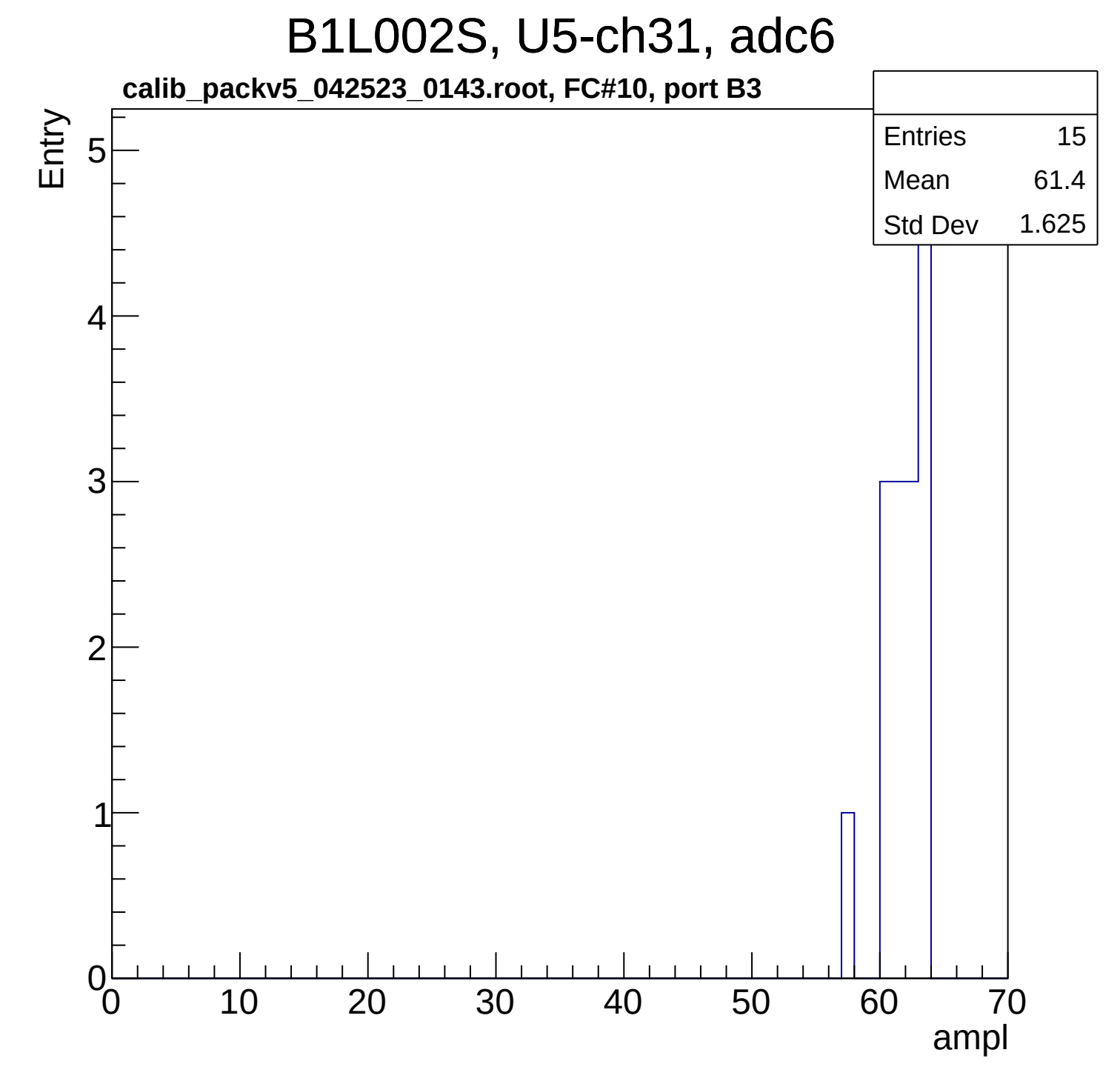
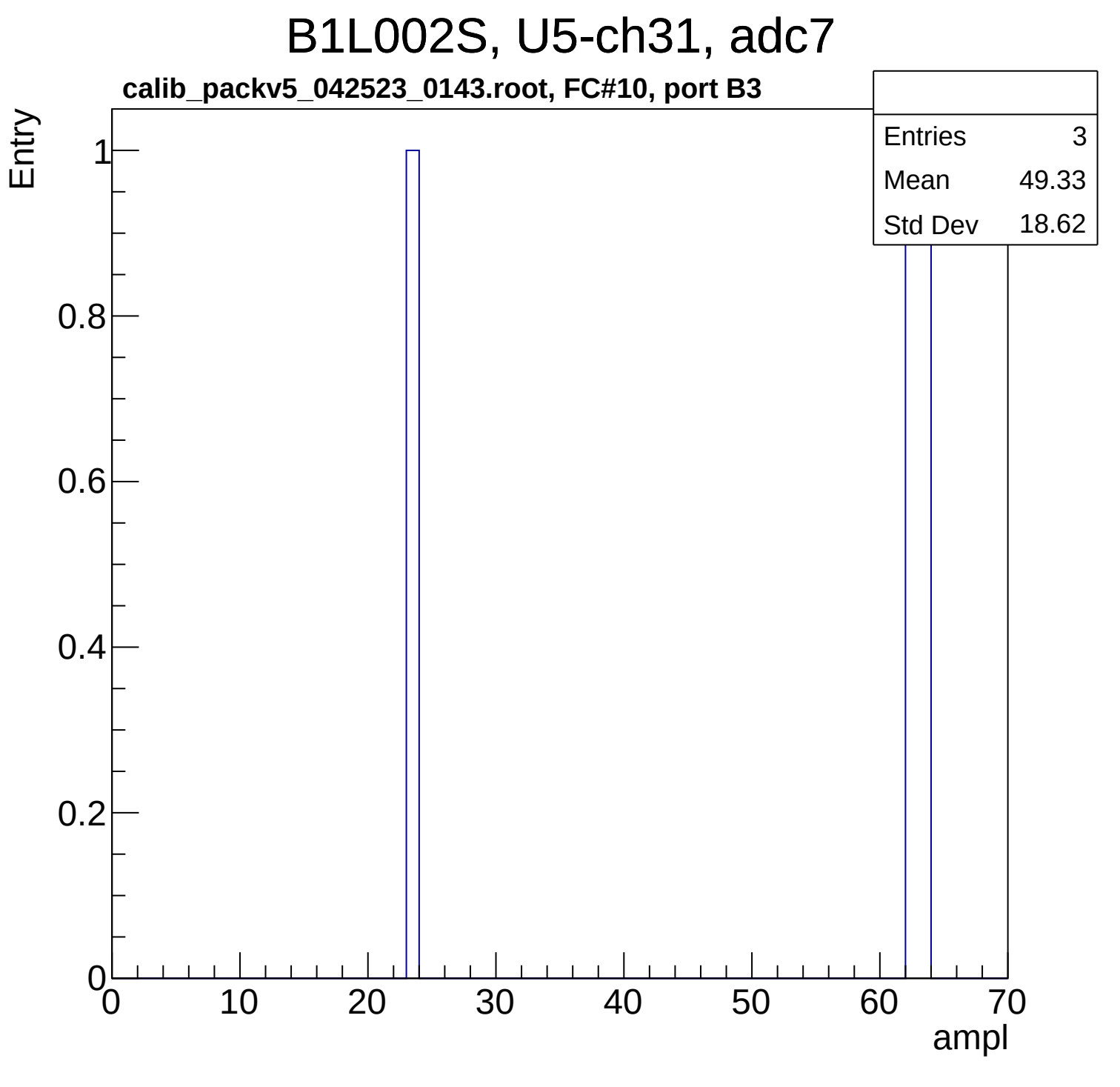
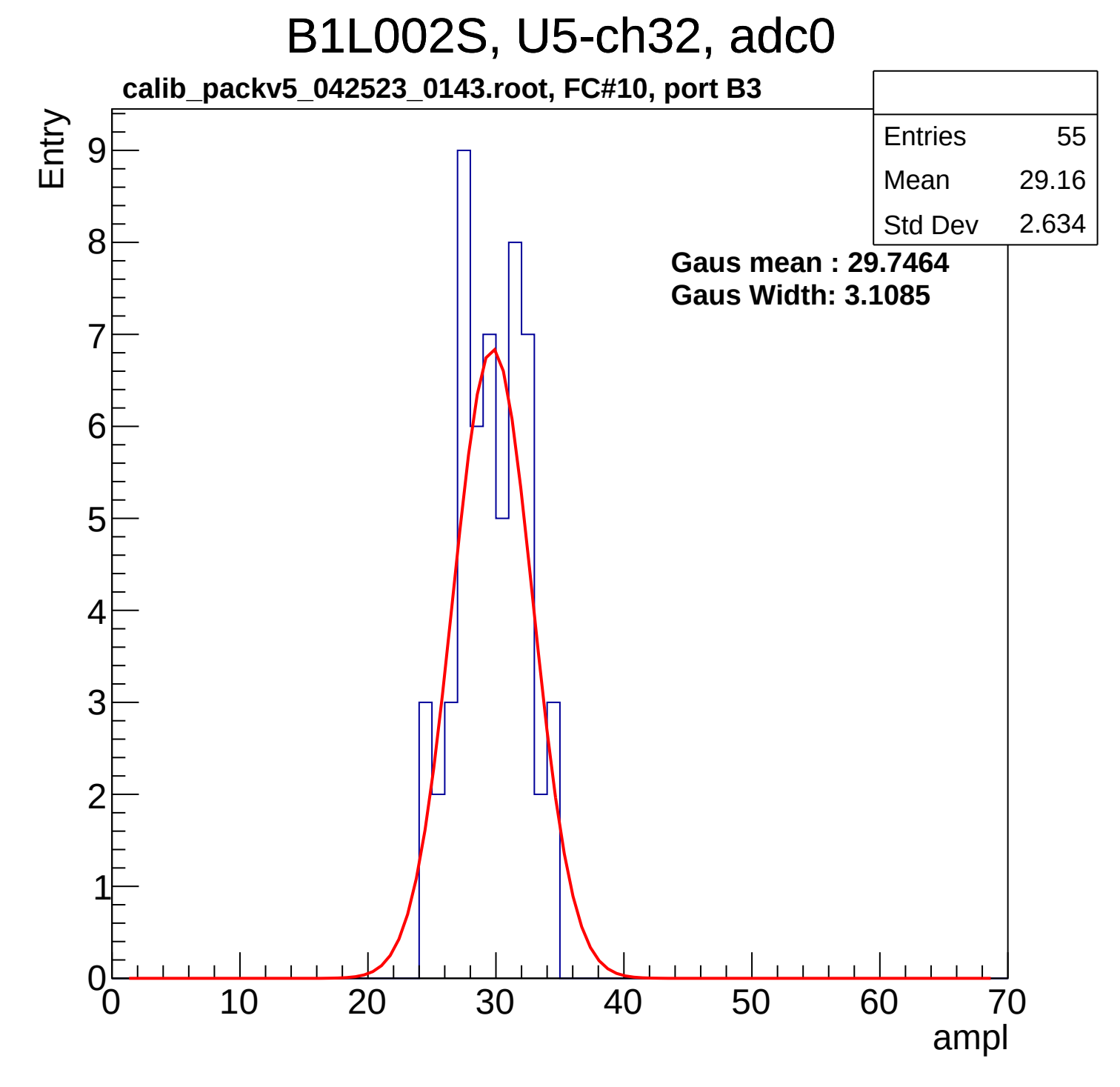
50
60
70
ampl


B1L002S, U5-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.4
Std Dev 1.625
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 49.33
Std Dev 18.62
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 29.16
Std Dev 2.634
Gaus mean : 29.7464
Gaus Width: 3.1085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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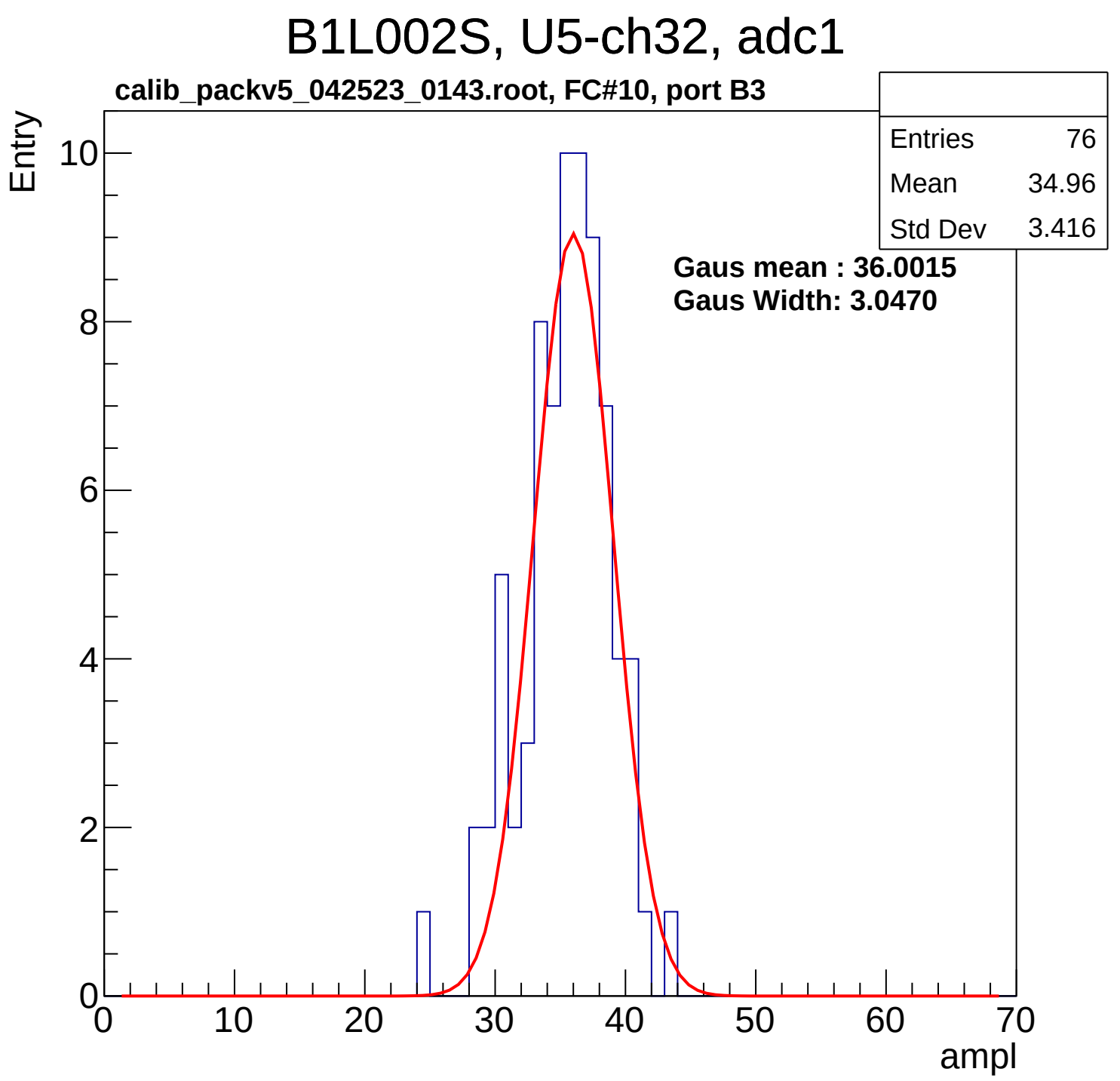
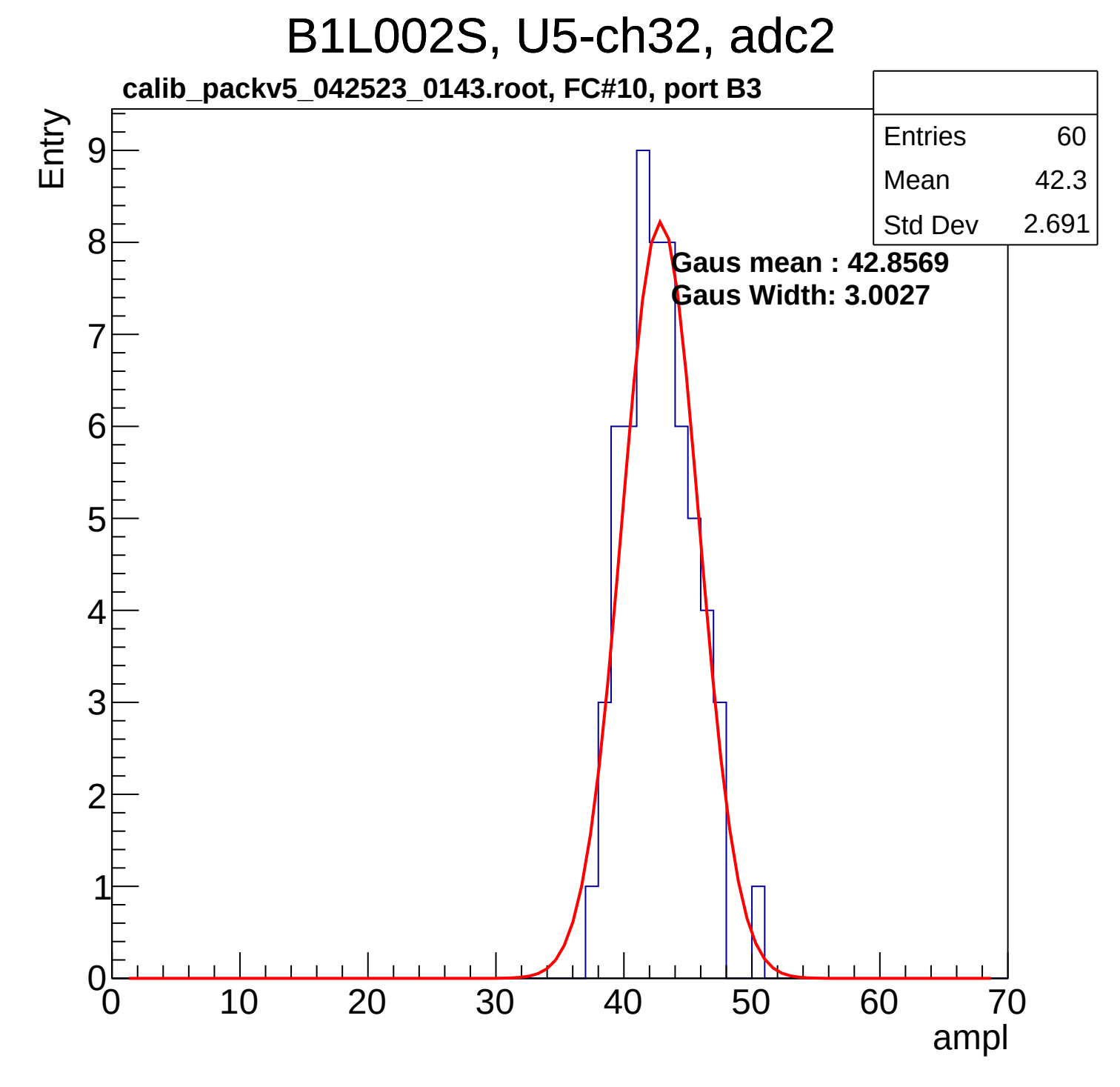

B1L002S, U5-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.96
Std Dev 3.416
Gaus mean : 36.0015
Gaus Width: 3.0470
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.3
Std Dev 2.691
Gaus mean : 42.8569
Gaus Width: 3.0027
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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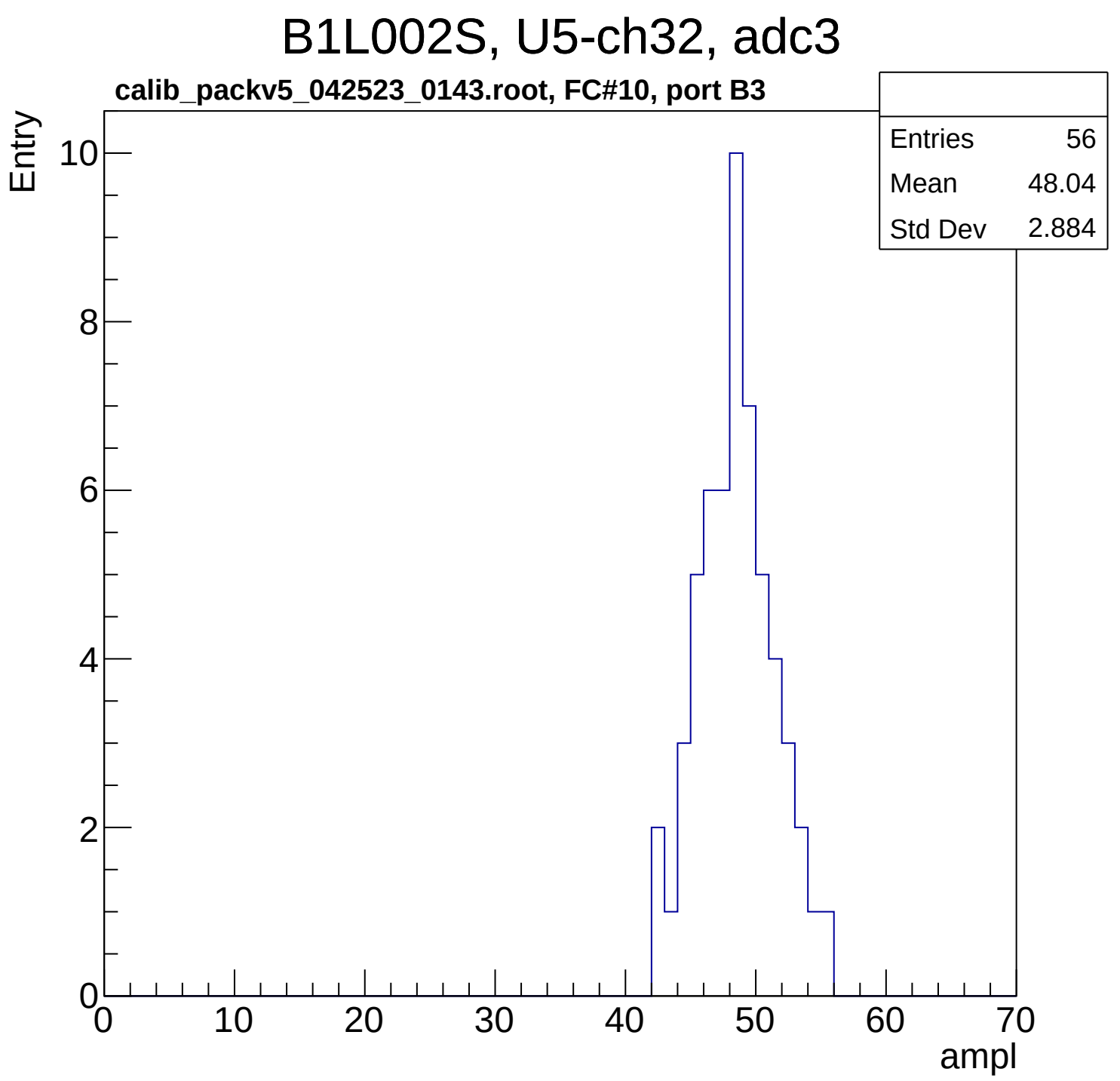
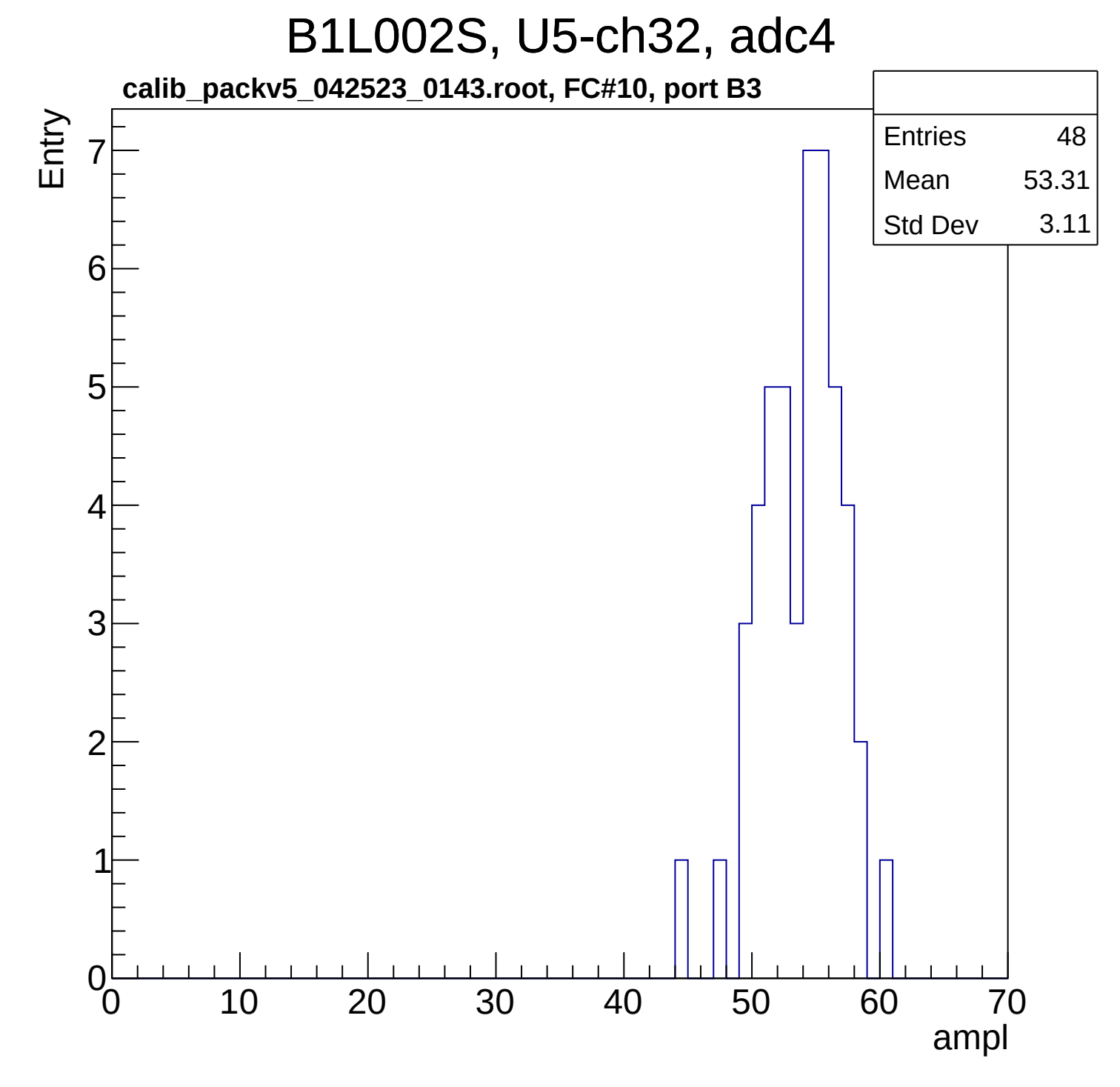

B1L002S, U5-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 48.04
Std Dev 2.884
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.31
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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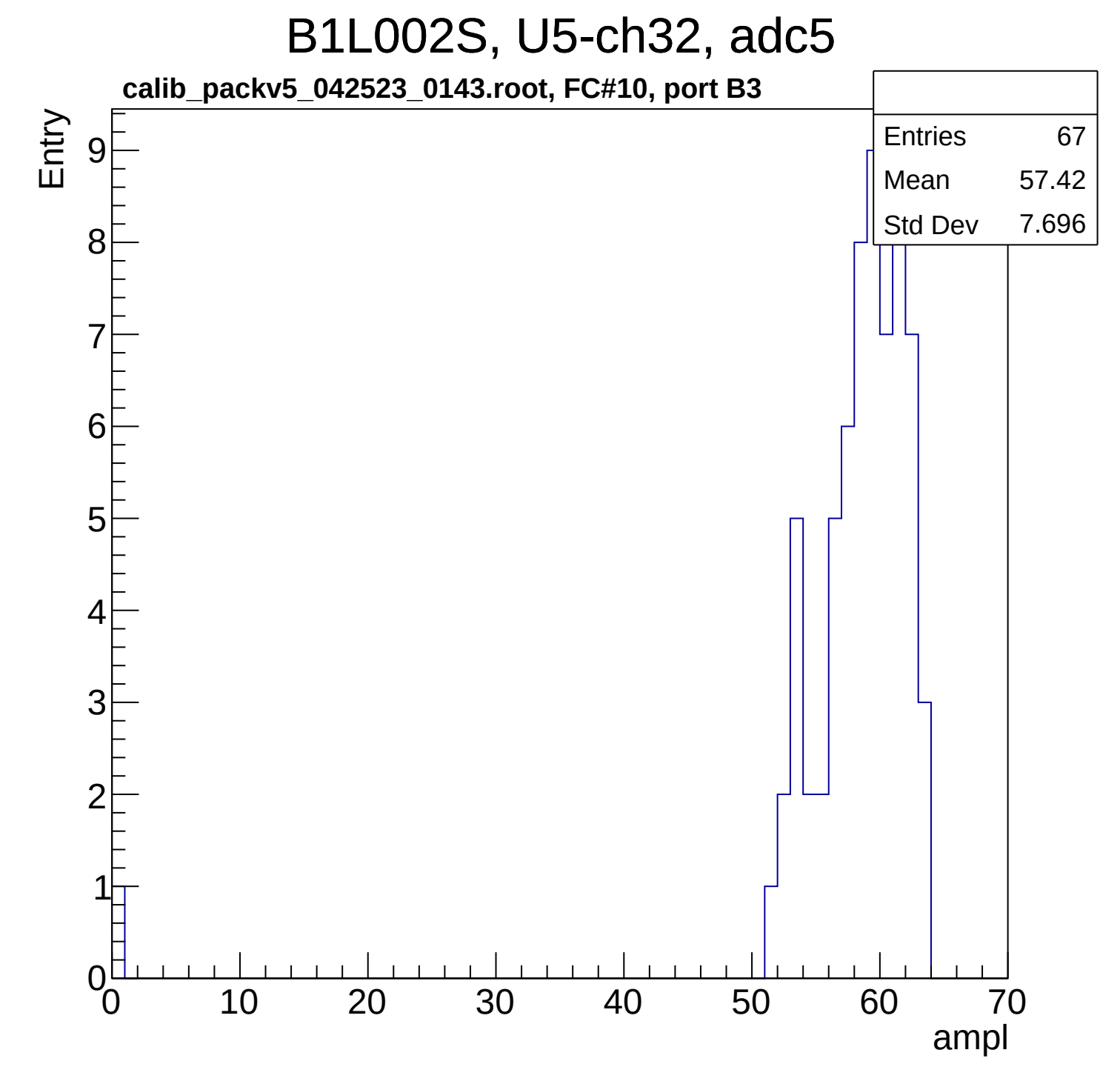
B1L002S, U5-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.42
Std Dev 7.696
Entry
ampl
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2
3
4
5
6
7
8
9
0
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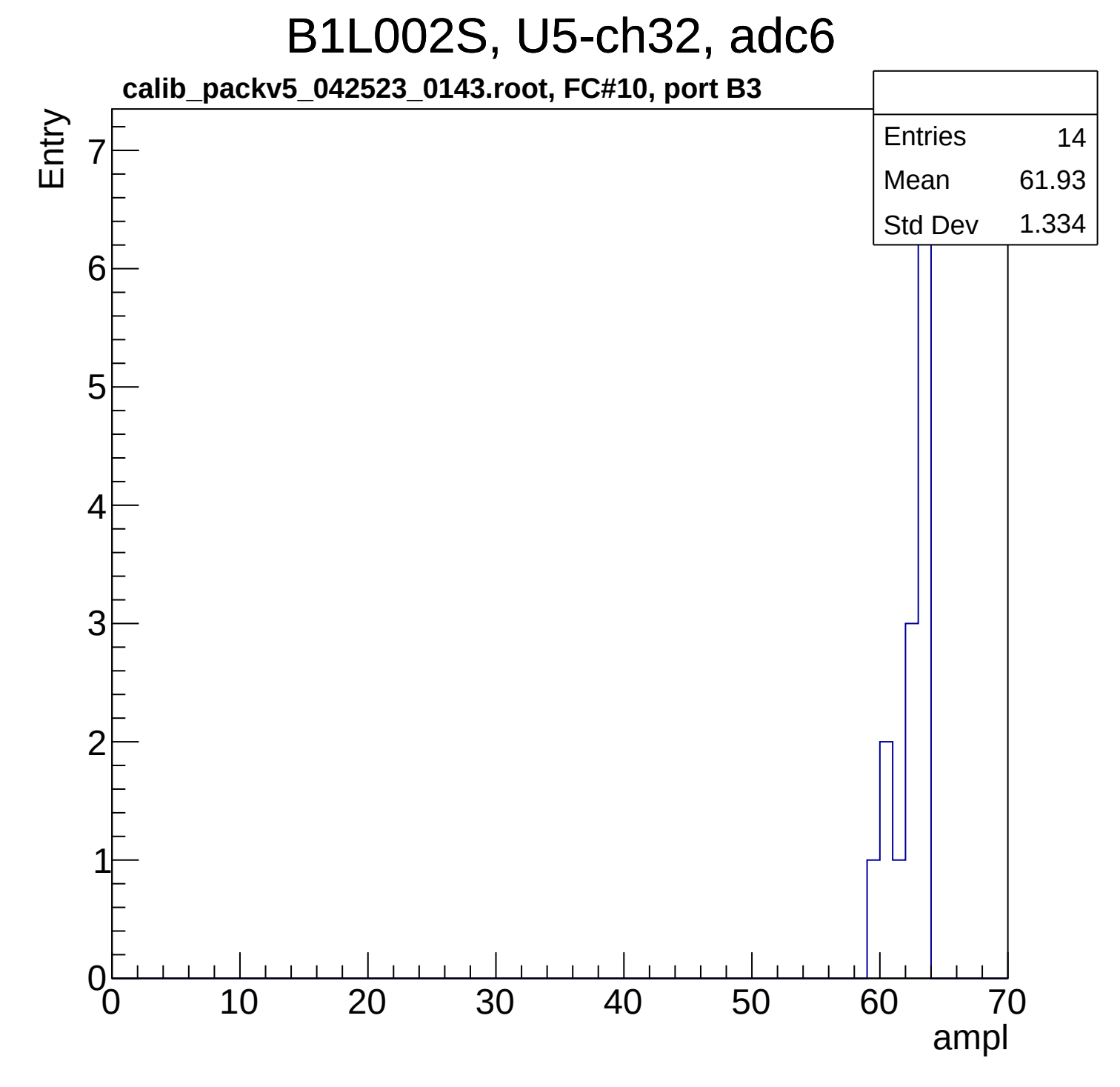
B1L002S, U5-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.93
Std Dev 1.334
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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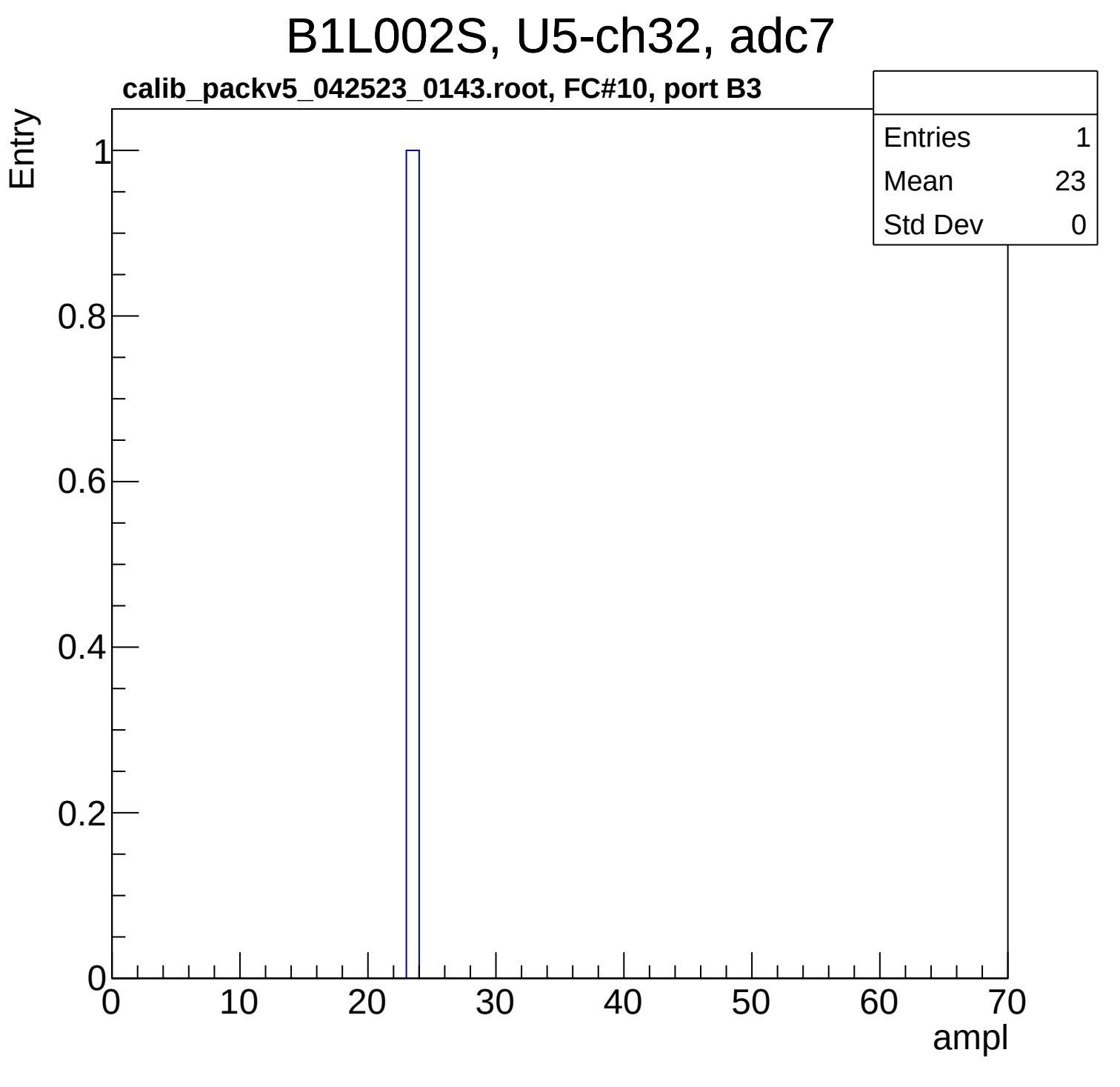
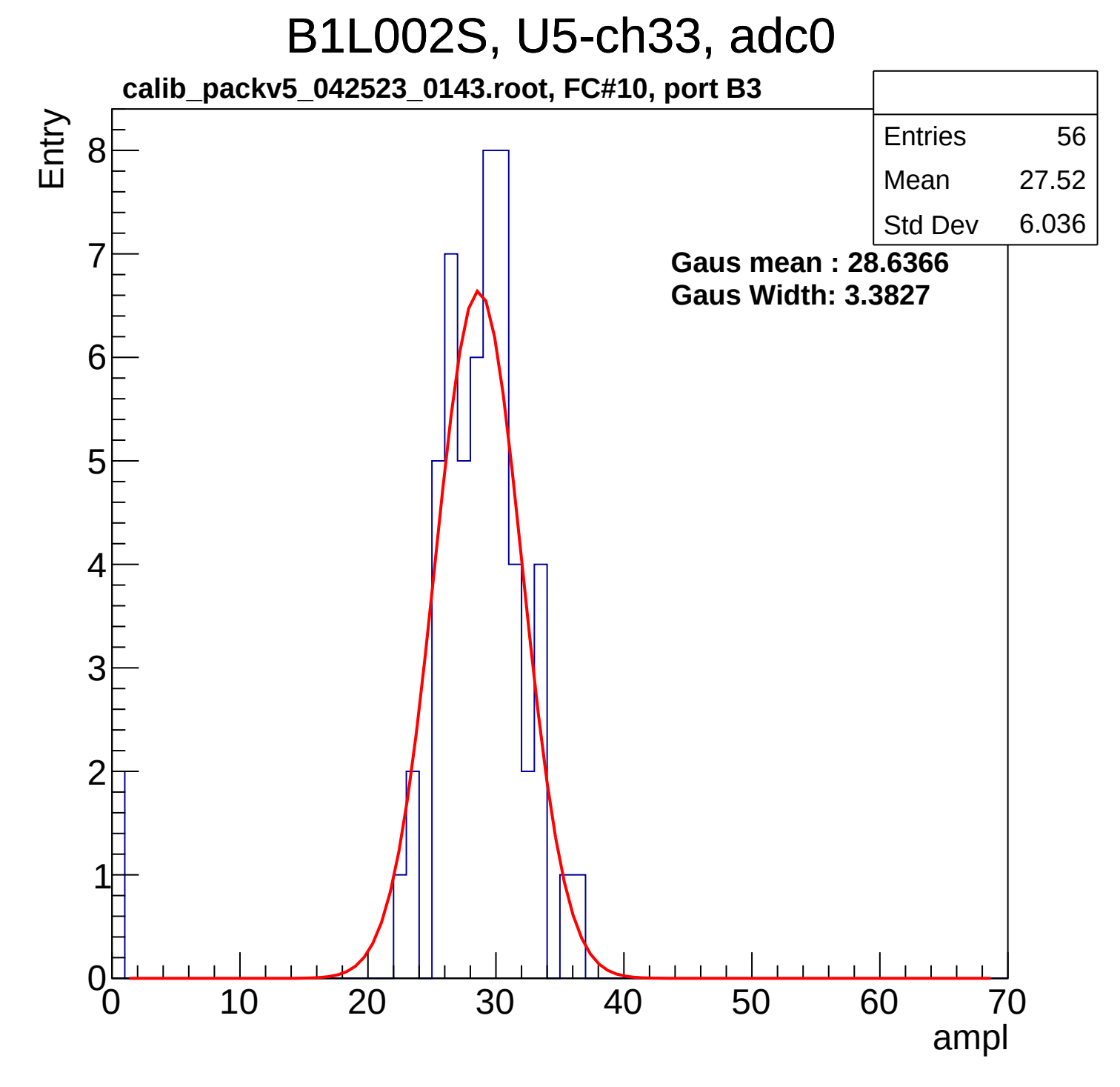

B1L002S, U5-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.52
Std Dev 6.036
Gaus mean : 28.6366
Gaus Width: 3.3827
Entry
ampl
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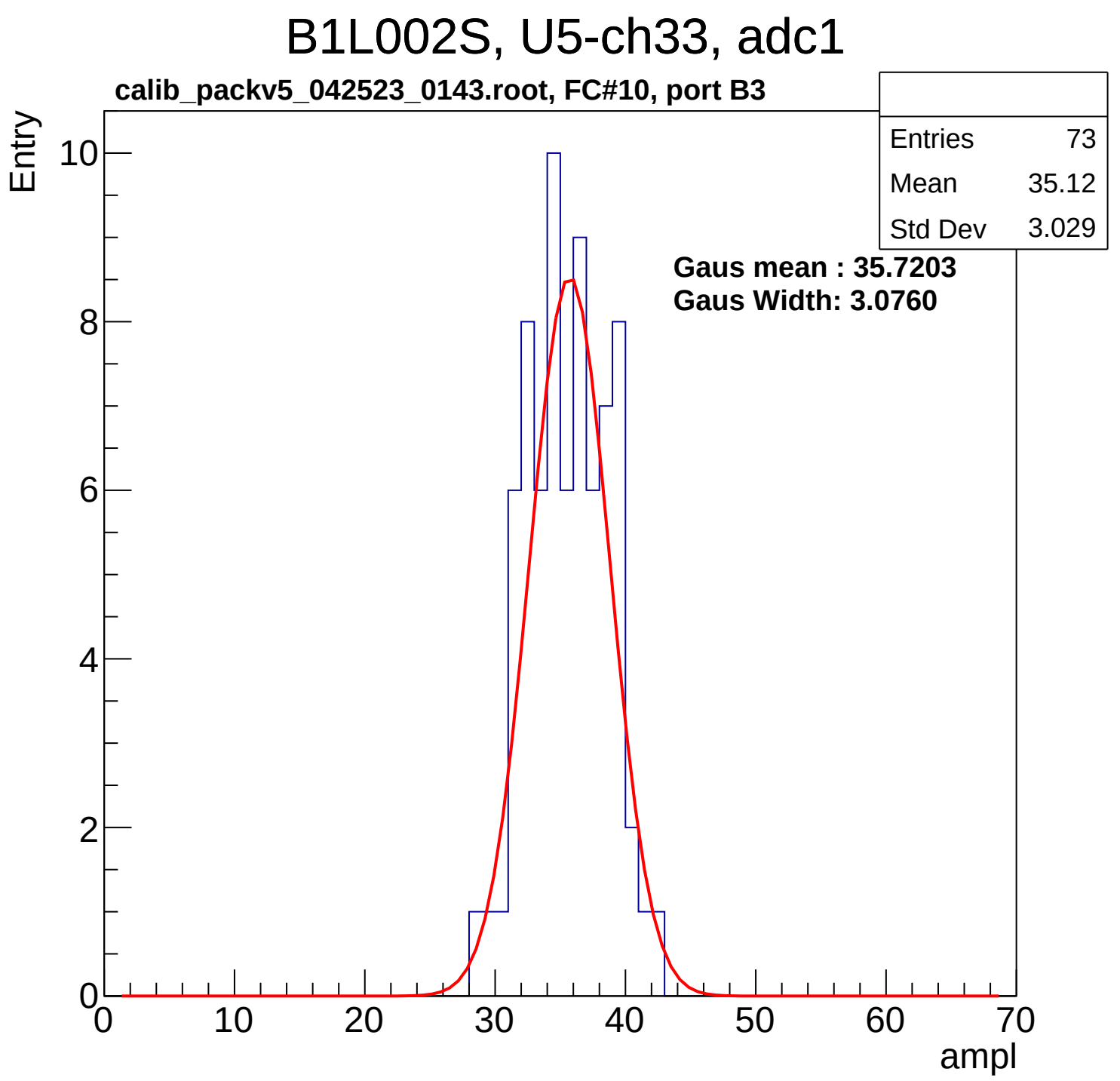
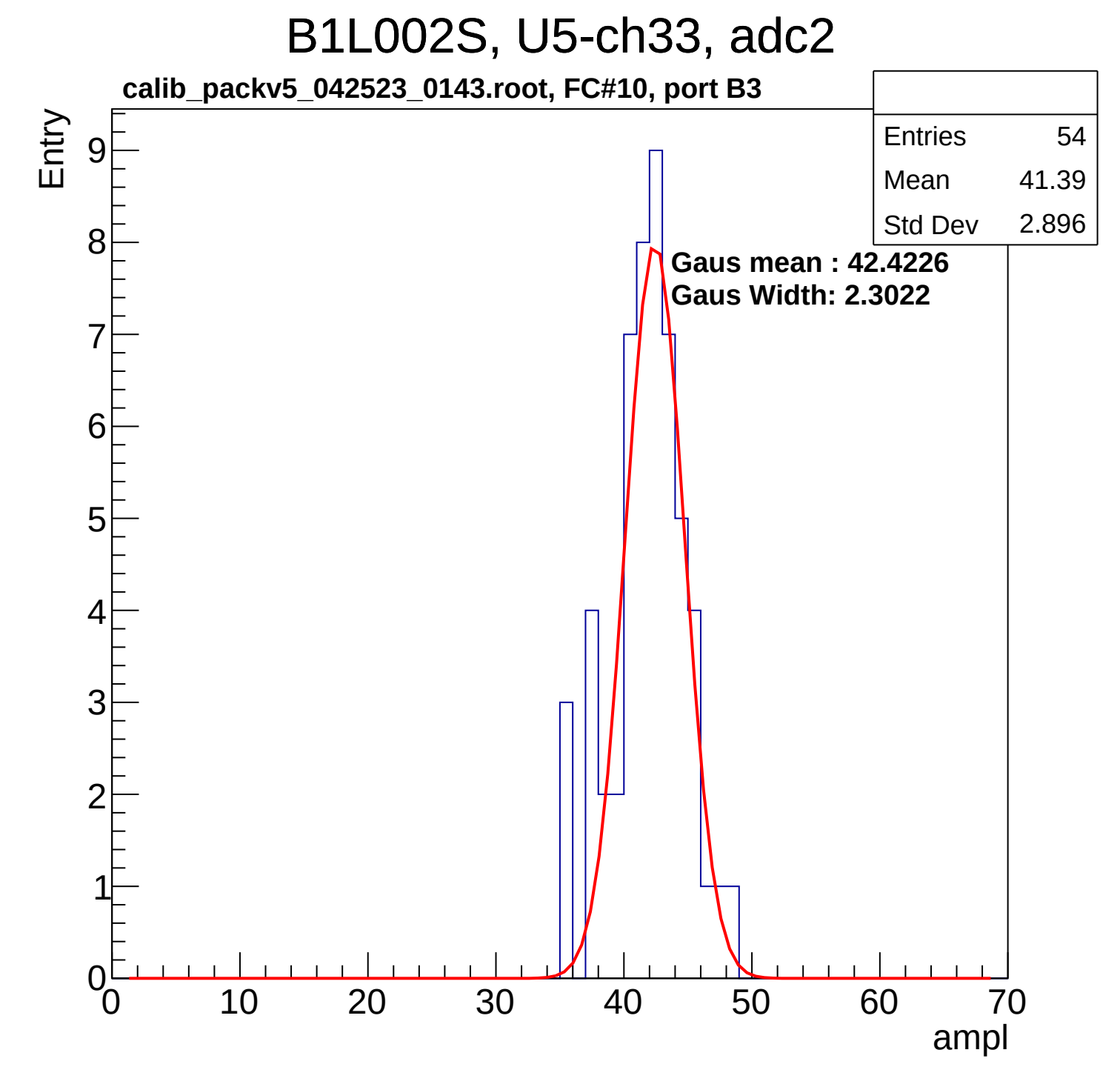
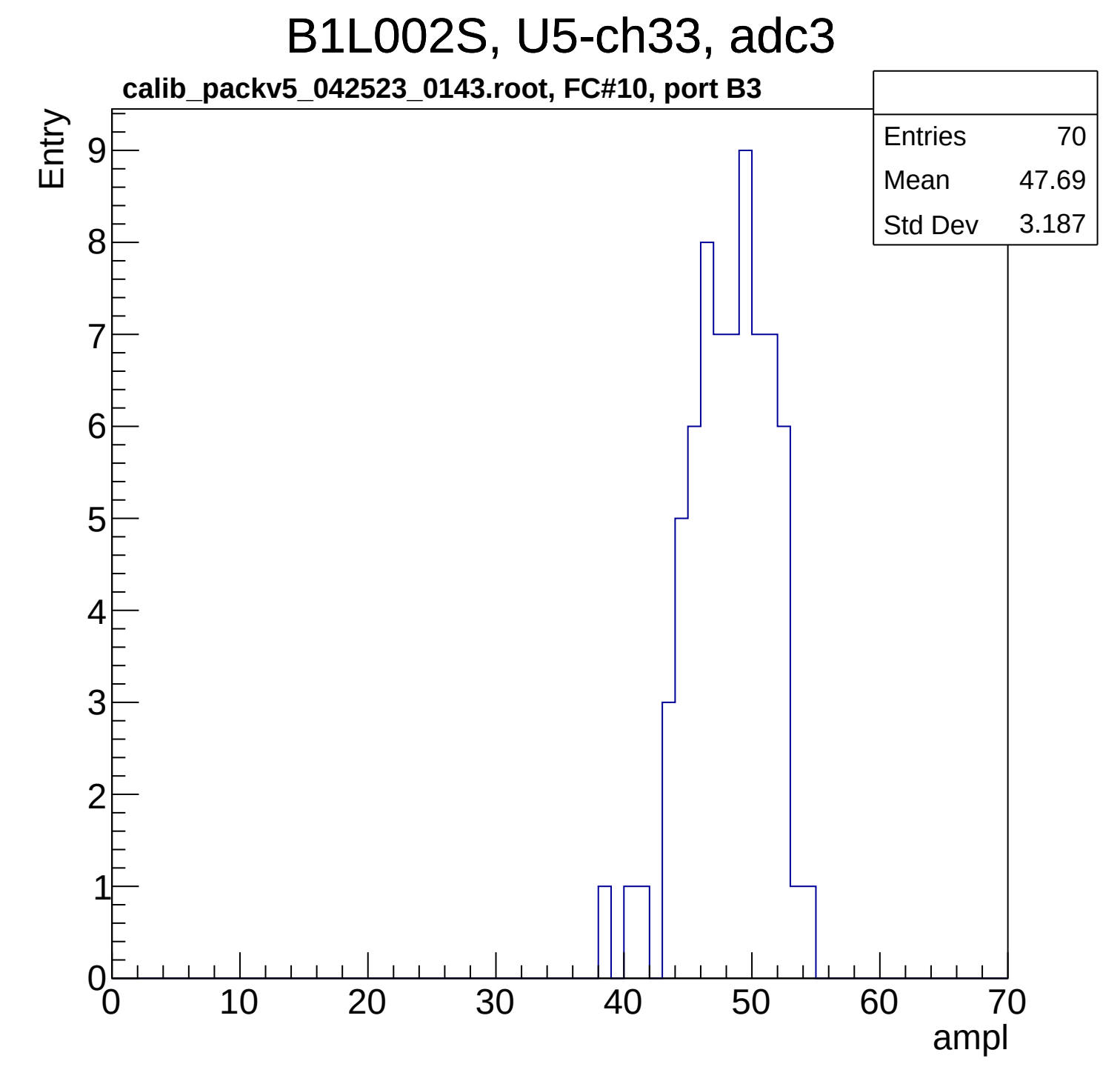
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.12
Std Dev 3.029
Gaus mean : 35.7203
Gaus Width: 3.0760
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 41.39
Std Dev 2.896
Gaus mean : 42.4226
Gaus Width: 2.3022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.69
Std Dev 3.187
Entry
ampl
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4
5
6
7
8
9
0
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20
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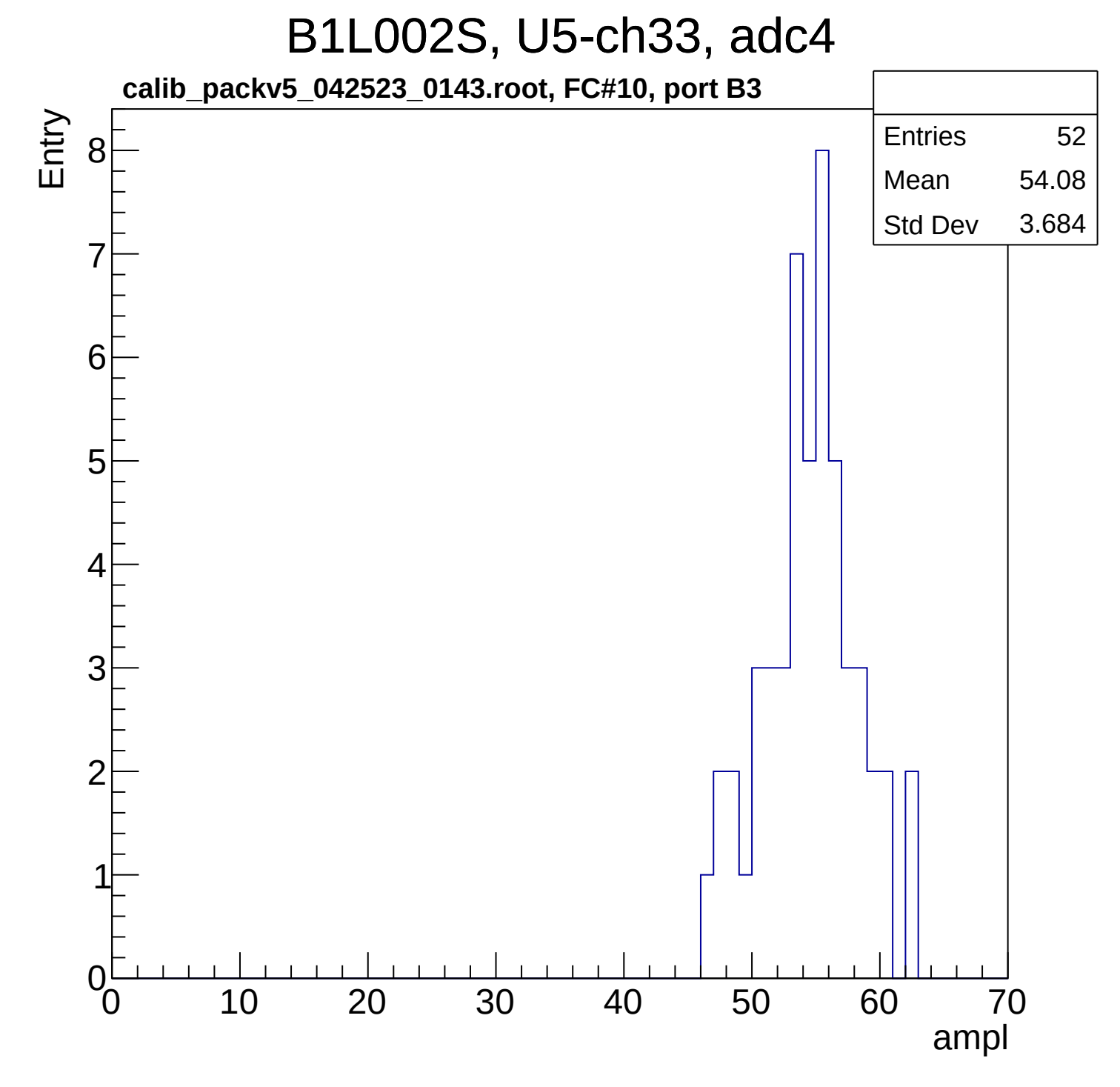
50
60
70


B1L002S, U5-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 54.08
Std Dev 3.684
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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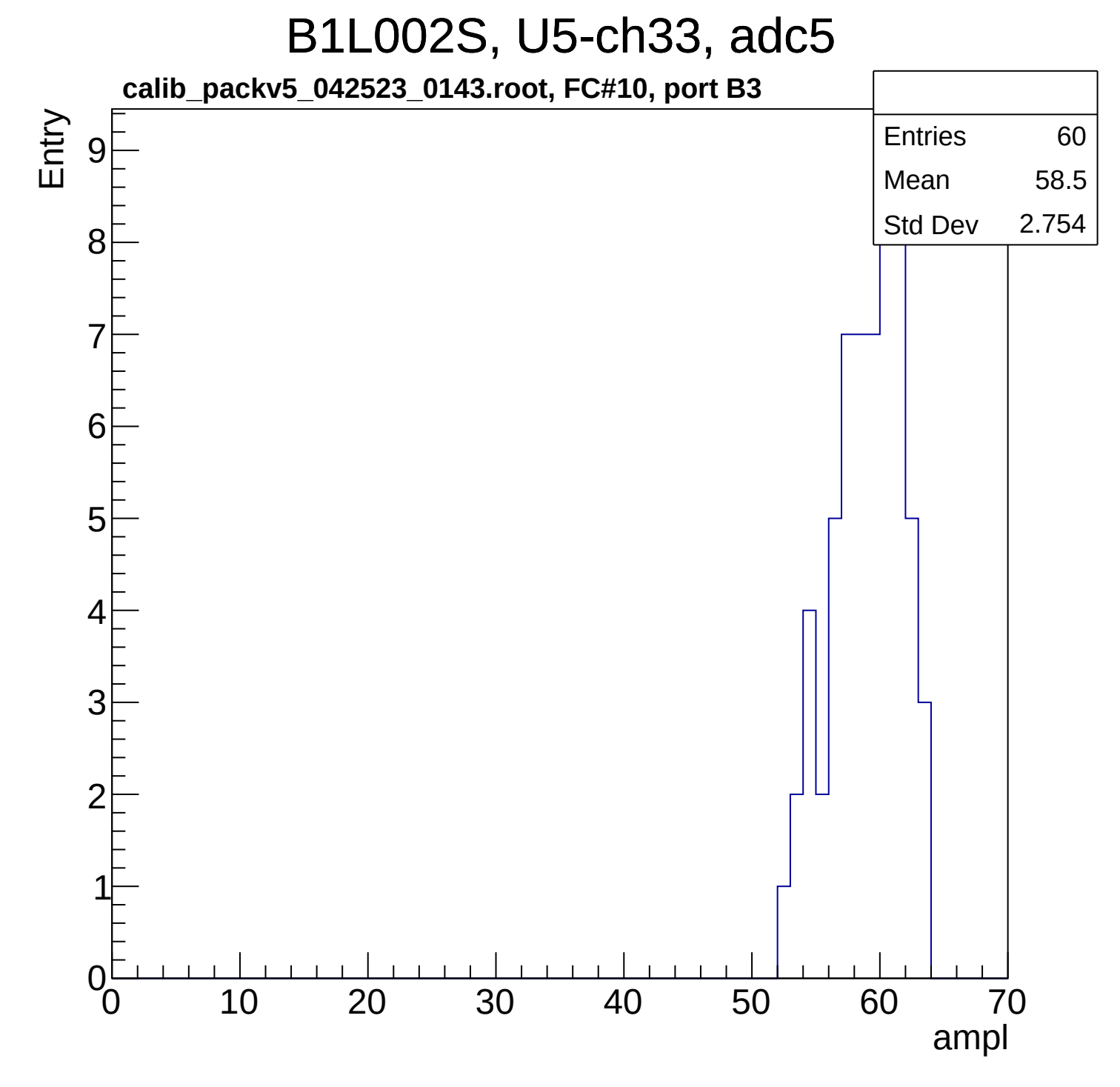
B1L002S, U5-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.5
Std Dev 2.754
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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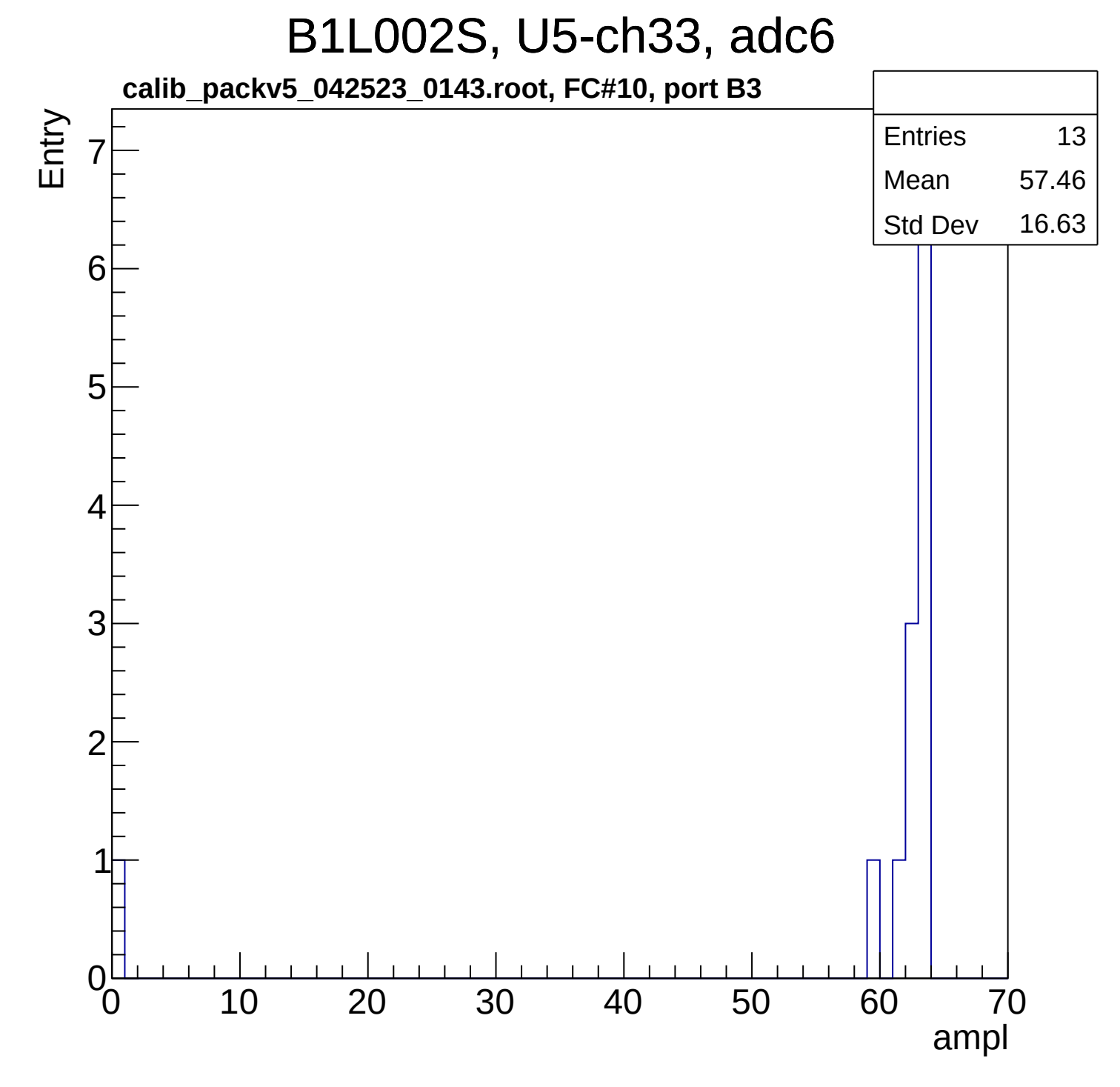
B1L002S, U5-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.46
Std Dev 16.63
Entry
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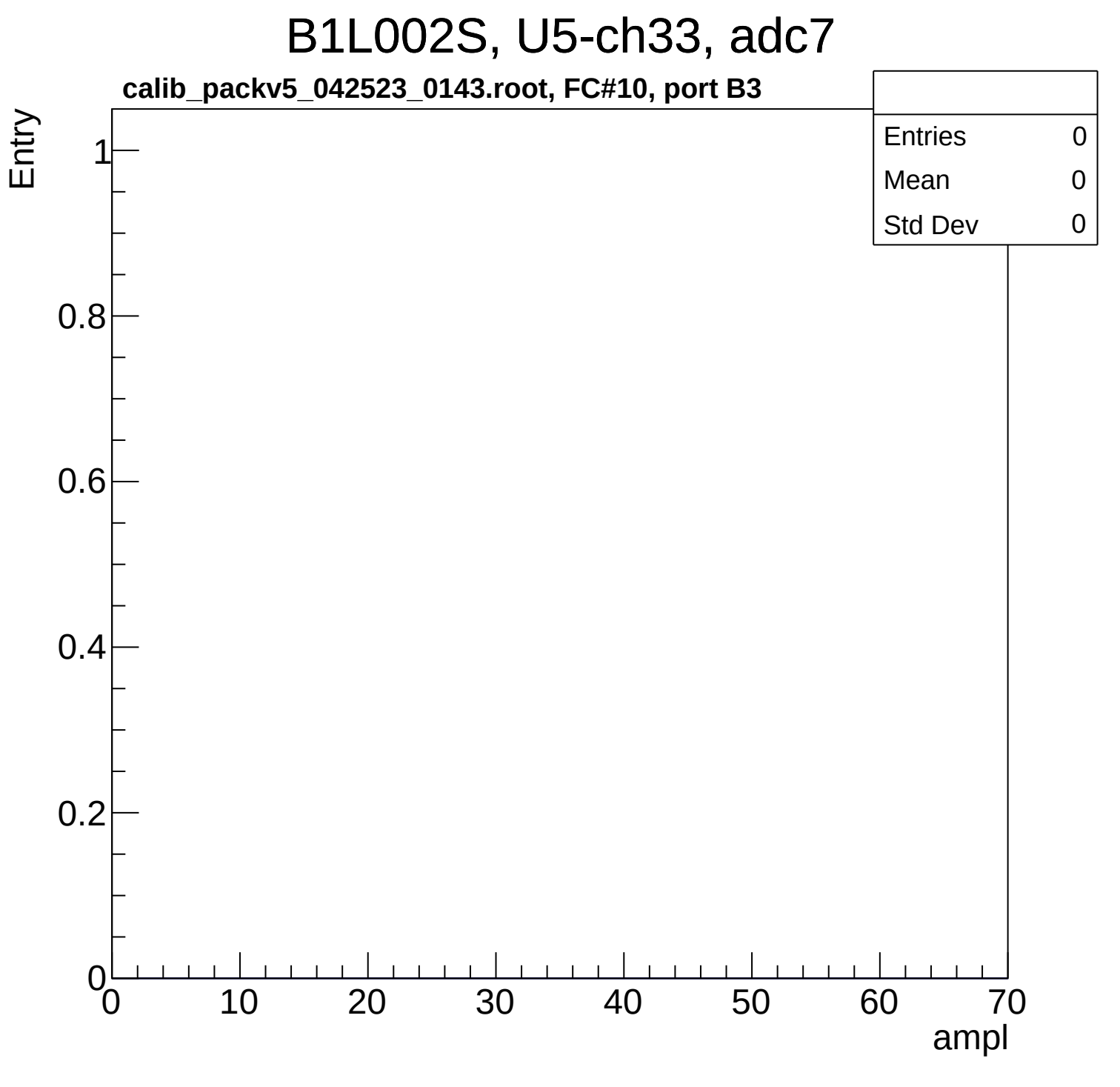
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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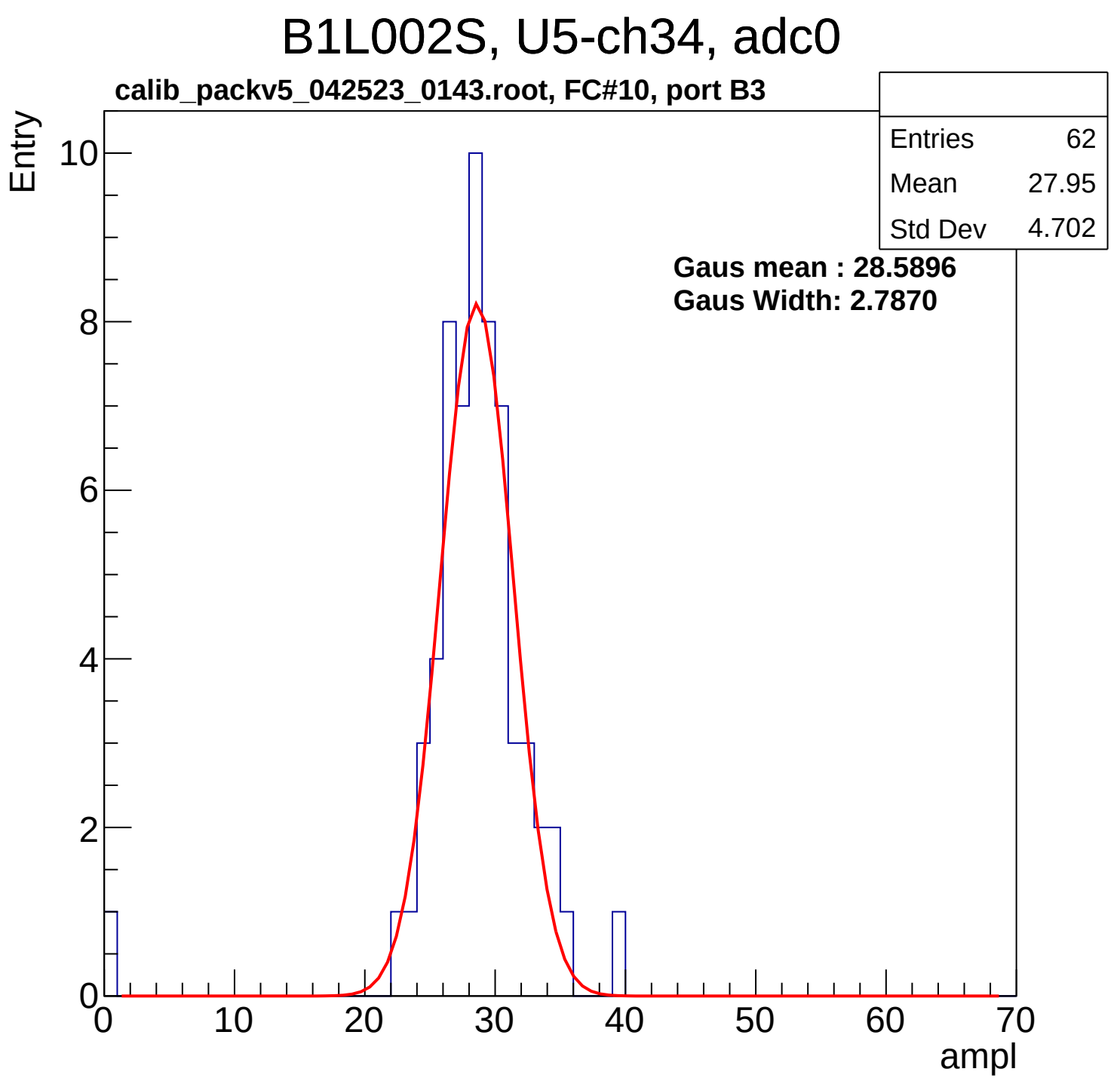
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.95
Std Dev 4.702
Gaus mean : 28.5896
Gaus Width: 2.7870
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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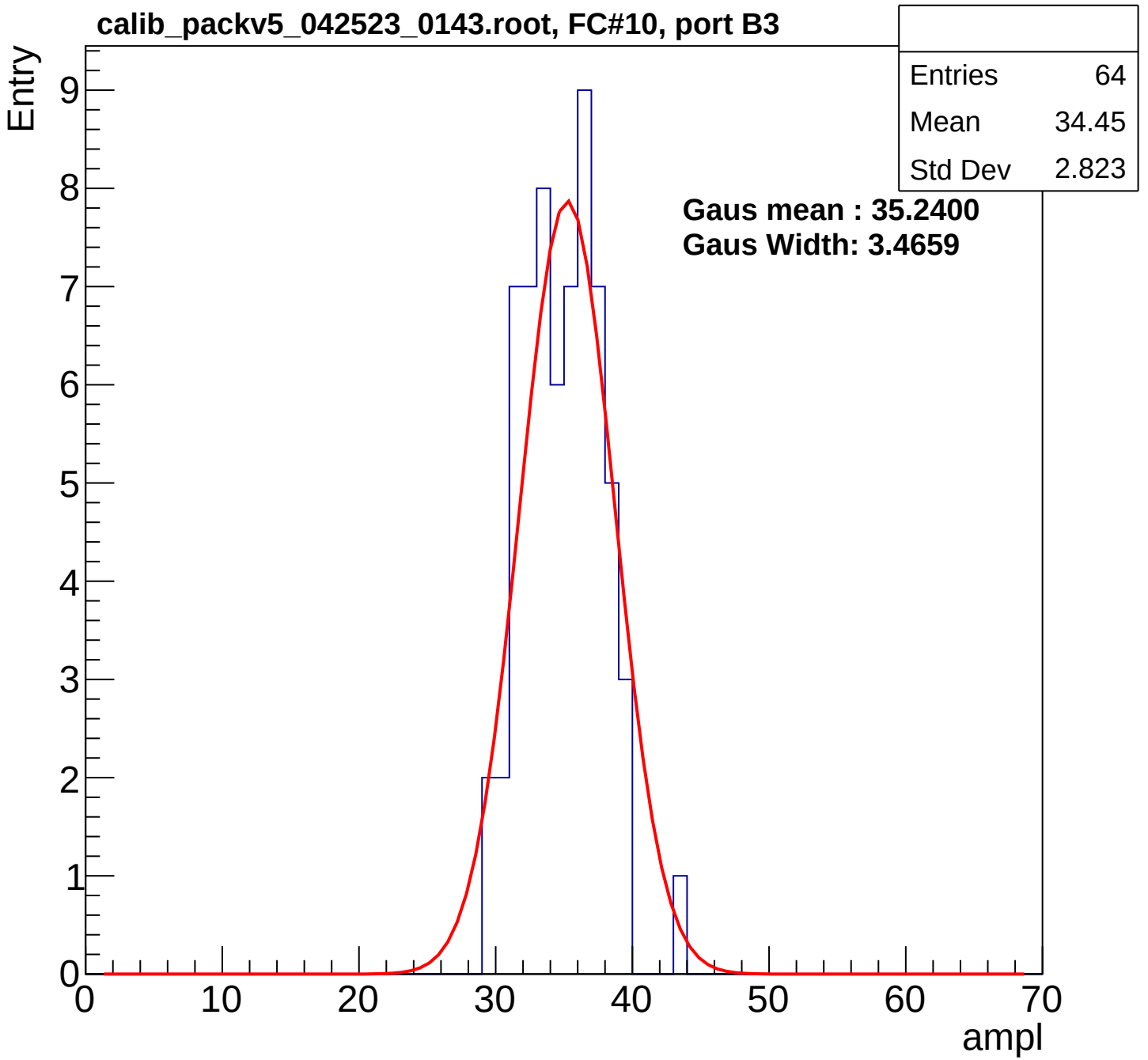
B1L002S, U5-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.45
Std Dev 2.823
Gaus mean : 35.2400
Gaus Width: 3.4659
Entry
ampl
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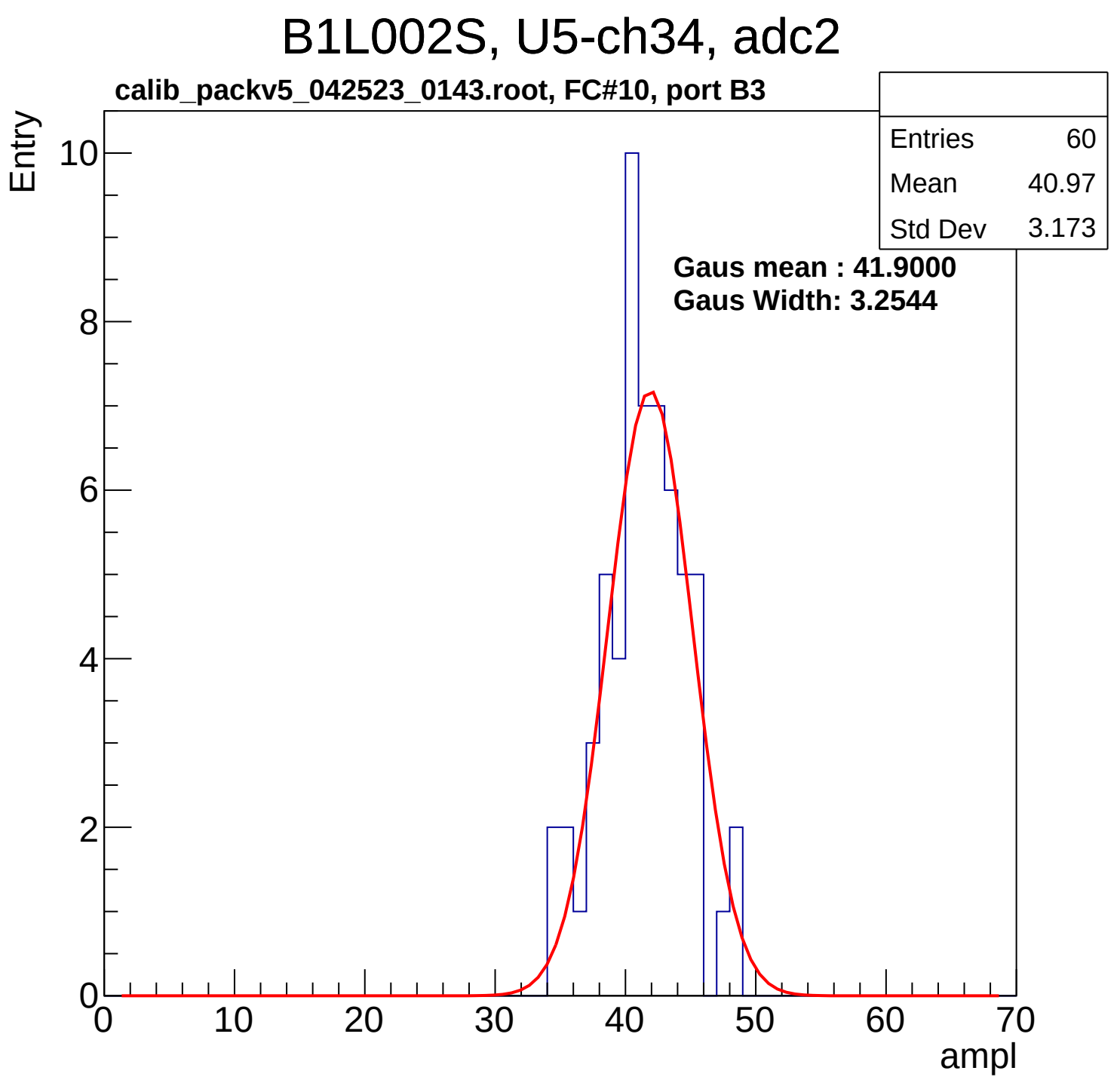
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.97
Std Dev 3.173
Gaus mean : 41.9000
Gaus Width: 3.2544
Entry
ampl
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2
4
6
8
10
0
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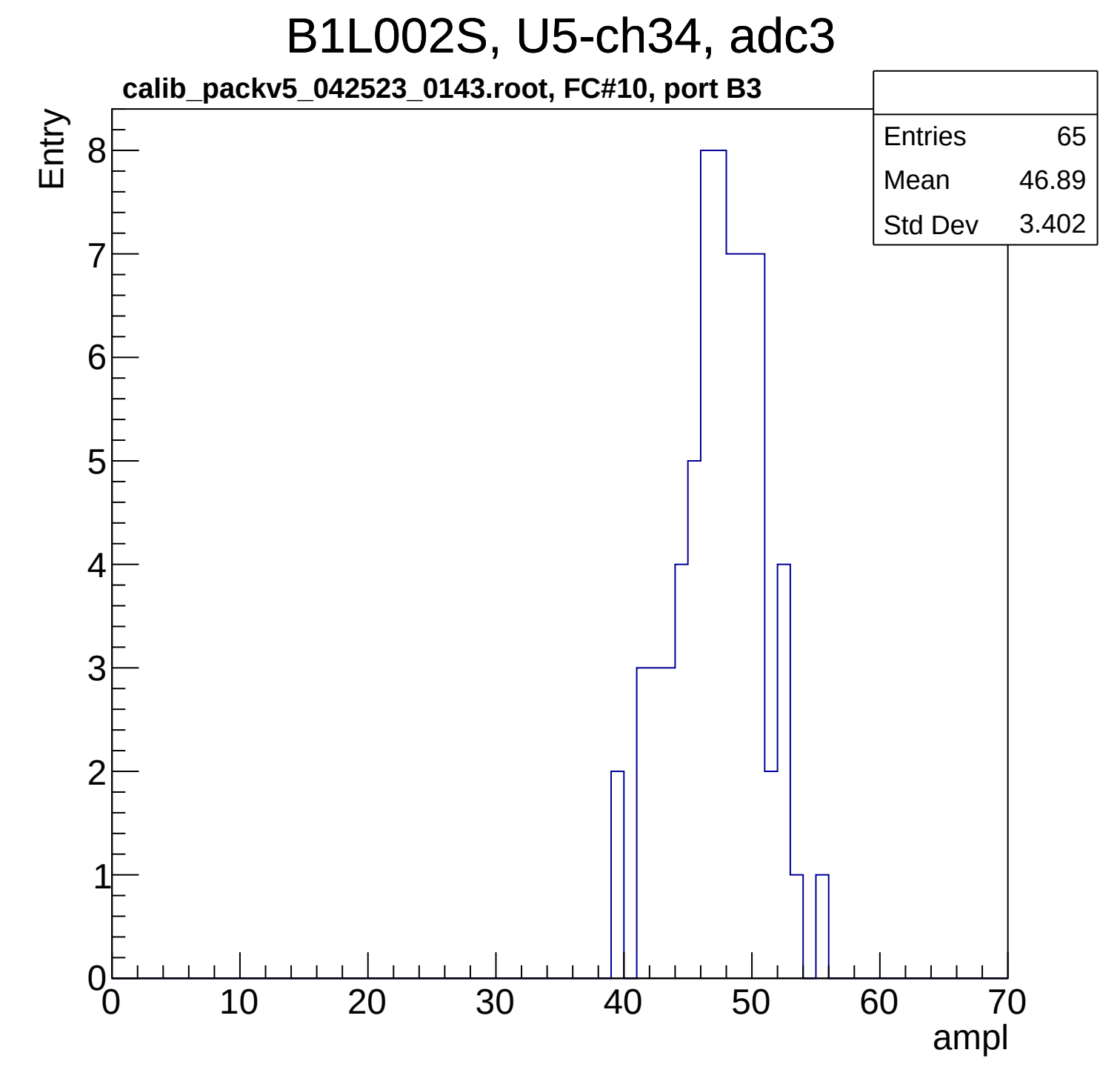
B1L002S, U5-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.89
Std Dev 3.402
Entry
ampl
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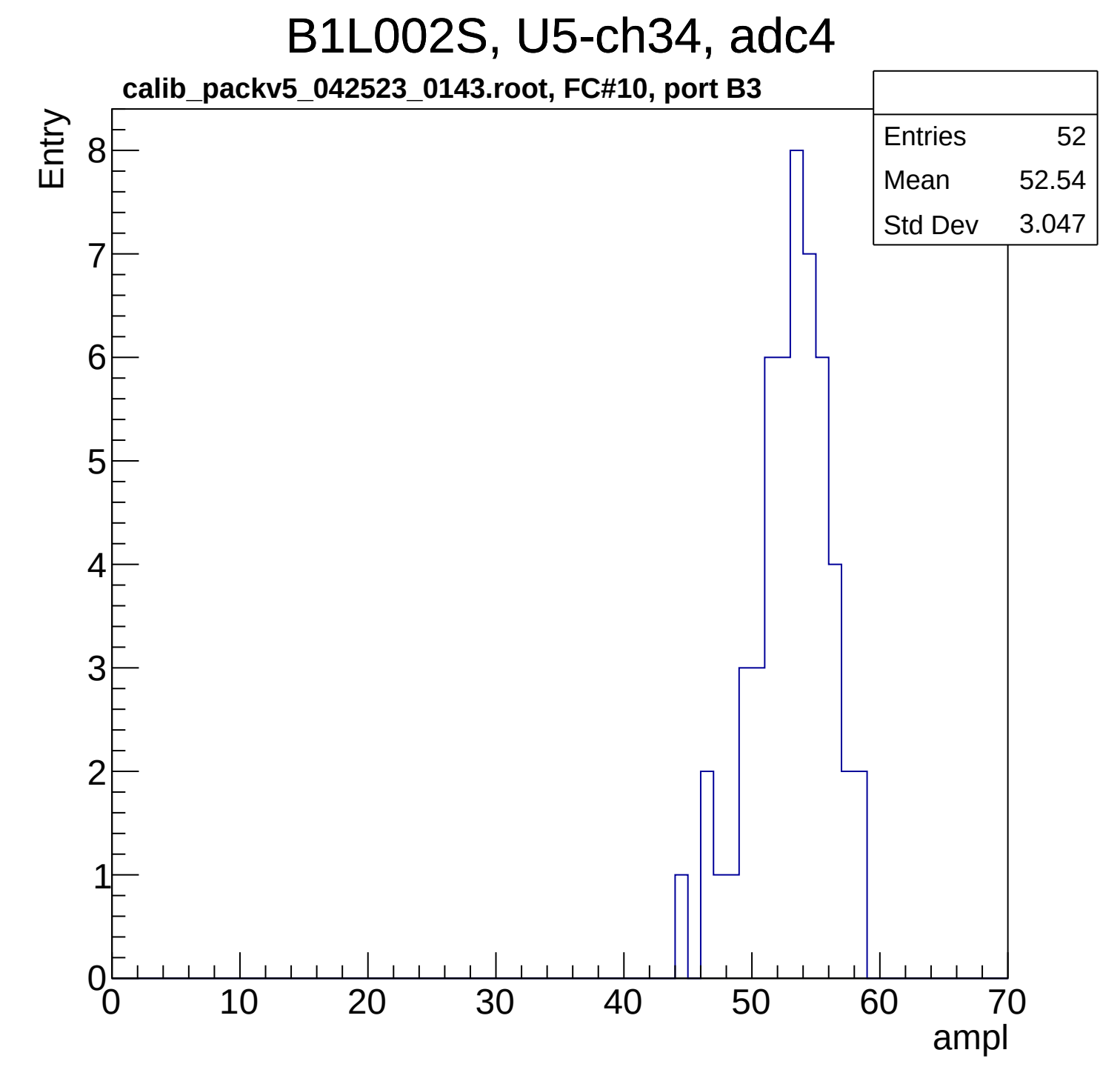
B1L002S, U5-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.54
Std Dev 3.047
Entry
ampl
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5
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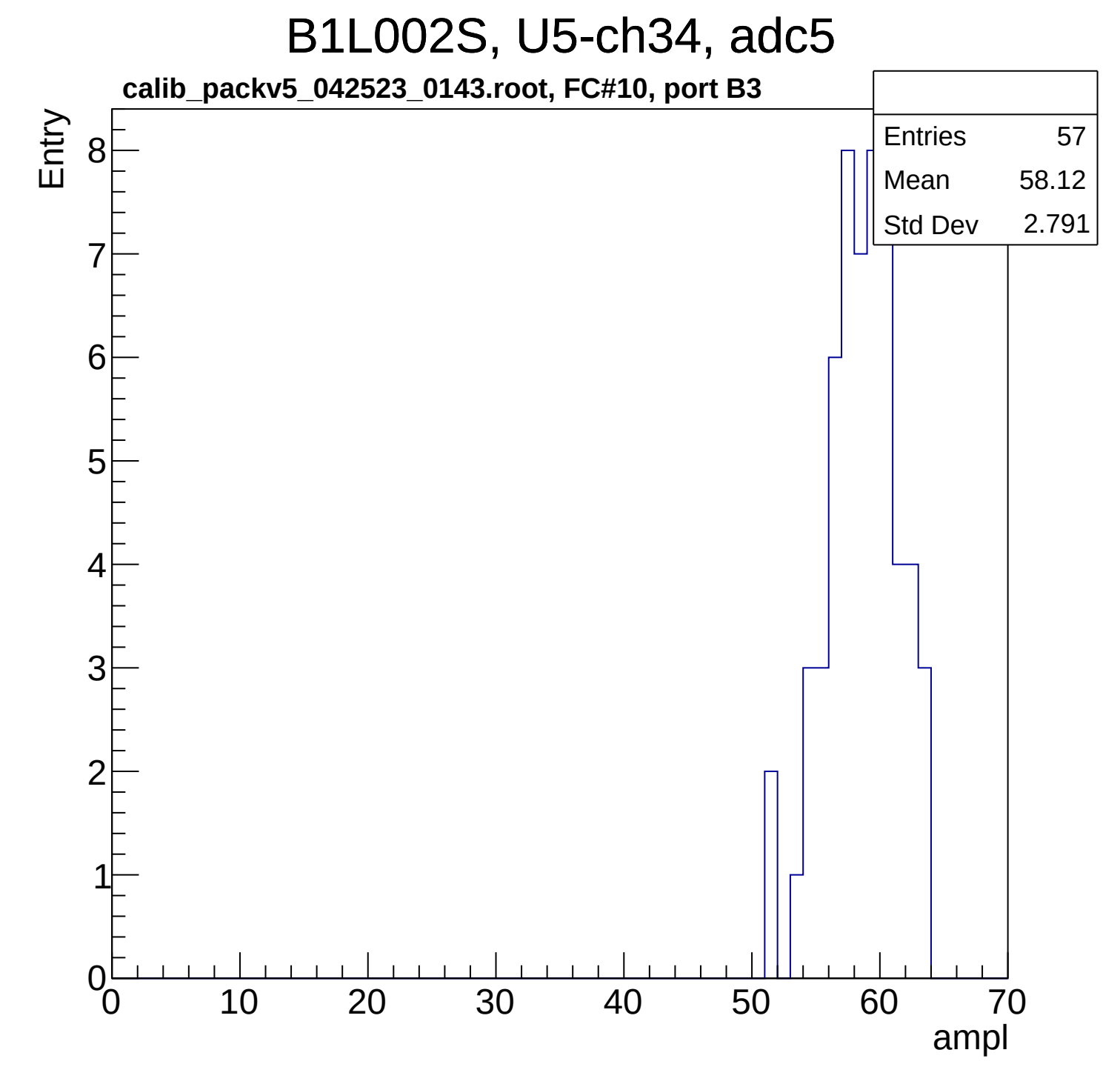
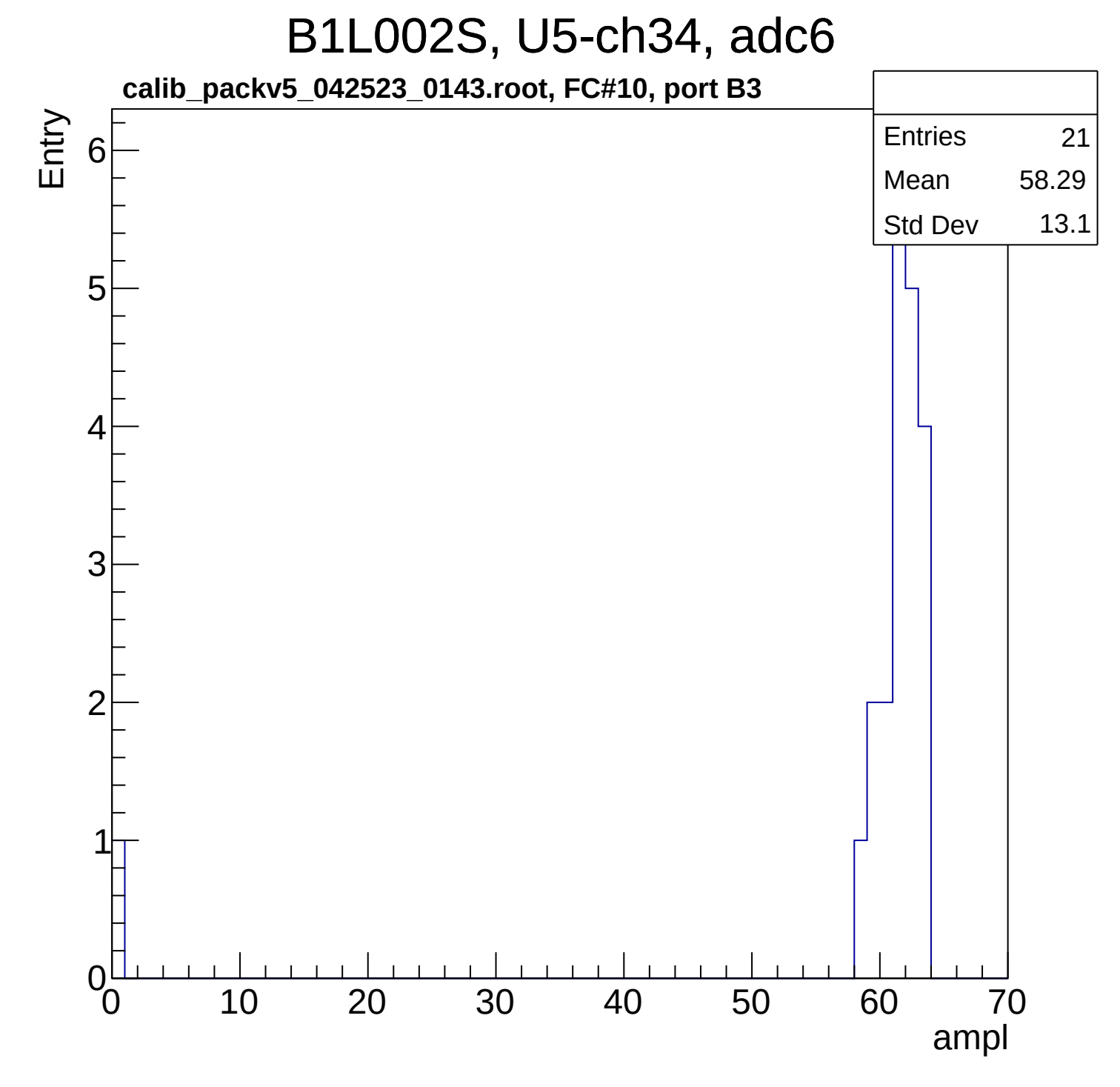

B1L002S, U5-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.12
Std Dev 2.791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.29
Std Dev 13.1
Entry
0
1
2
3
4
5
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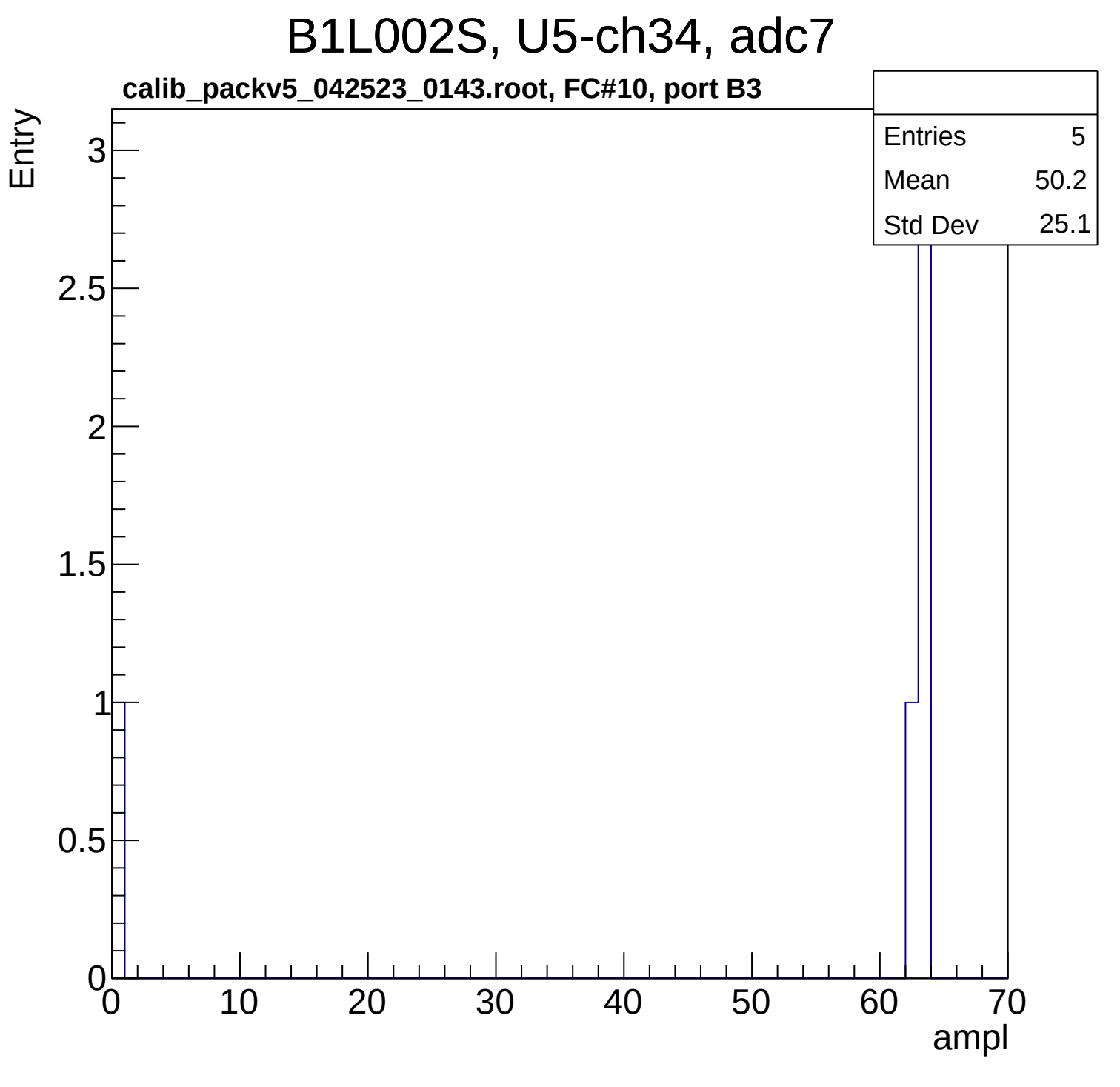
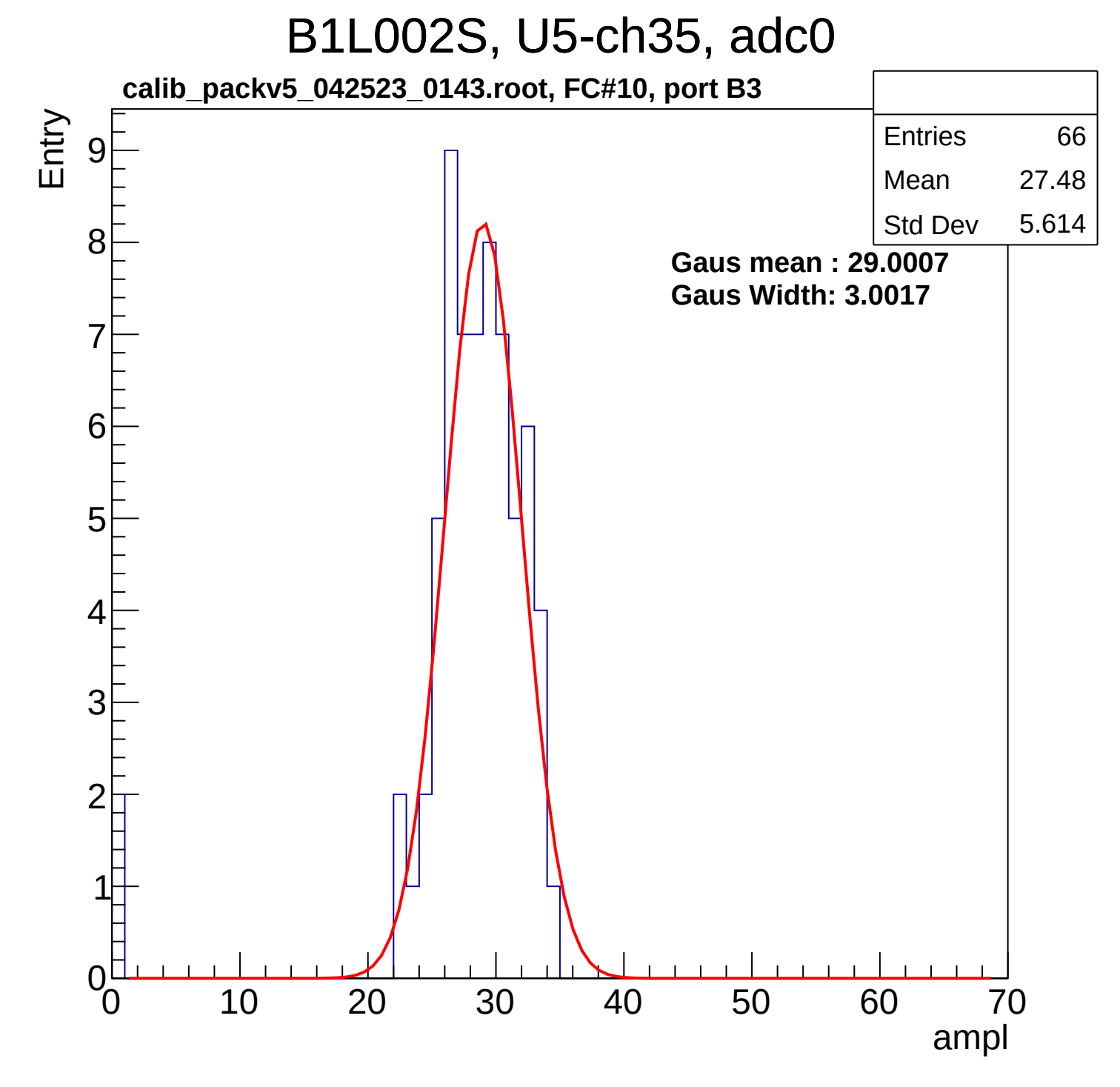
70
ampl


B1L002S, U5-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.48
Std Dev 5.614
Gaus mean : 29.0007
Gaus Width: 3.0017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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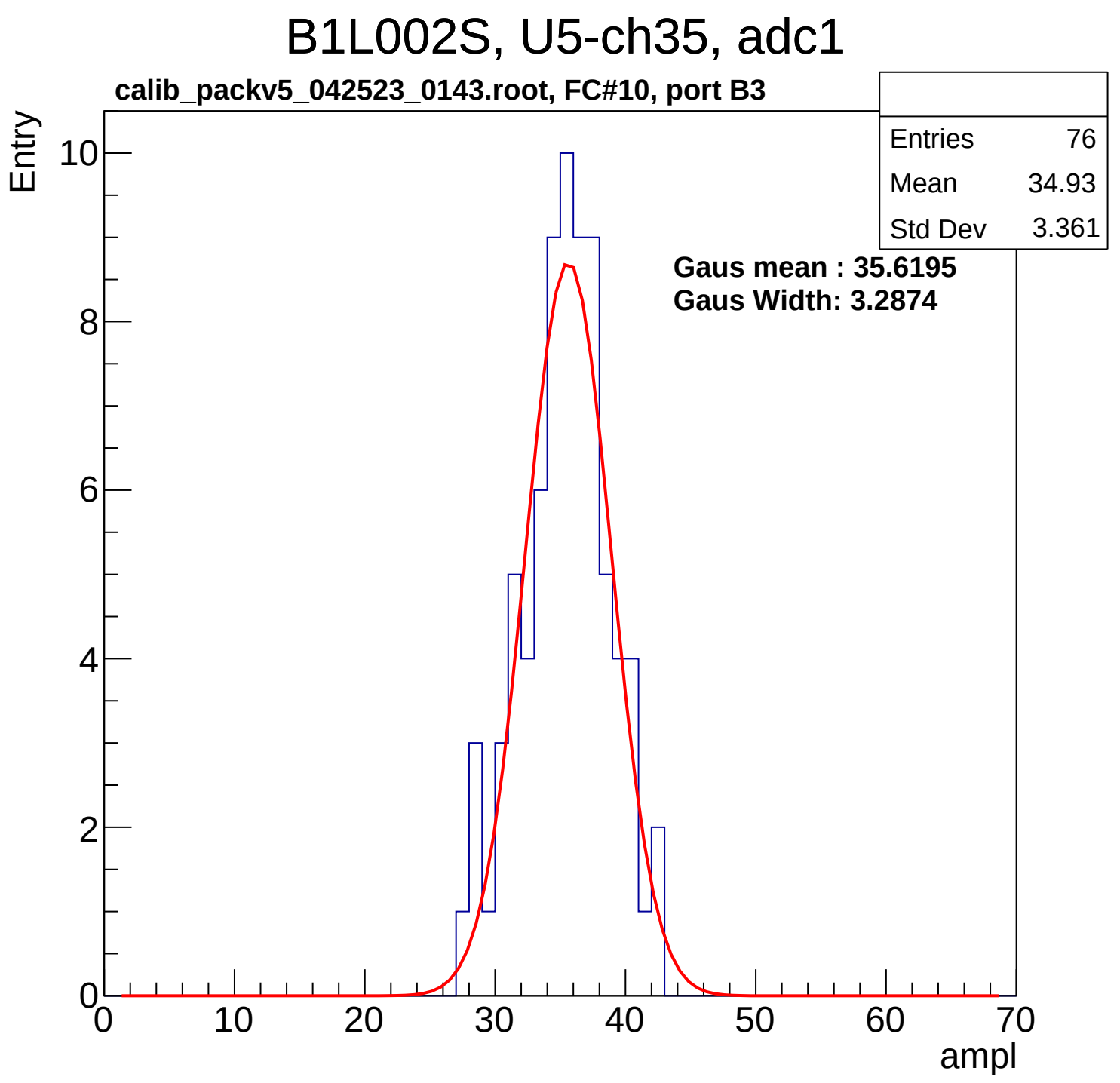
B1L002S, U5-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.93
Std Dev 3.361
Gaus mean : 35.6195
Gaus Width: 3.2874
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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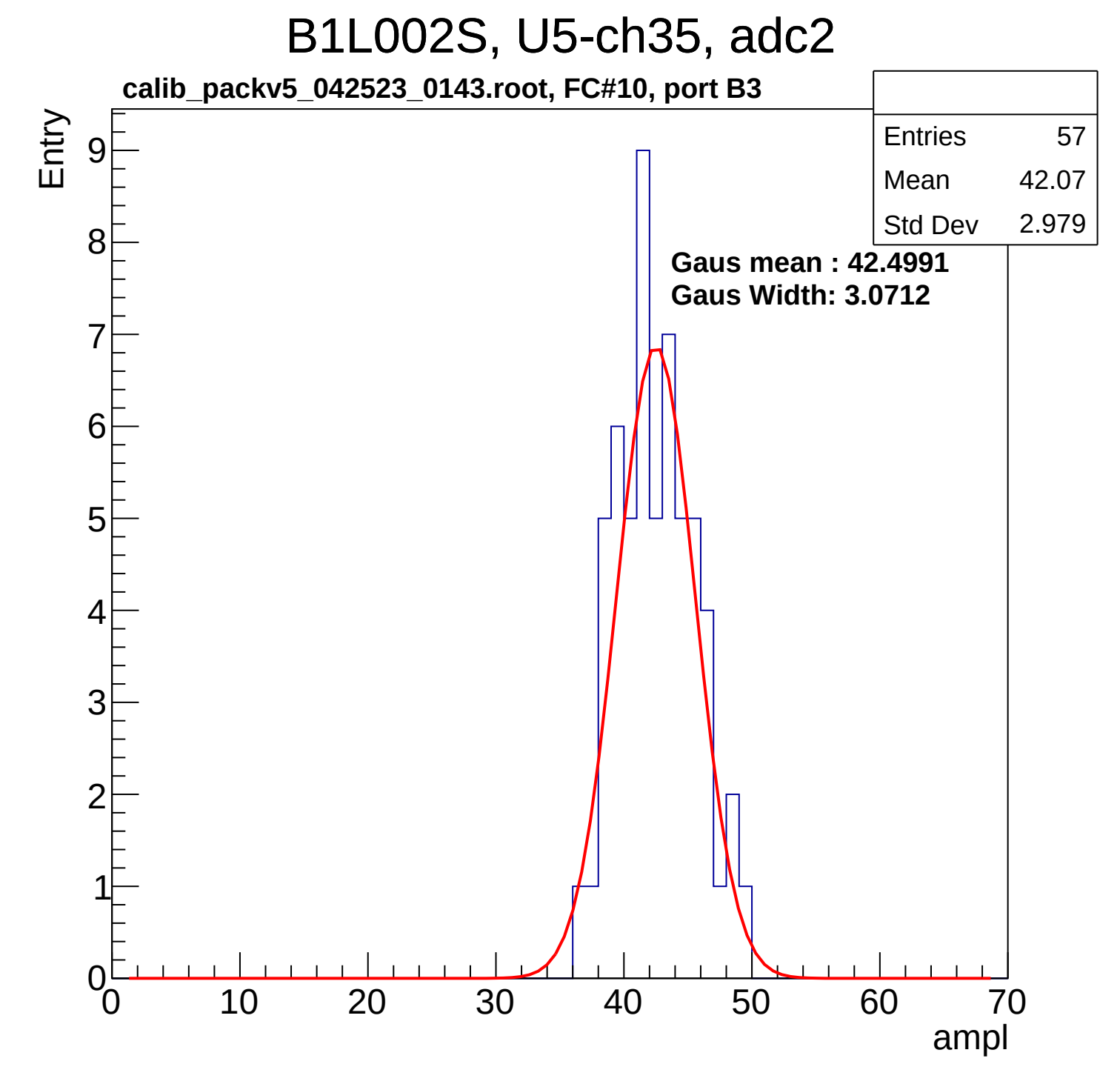
B1L002S, U5-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.07
Std Dev 2.979
Gaus mean : 42.4991
Gaus Width: 3.0712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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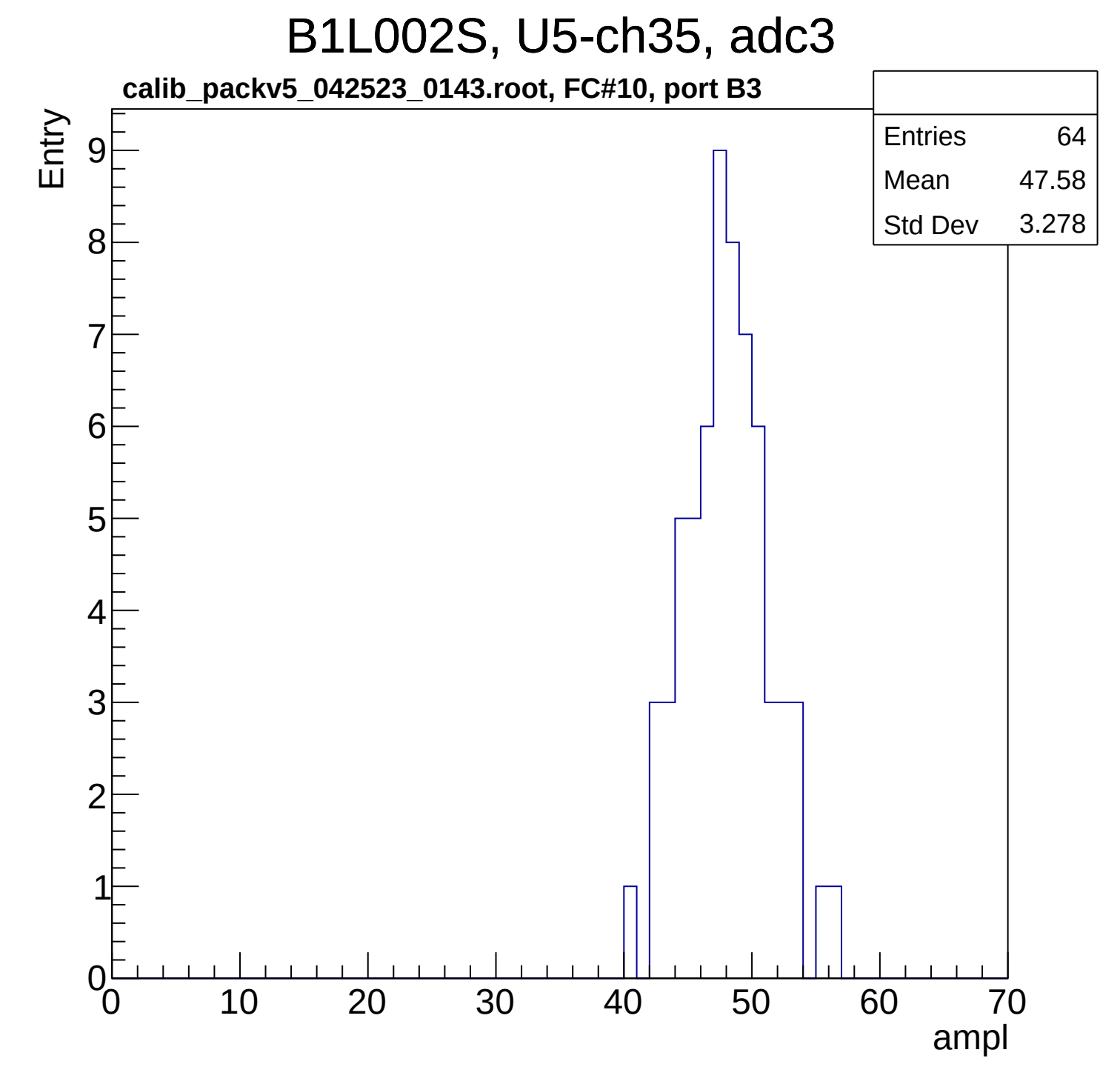
B1L002S, U5-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.58
Std Dev 3.278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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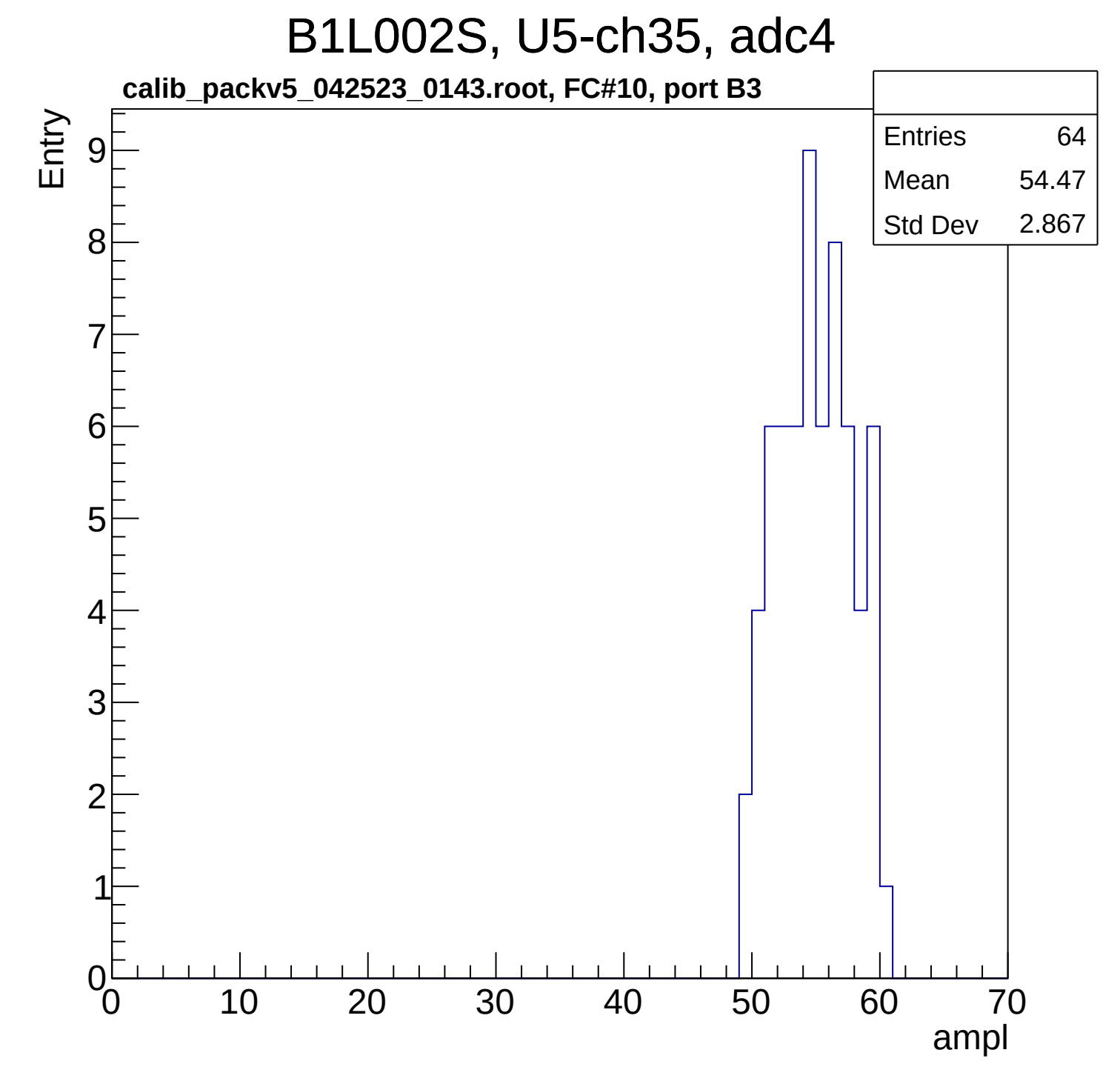
B1L002S, U5-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.47
Std Dev 2.867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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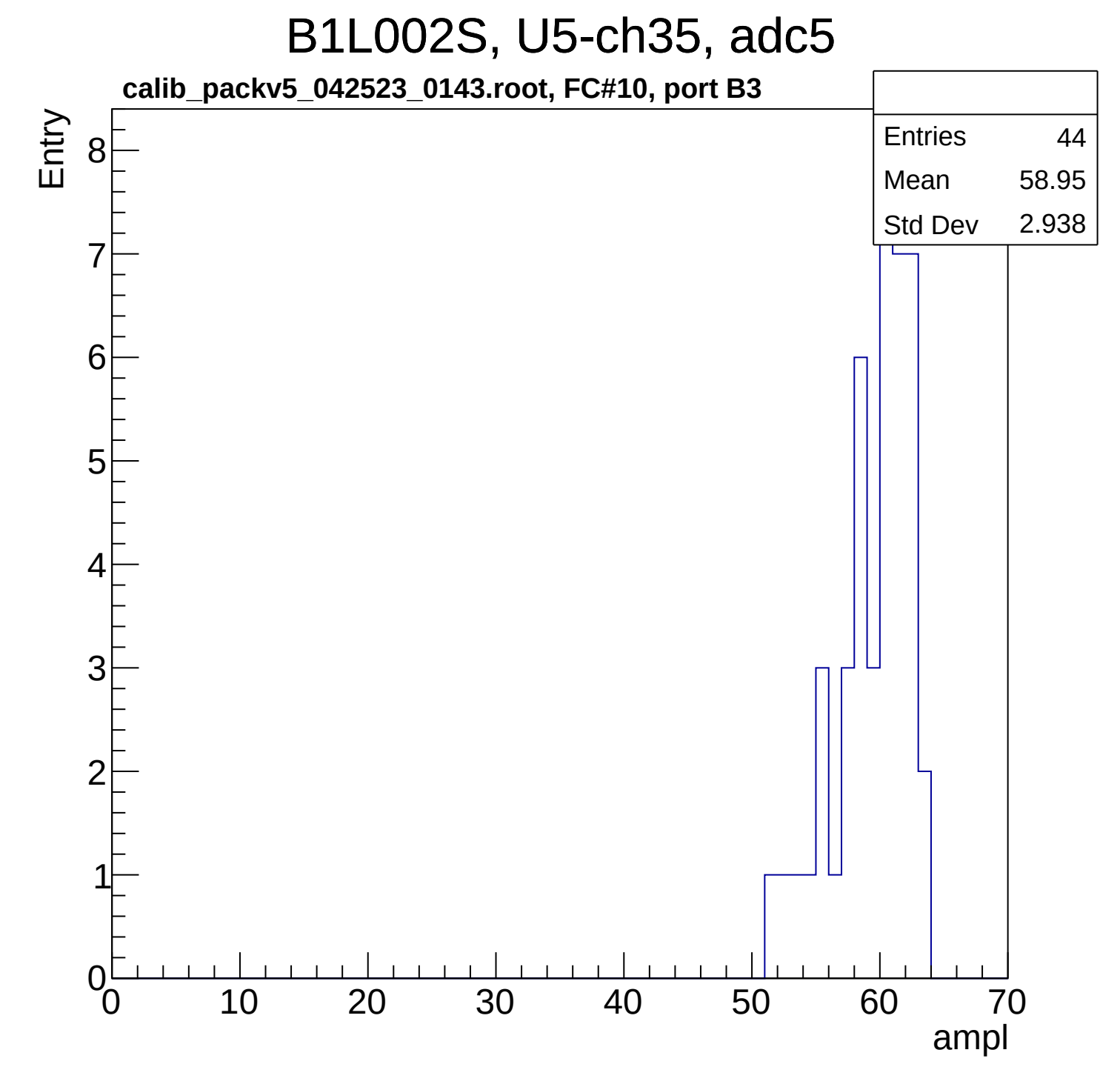
B1L002S, U5-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.95
Std Dev 2.938
Entry
0
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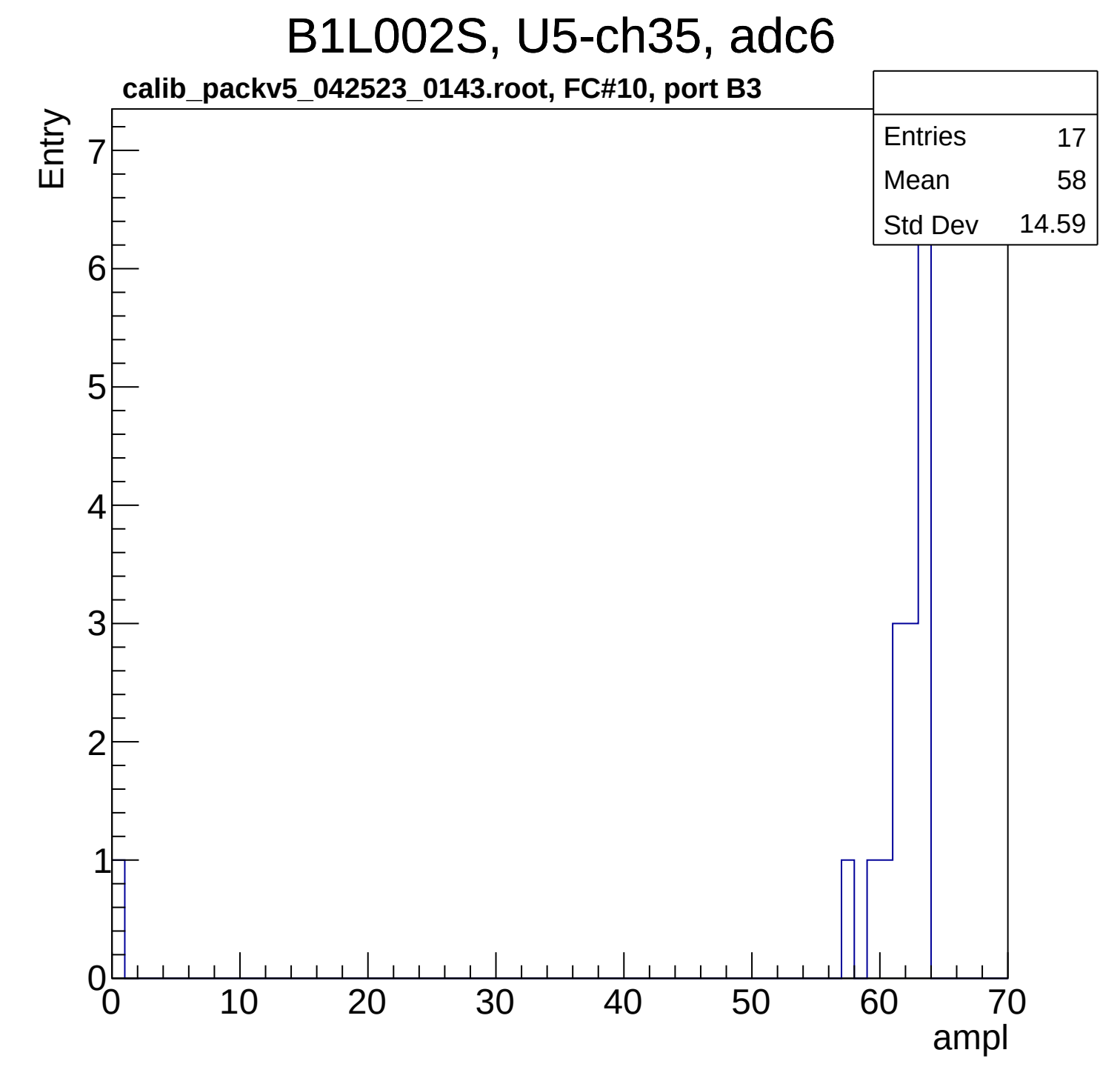
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58
Std Dev 14.59
Entry
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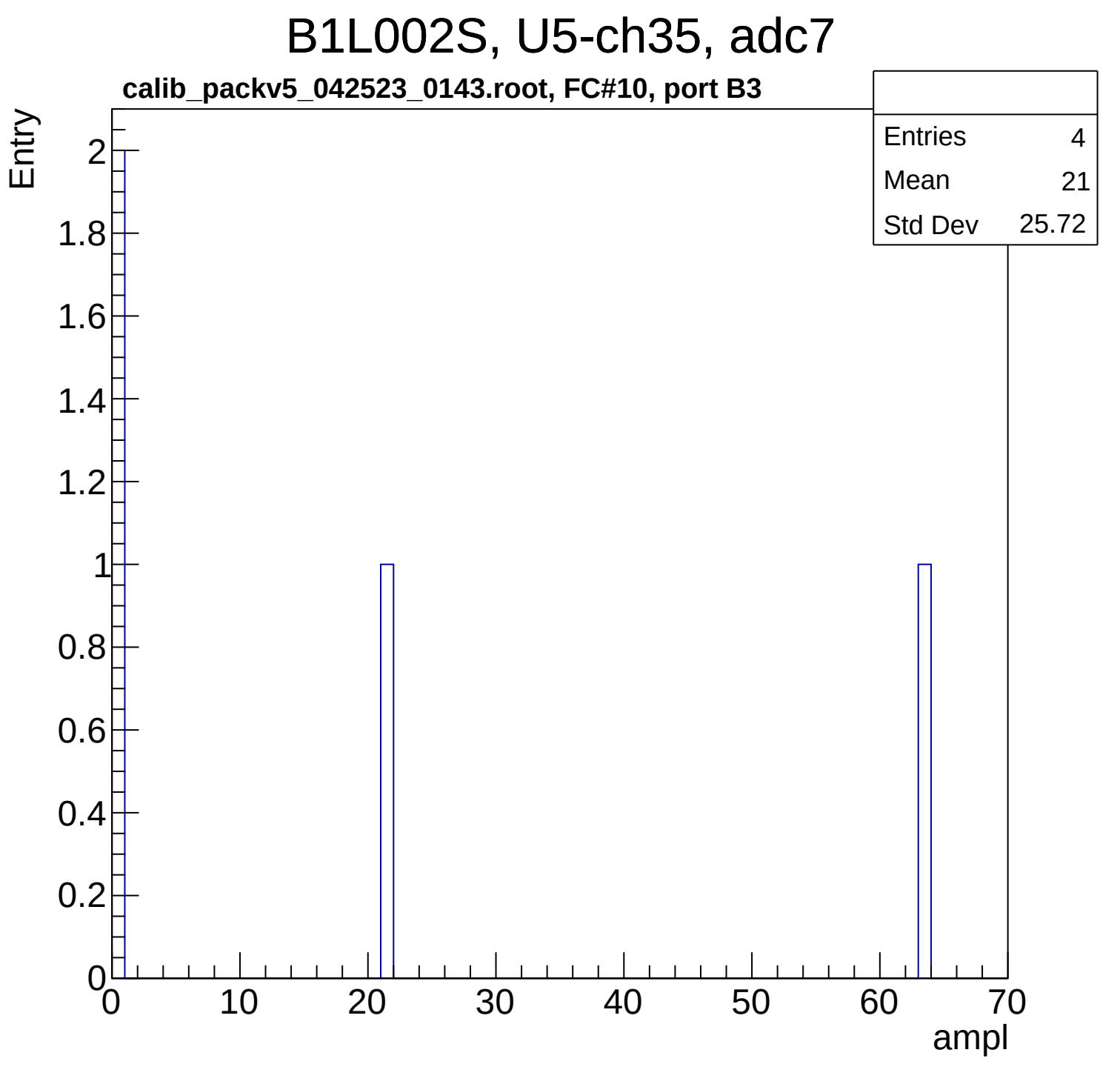
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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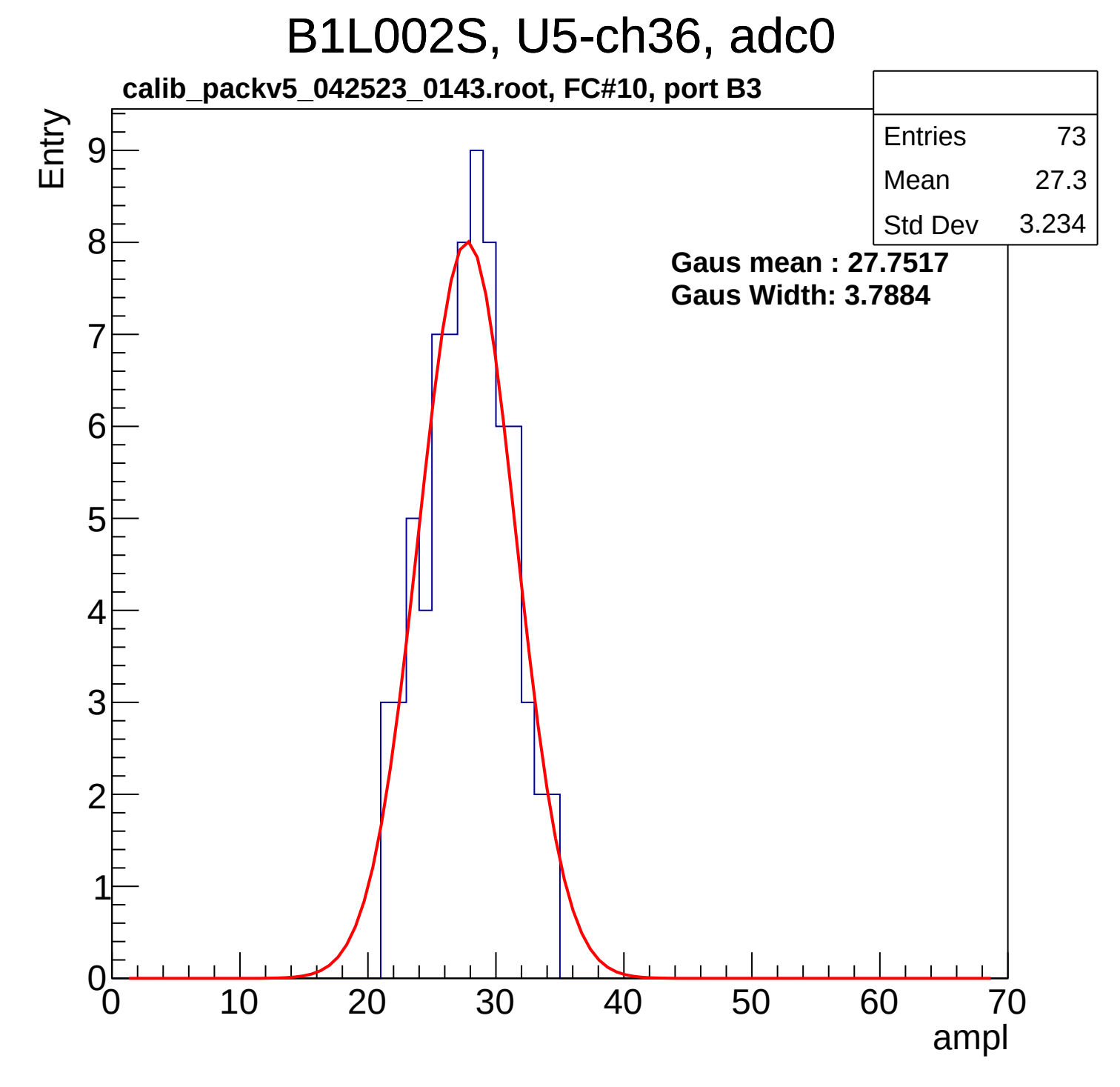
40
50
60
70


B1L002S, U5-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.3
Std Dev 3.234
Gaus mean : 27.7517
Gaus Width: 3.7884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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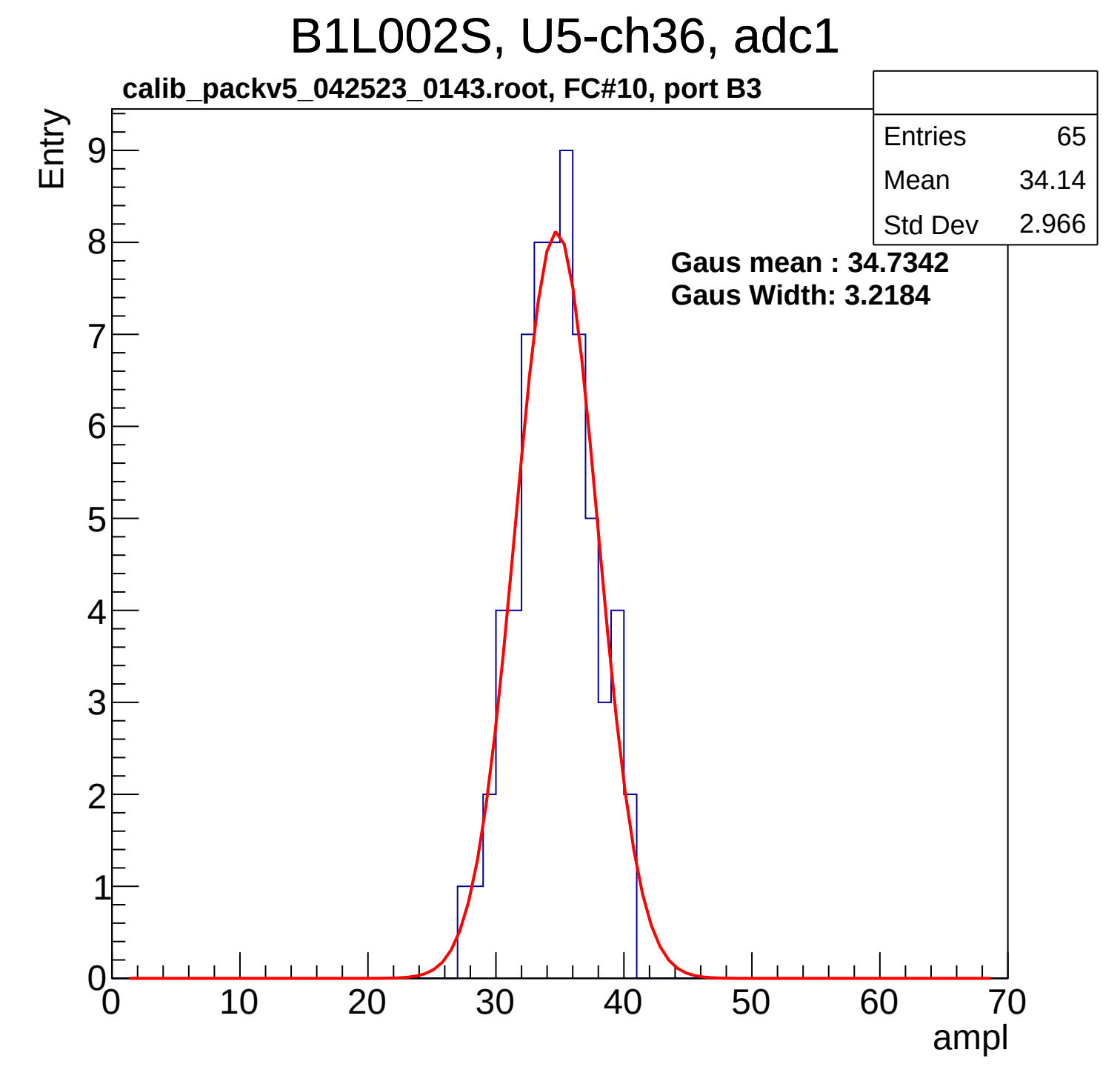
B1L002S, U5-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.14
Std Dev 2.966
Gaus mean : 34.7342
Gaus Width: 3.2184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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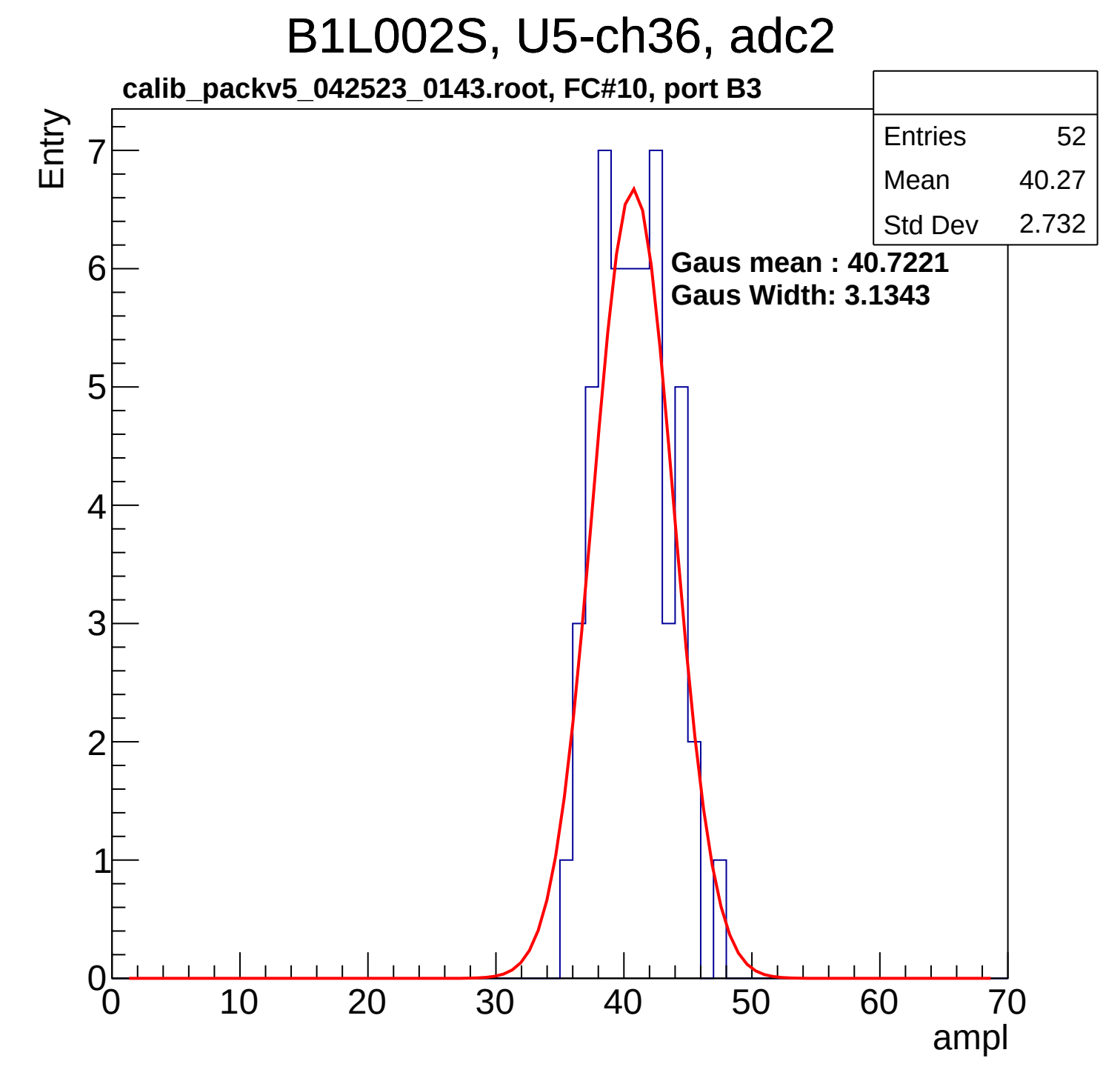
B1L002S, U5-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.27
Std Dev 2.732
Gaus mean : 40.7221
Gaus Width: 3.1343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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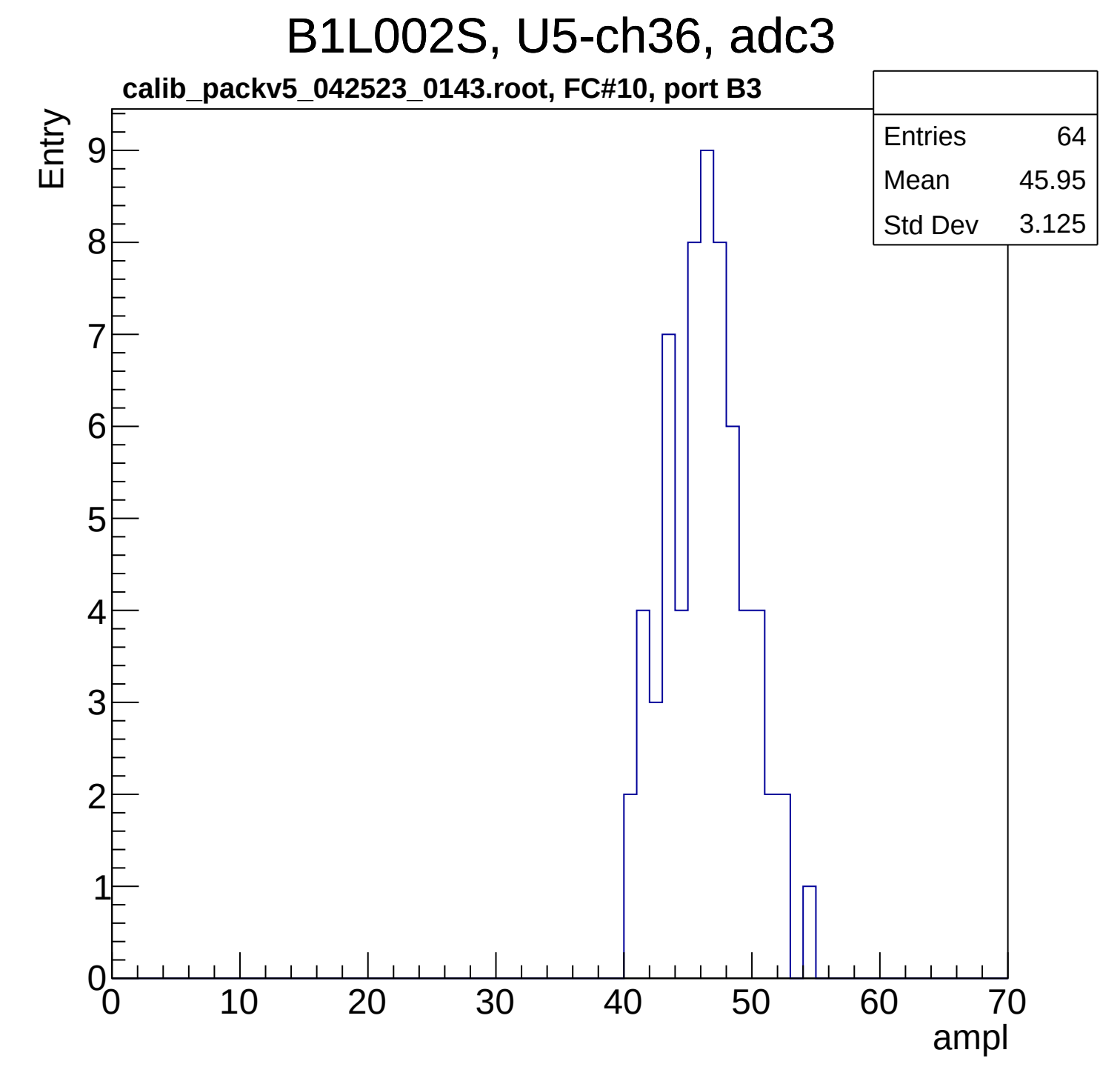
B1L002S, U5-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.95
Std Dev 3.125
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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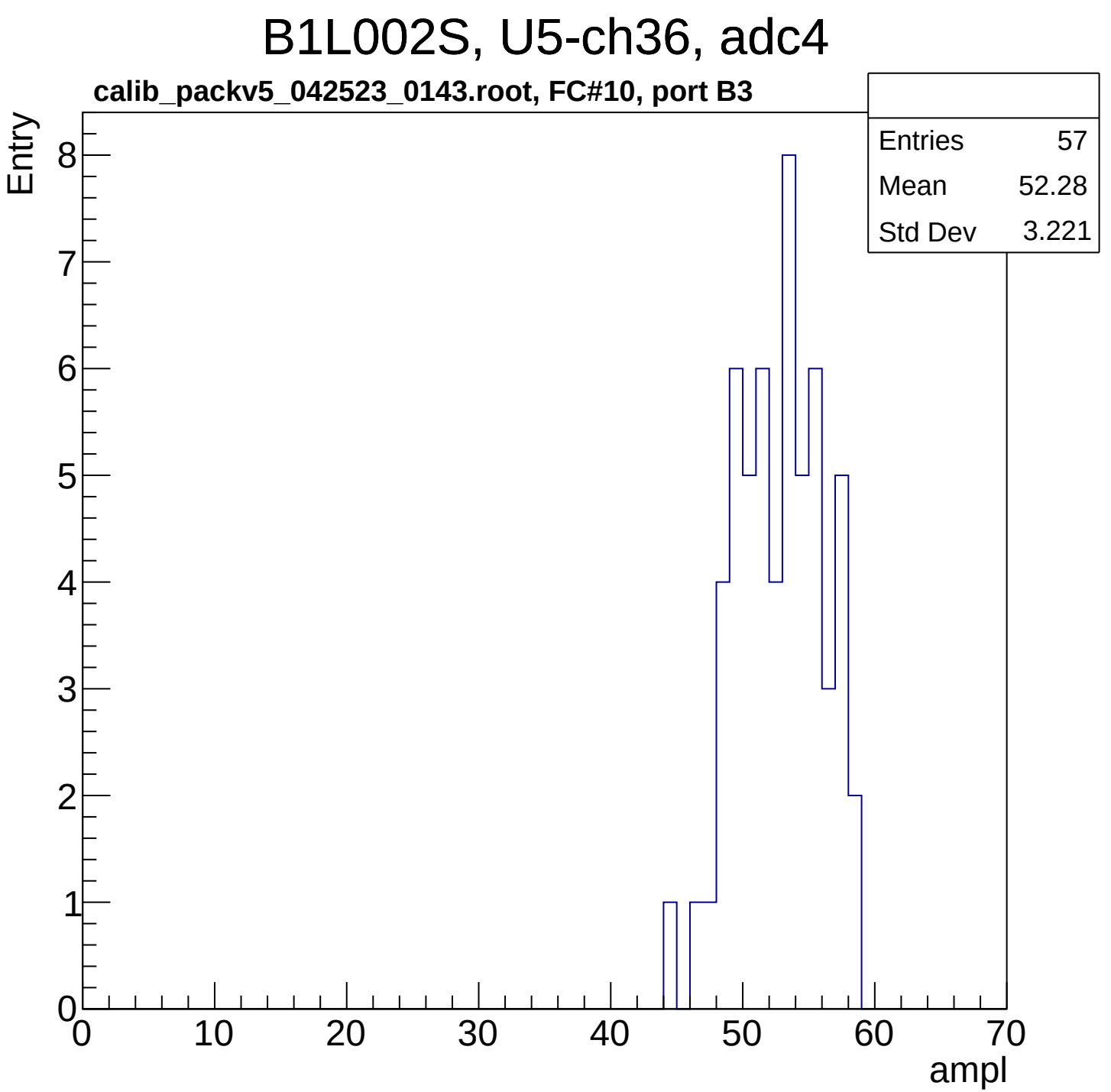
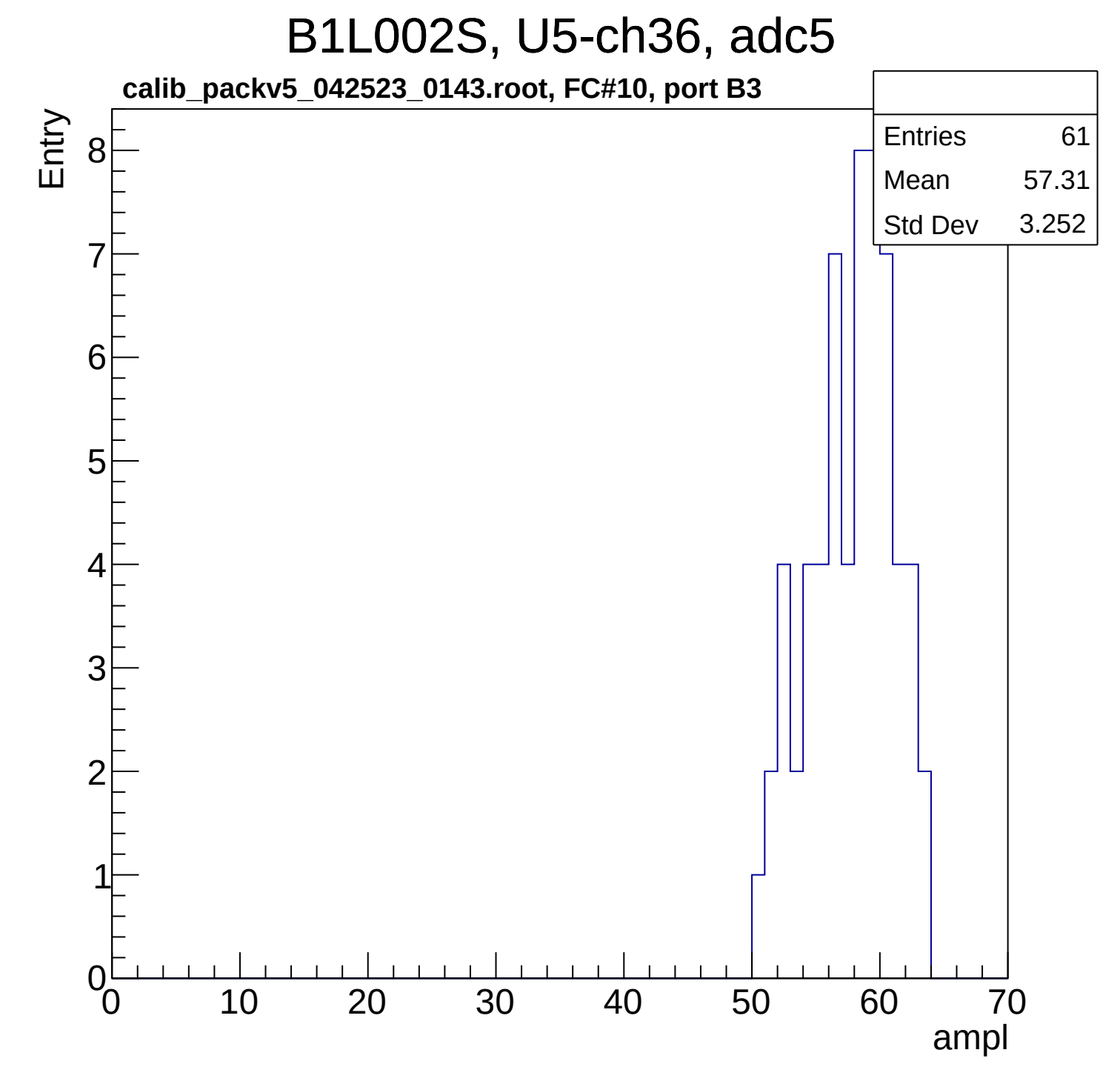

B1L002S, U5-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.28
Std Dev 3.221
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.31
Std Dev 3.252
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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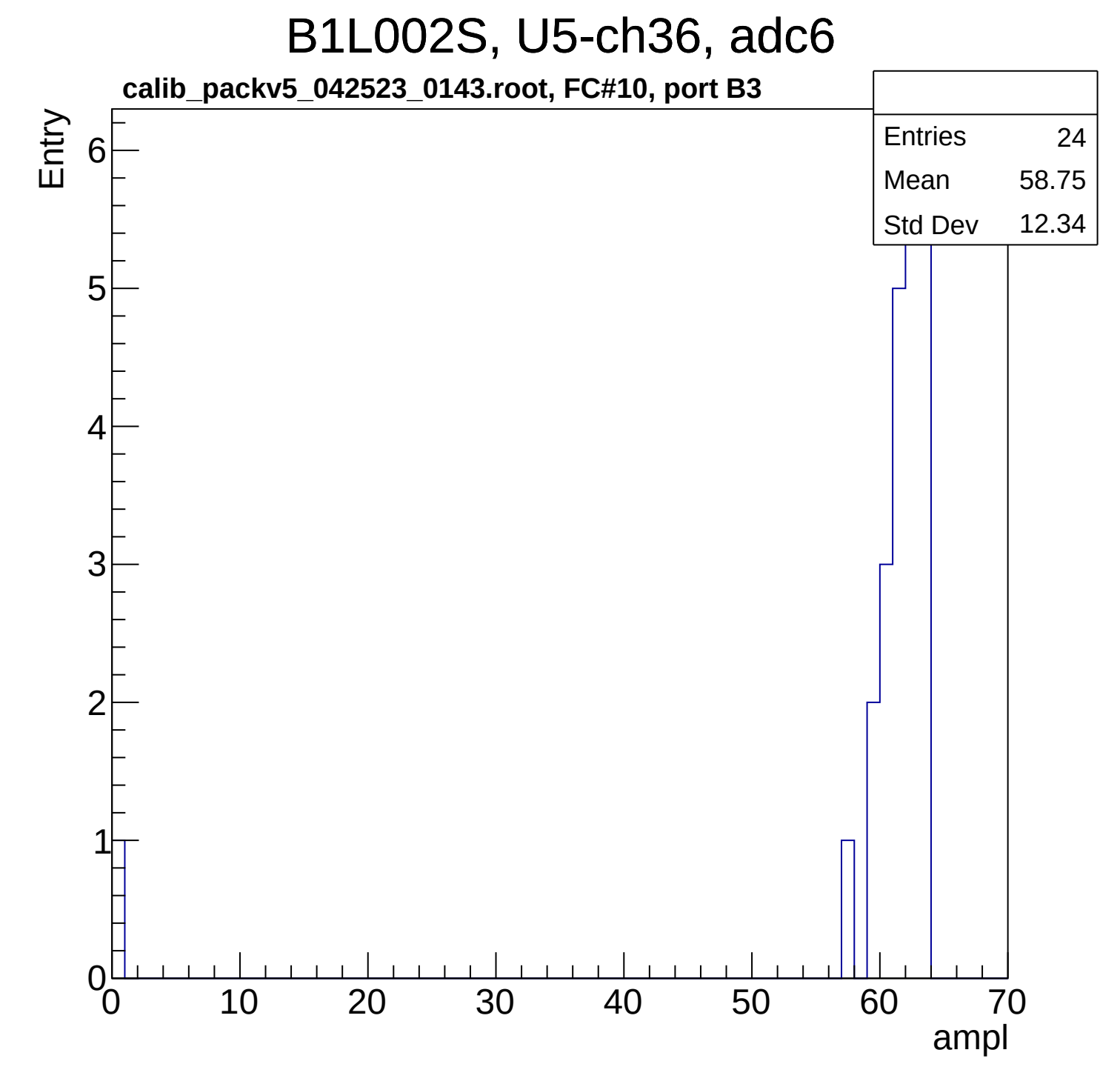
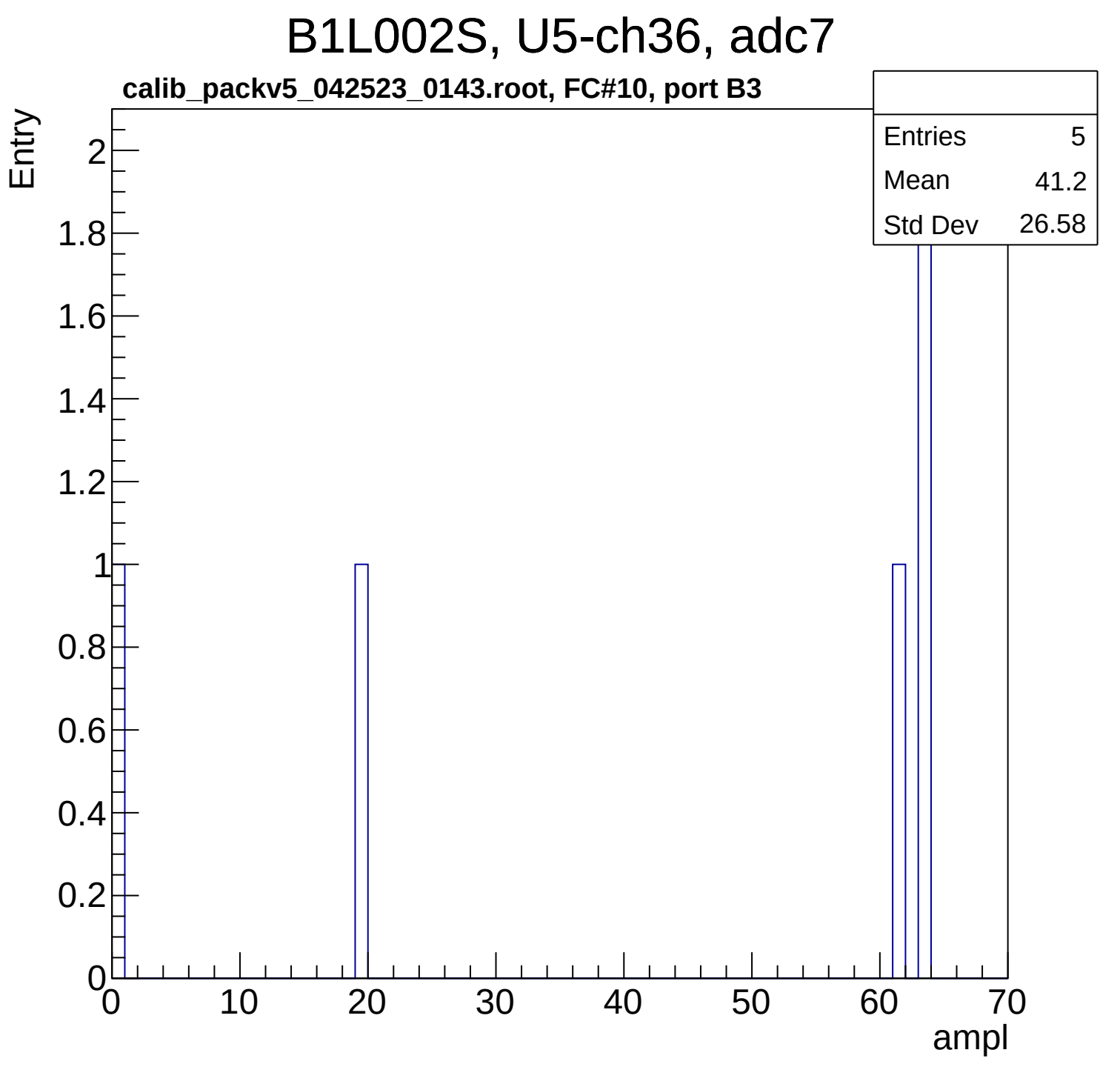

B1L002S, U5-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.75
Std Dev 12.34
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U5-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 41.2
Std Dev 26.58
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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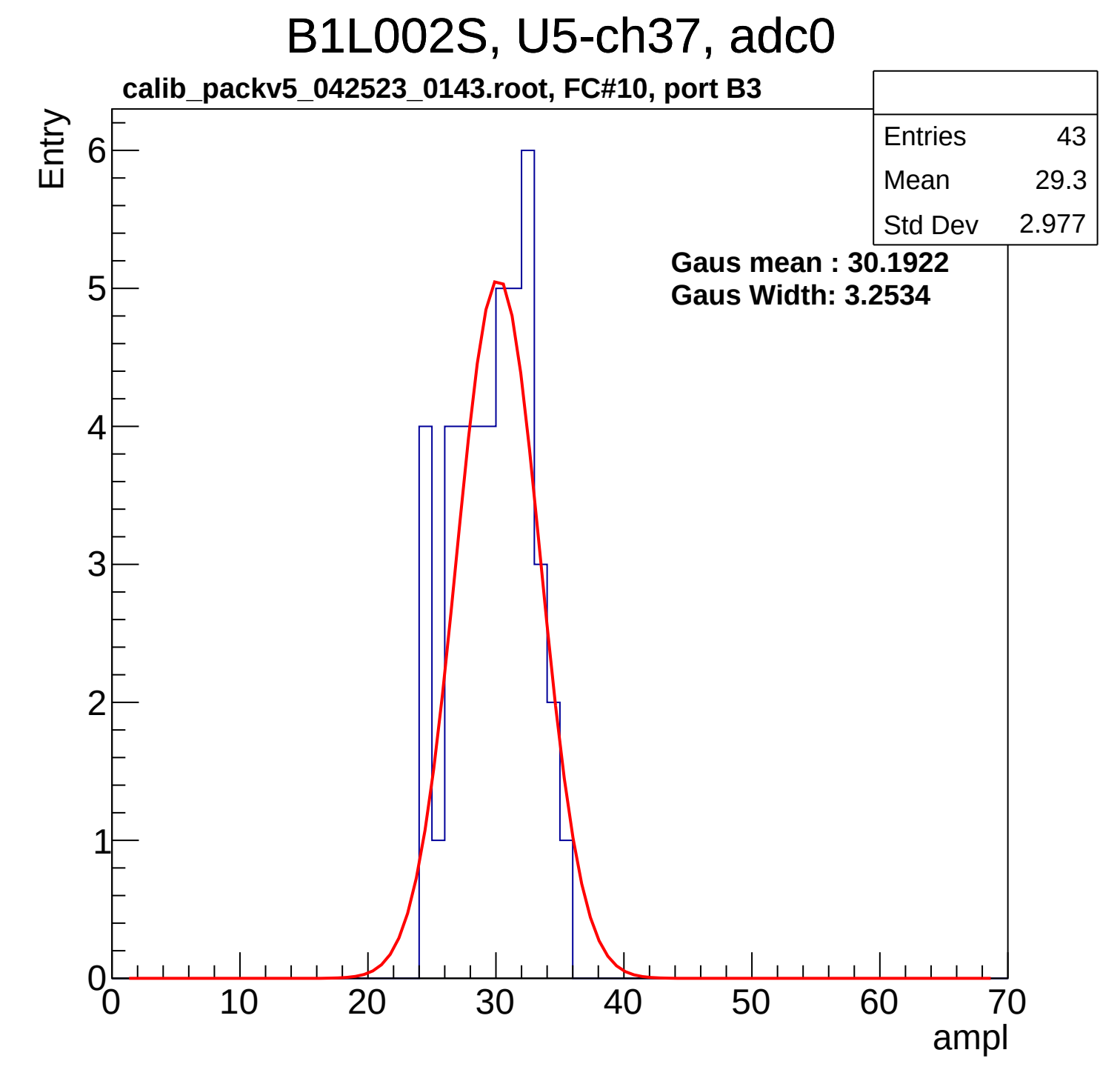
B1L002S, U5-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 29.3
Std Dev 2.977
Gaus mean : 30.1922
Gaus Width: 3.2534
Entry
ampl
0
1
2
3
4
5
6
0
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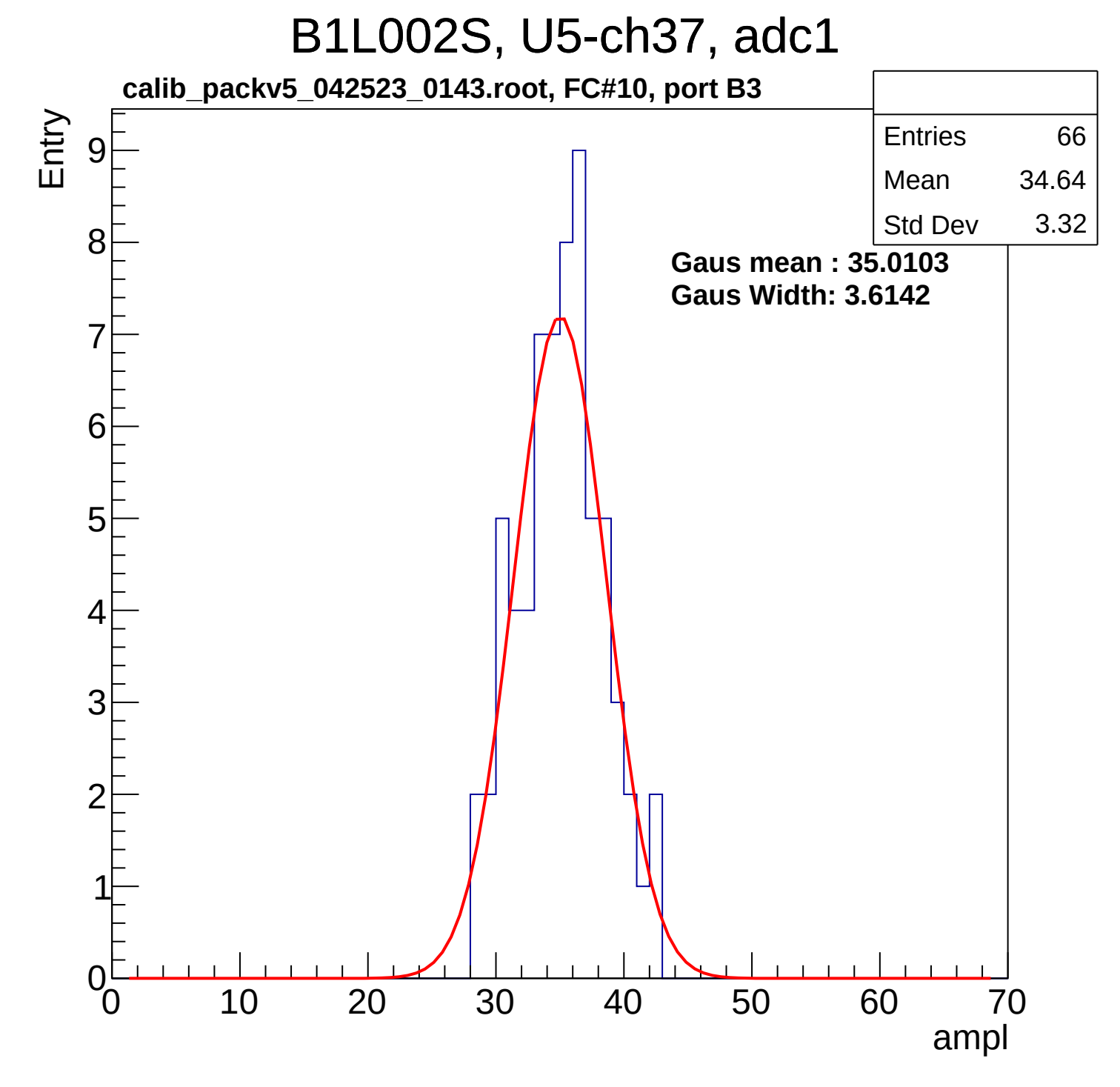
20
30
40
50
60
70


B1L002S, U5-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.64
Std Dev 3.32
Gaus mean : 35.0103
Gaus Width: 3.6142
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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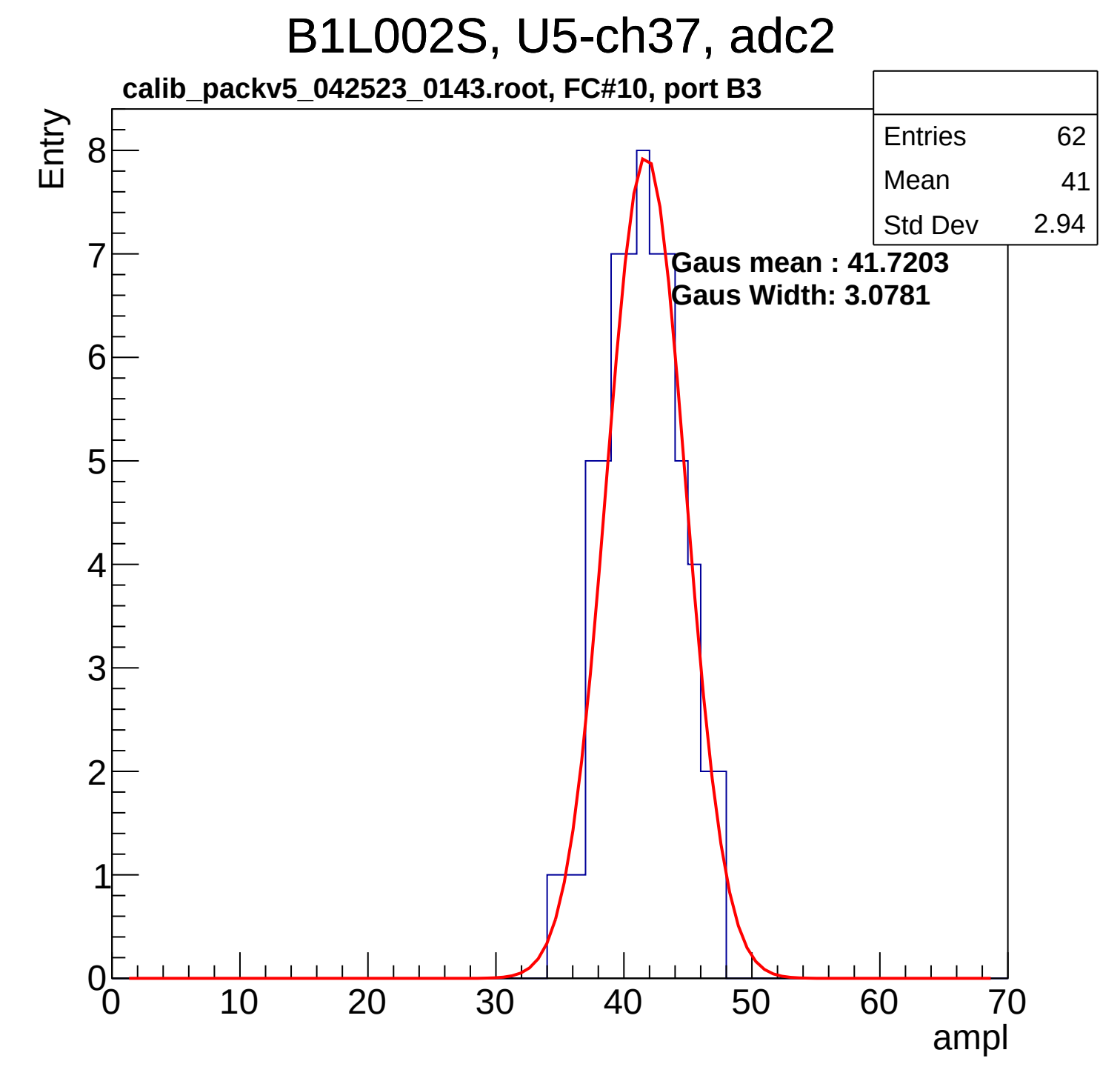
B1L002S, U5-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41
Std Dev 2.94
Gaus mean : 41.7203
Gaus Width: 3.0781
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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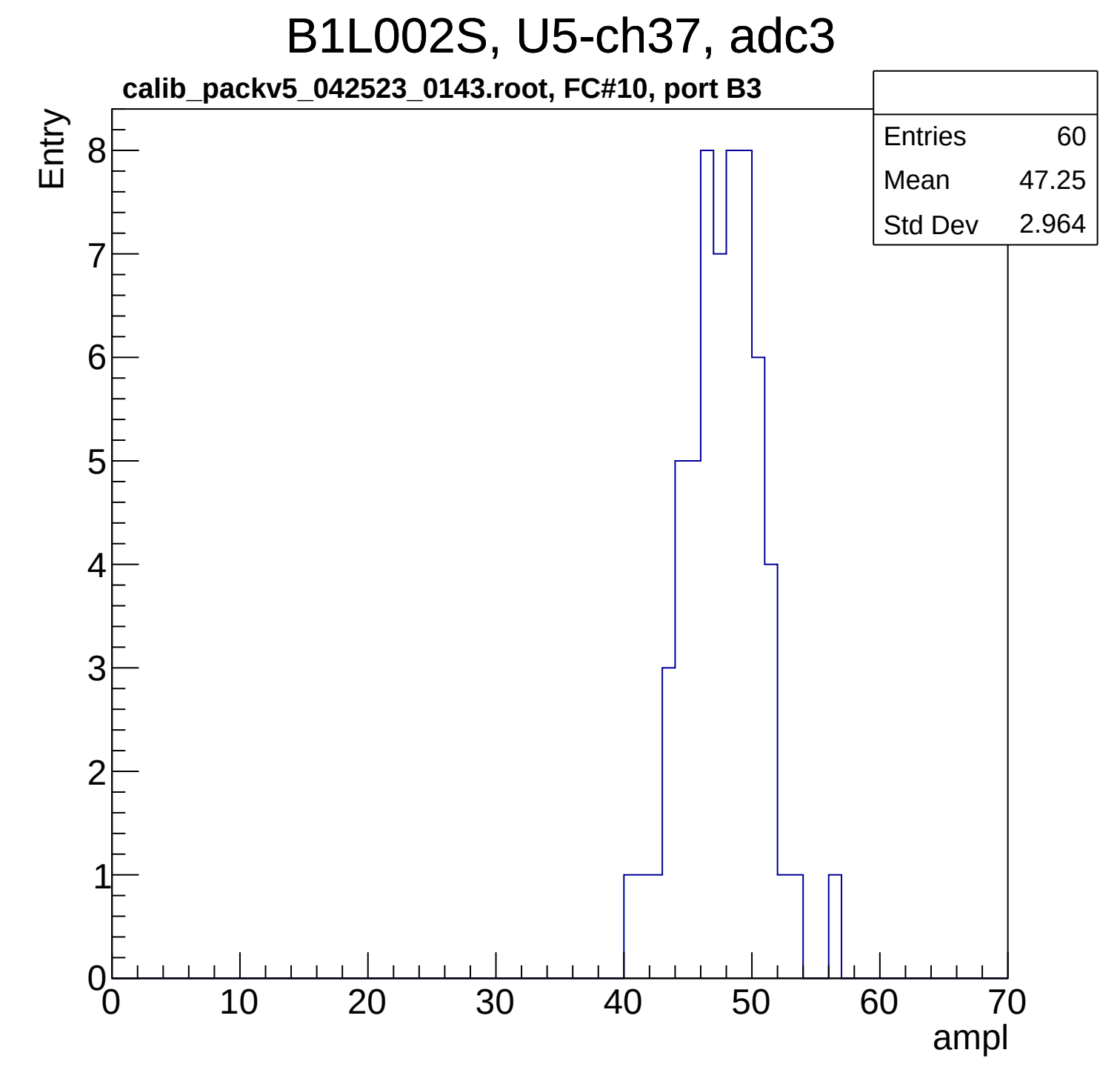
B1L002S, U5-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.25
Std Dev 2.964
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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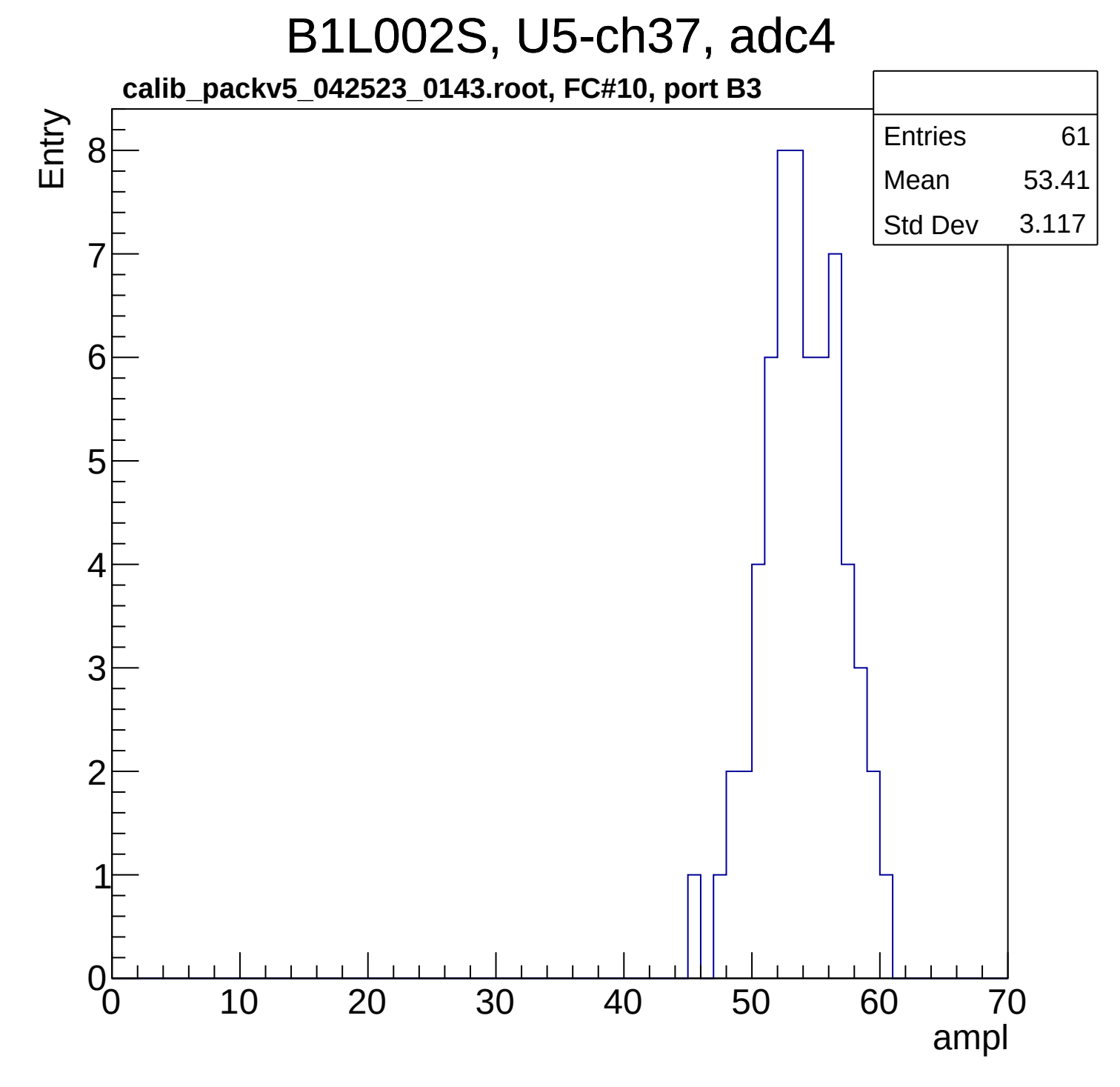
40
50
60
70


B1L002S, U5-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.41
Std Dev 3.117
Entry
ampl
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1
2
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4
5
6
7
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0
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20
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60
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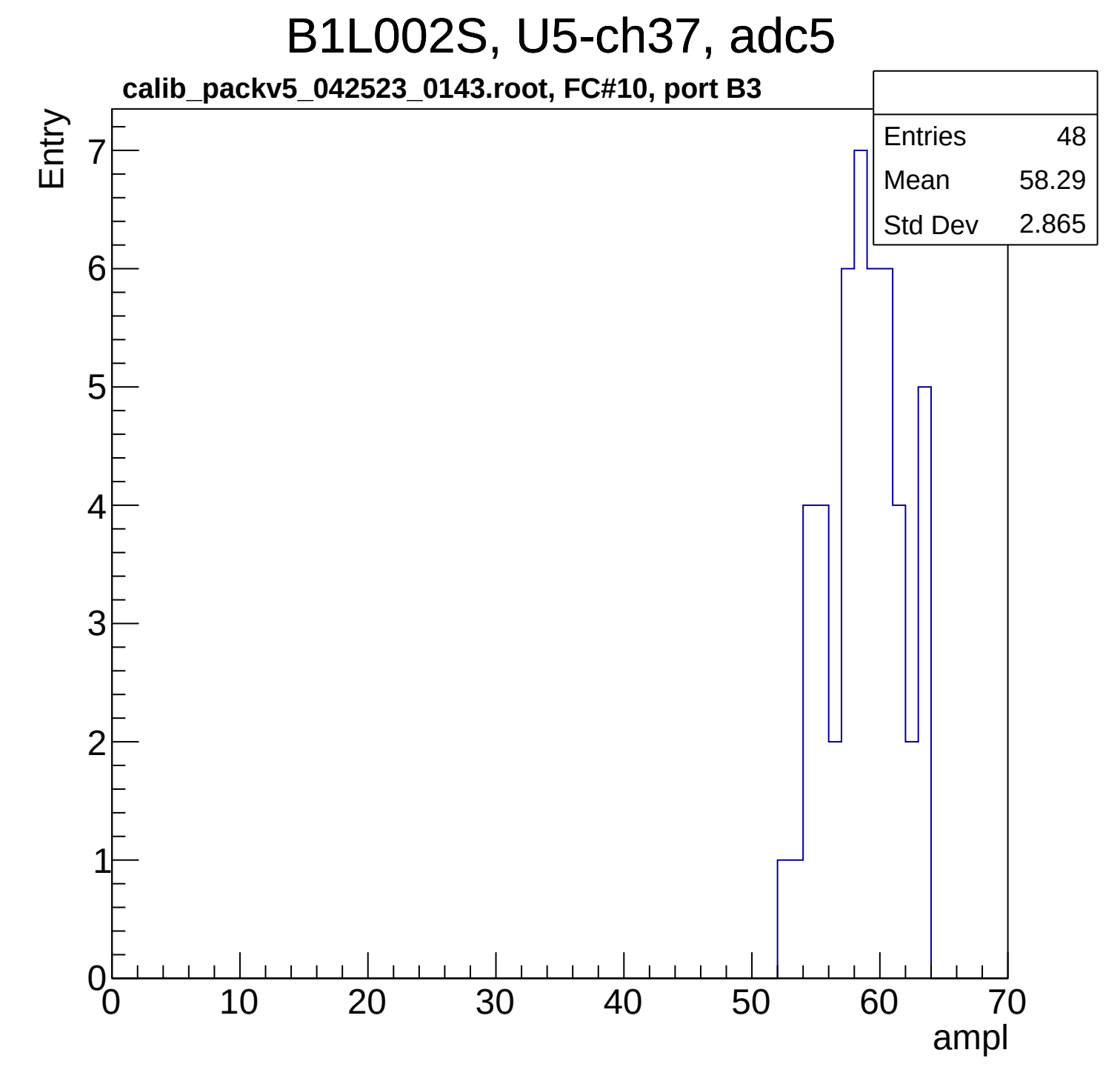
B1L002S, U5-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.29
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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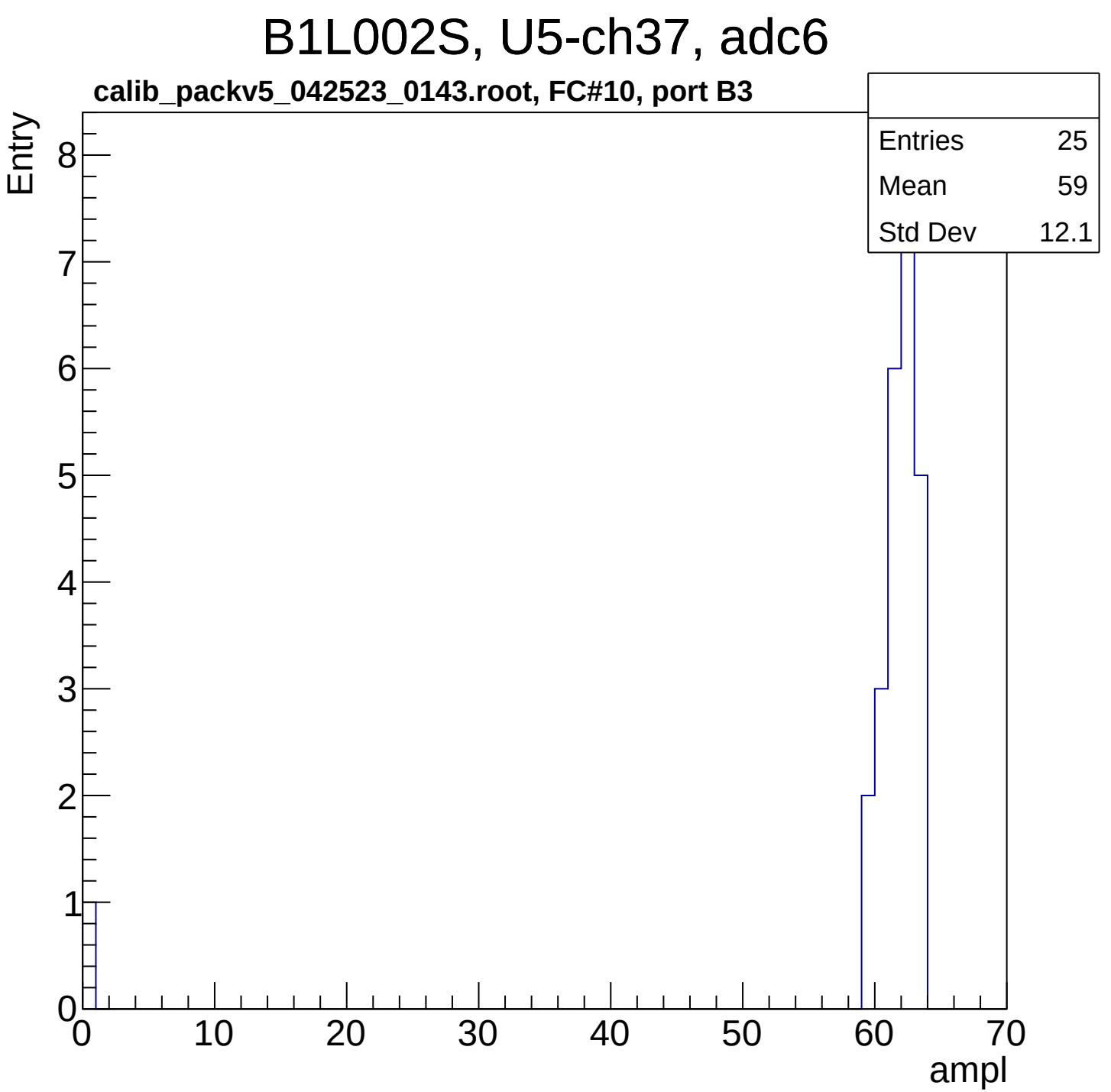
B1L002S, U5-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59
Std Dev 12.1
Entry
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2
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4
5
6
7
8
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10
20
30
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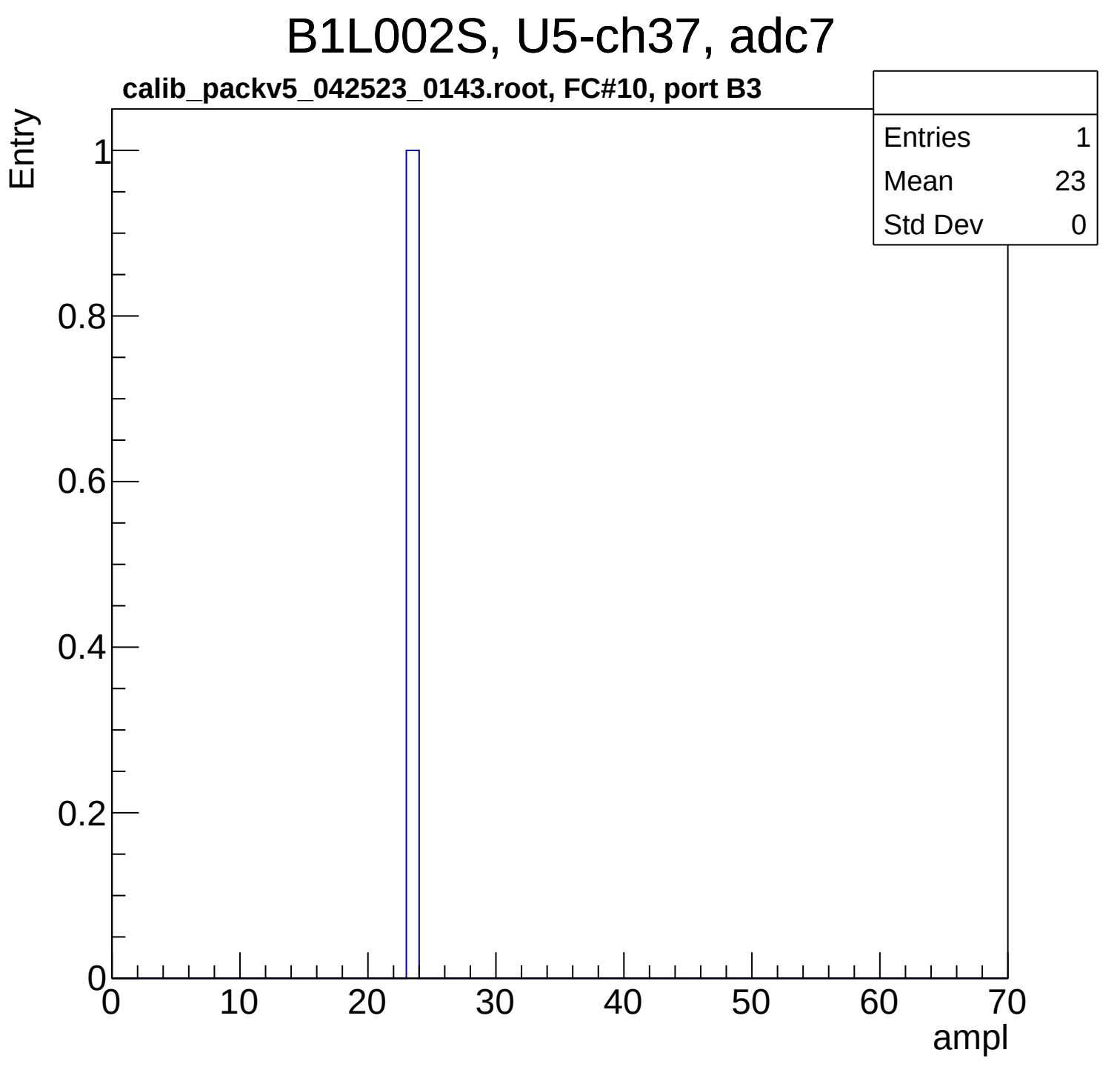
50
60
70
ampl


B1L002S, U5-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
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10
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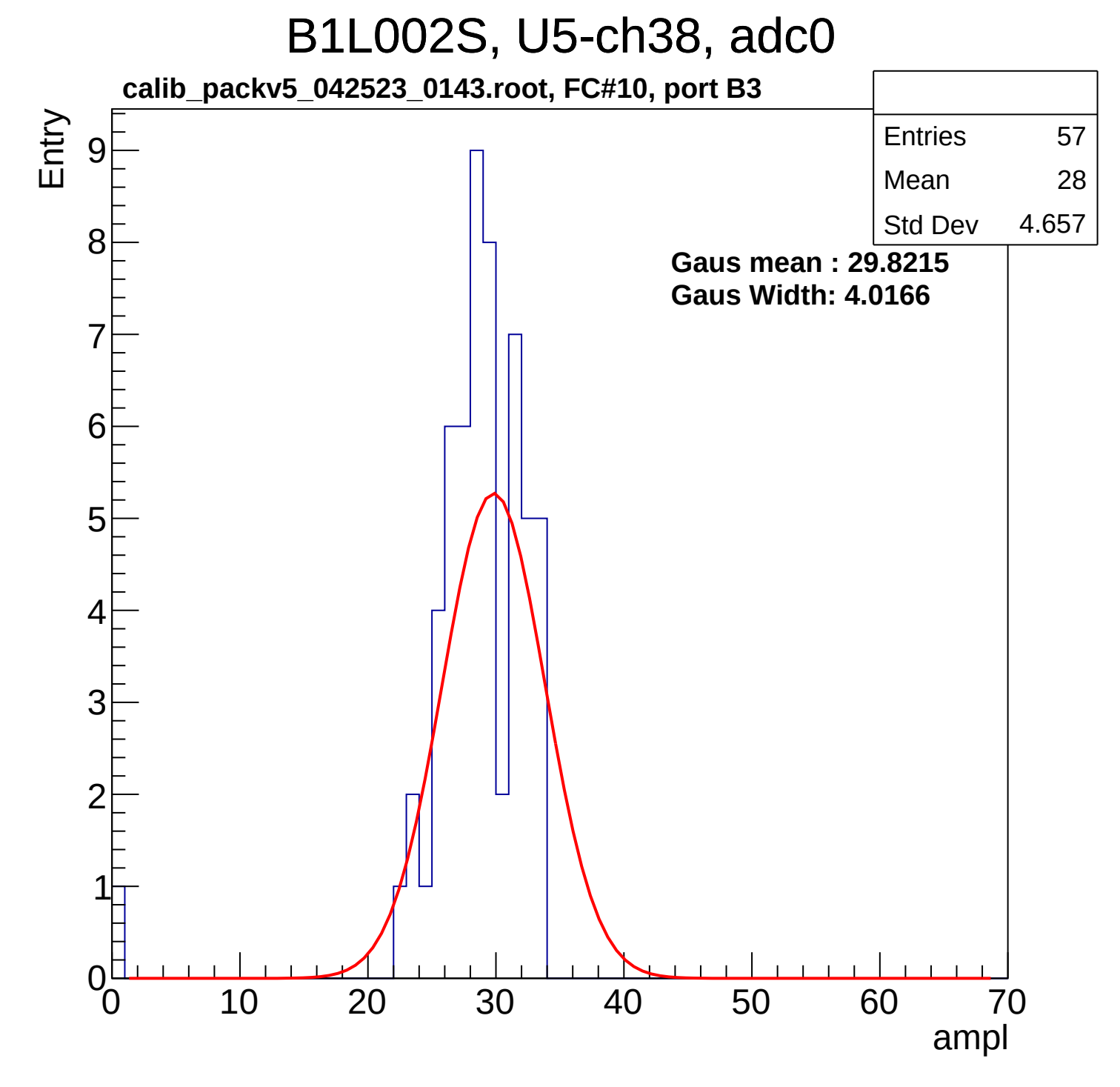
50
60
70
ampl


B1L002S, U5-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28
Std Dev 4.657
Gaus mean : 29.8215
Gaus Width: 4.0166
Entry
ampl
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3
4
5
6
7
8
9
0
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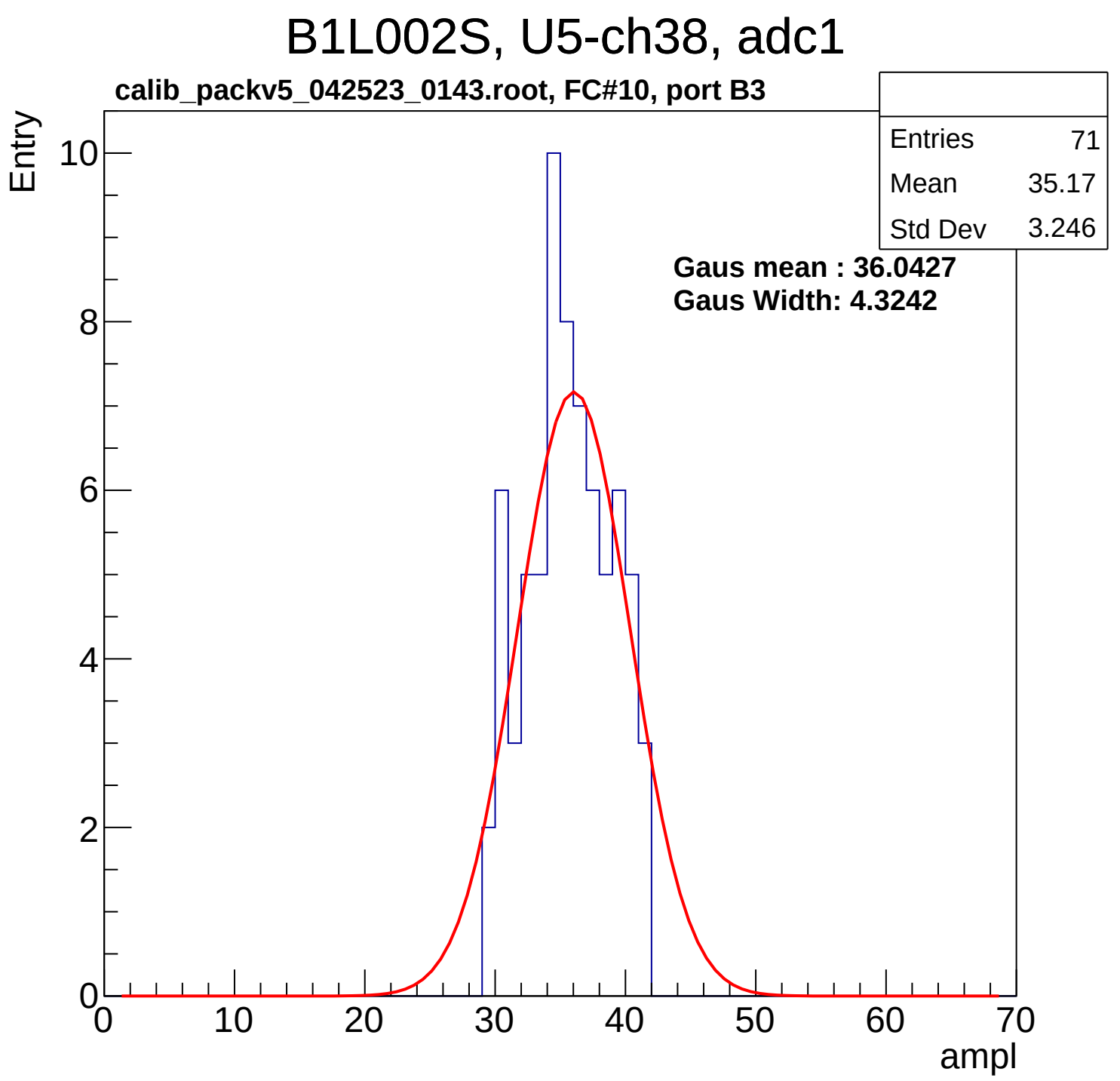
B1L002S, U5-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.17
Std Dev 3.246
Gaus mean : 36.0427
Gaus Width: 4.3242
Entry
ampl
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0
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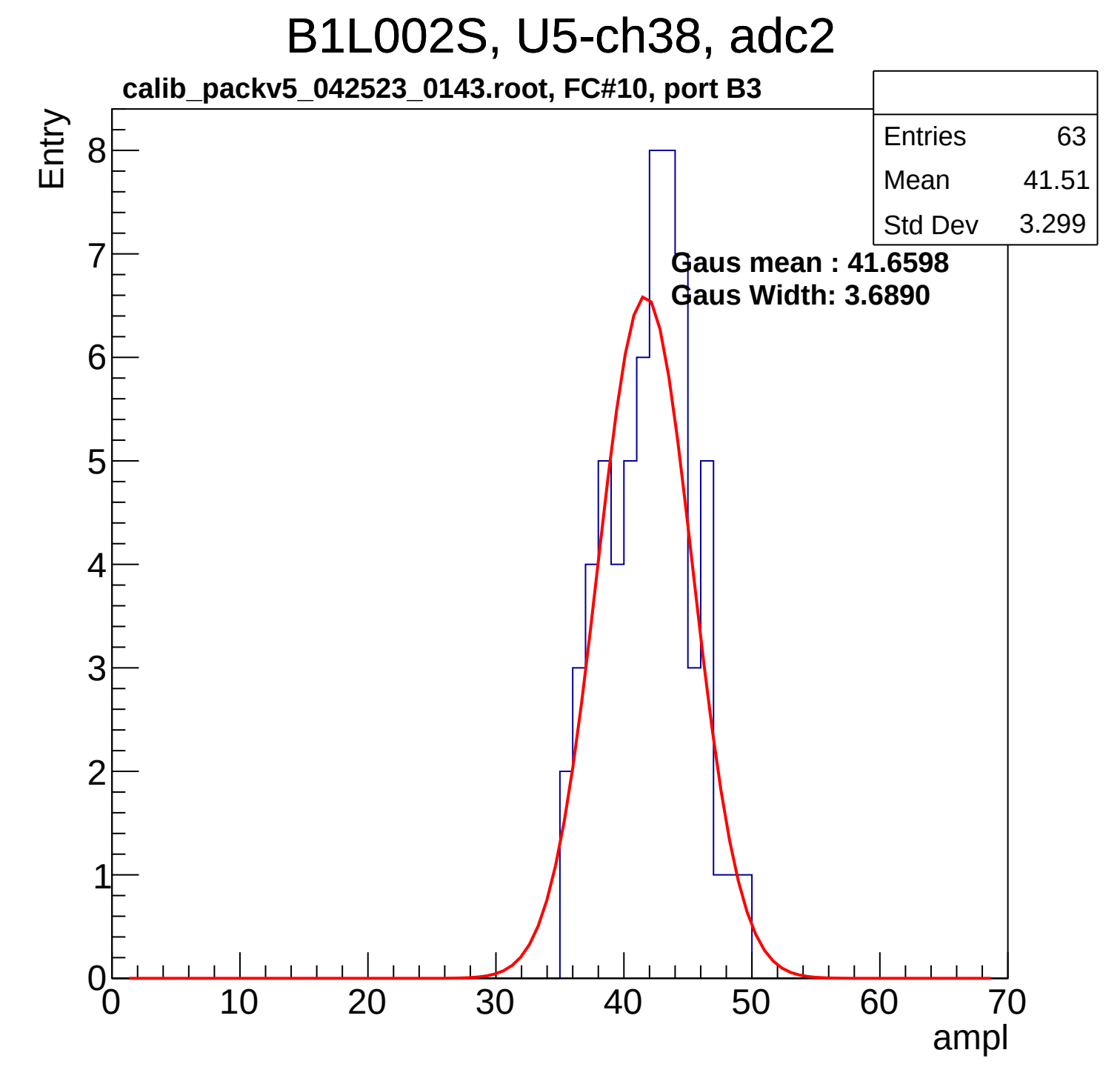
B1L002S, U5-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.51
Std Dev 3.299
Gaus mean : 41.6598
Gaus Width: 3.6890
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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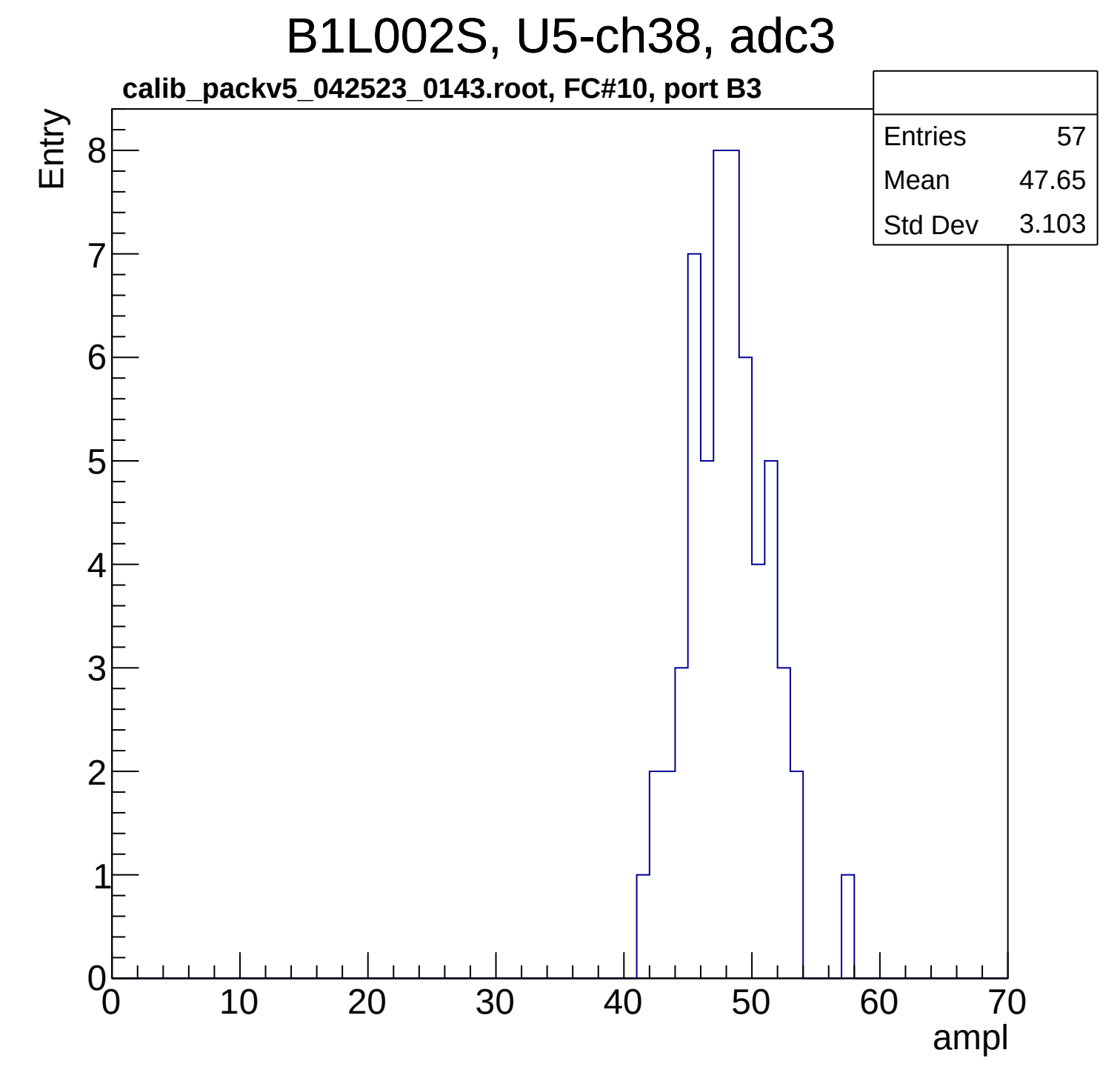
B1L002S, U5-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.65
Std Dev 3.103
Entry
ampl
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3
4
5
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0
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20
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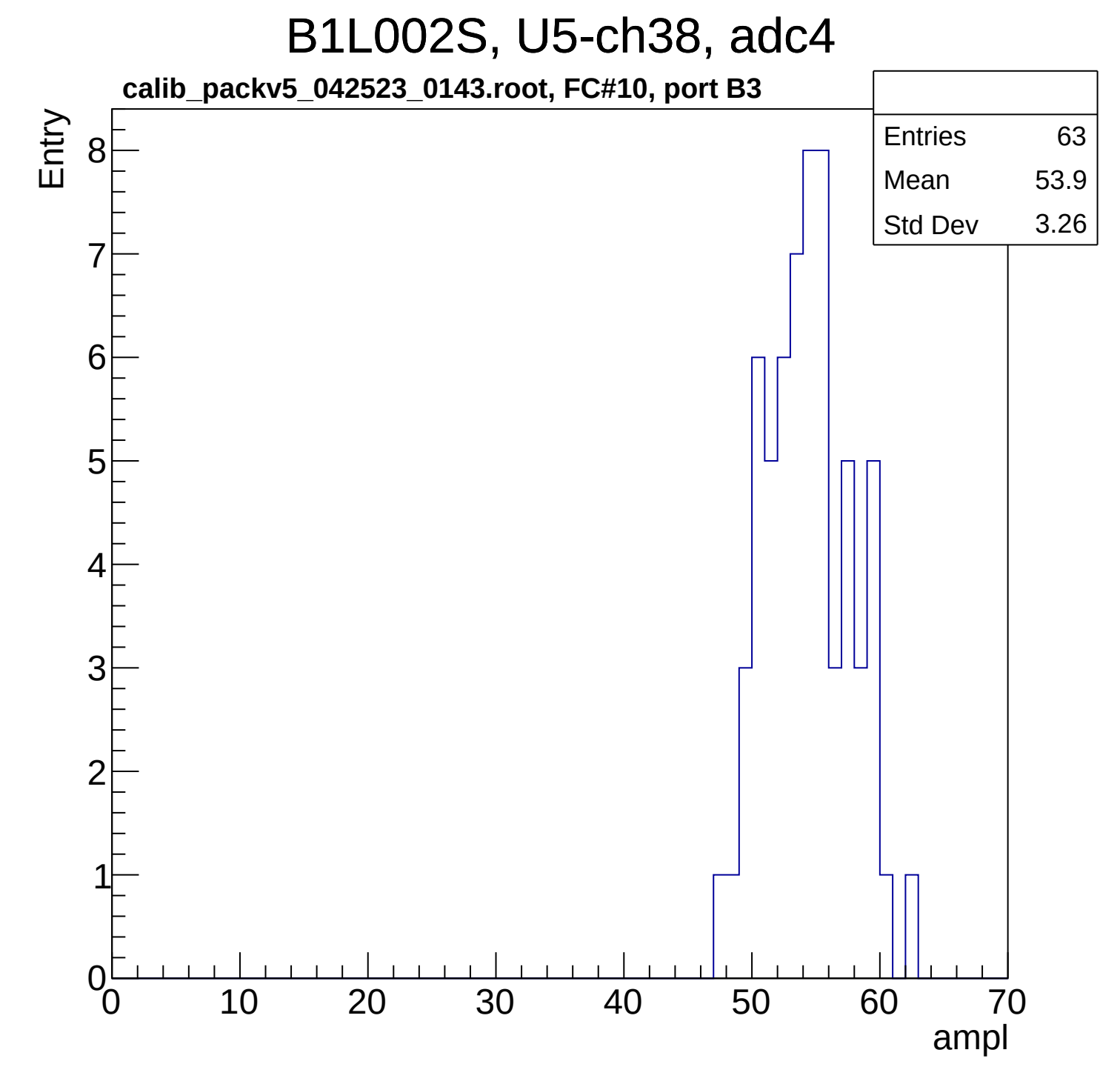
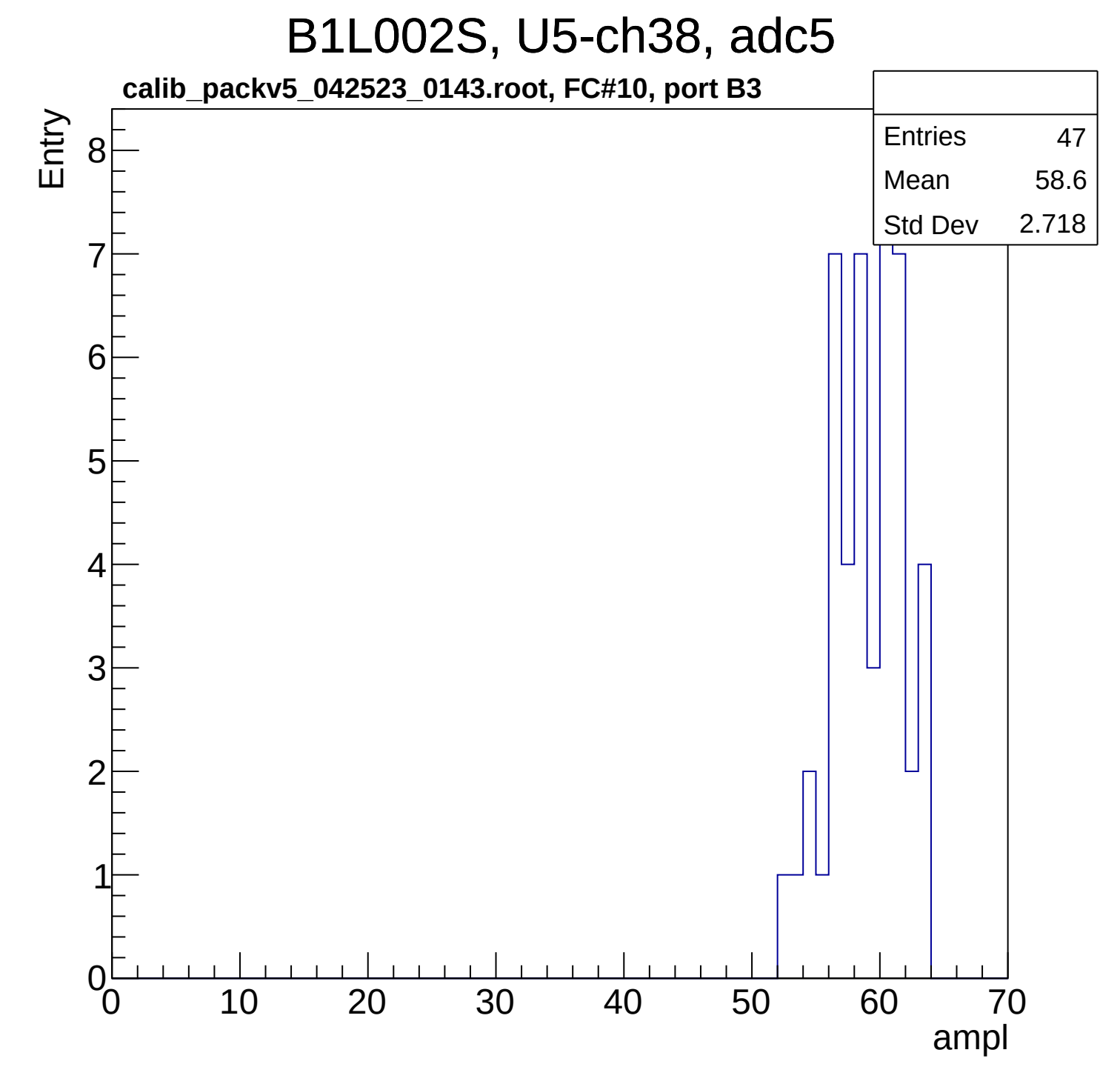
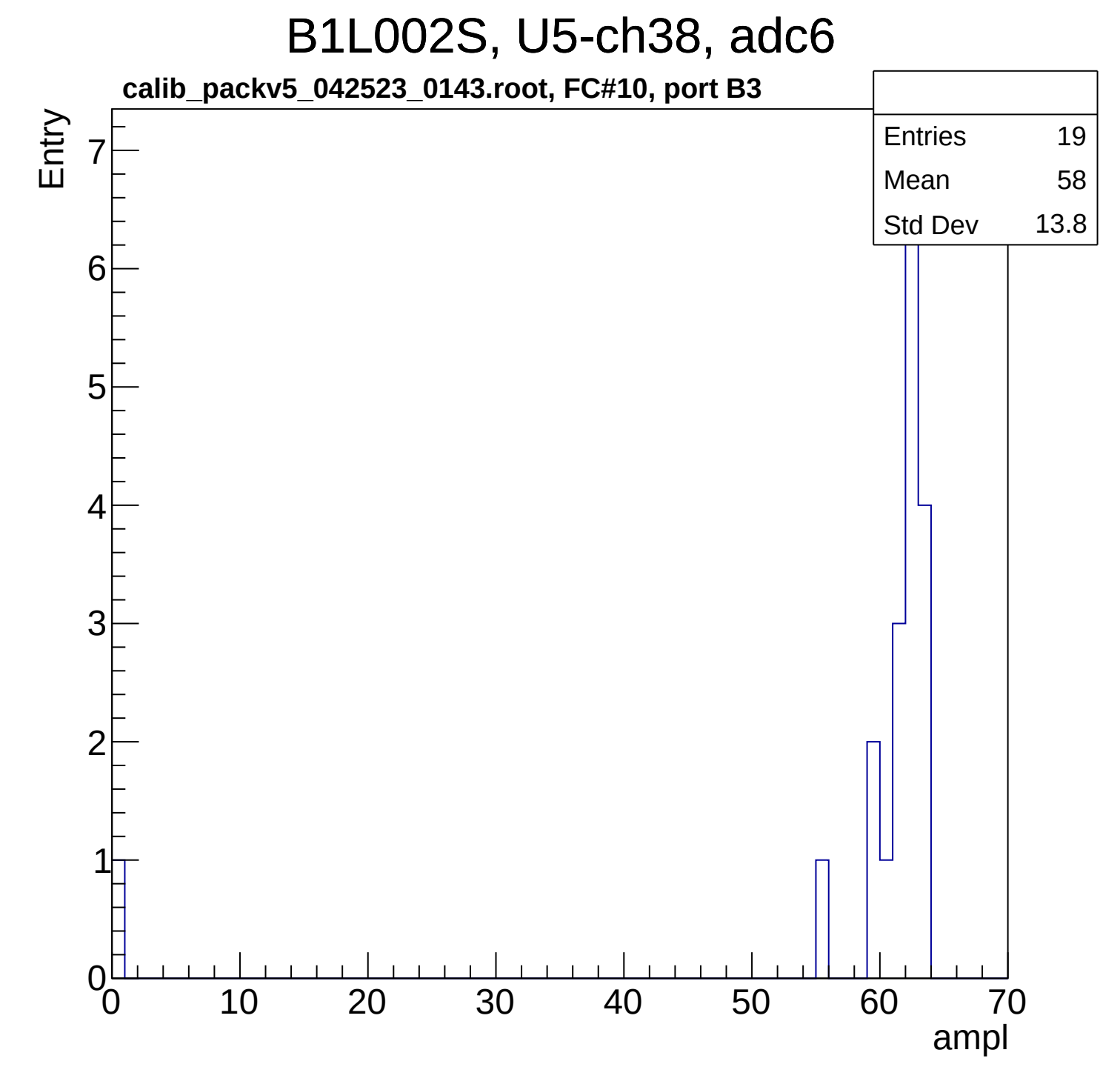

B1L002S, U5-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.9
Std Dev 3.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.6
Std Dev 2.718
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58
Std Dev 13.8
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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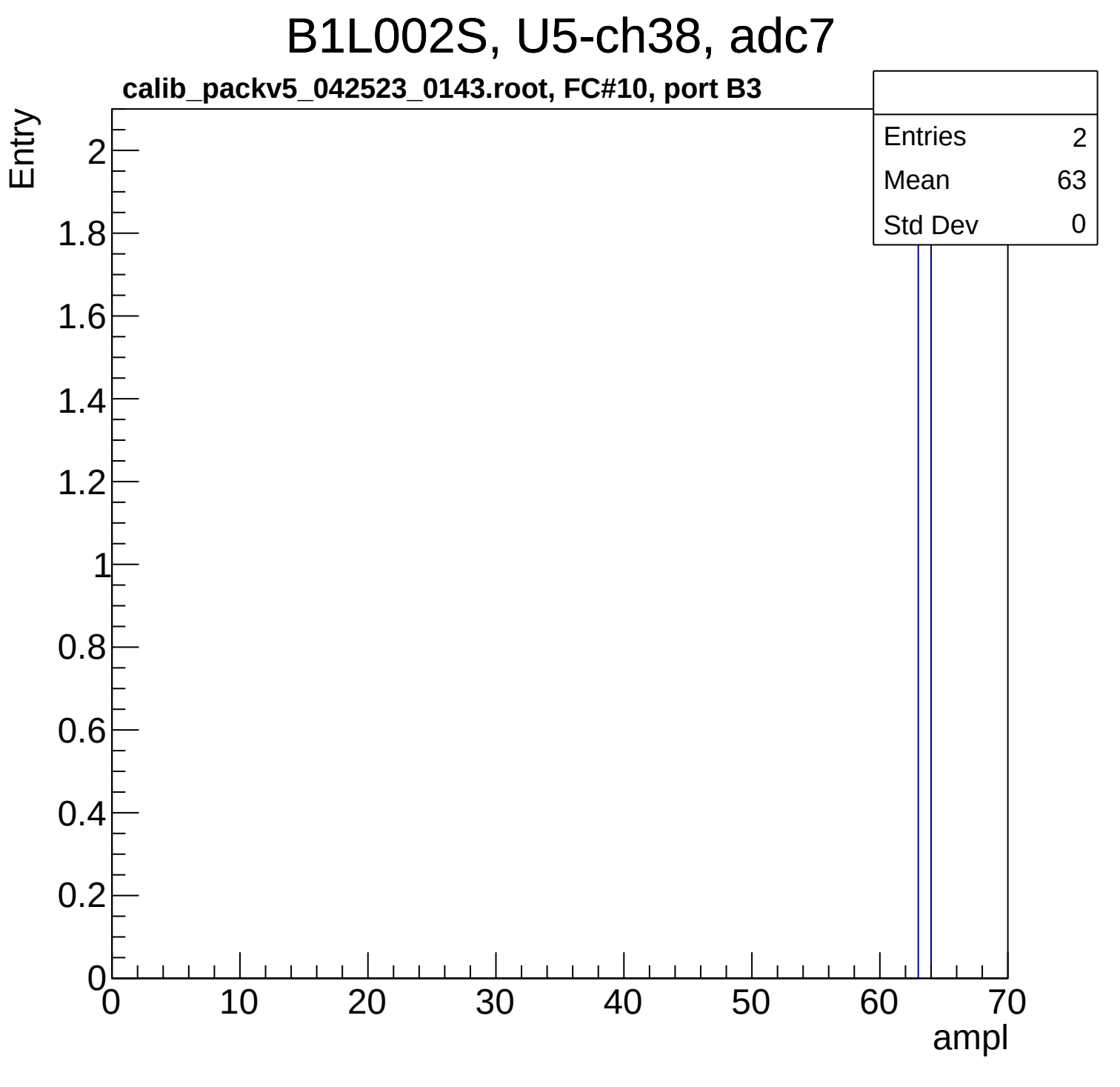
B1L002S, U5-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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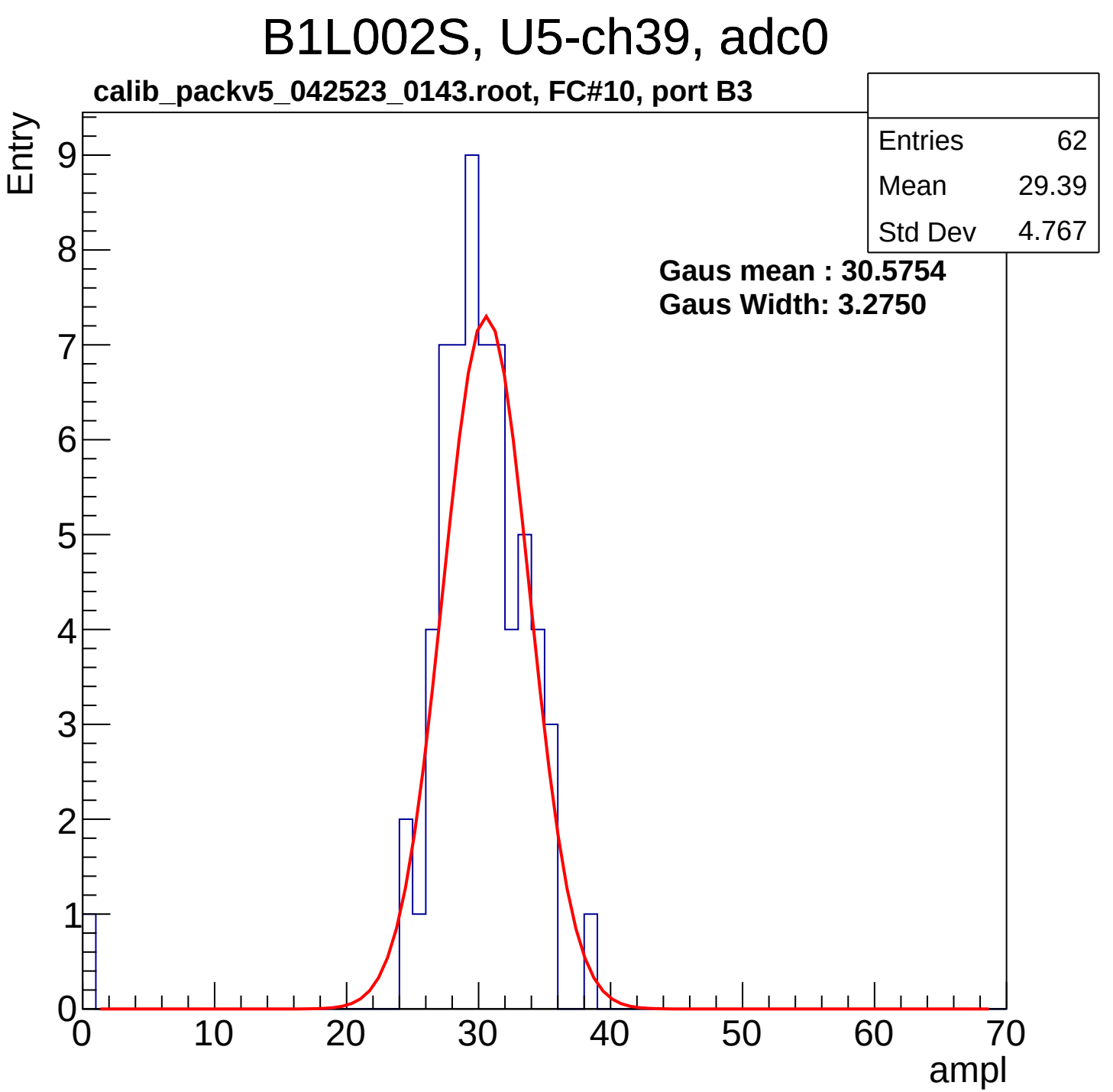
B1L002S, U5-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 29.39
Std Dev 4.767
Gaus mean : 30.5754
Gaus Width: 3.2750
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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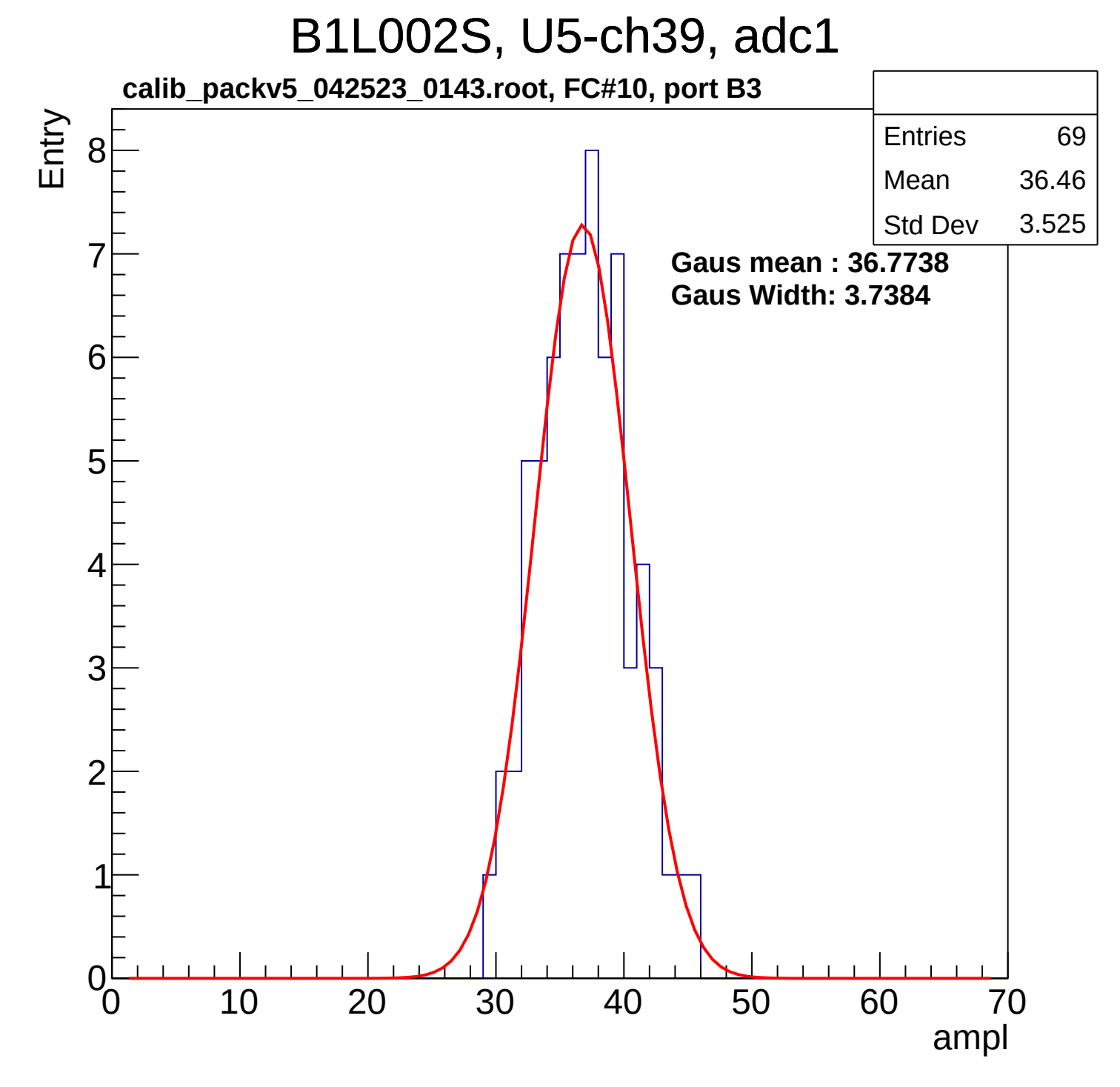
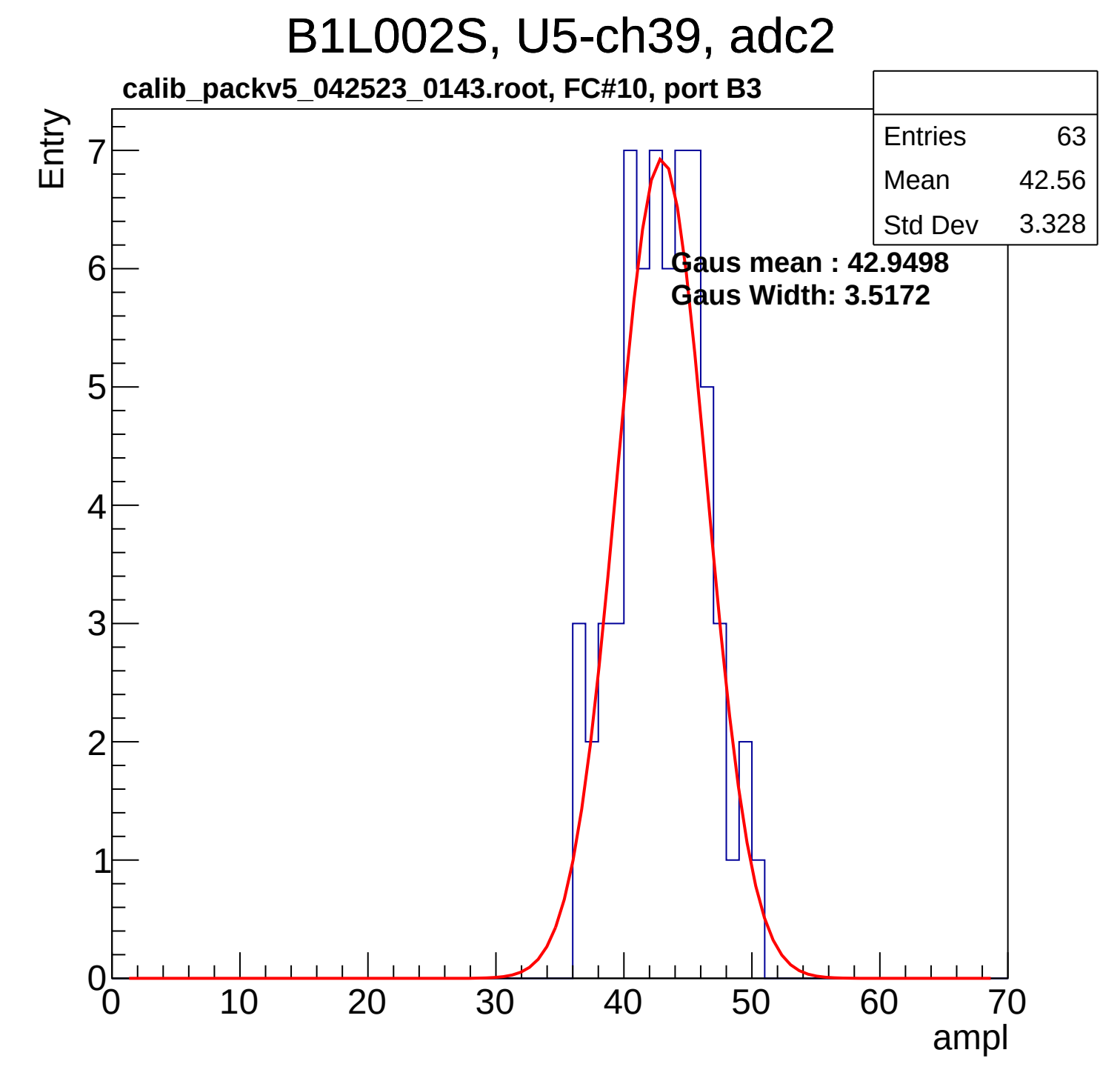
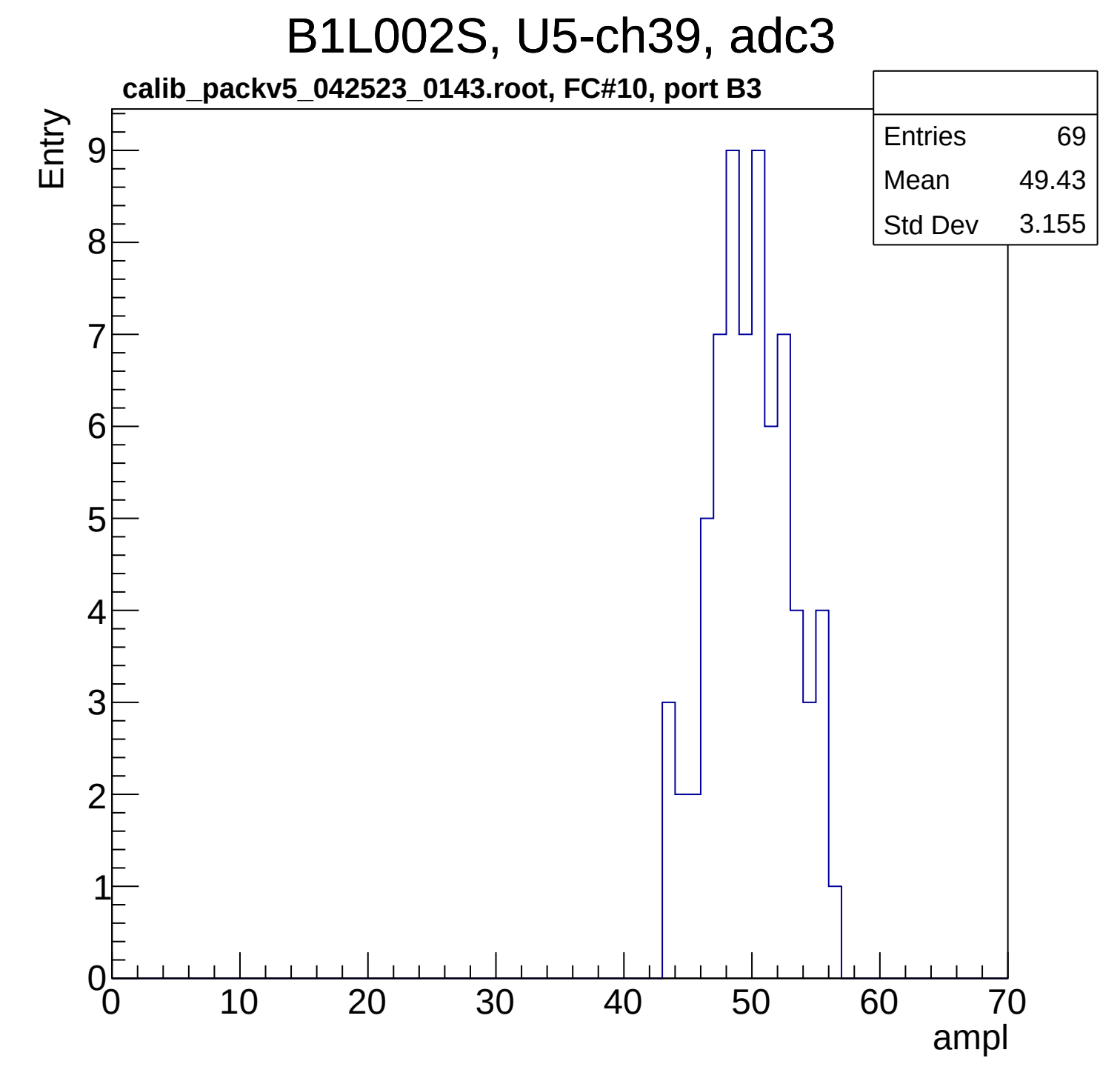

B1L002S, U5-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 36.46
Std Dev 3.525
Gaus mean : 36.7738
Gaus Width: 3.7384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.56
Std Dev 3.328
Gaus mean : 42.9498
Gaus Width: 3.5172
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 49.43
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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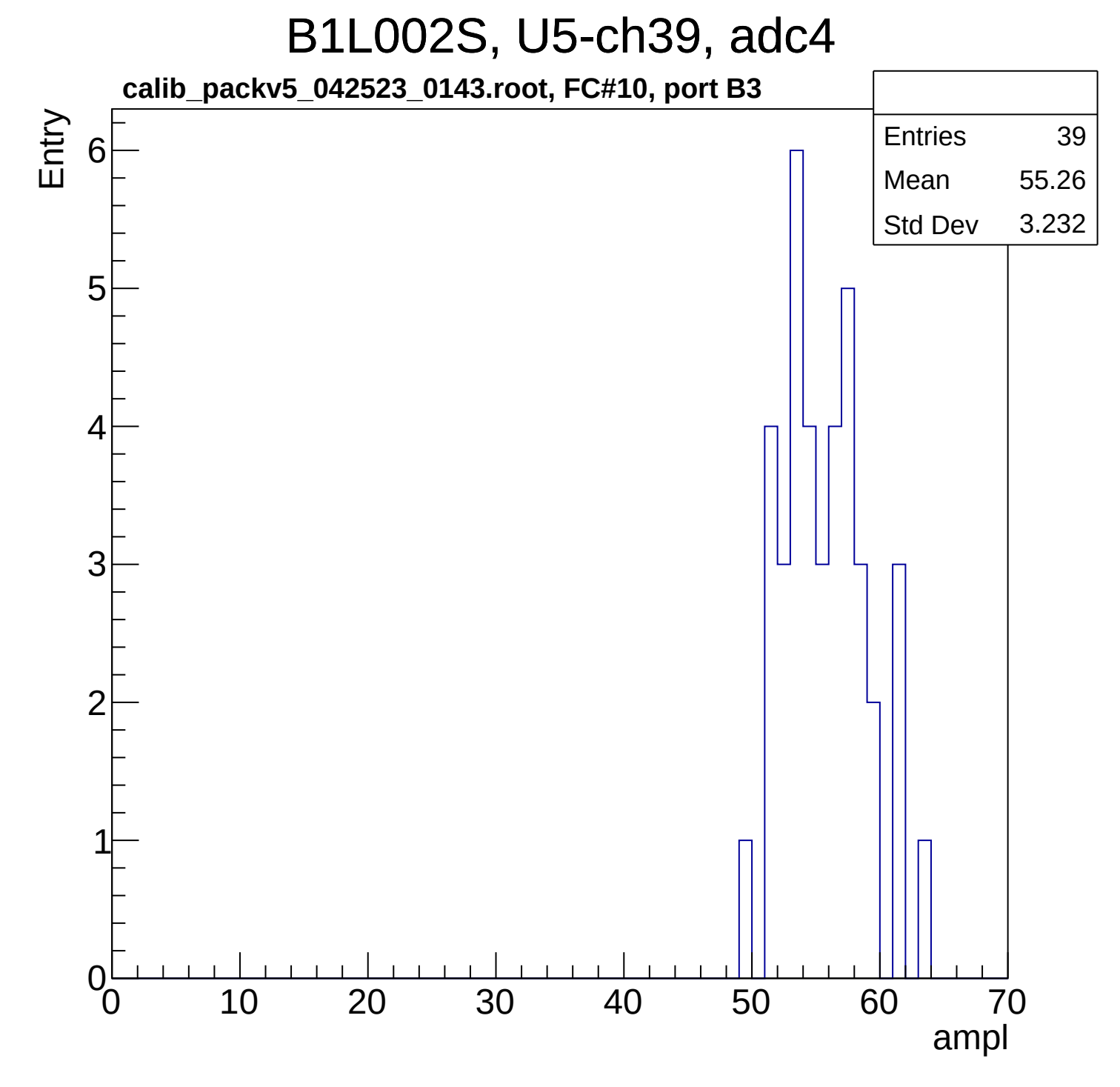
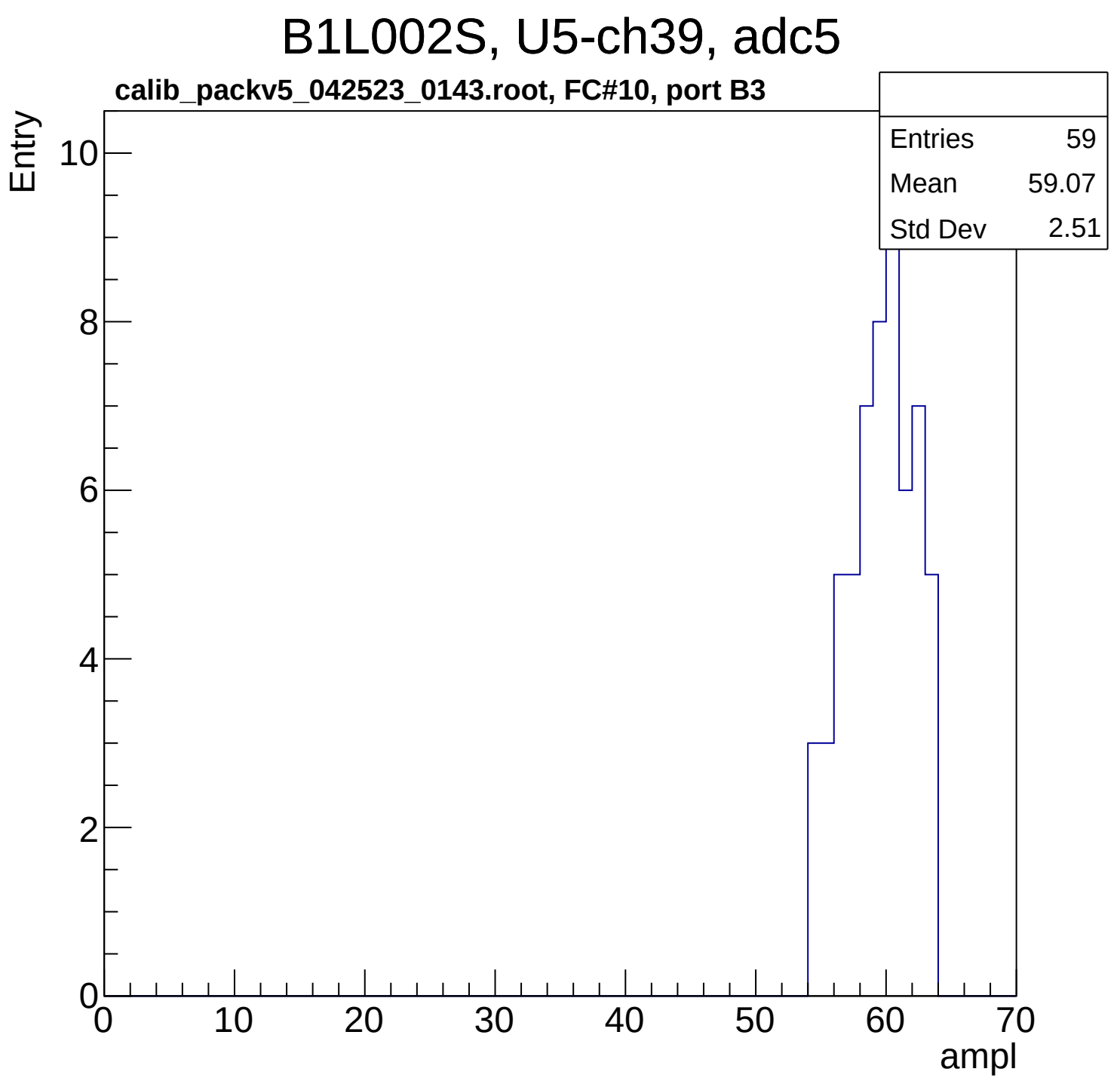
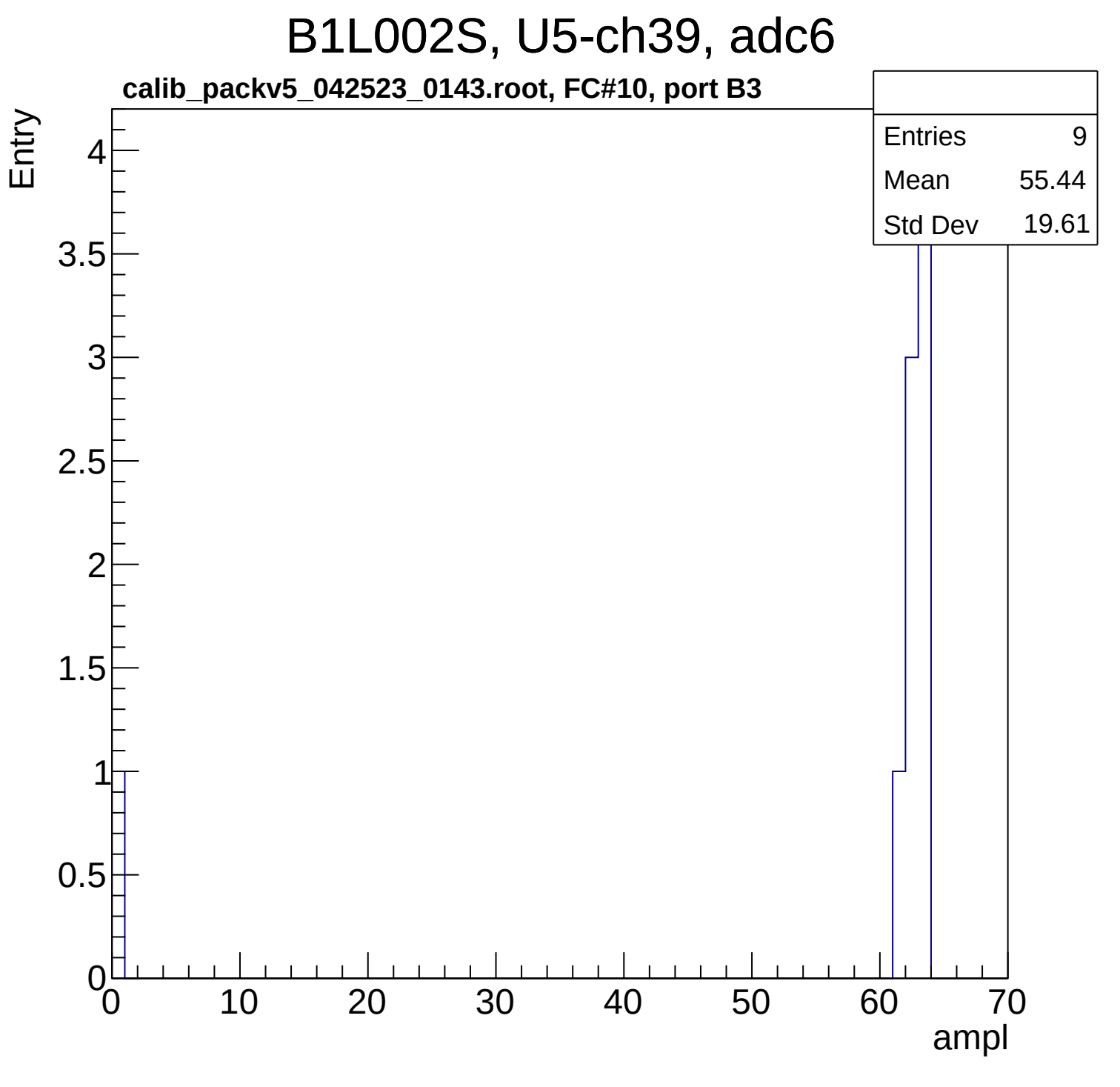
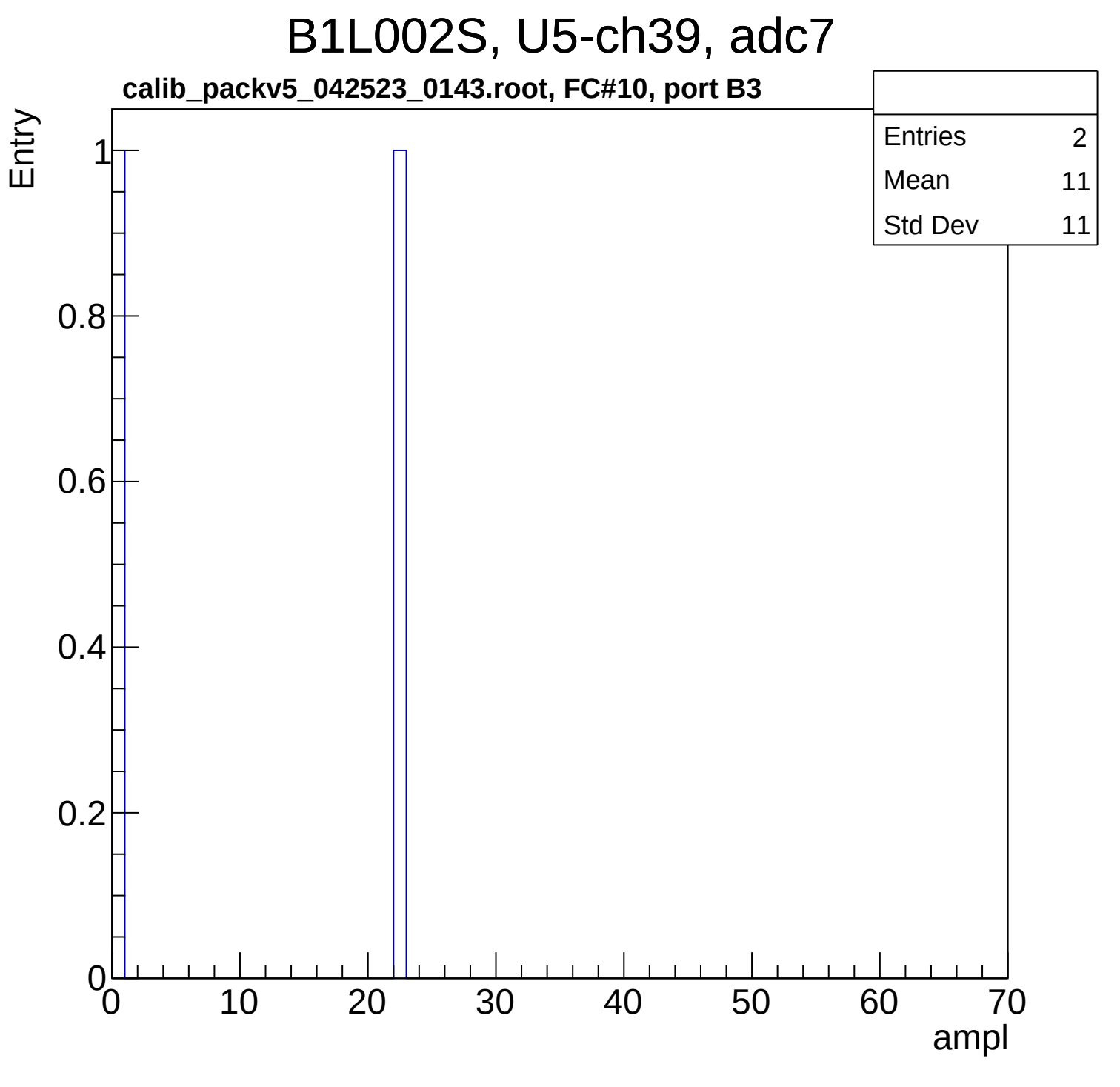
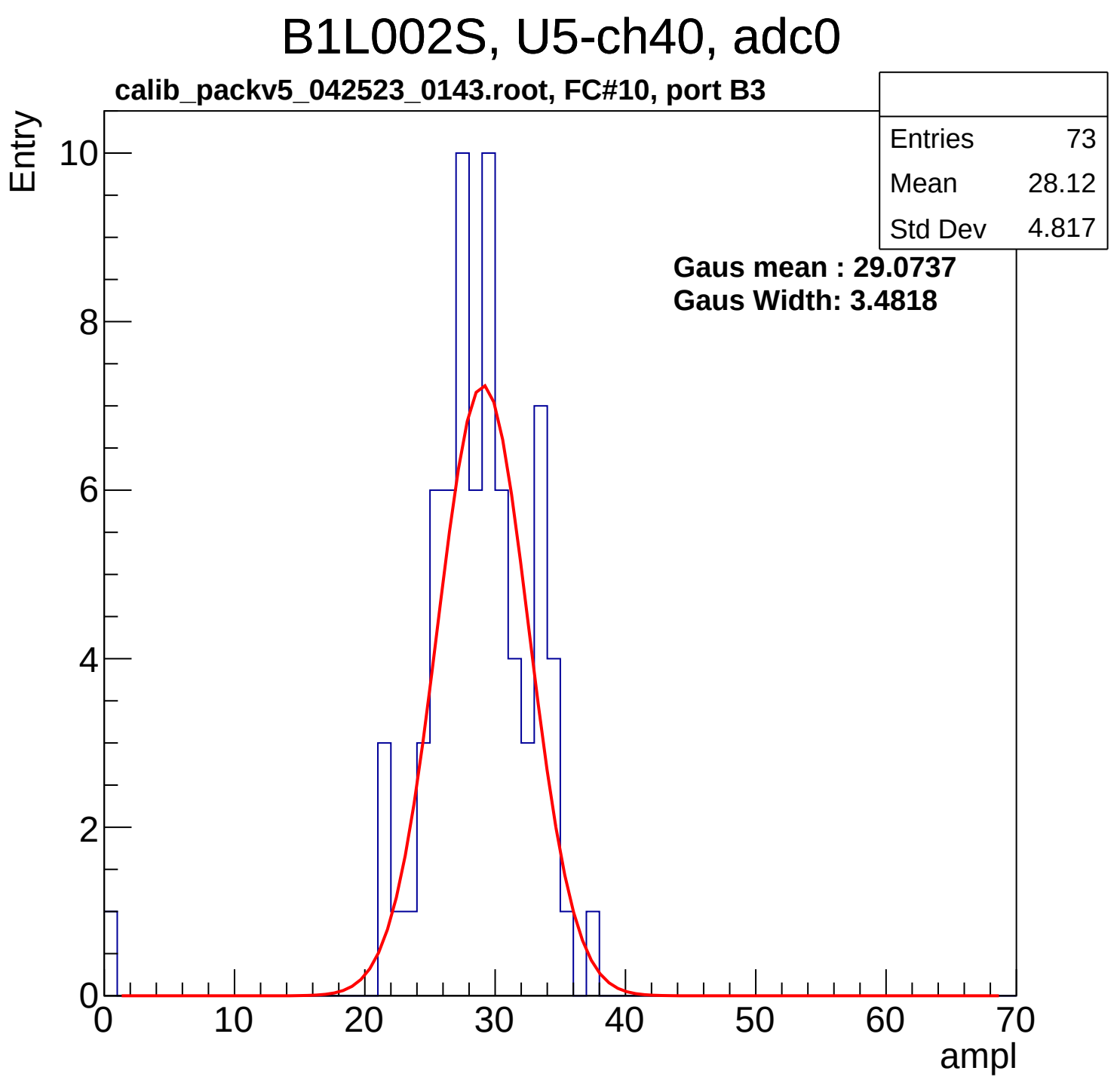
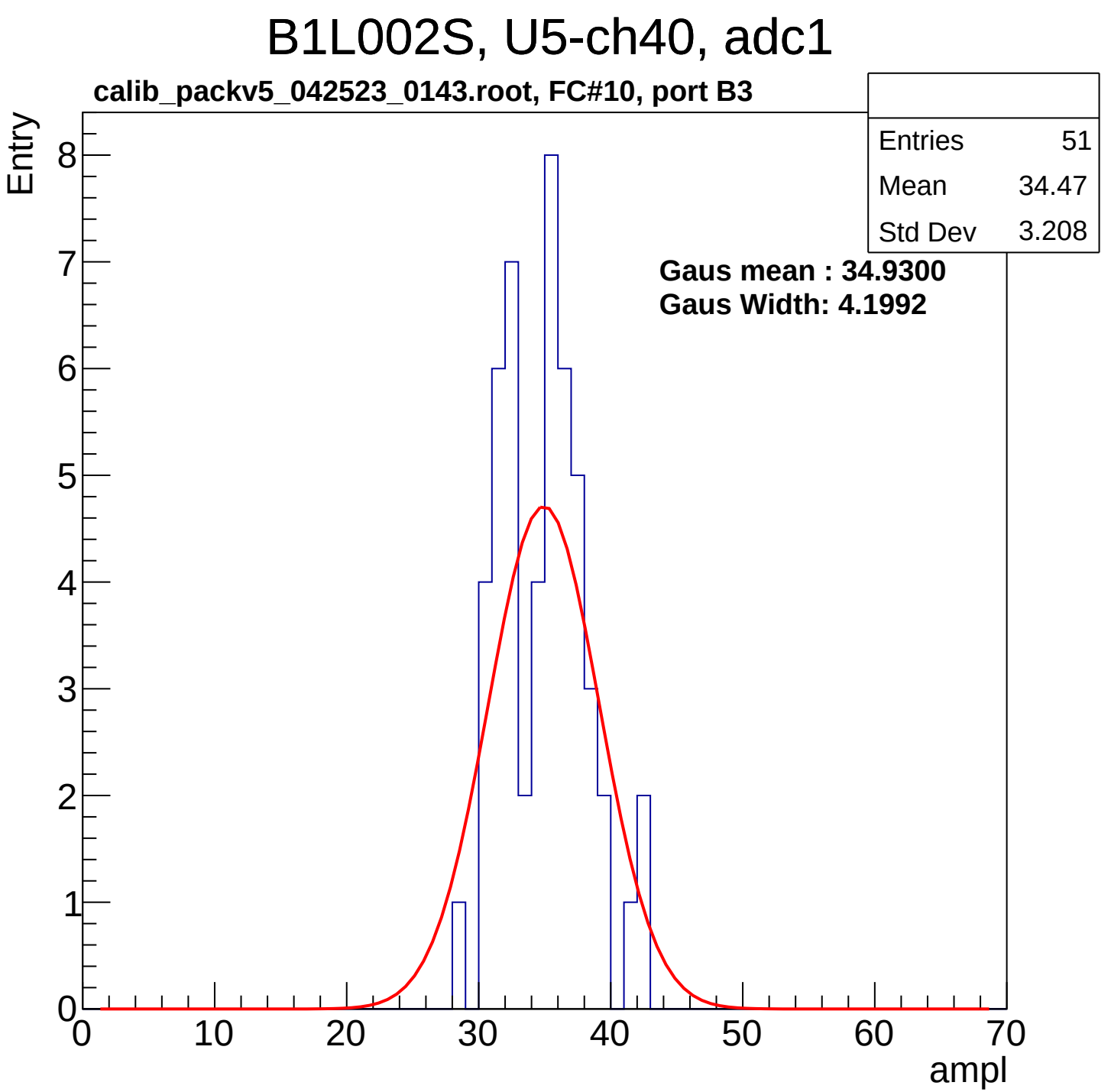

B1L002S, U5-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 55.26
Std Dev 3.232
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

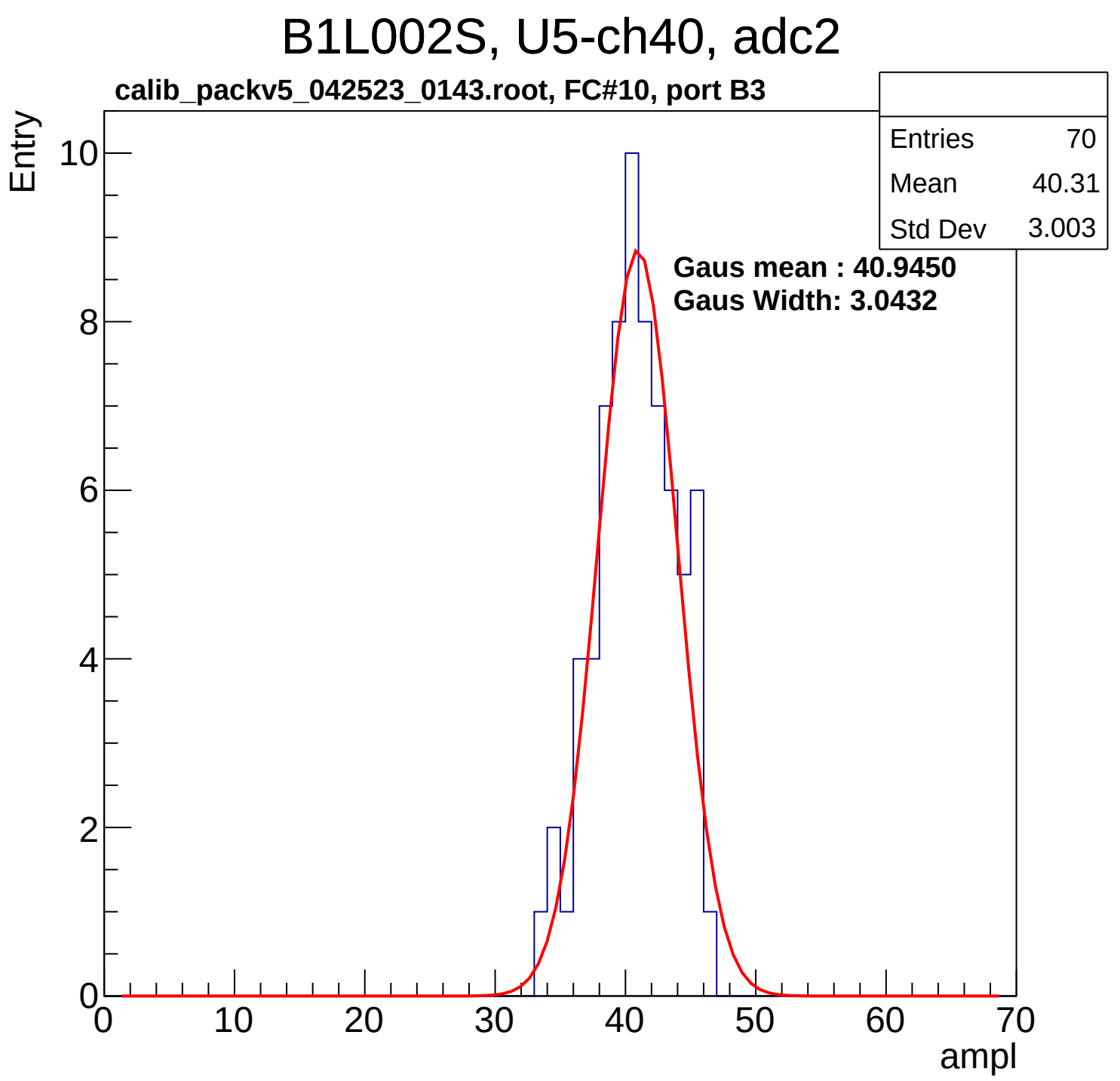

B1L002S, U5-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.07
Std Dev 2.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

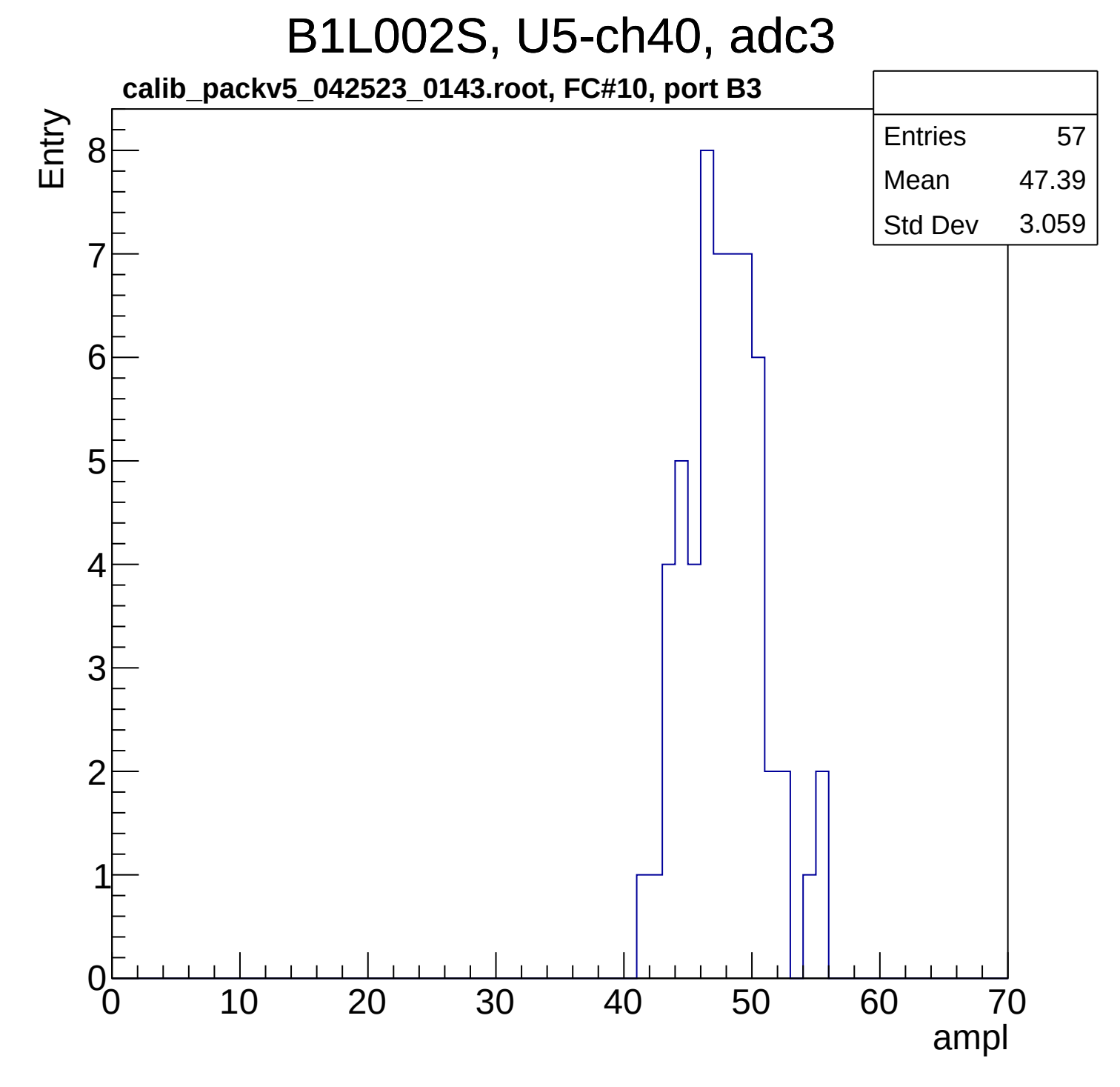

B1L002S, U5-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.44
Std Dev 19.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

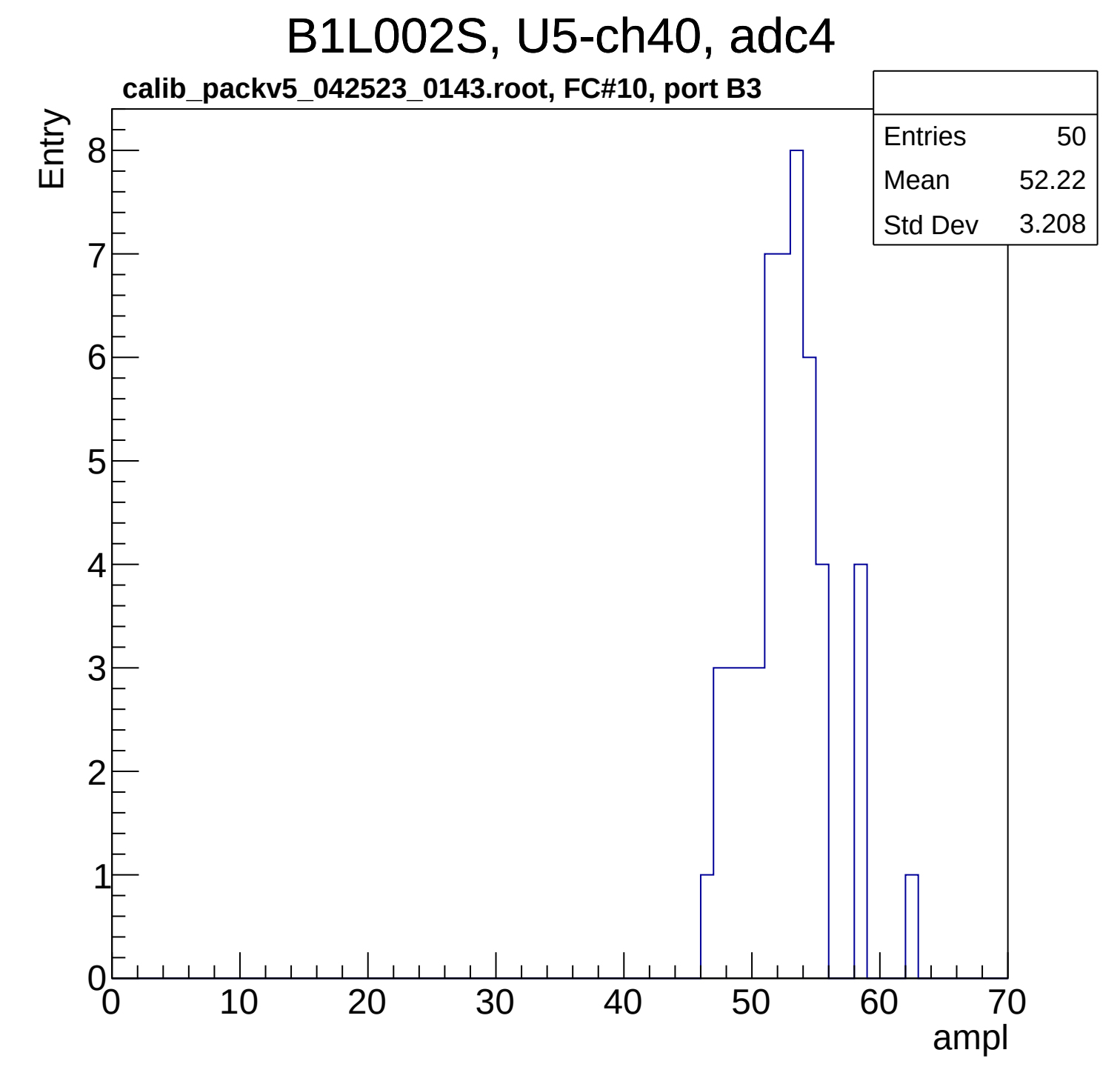

B1L002S, U5-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

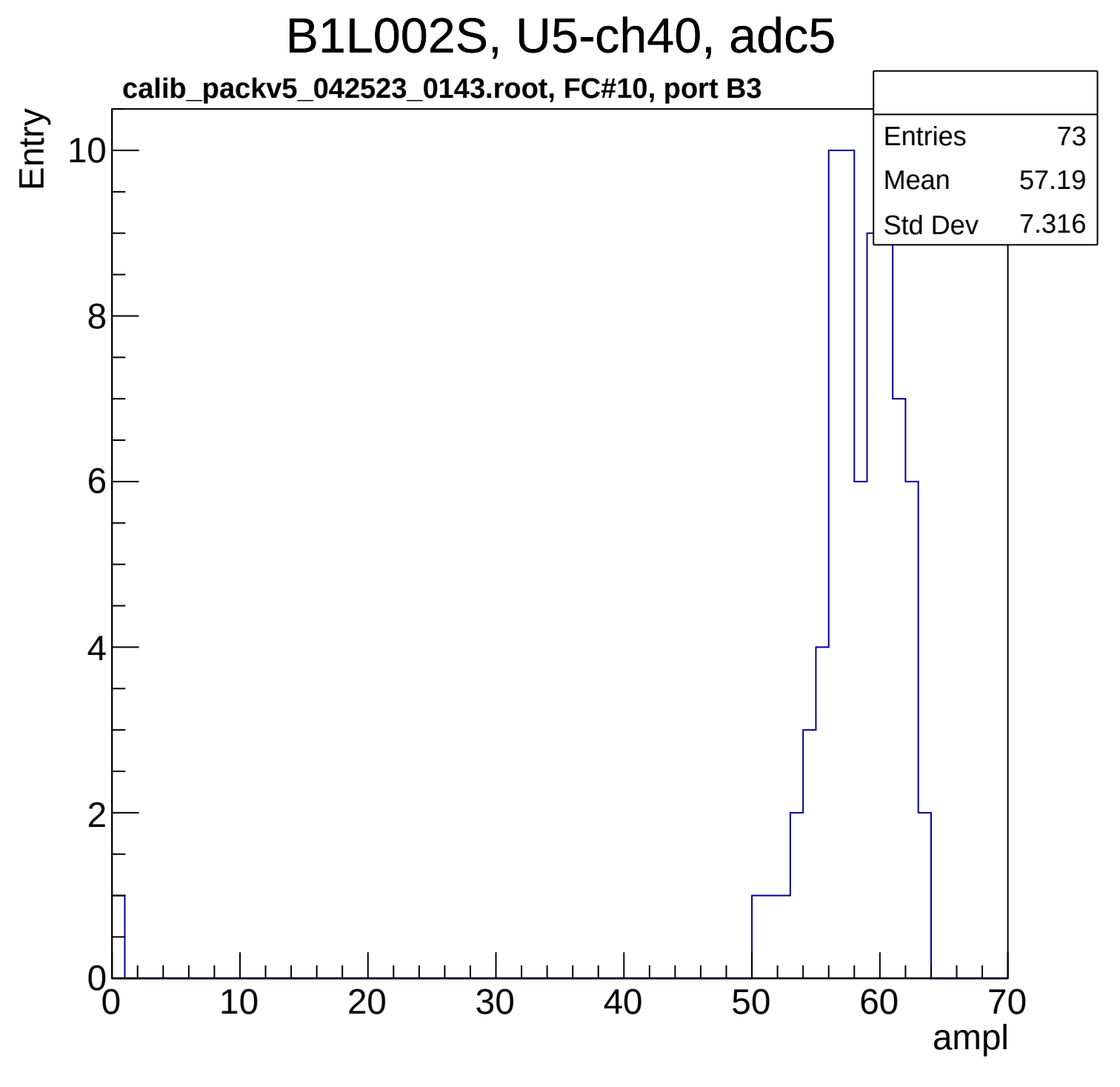

B1L002S, U5-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.12
Std Dev 4.817
Gaus mean : 29.0737
Gaus Width: 3.4818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

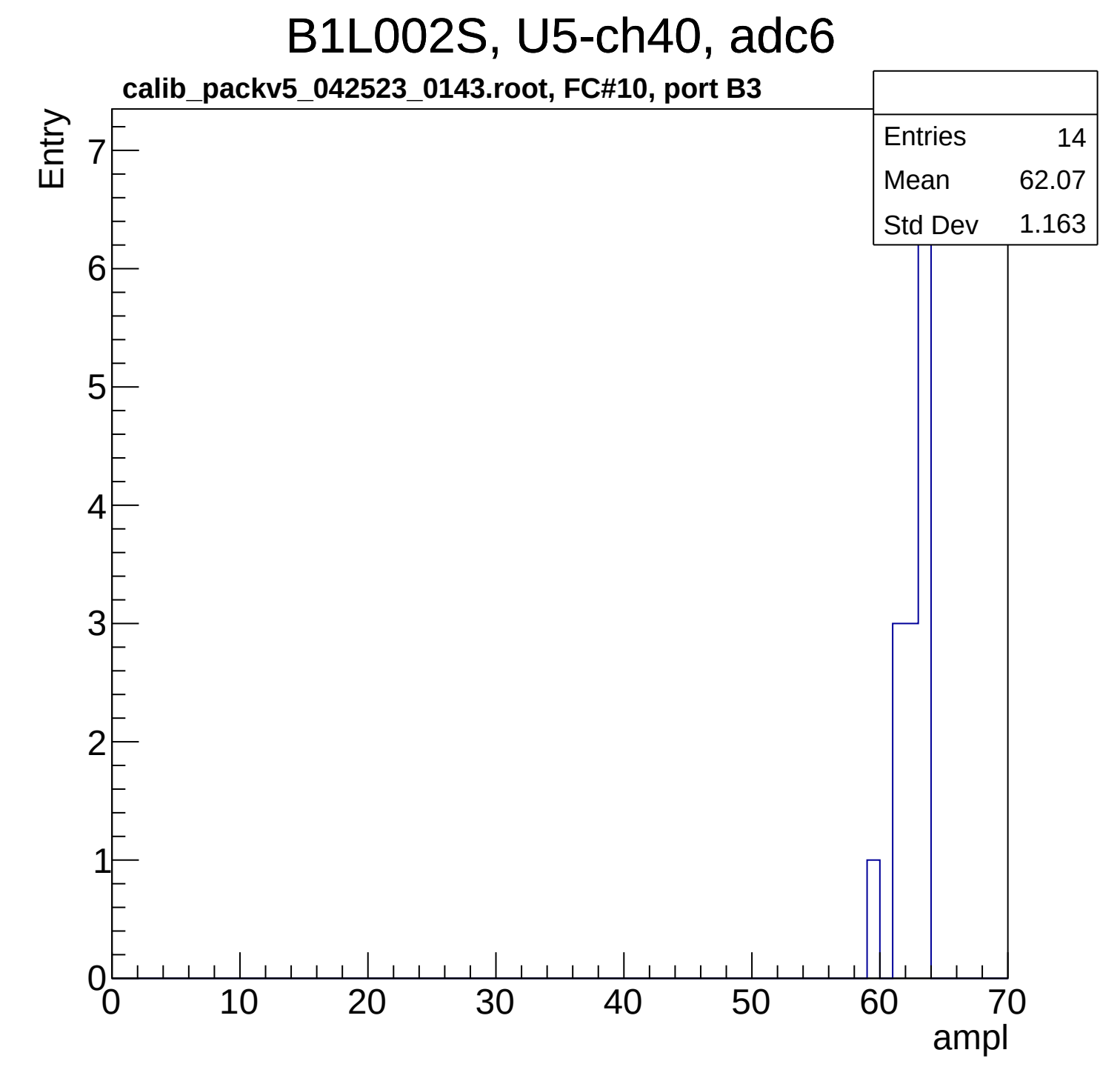

B1L002S, U5-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 34.47
Std Dev 3.208
Gaus mean : 34.9300
Gaus Width: 4.1992
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

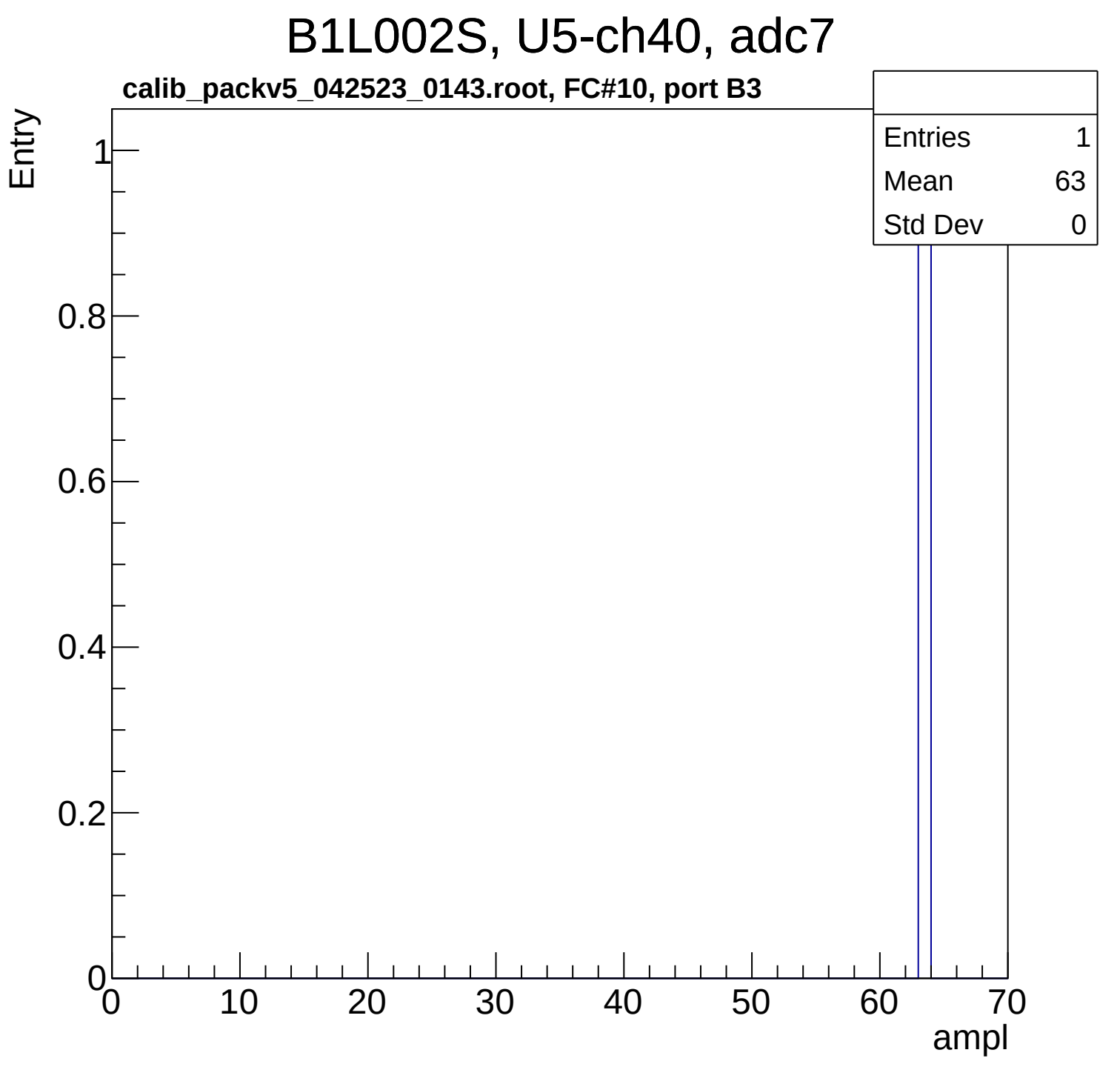

B1L002S, U5-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 40.31
Std Dev 3.003
Gaus mean : 40.9450
Gaus Width: 3.0432
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

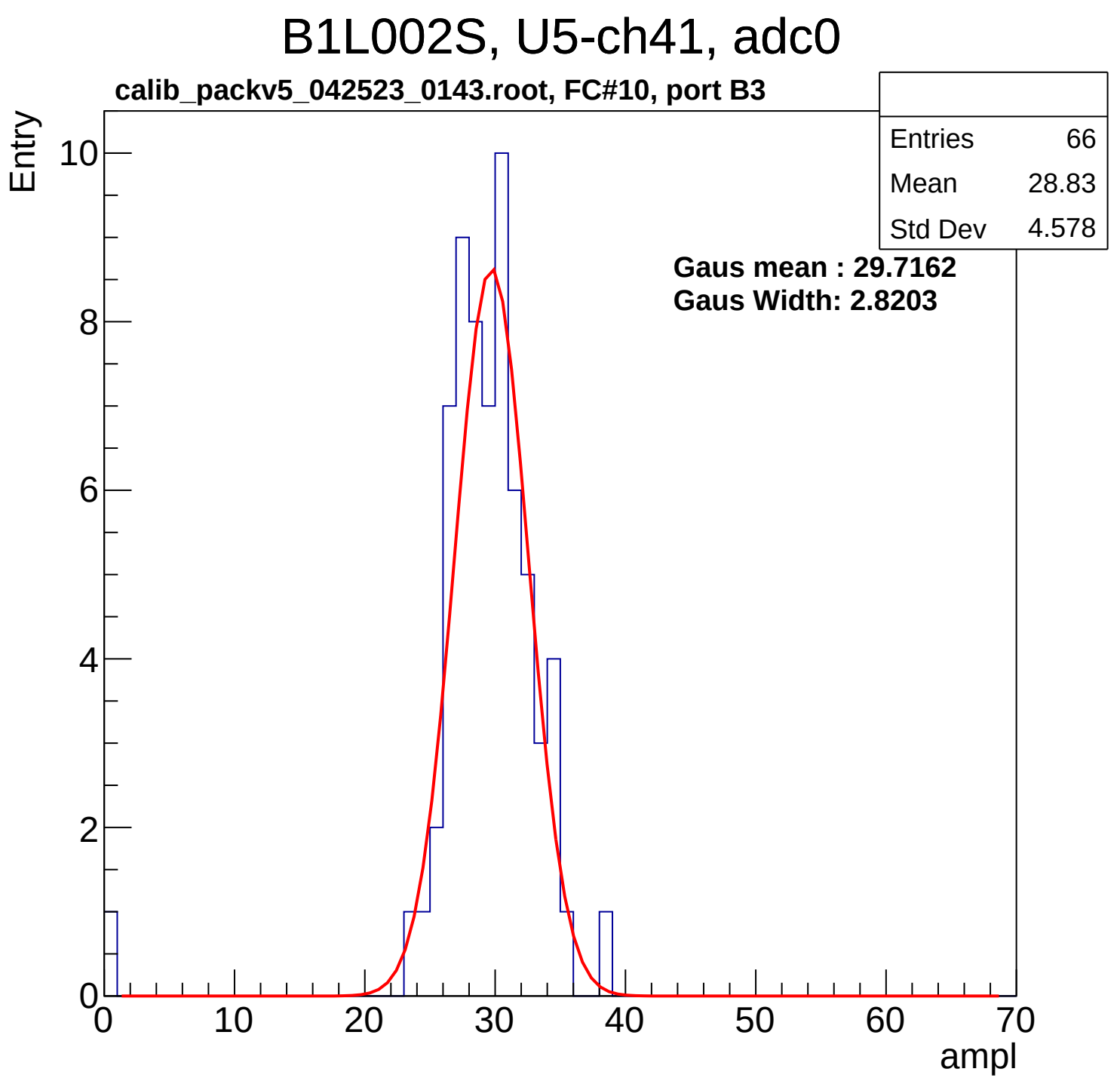

B1L002S, U5-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.39
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

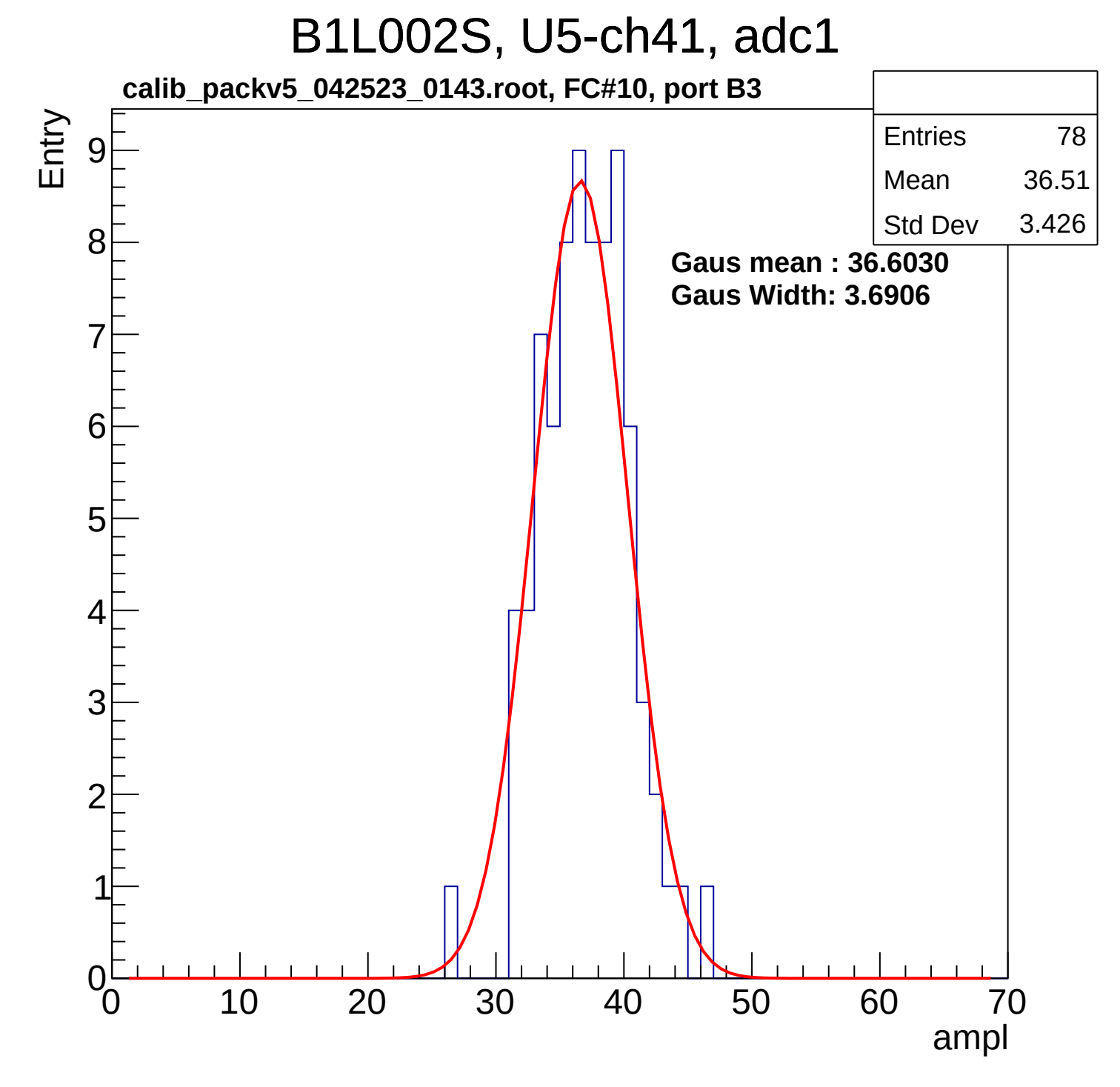

B1L002S, U5-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.22
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

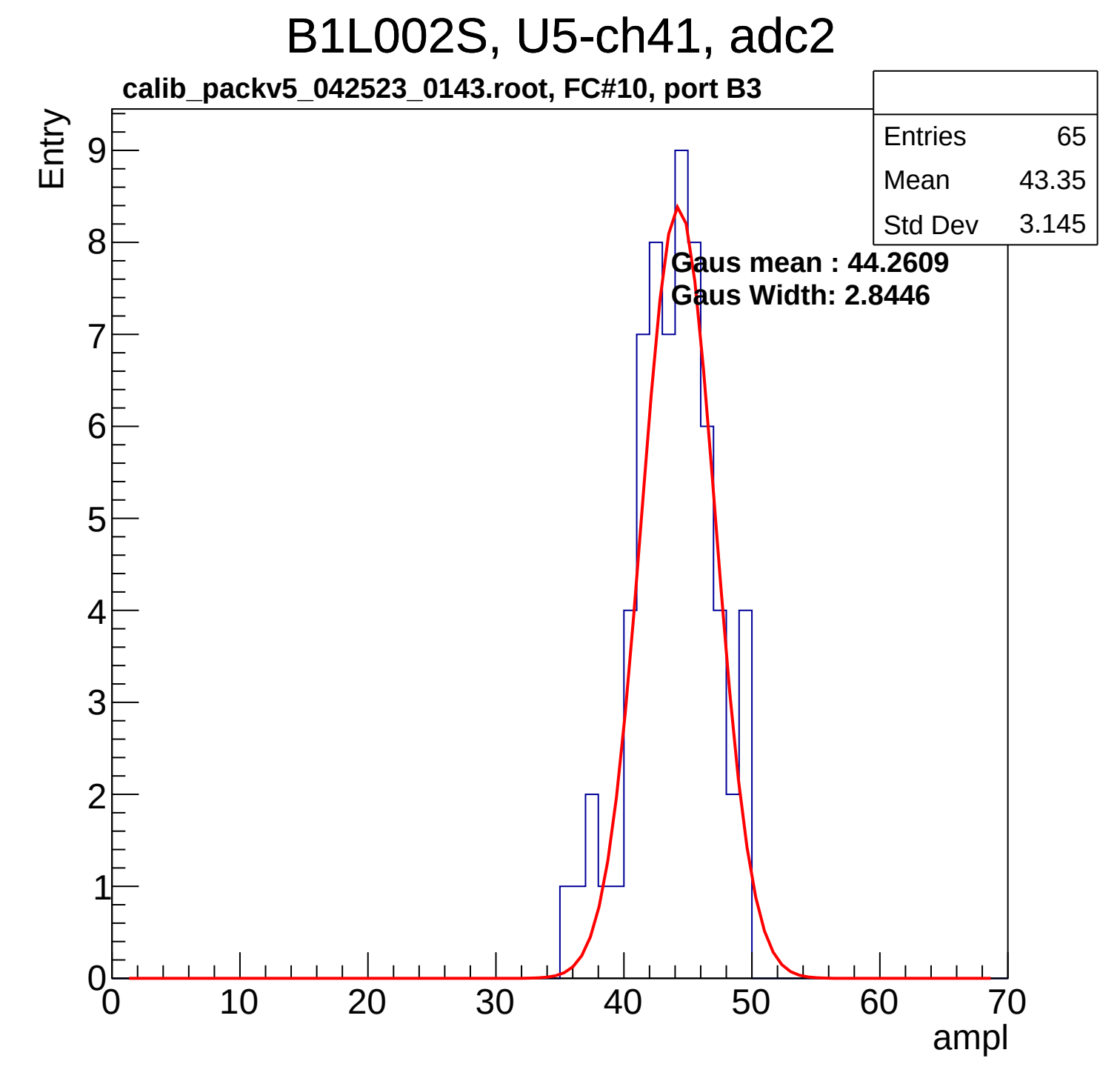

B1L002S, U5-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 57.19
Std Dev 7.316
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62.07
Std Dev 1.163
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.83
Std Dev 4.578
Gaus mean : 29.7162
Gaus Width: 2.8203
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 36.51
Std Dev 3.426
Gaus mean : 36.6030
Gaus Width: 3.6906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 43.35
Std Dev 3.145
Gaus mean : 44.2609
Gaus Width: 2.8446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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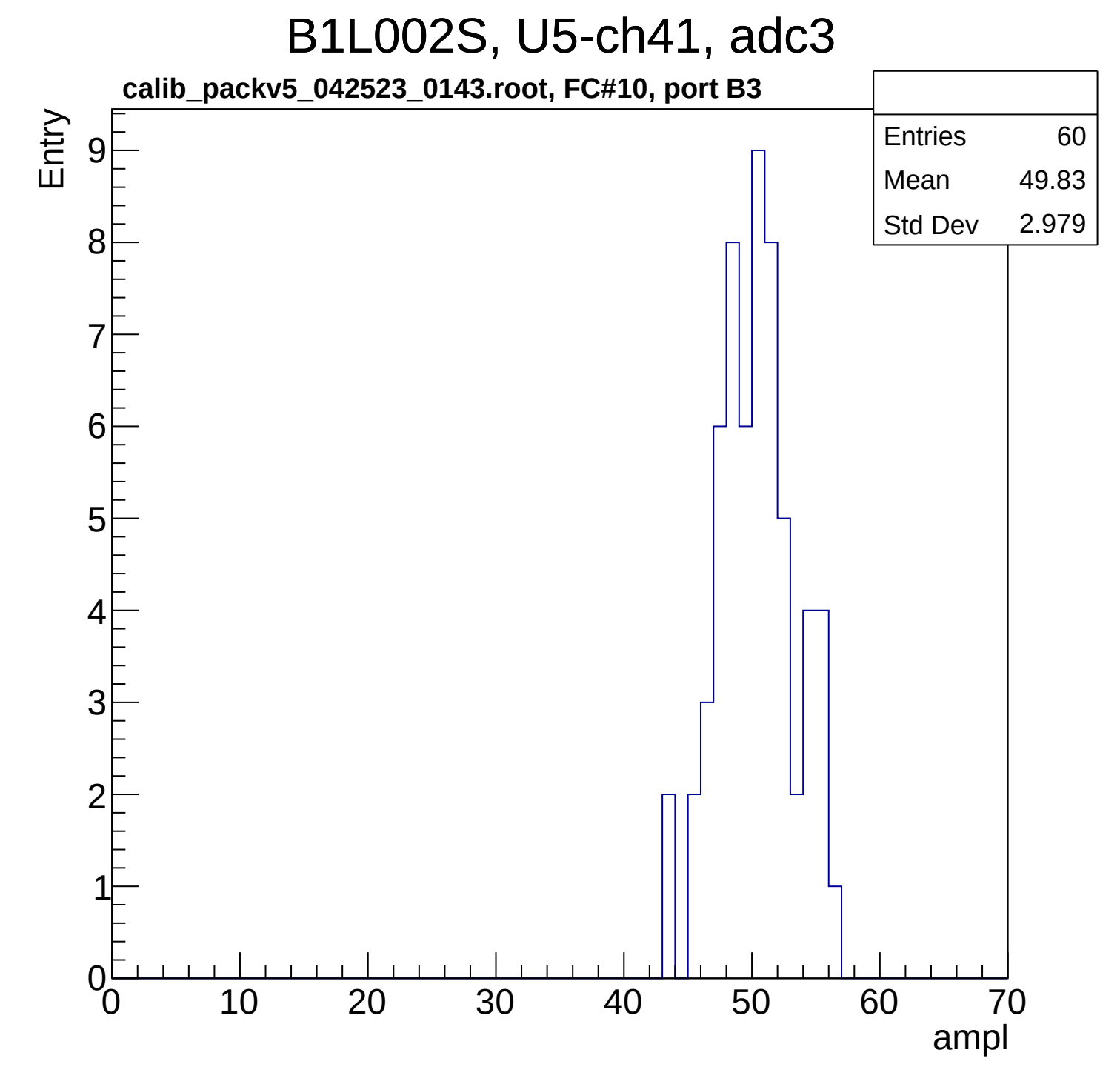
B1L002S, U5-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 49.83
Std Dev 2.979
Entry
ampl
0
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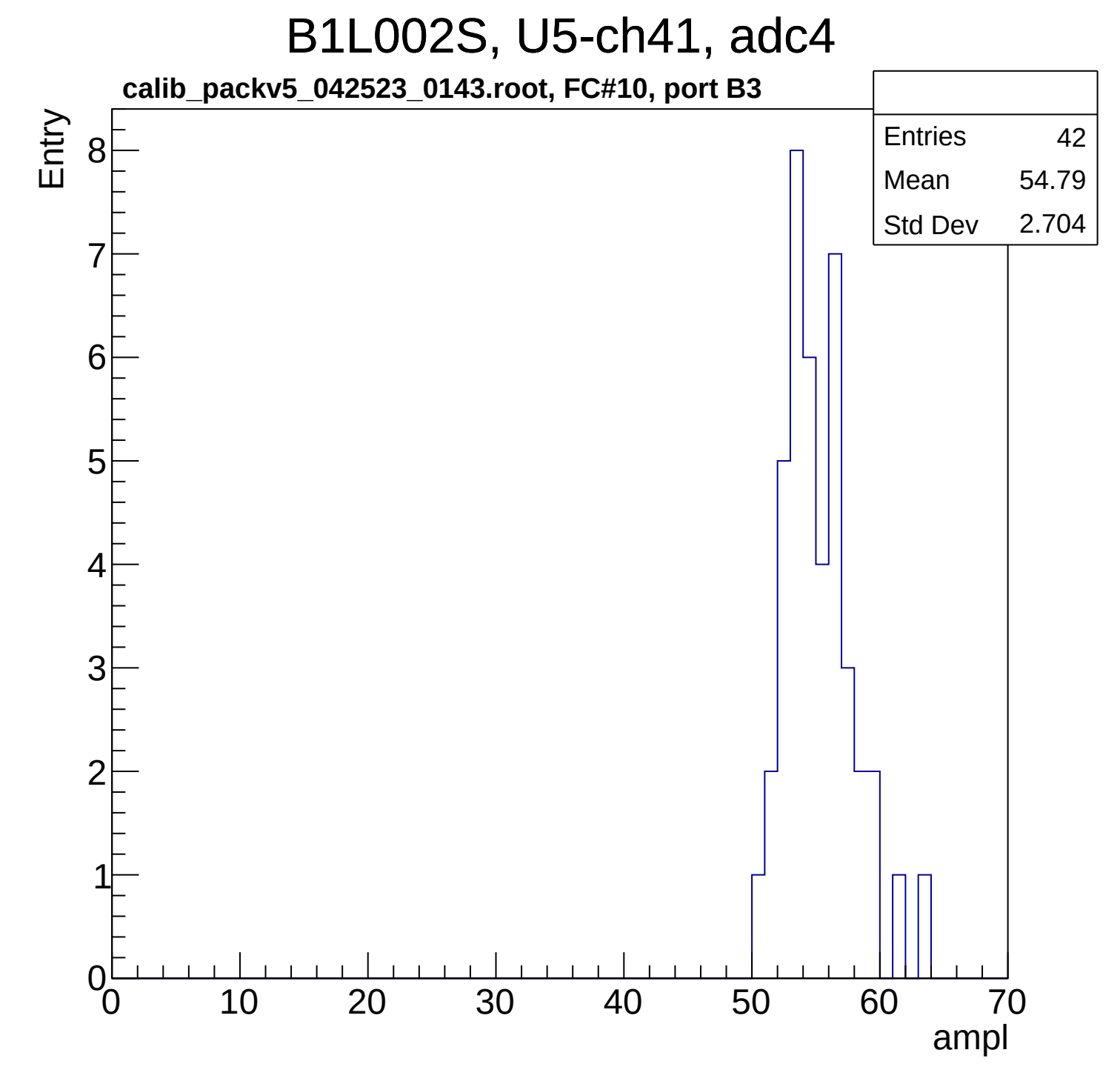
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 54.79
Std Dev 2.704
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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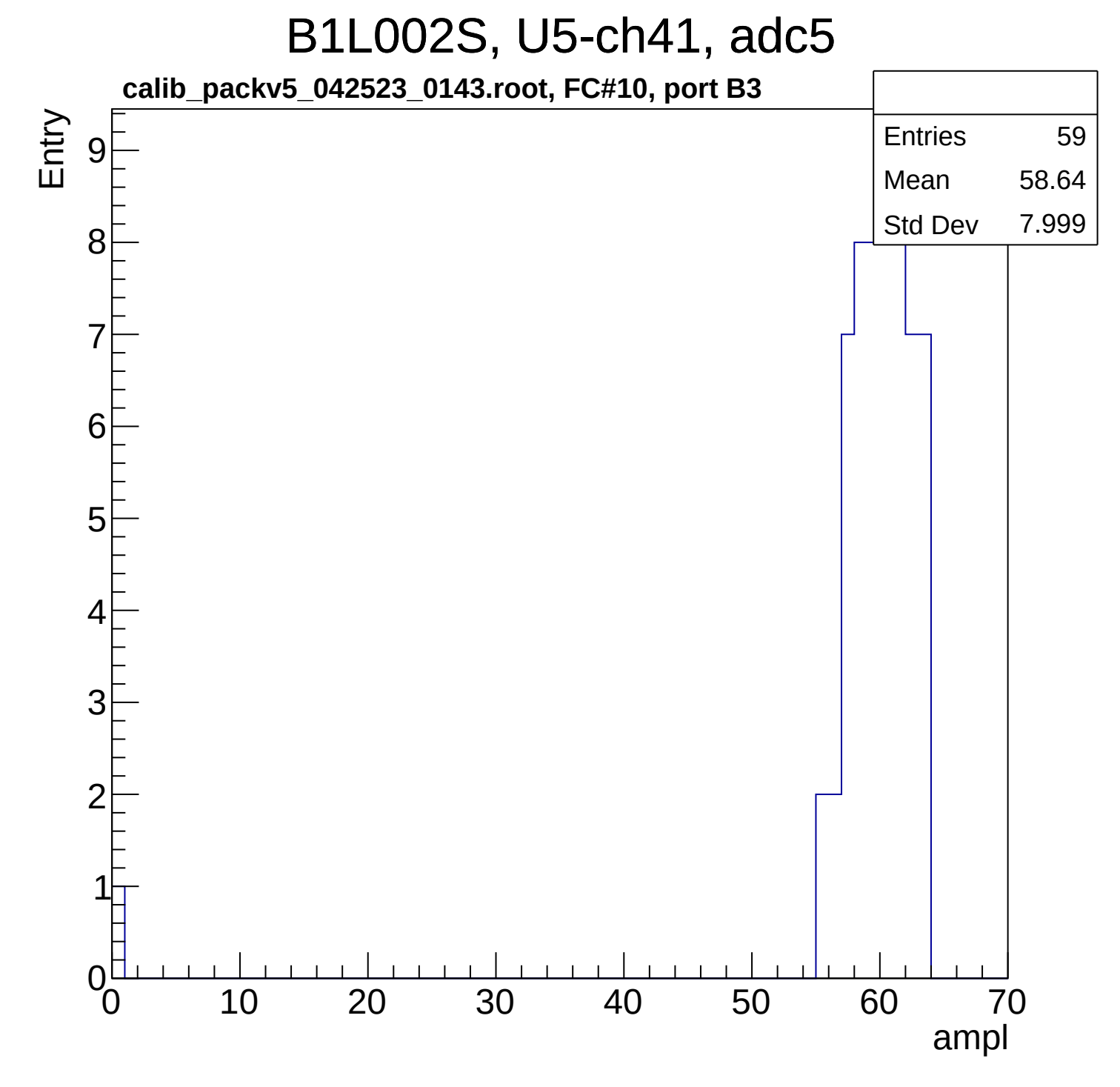
B1L002S, U5-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.64
Std Dev 7.999
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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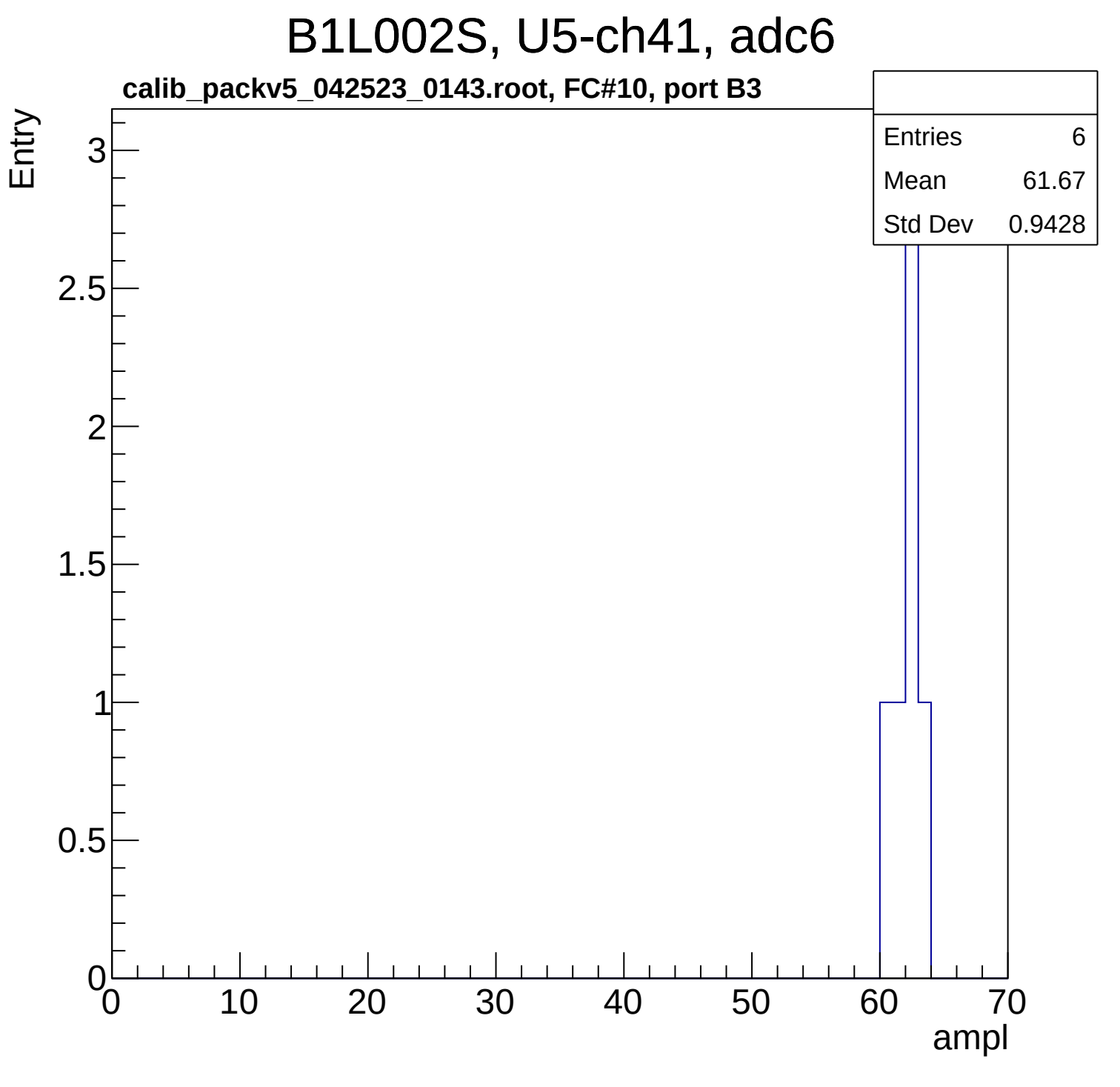
B1L002S, U5-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.67
Std Dev 0.9428
Entry
ampl
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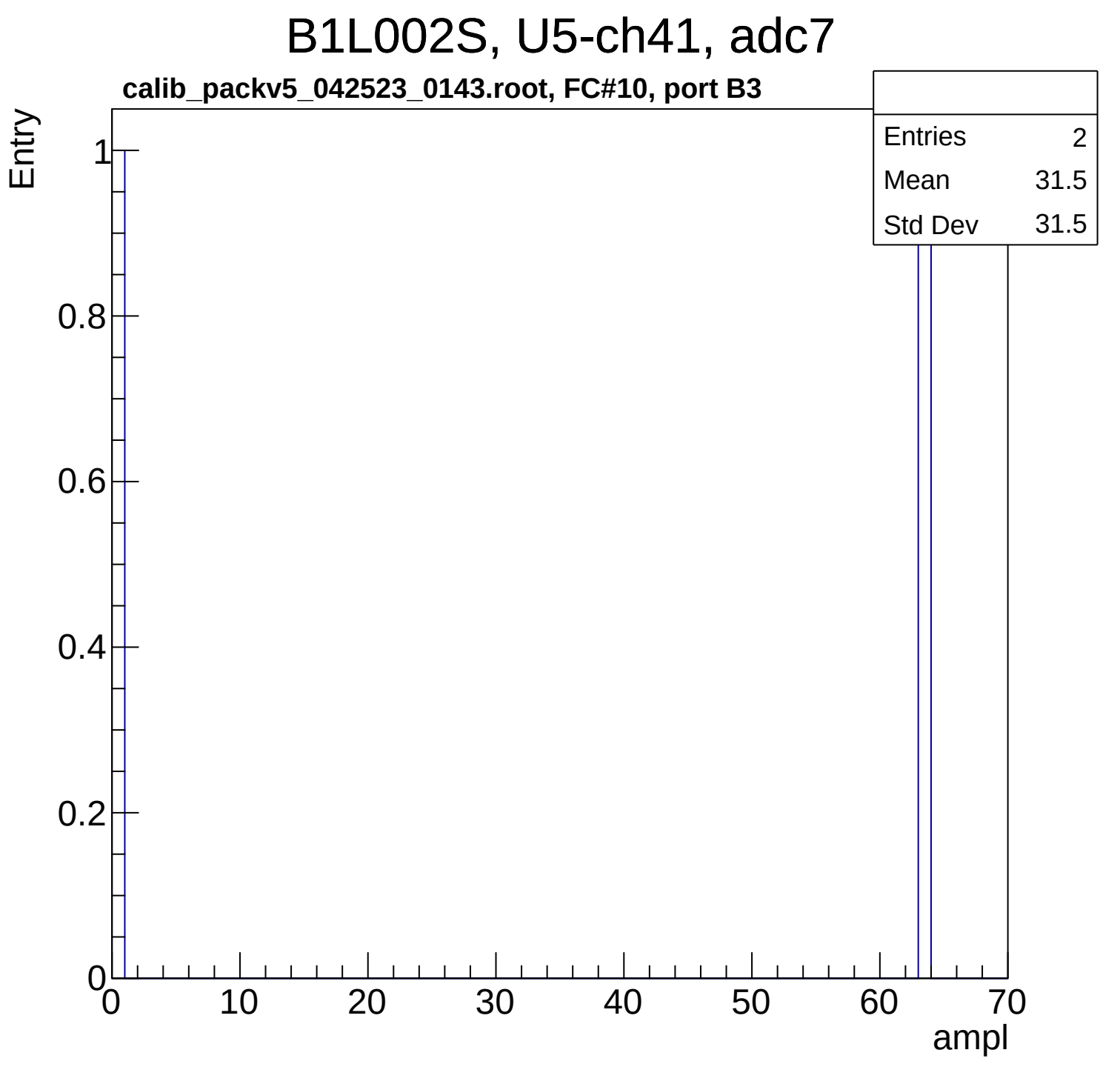
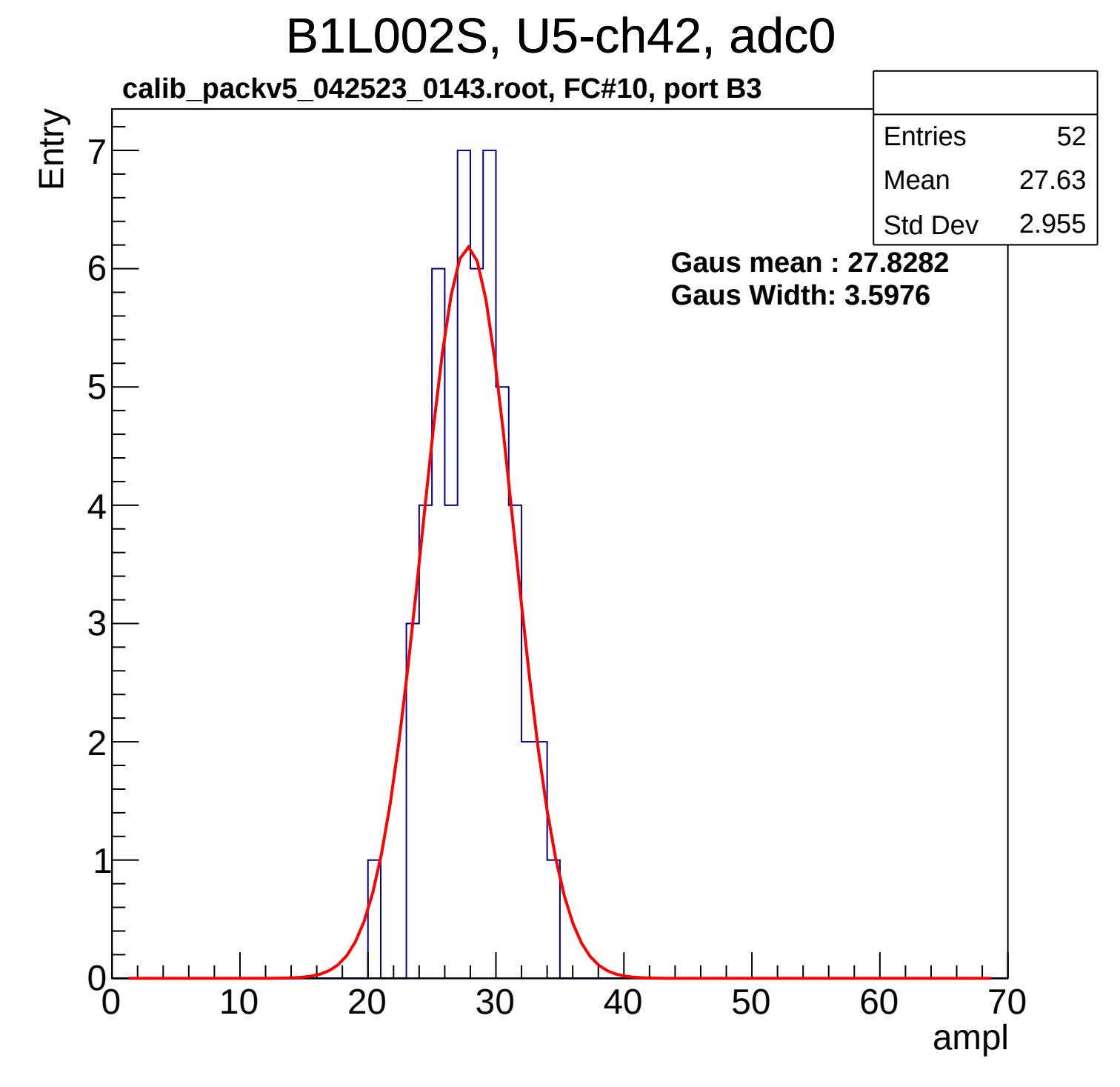
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U5-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.63
Std Dev 2.955
Gaus mean : 27.8282
Gaus Width: 3.5976
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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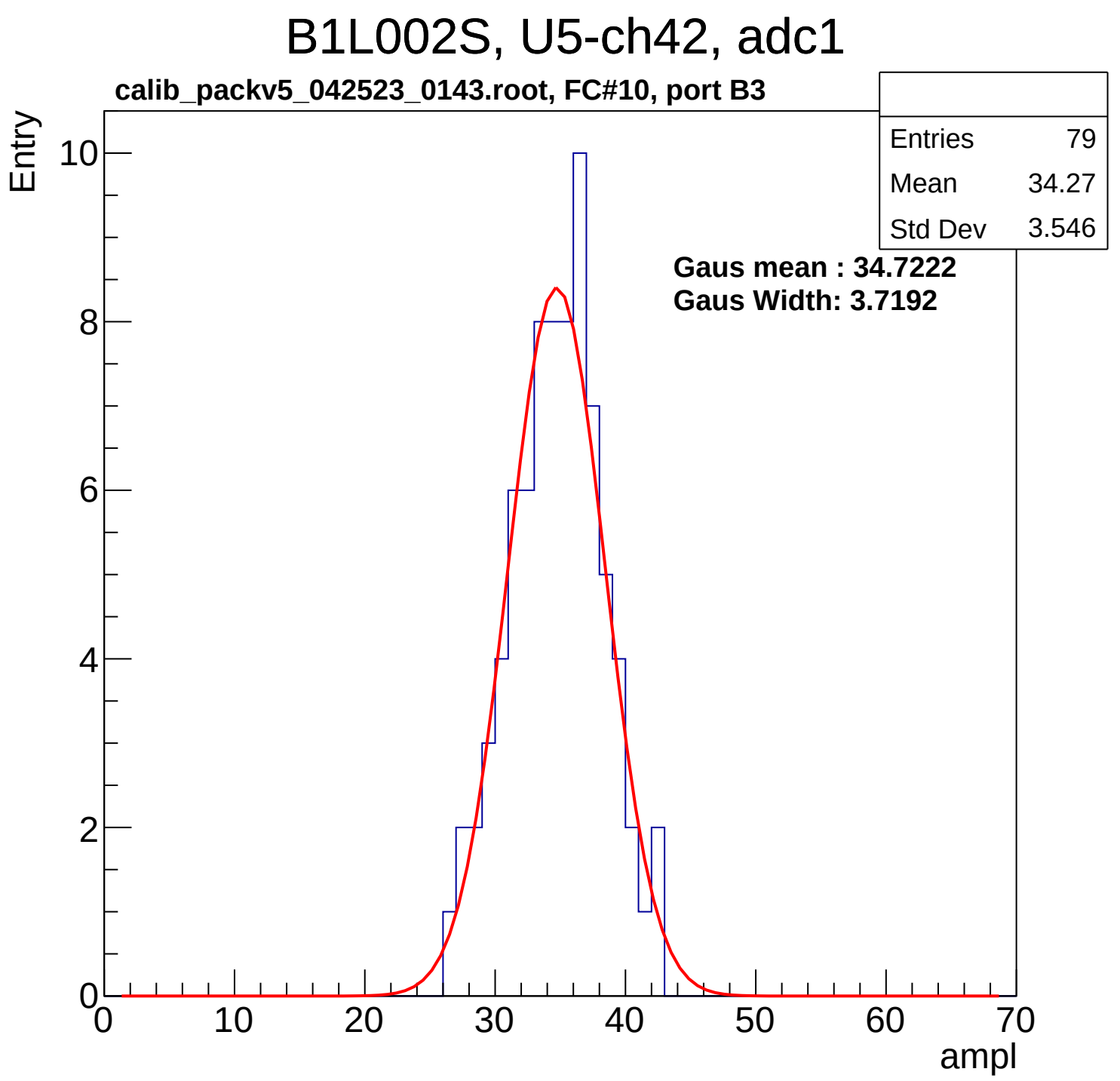
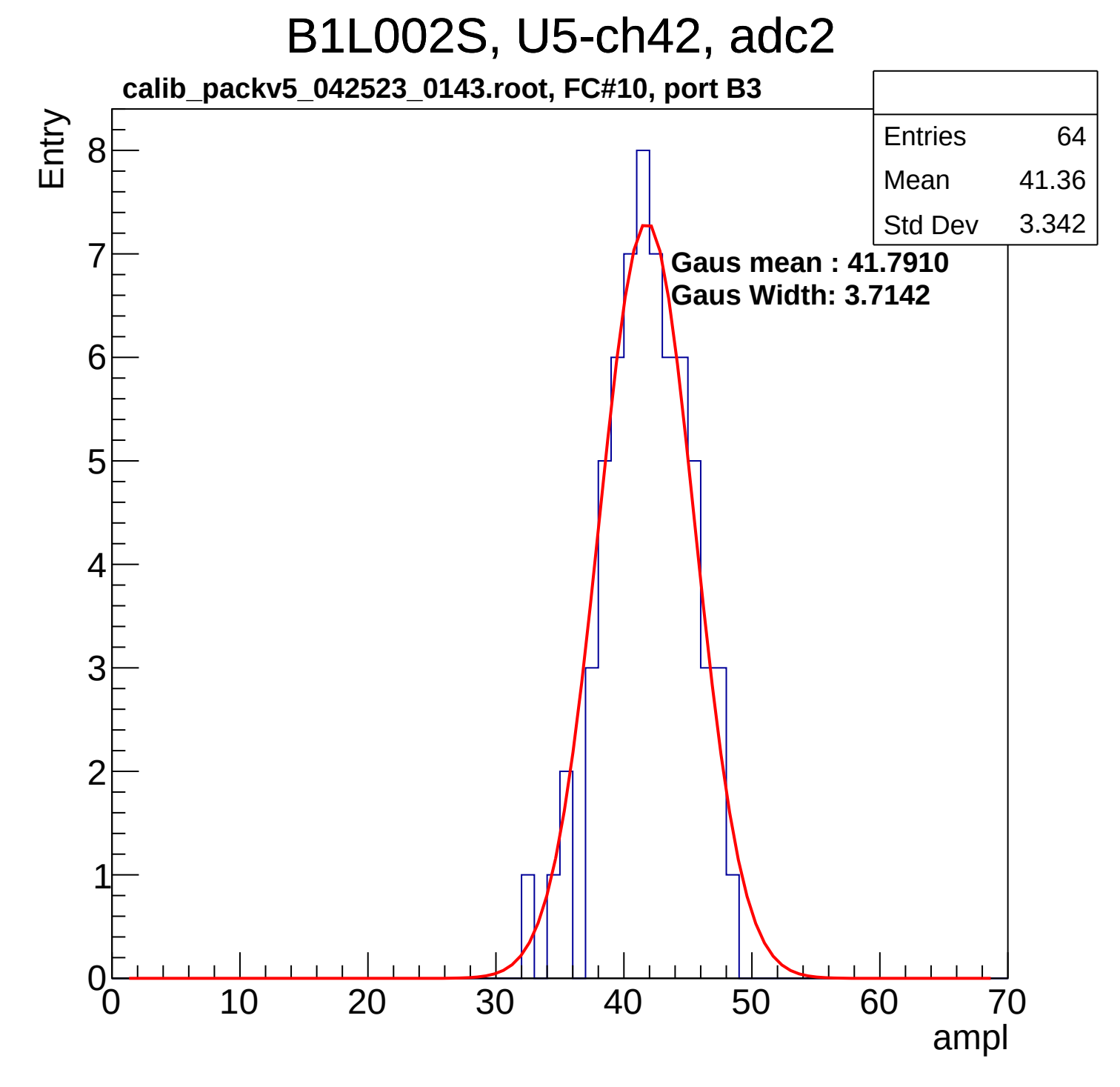

B1L002S, U5-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.27
Std Dev 3.546
Gaus mean : 34.7222
Gaus Width: 3.7192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.36
Std Dev 3.342
Gaus mean : 41.7910
Gaus Width: 3.7142
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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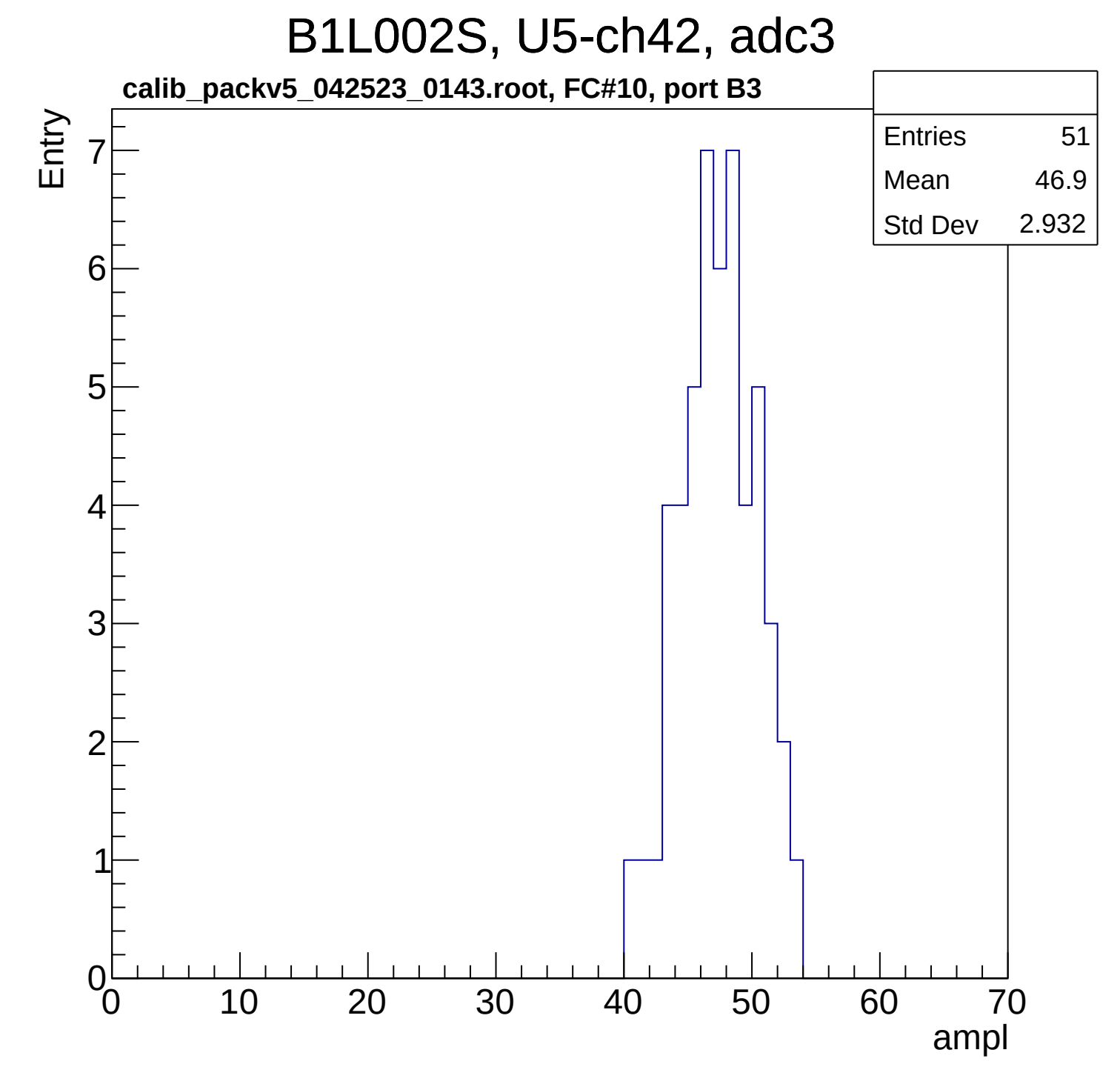
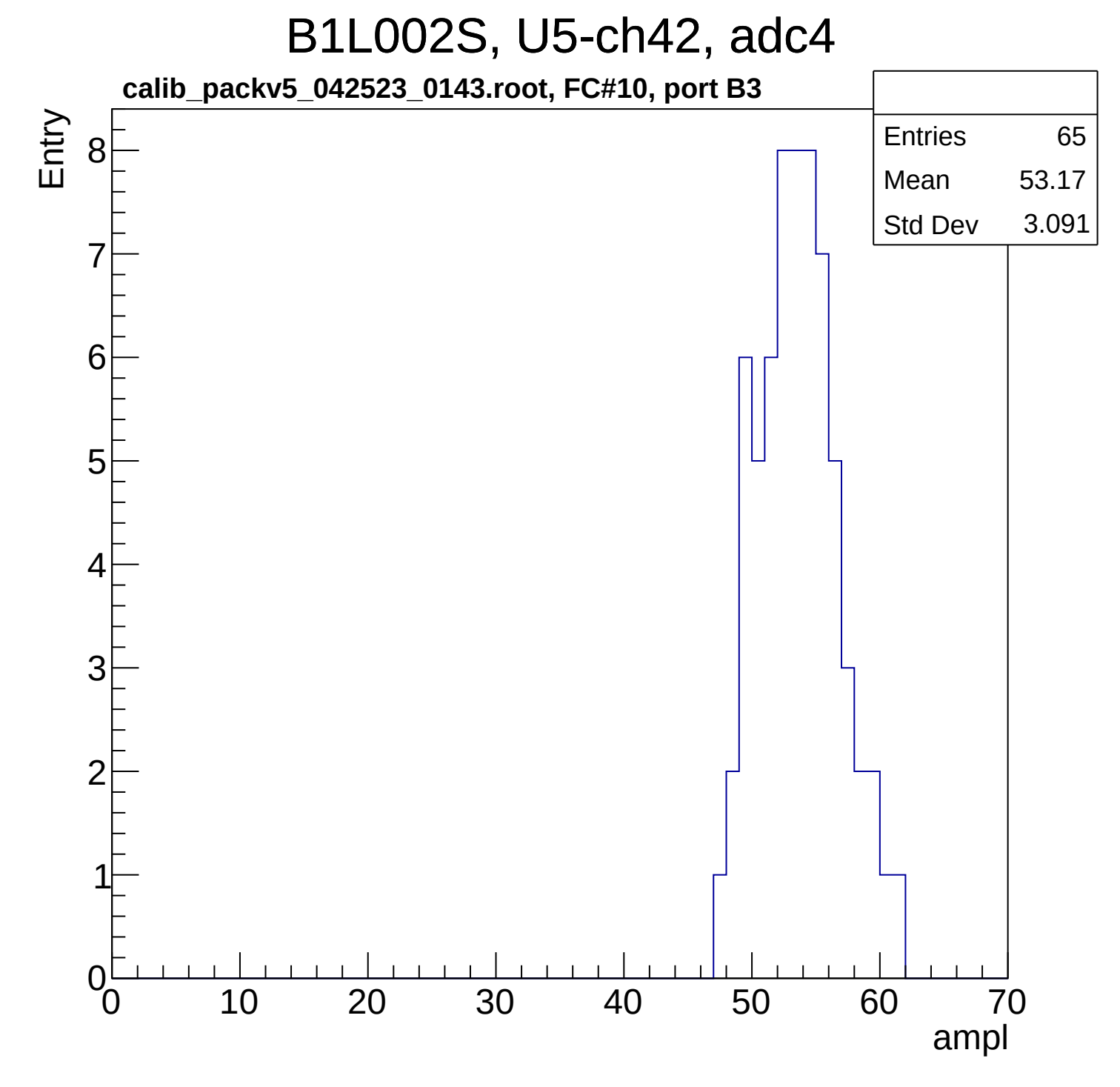

B1L002S, U5-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.9
Std Dev 2.932
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.17
Std Dev 3.091
Entry
ampl
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1
2
3
4
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7
8
0
10
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40
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60
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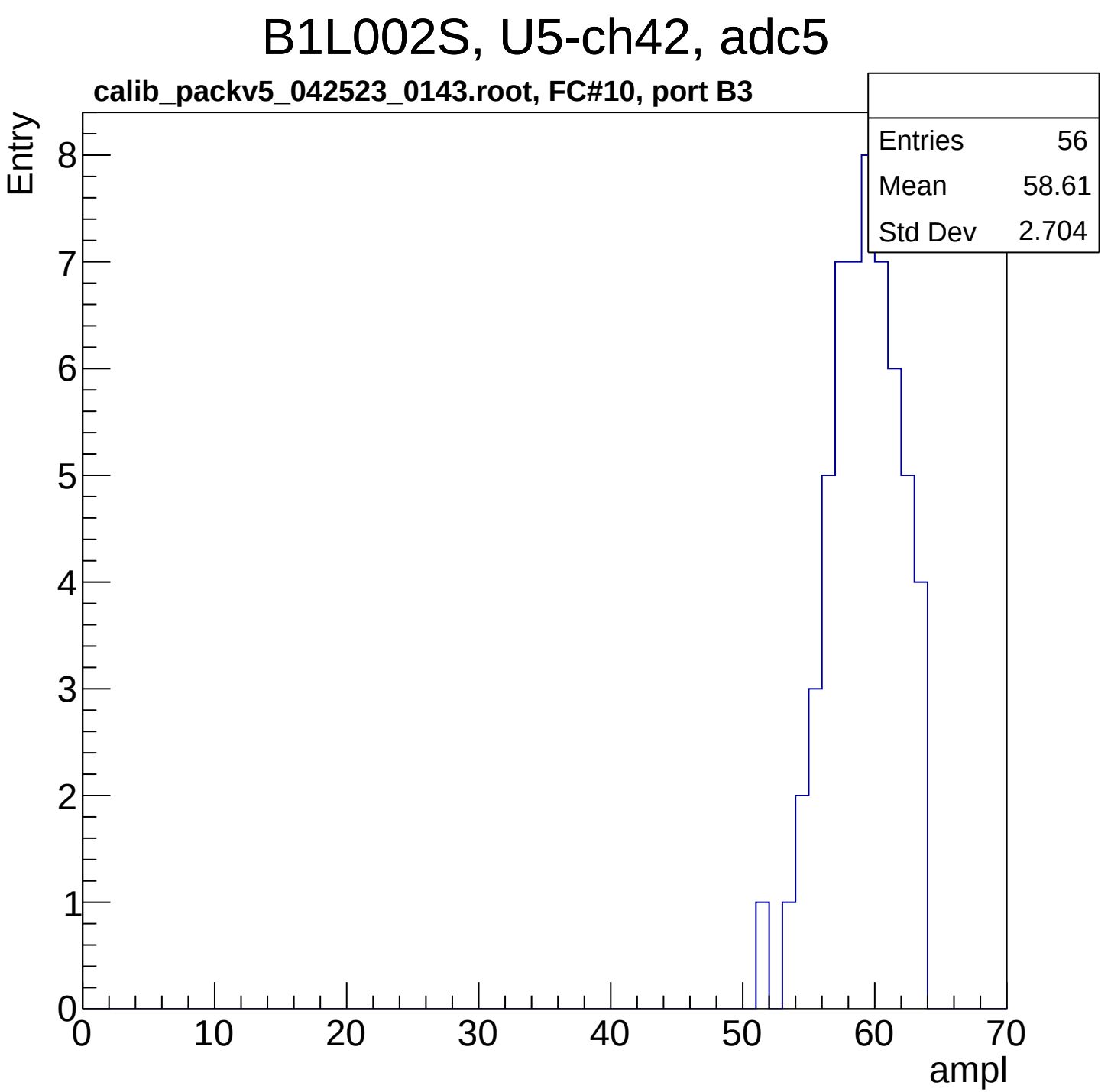
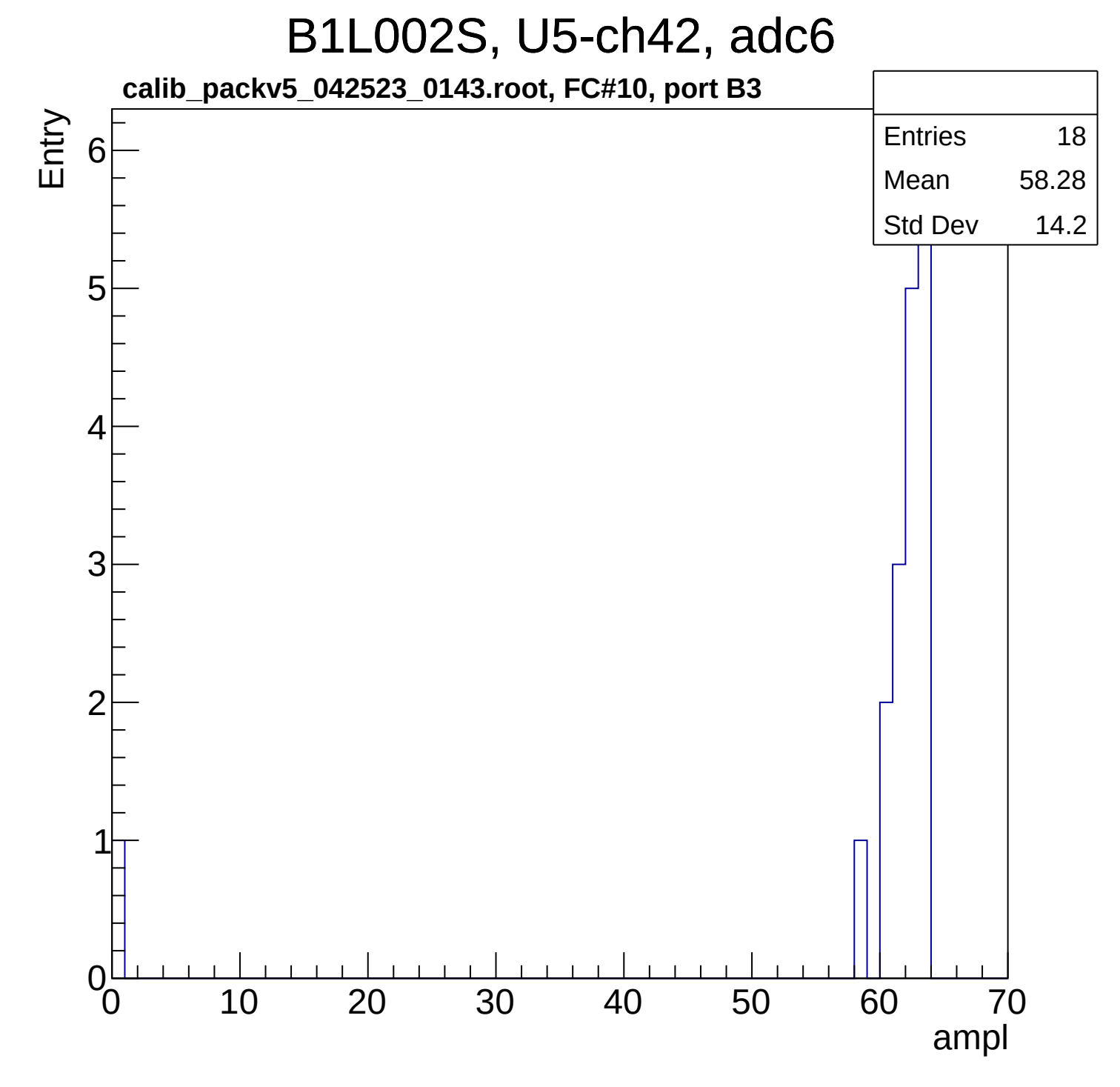
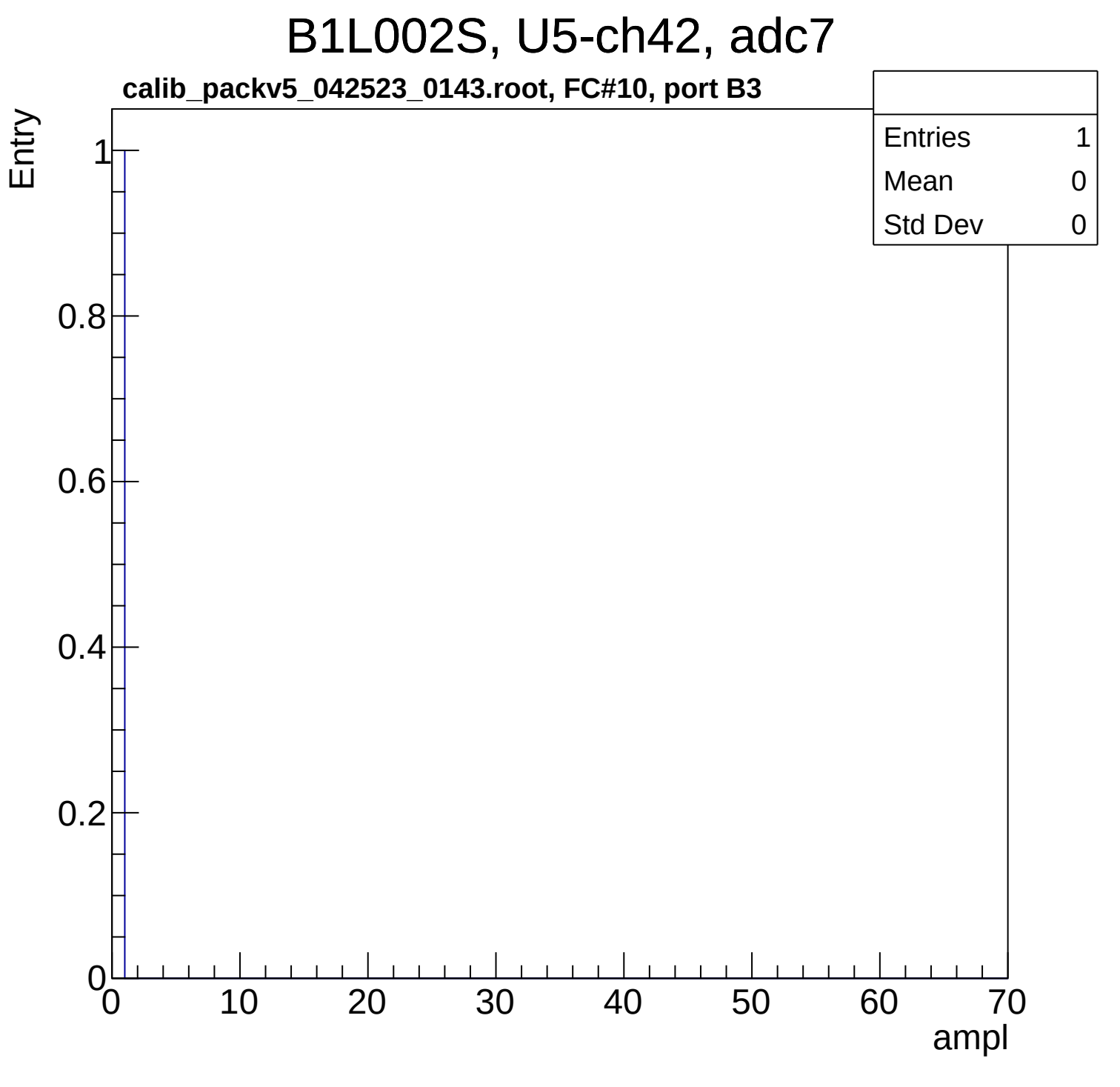
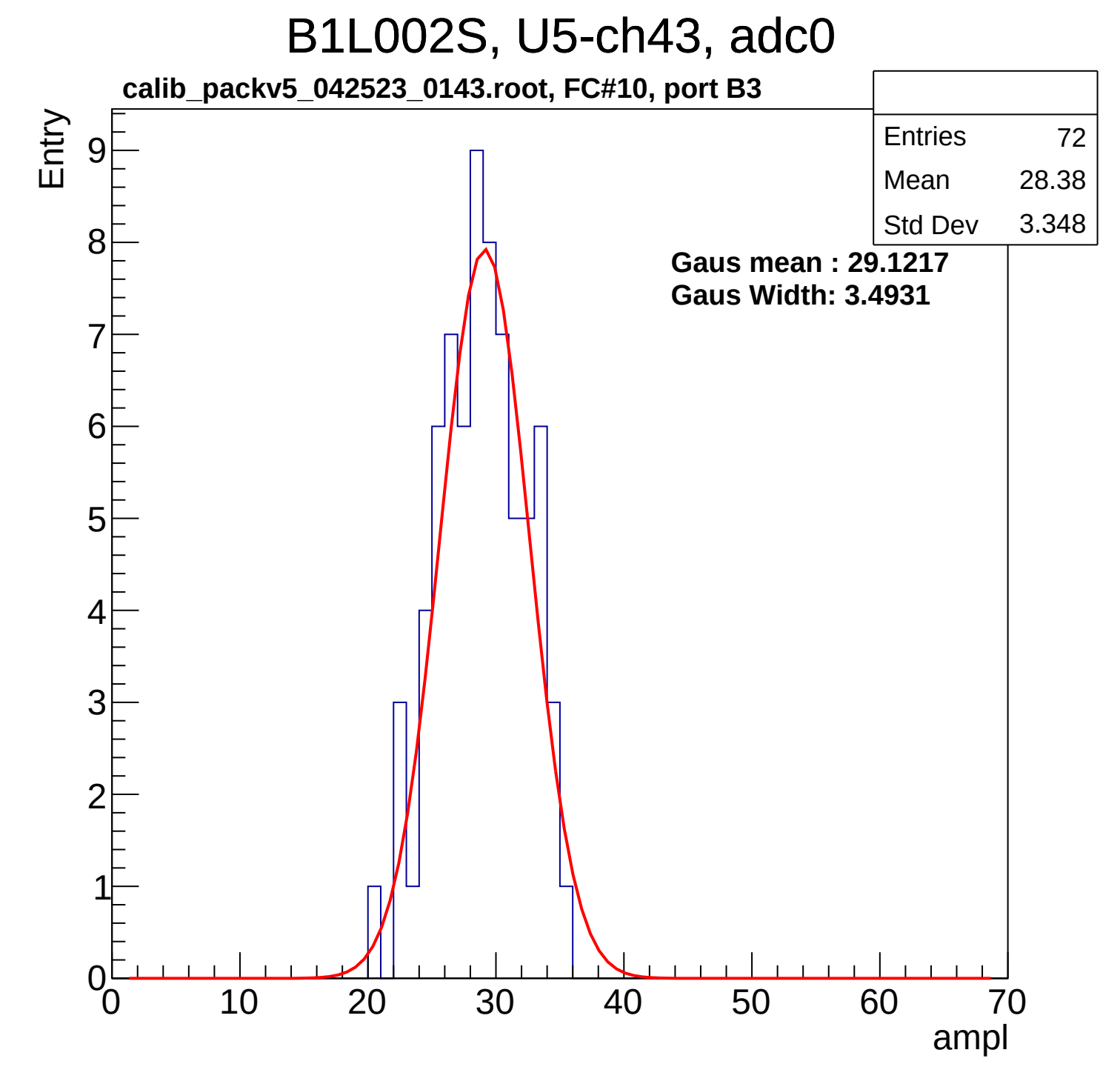
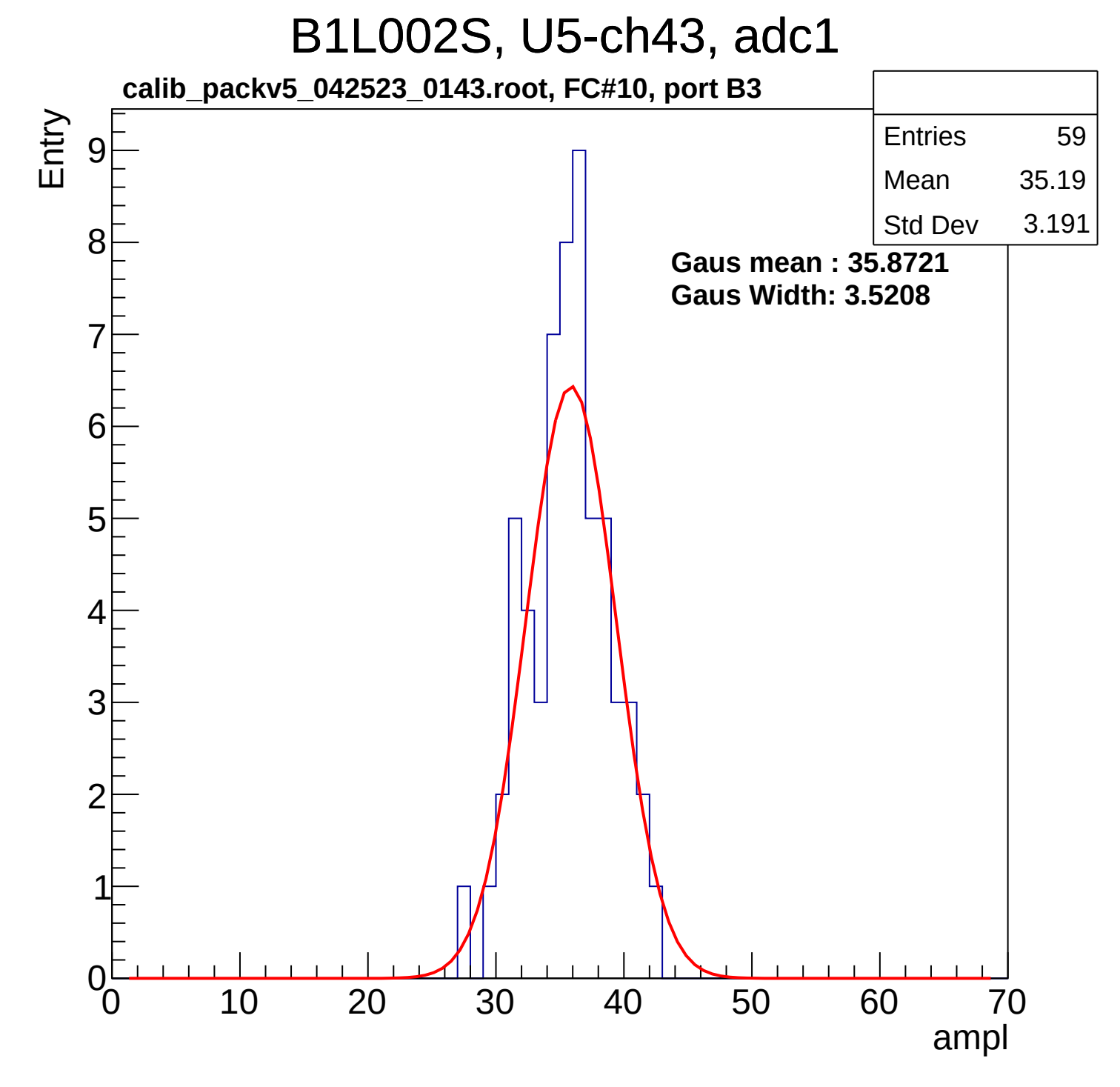

B1L002S, U5-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.61
Std Dev 2.704
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.28
Std Dev 14.2
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.38
Std Dev 3.348
Gaus mean : 29.1217
Gaus Width: 3.4931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.19
Std Dev 3.191
Gaus mean : 35.8721
Gaus Width: 3.5208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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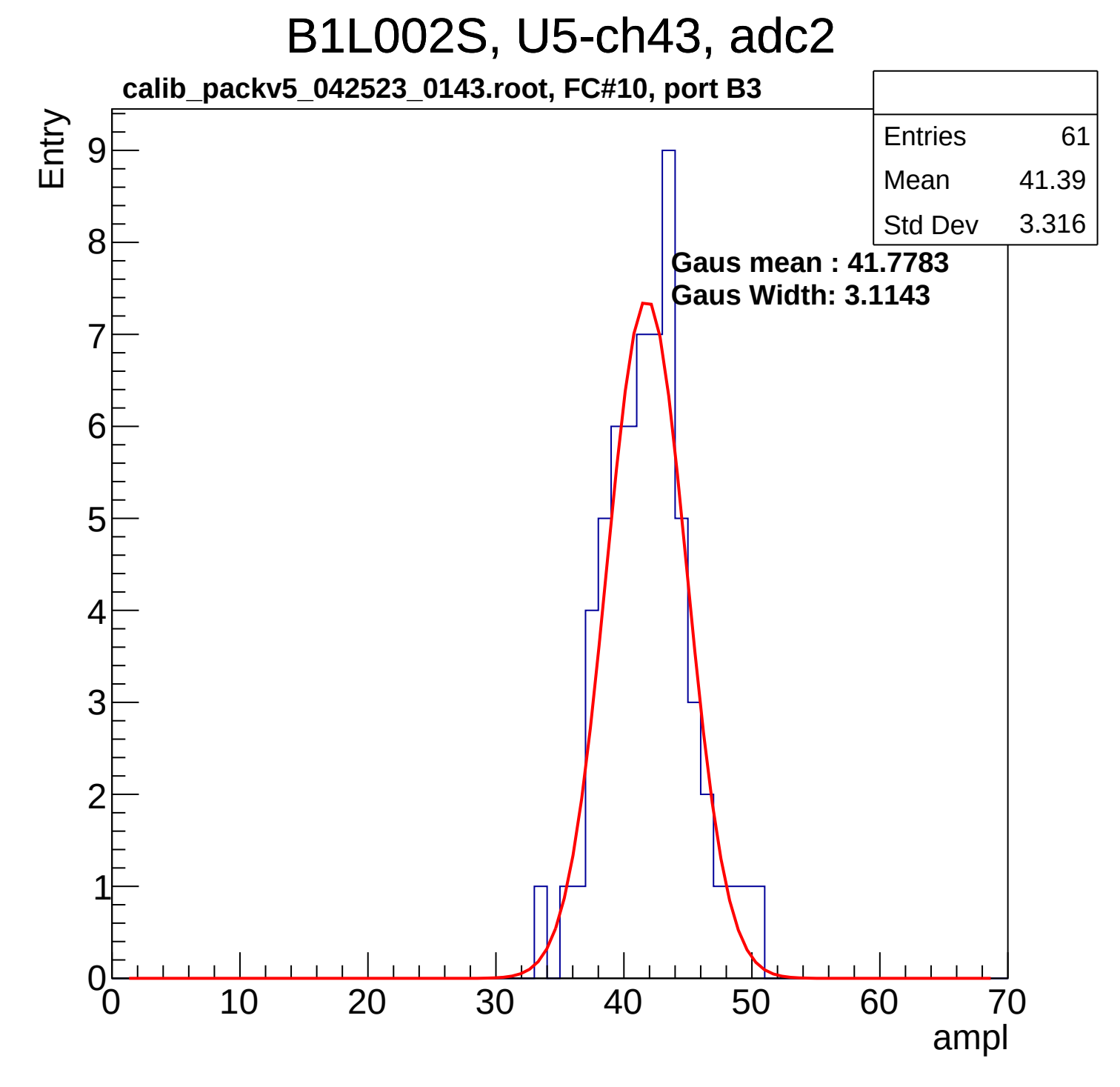
B1L002S, U5-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.39
Std Dev 3.316
Gaus mean : 41.7783
Gaus Width: 3.1143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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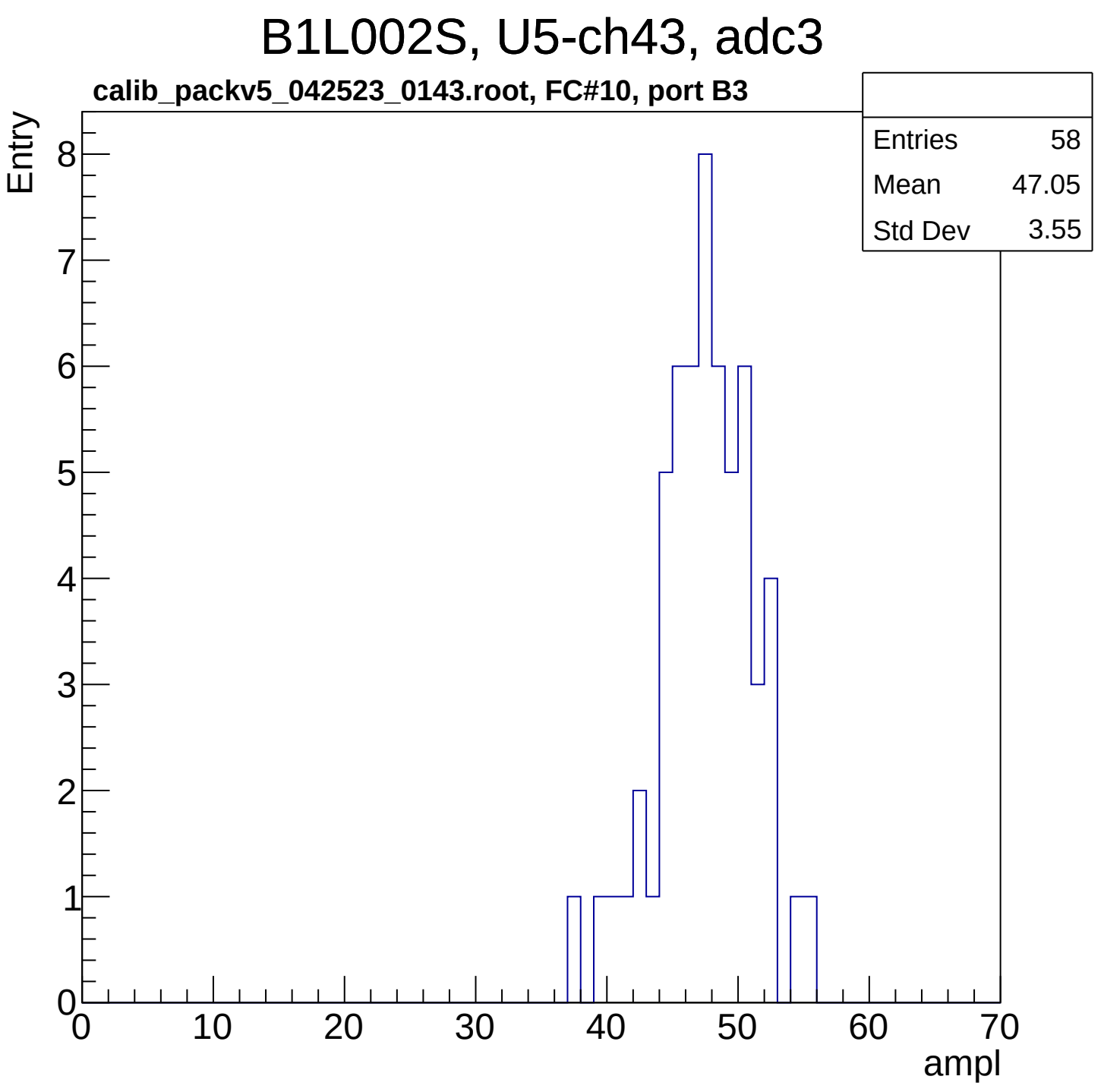
B1L002S, U5-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.05
Std Dev 3.55
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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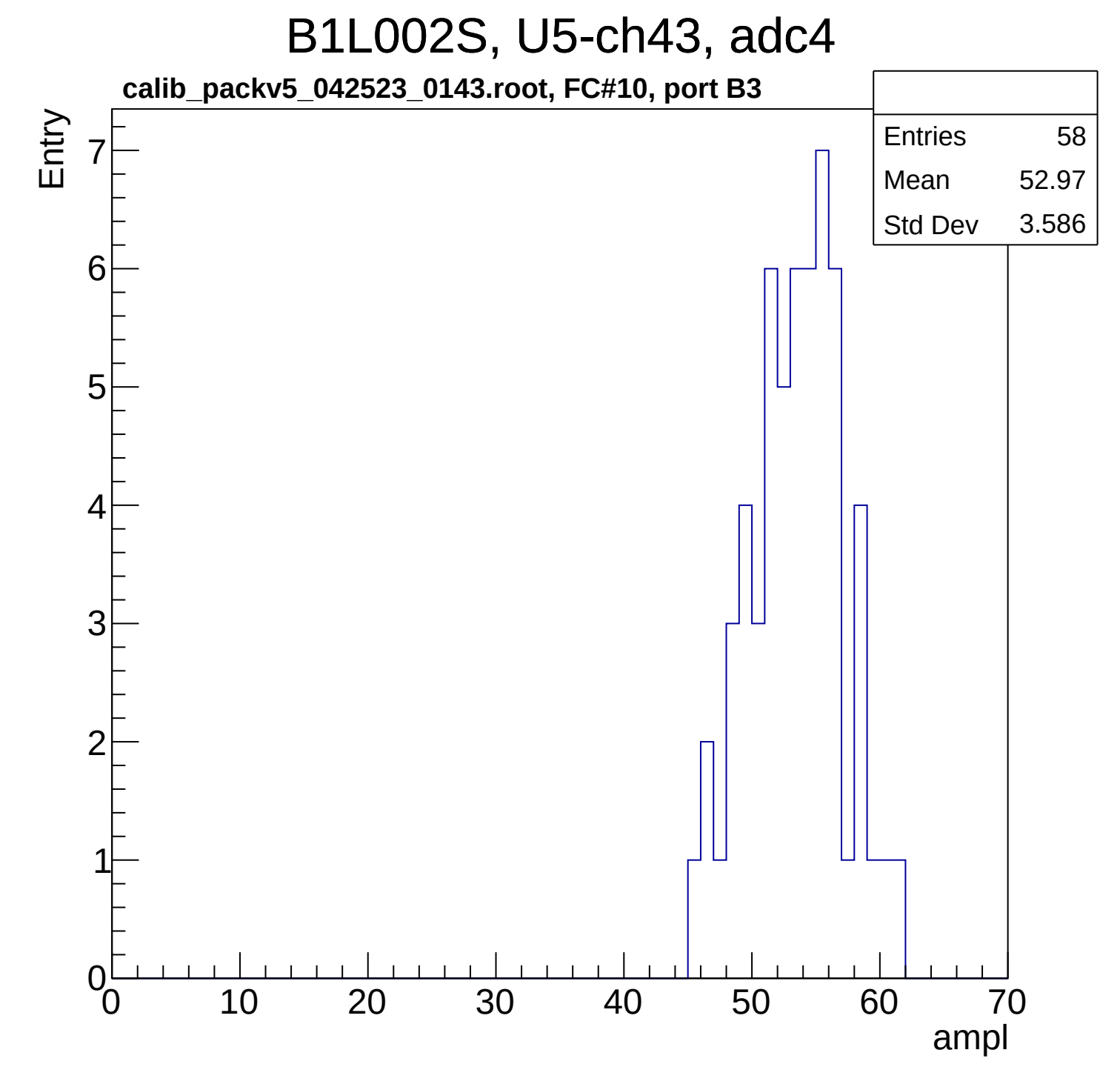
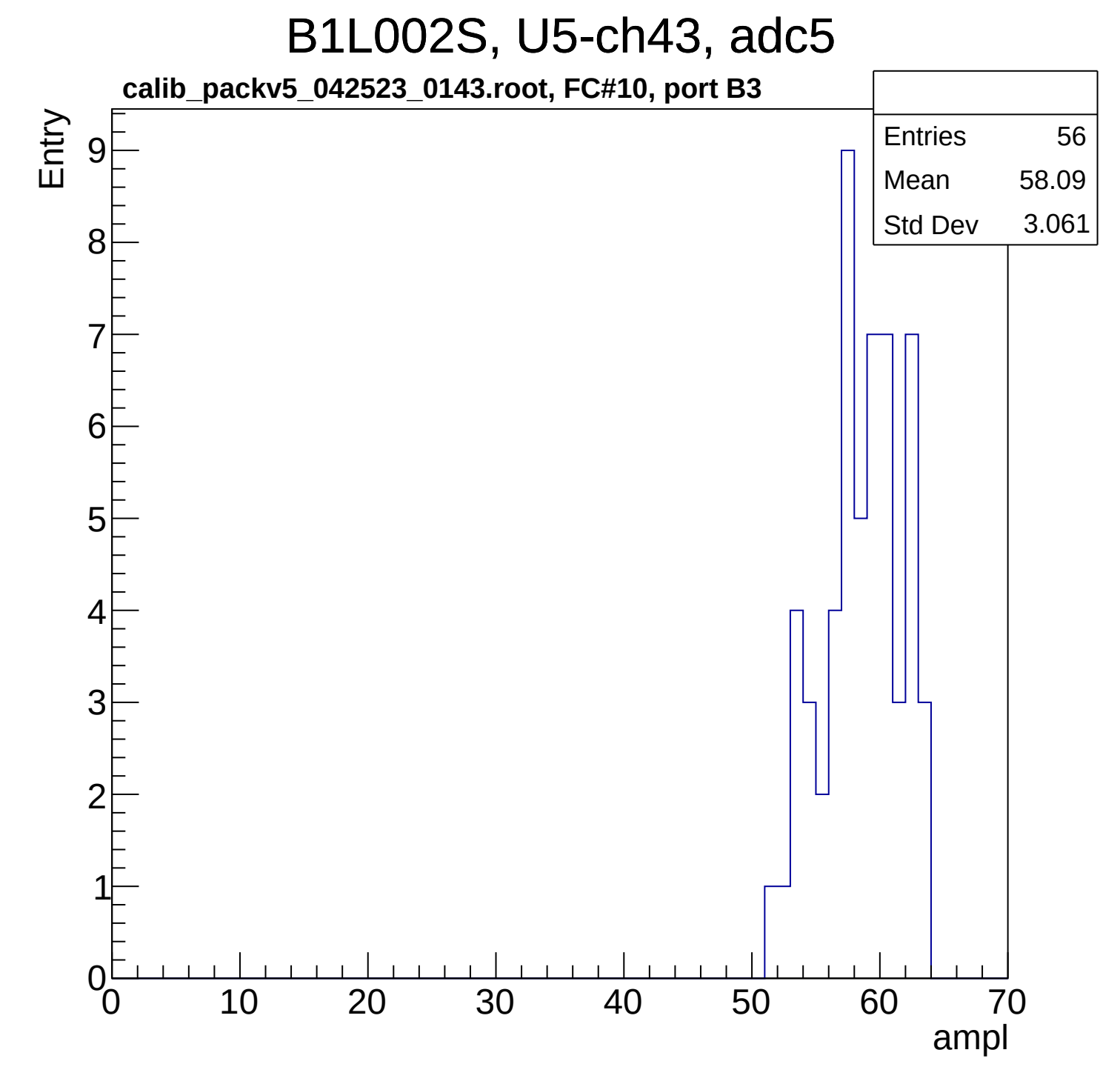

B1L002S, U5-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.97
Std Dev 3.586
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.09
Std Dev 3.061
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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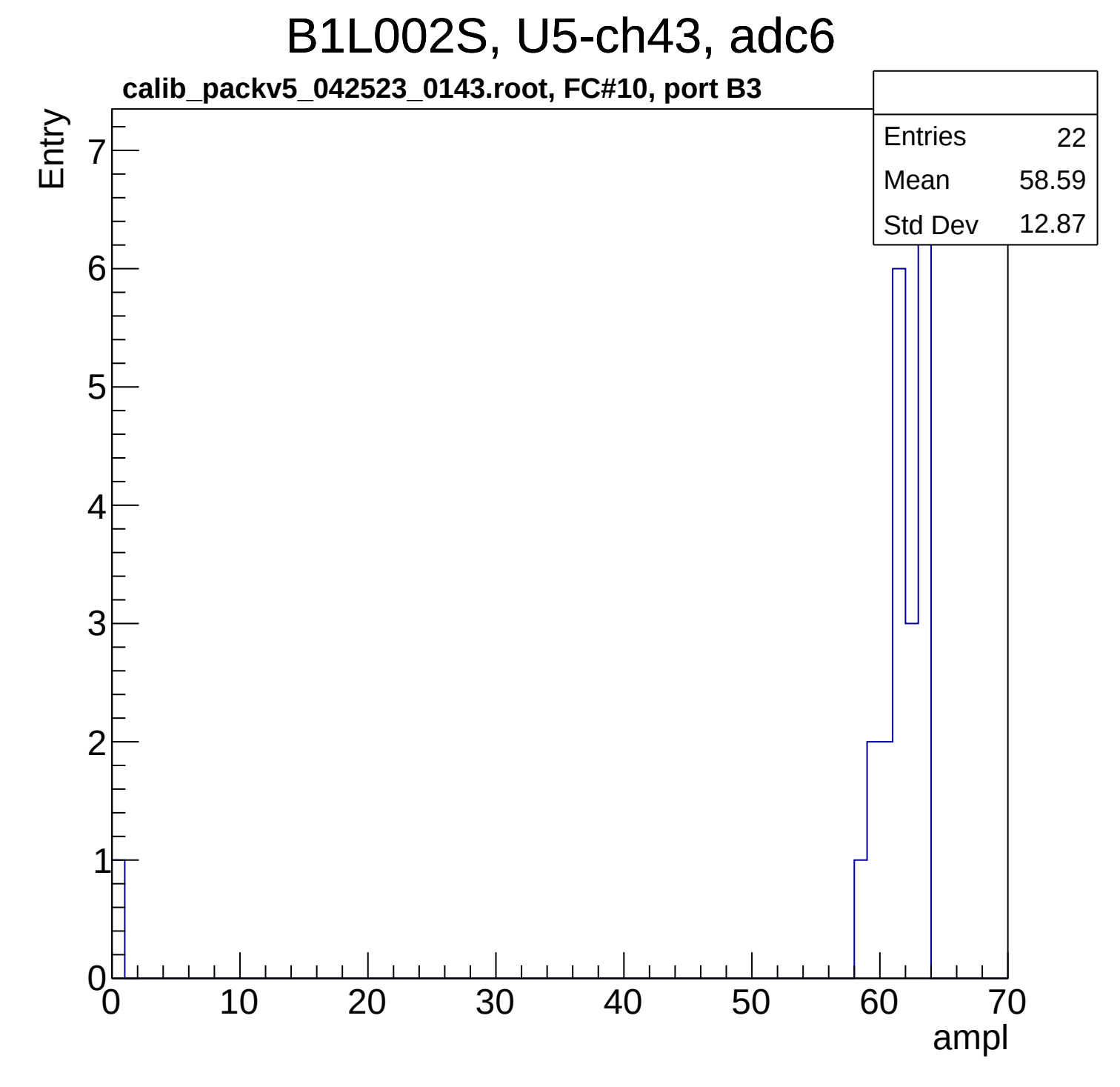
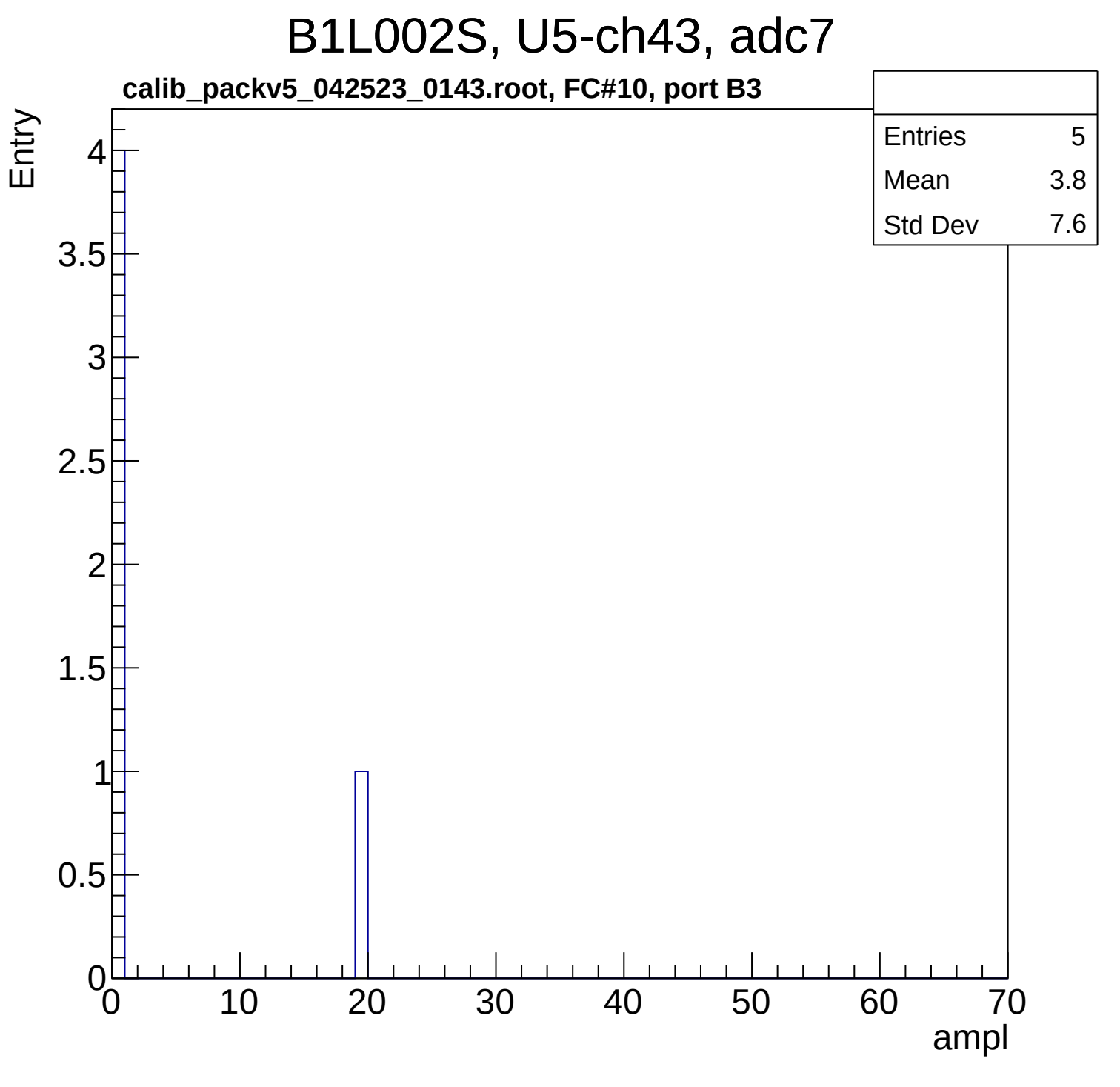
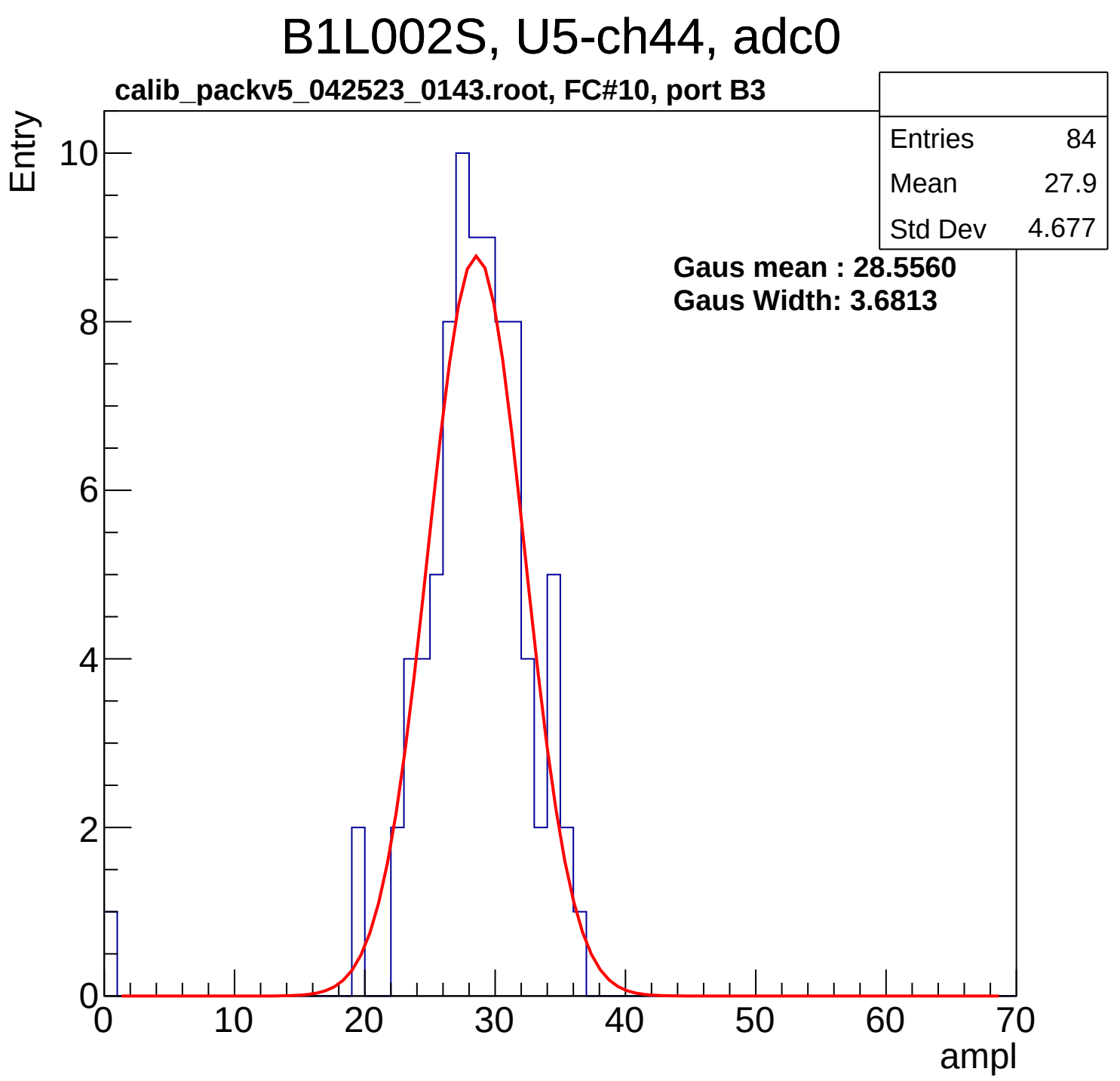
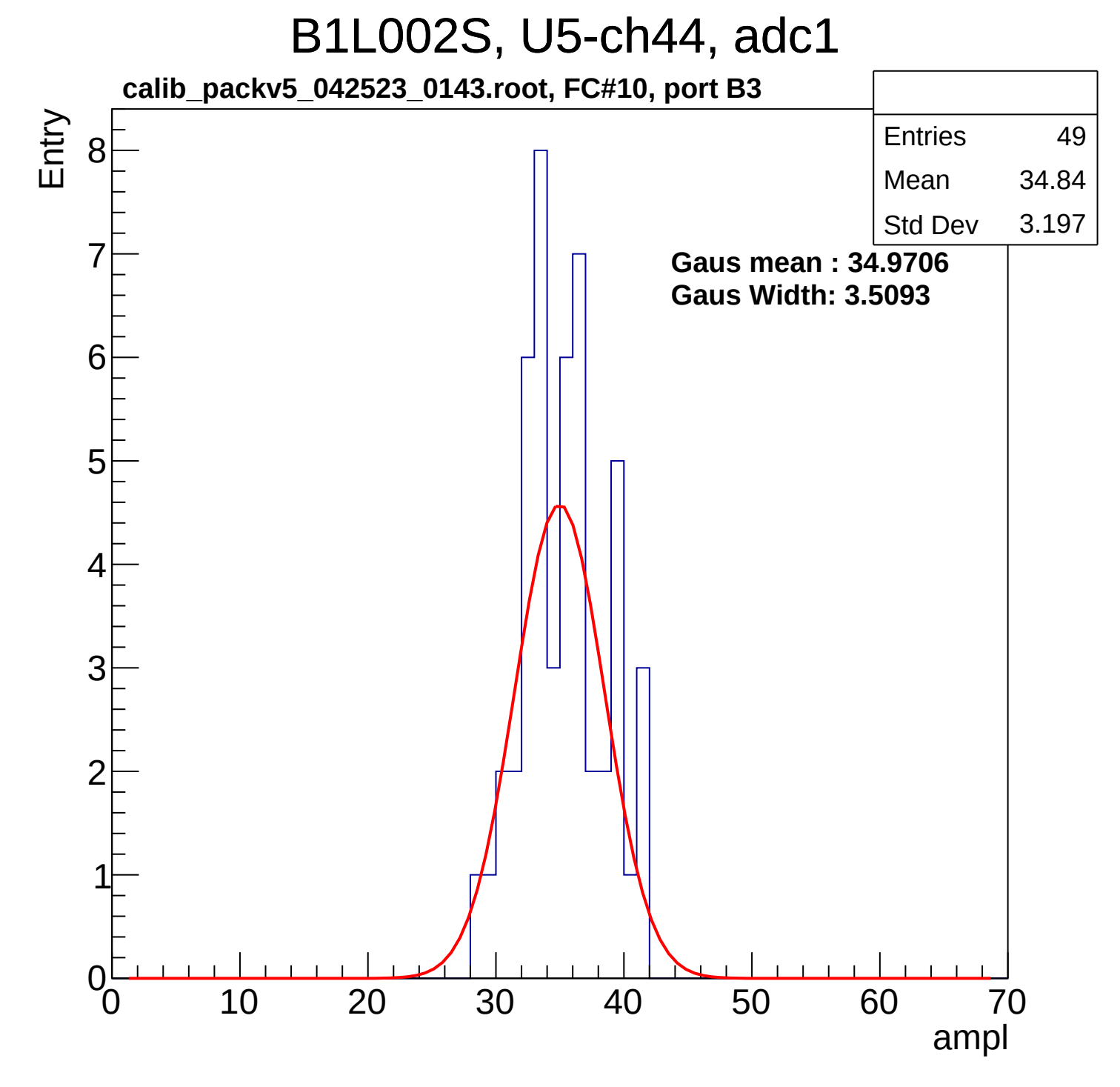
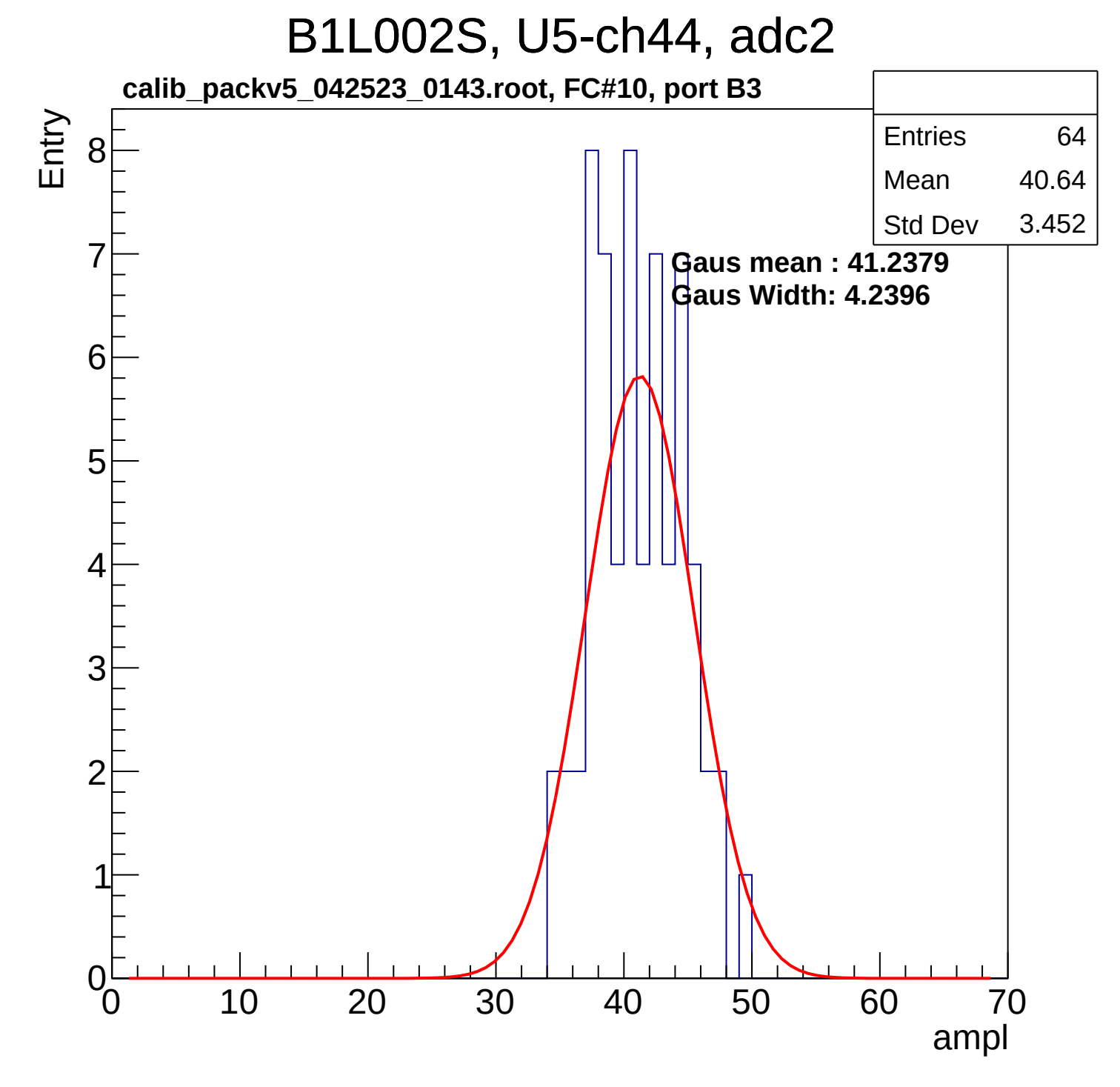
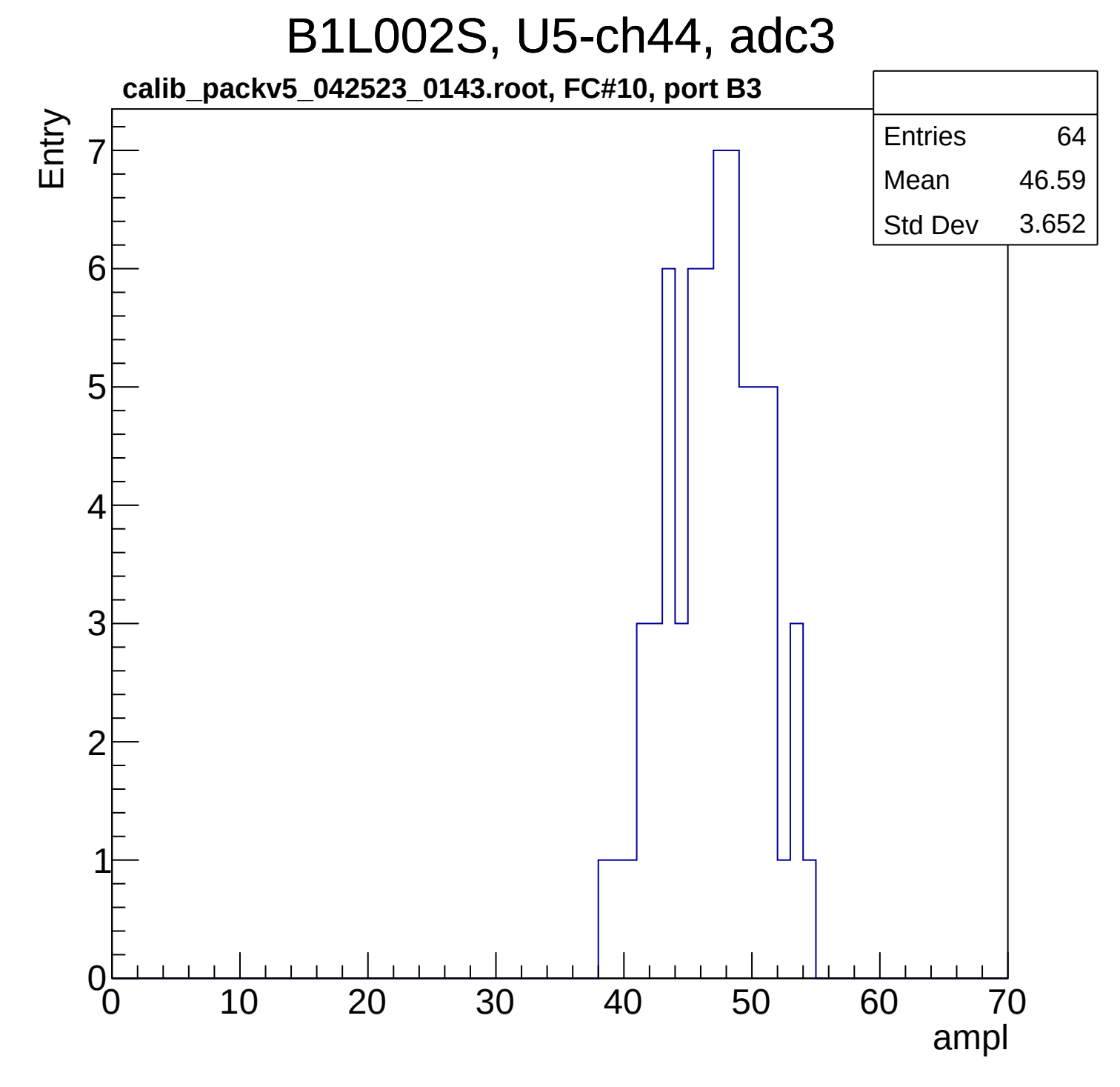

B1L002S, U5-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.59
Std Dev 12.87
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 3.8
Std Dev 7.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U5-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 27.9
Std Dev 4.677
Gaus mean : 28.5560
Gaus Width: 3.6813
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 34.84
Std Dev 3.197
Gaus mean : 34.9706
Gaus Width: 3.5093
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.64
Std Dev 3.452
Gaus mean : 41.2379
Gaus Width: 4.2396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.59
Std Dev 3.652
Entry
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2
3
4
5
6
7
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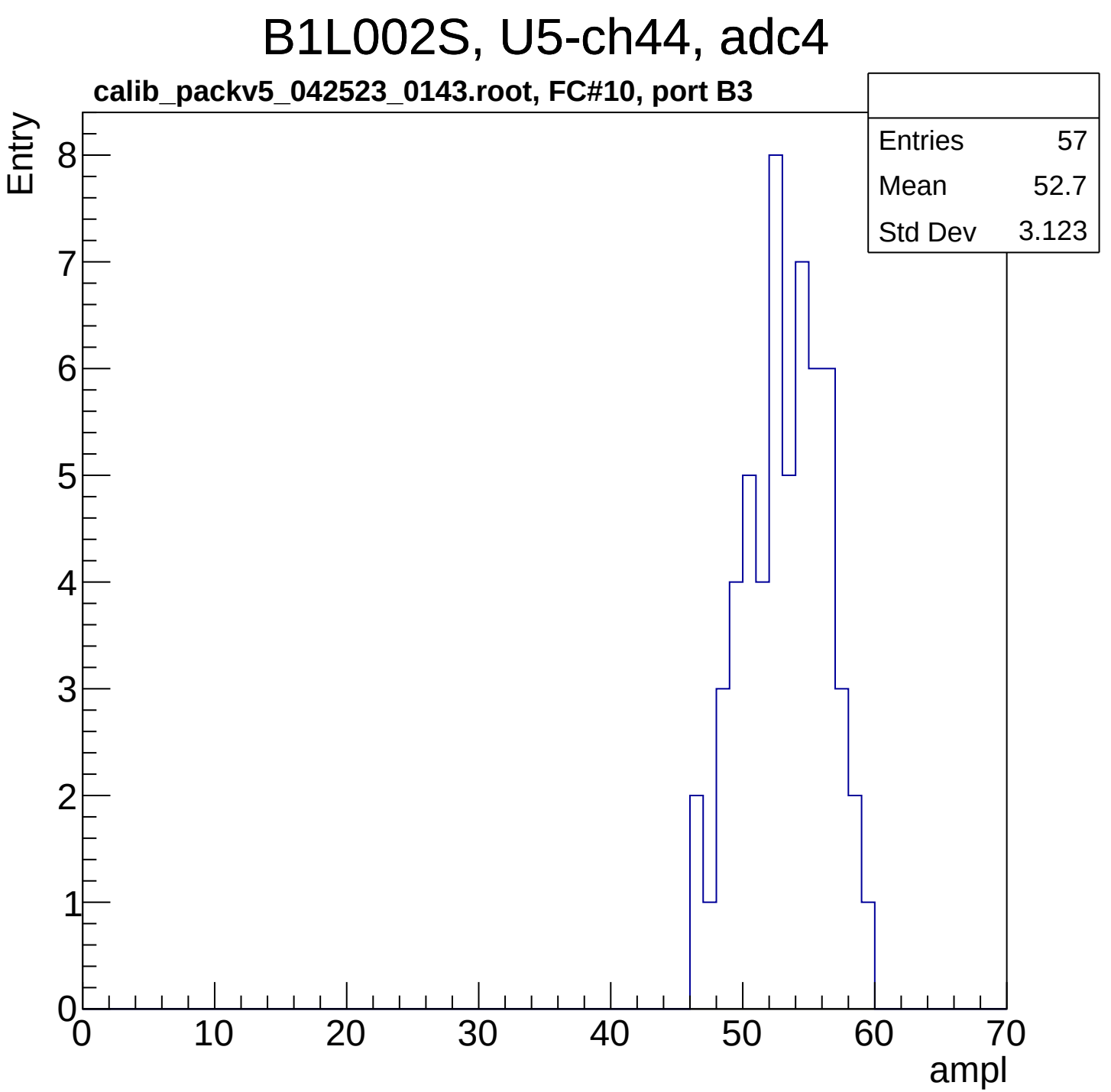
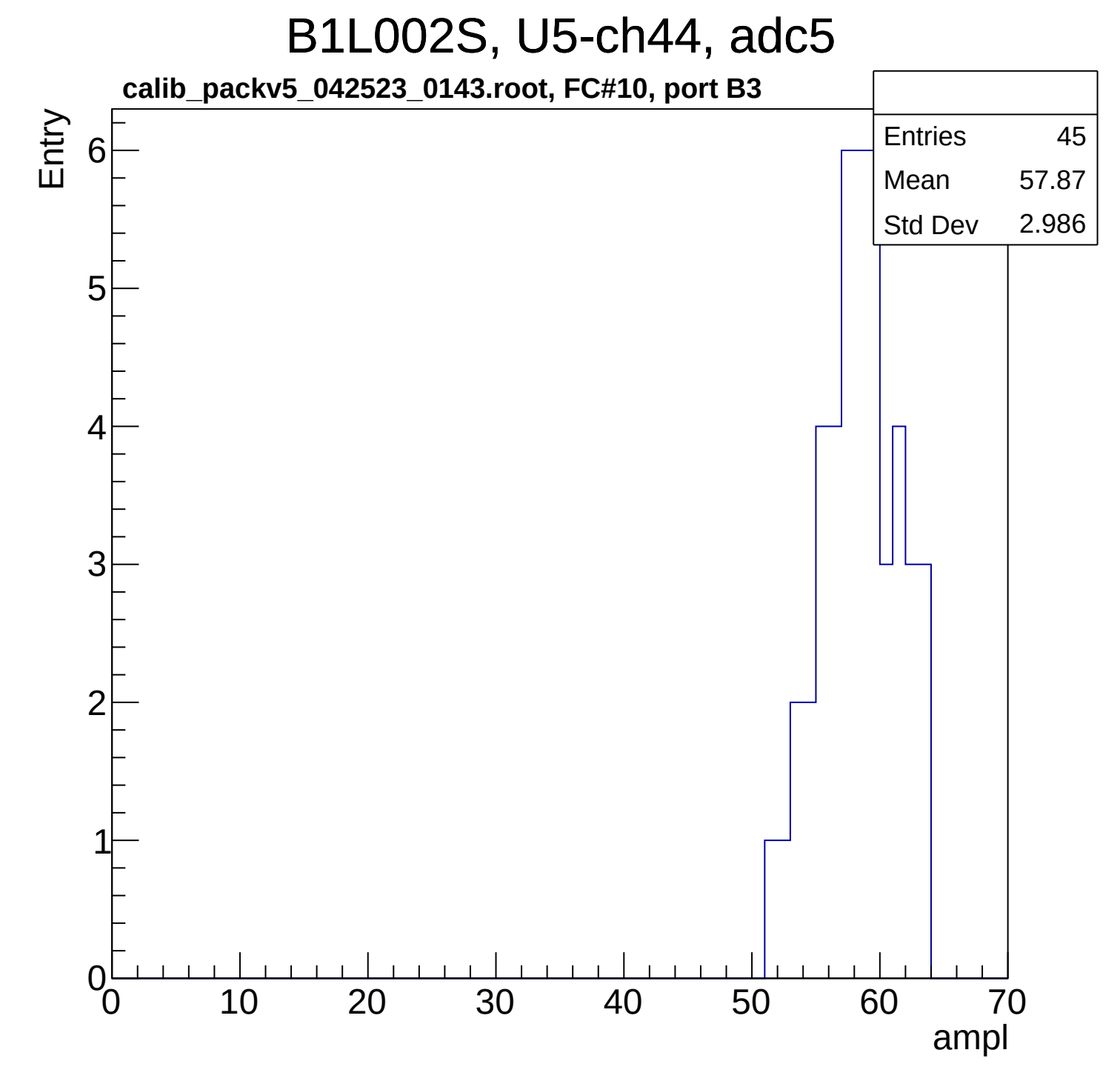
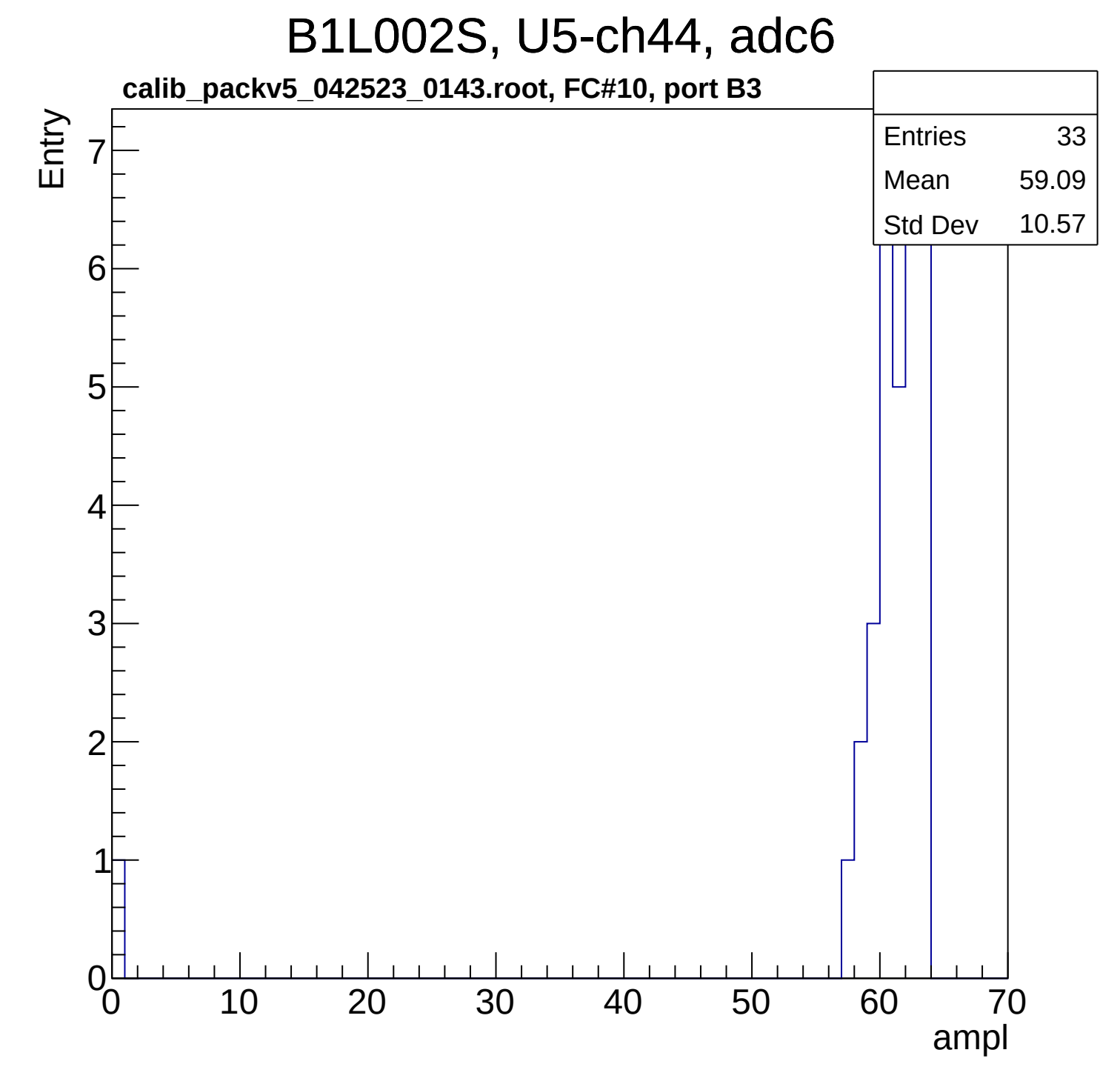
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.7
Std Dev 3.123
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.87
Std Dev 2.986
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 59.09
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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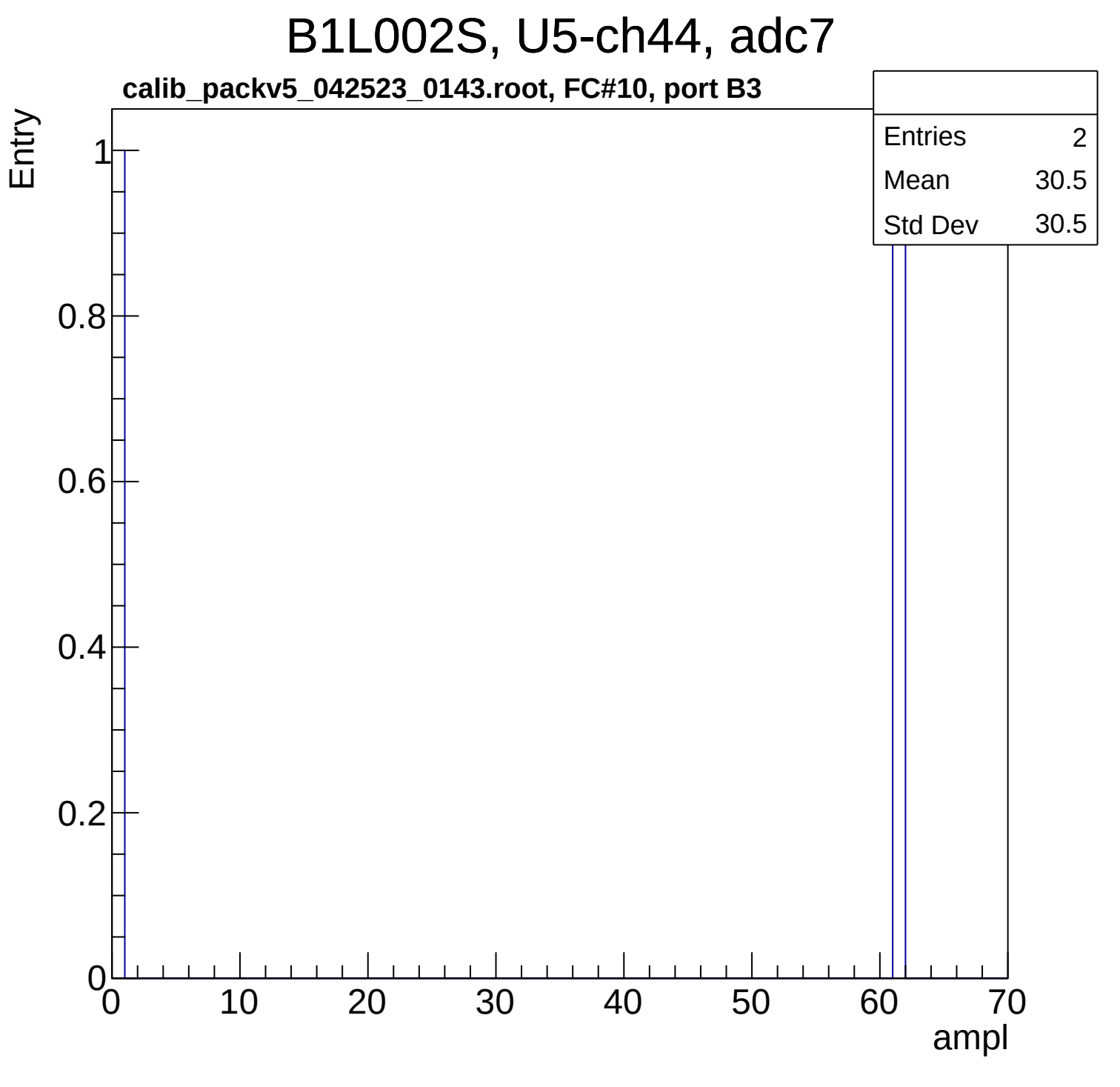
B1L002S, U5-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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0.6
0.4
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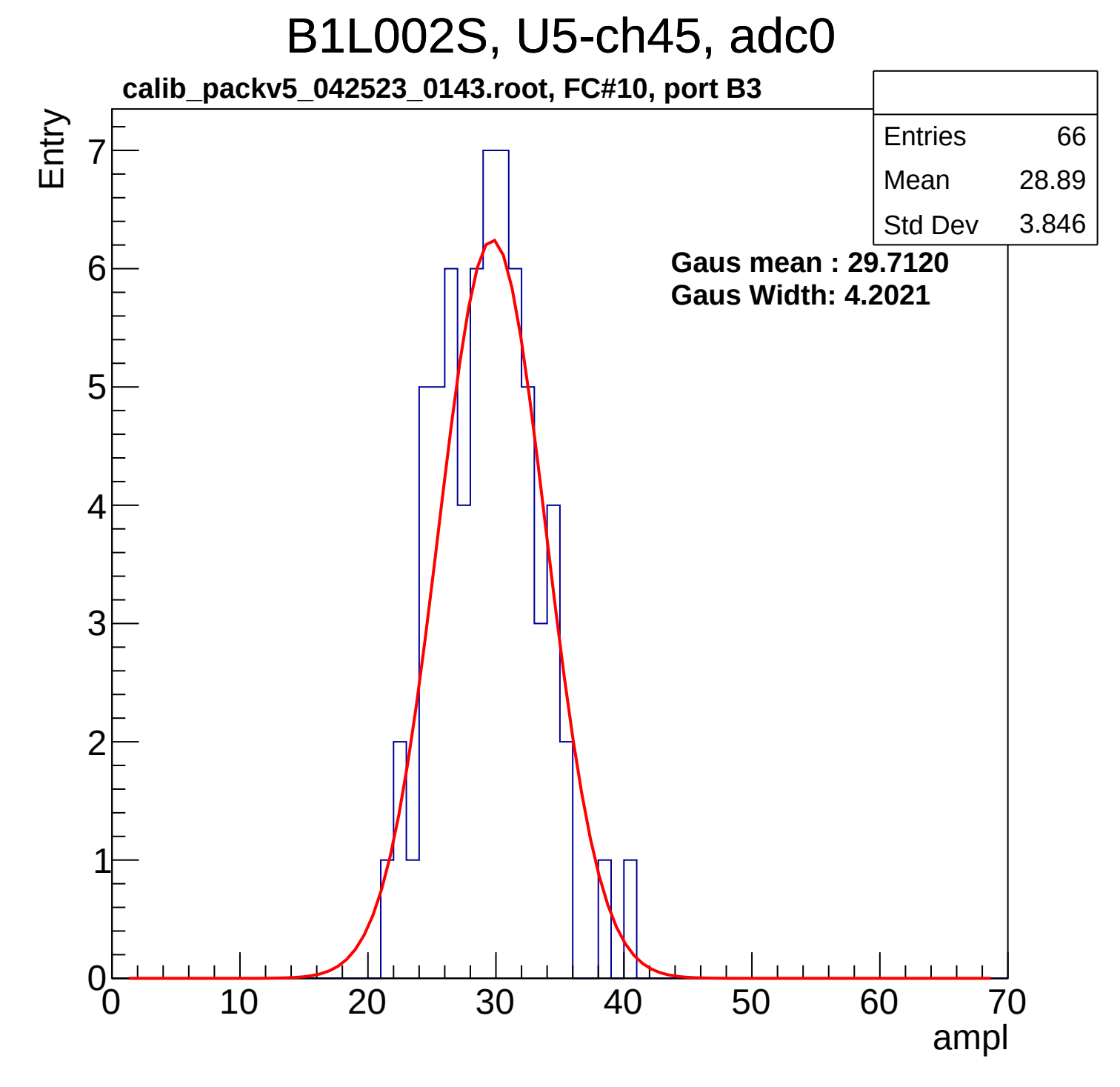
70
ampl


B1L002S, U5-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.89
Std Dev 3.846
Gaus mean : 29.7120
Gaus Width: 4.2021
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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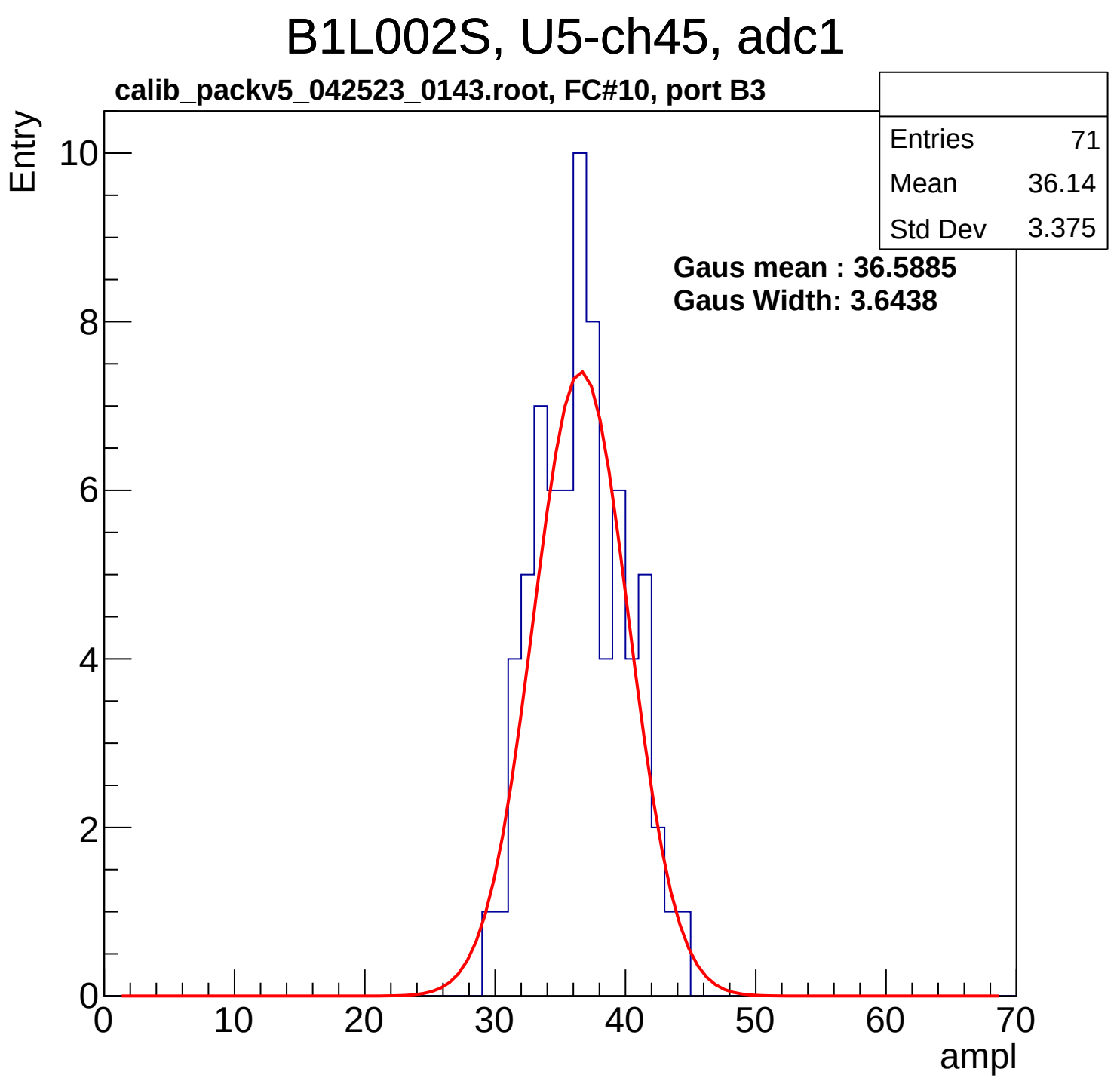
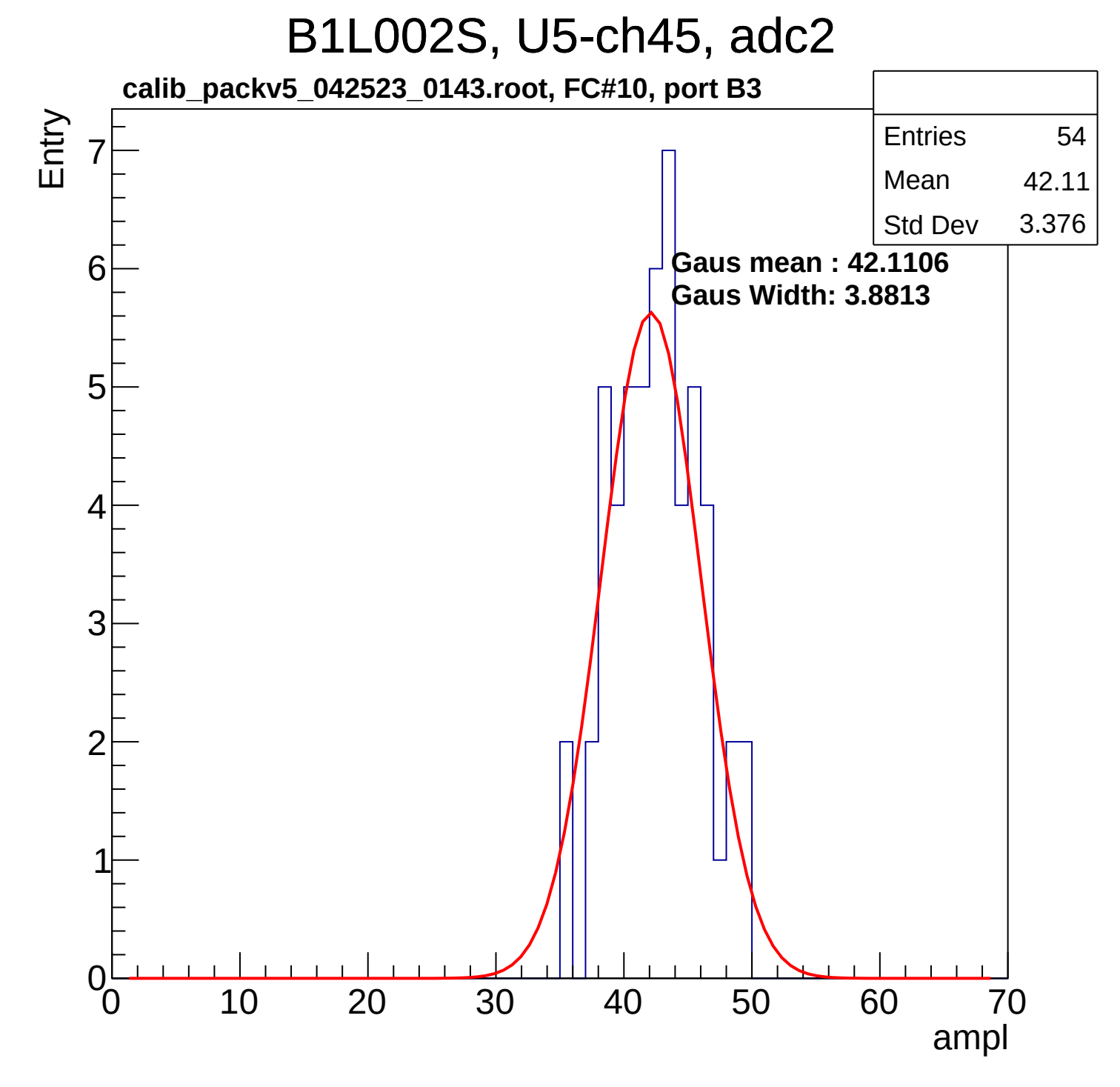
60
70


B1L002S, U5-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 36.14
Std Dev 3.375
Gaus mean : 36.5885
Gaus Width: 3.6438
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 42.11
Std Dev 3.376
Gaus mean : 42.1106
Gaus Width: 3.8813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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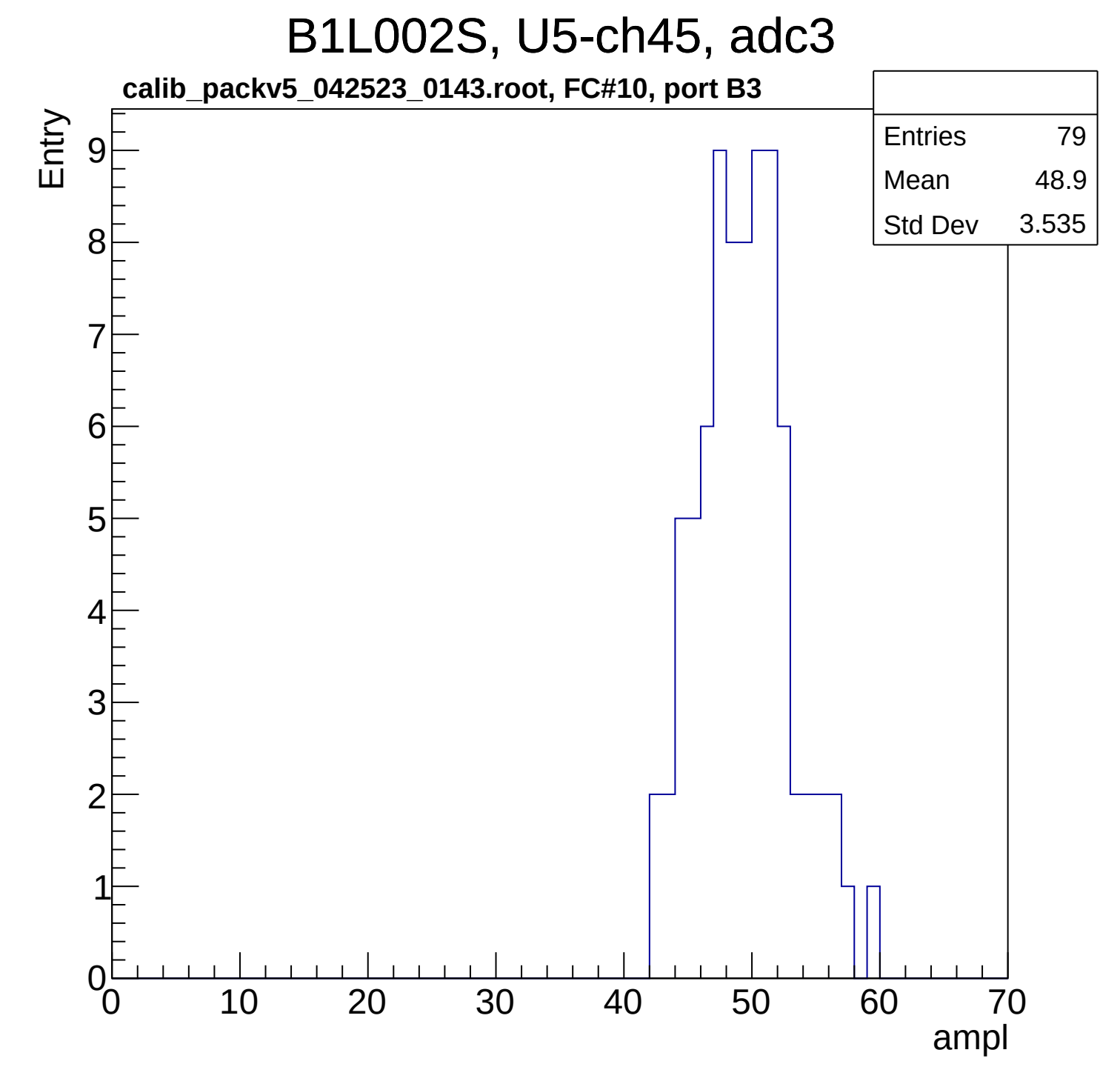
B1L002S, U5-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 48.9
Std Dev 3.535
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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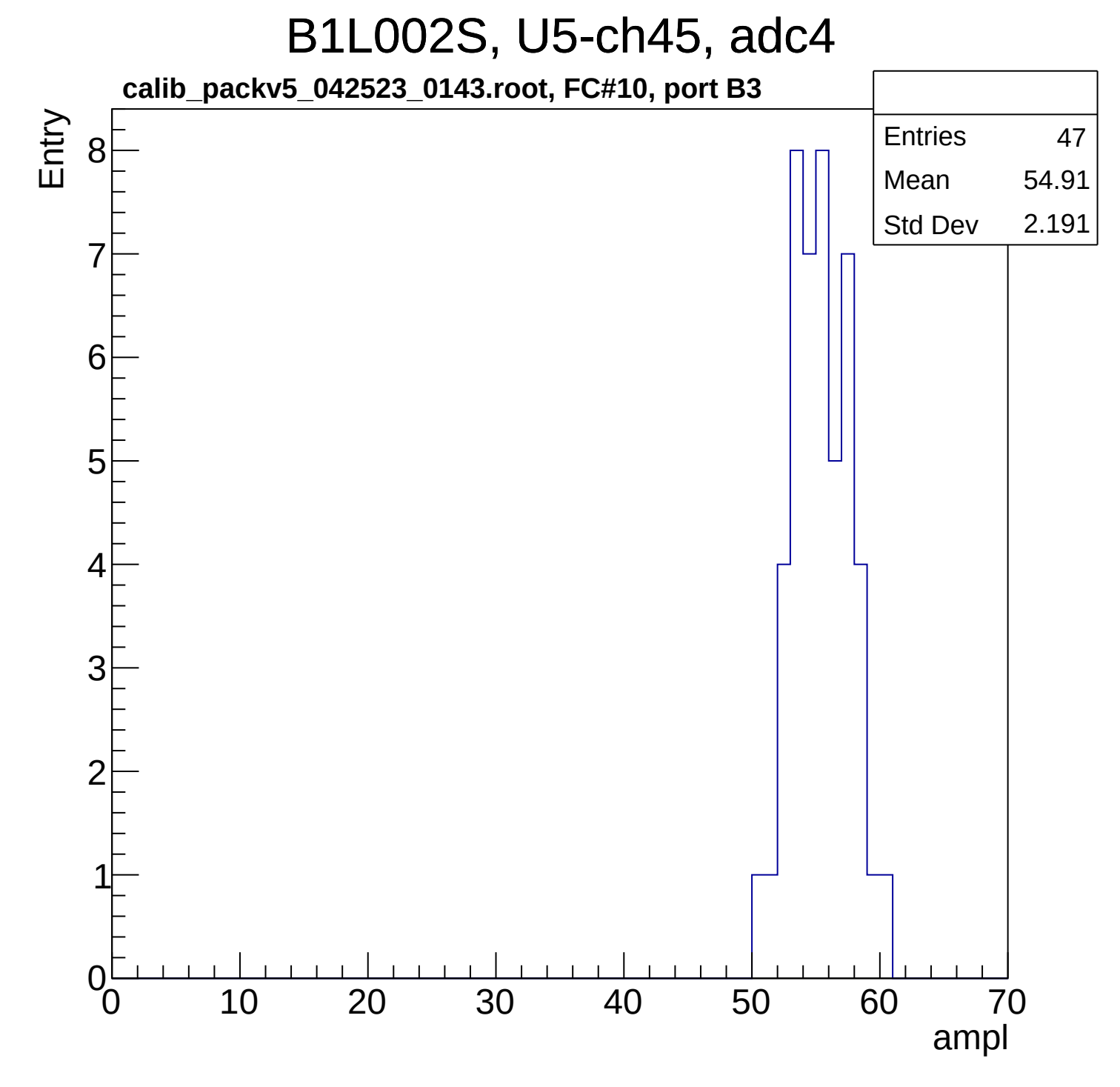
B1L002S, U5-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 54.91
Std Dev 2.191
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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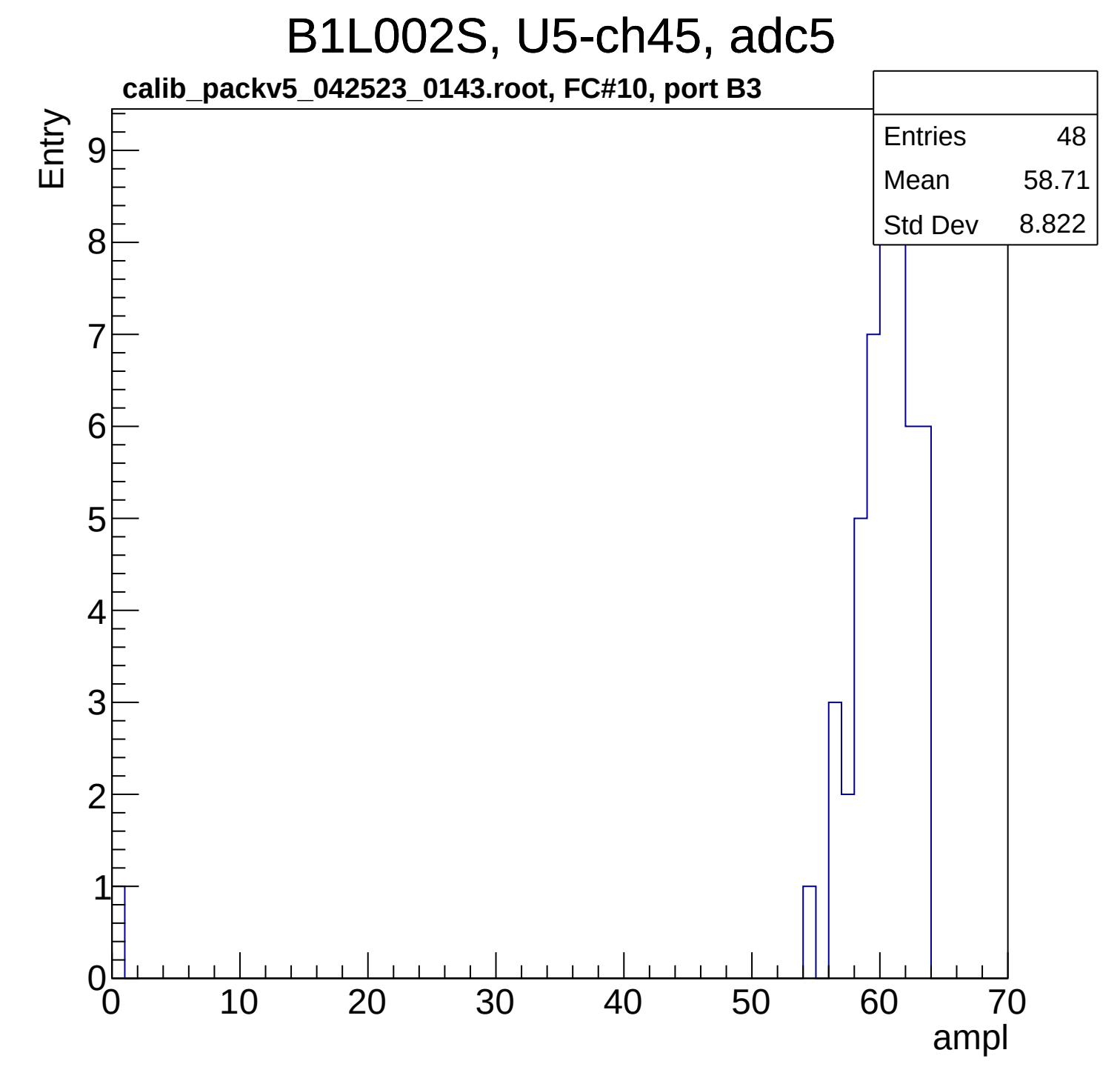
B1L002S, U5-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.71
Std Dev 8.822
Entry
ampl
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3
4
5
6
7
8
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0
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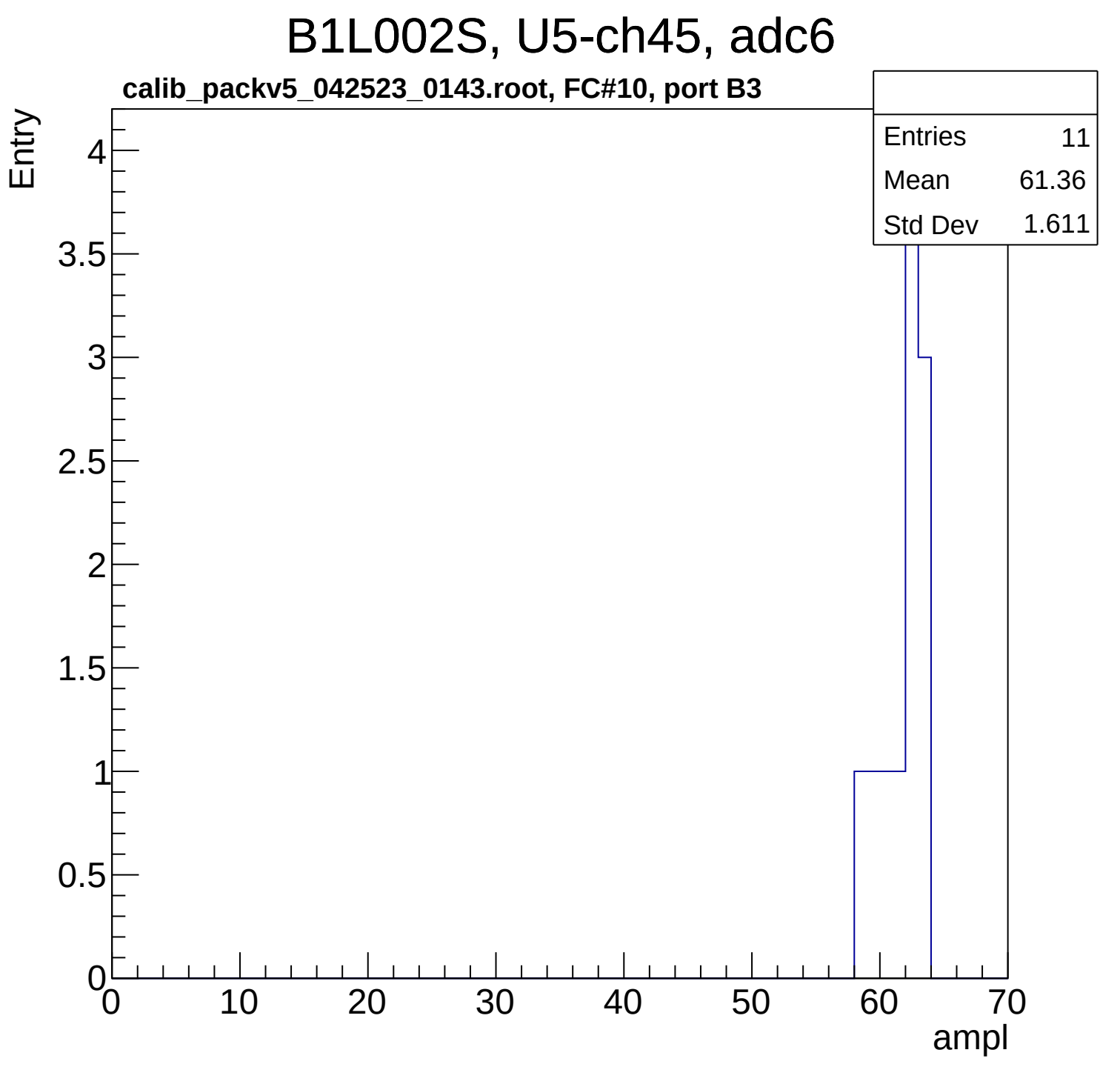
B1L002S, U5-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.36
Std Dev 1.611
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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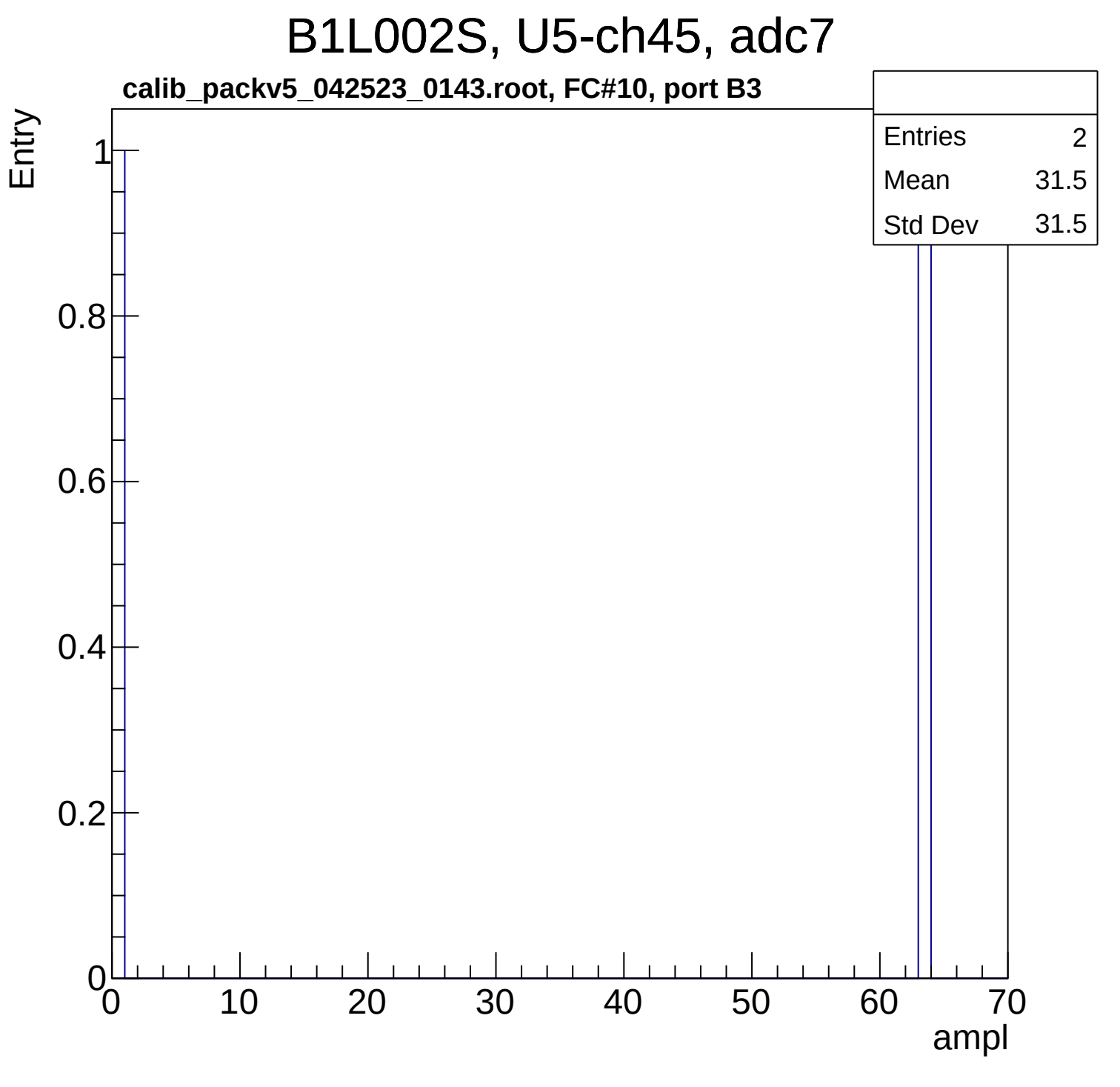
40
50
60
70


B1L002S, U5-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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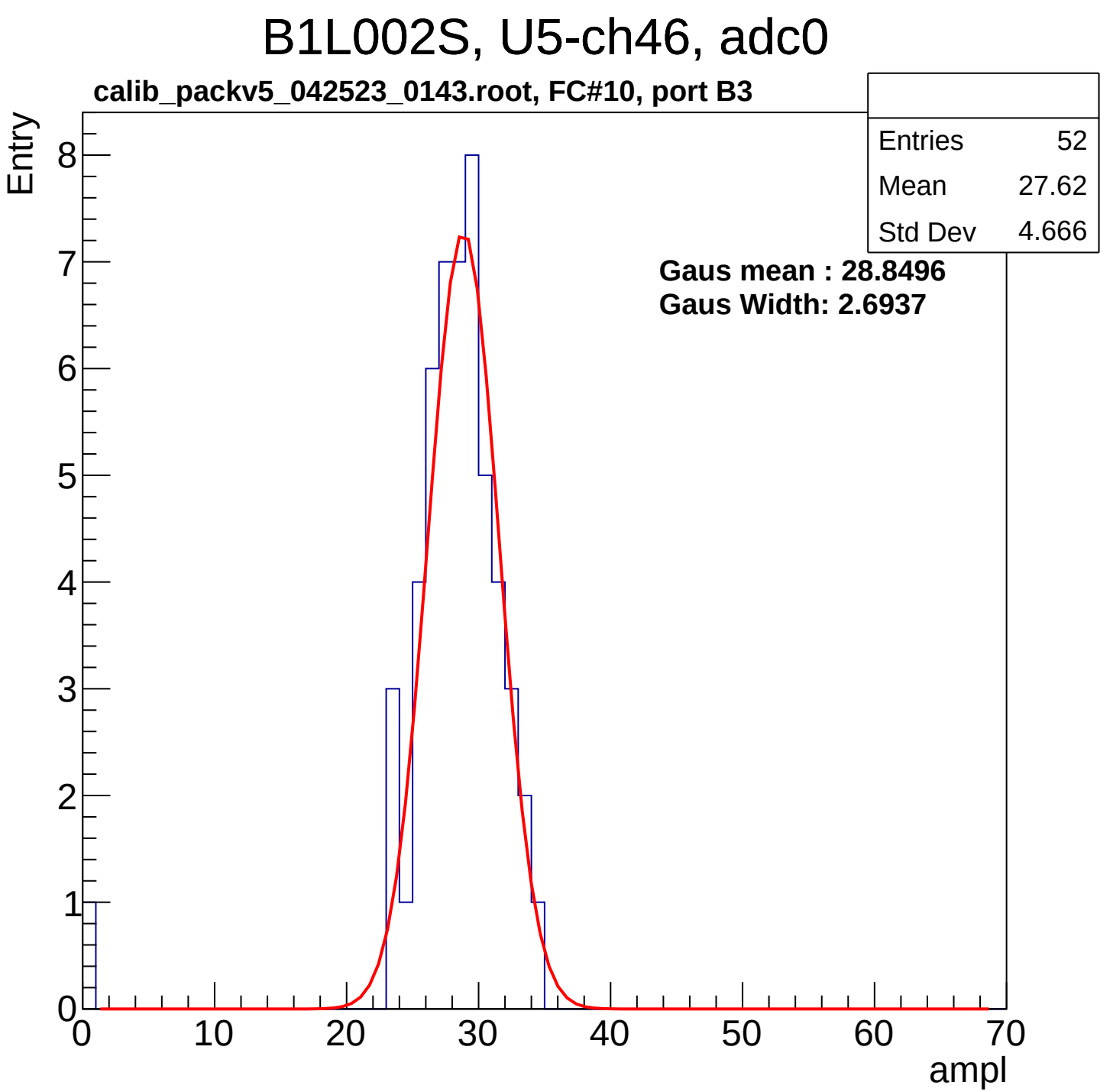
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.62
Std Dev 4.666
Gaus mean : 28.8496
Gaus Width: 2.6937
Entry
ampl
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1
2
3
4
5
6
7
8
0
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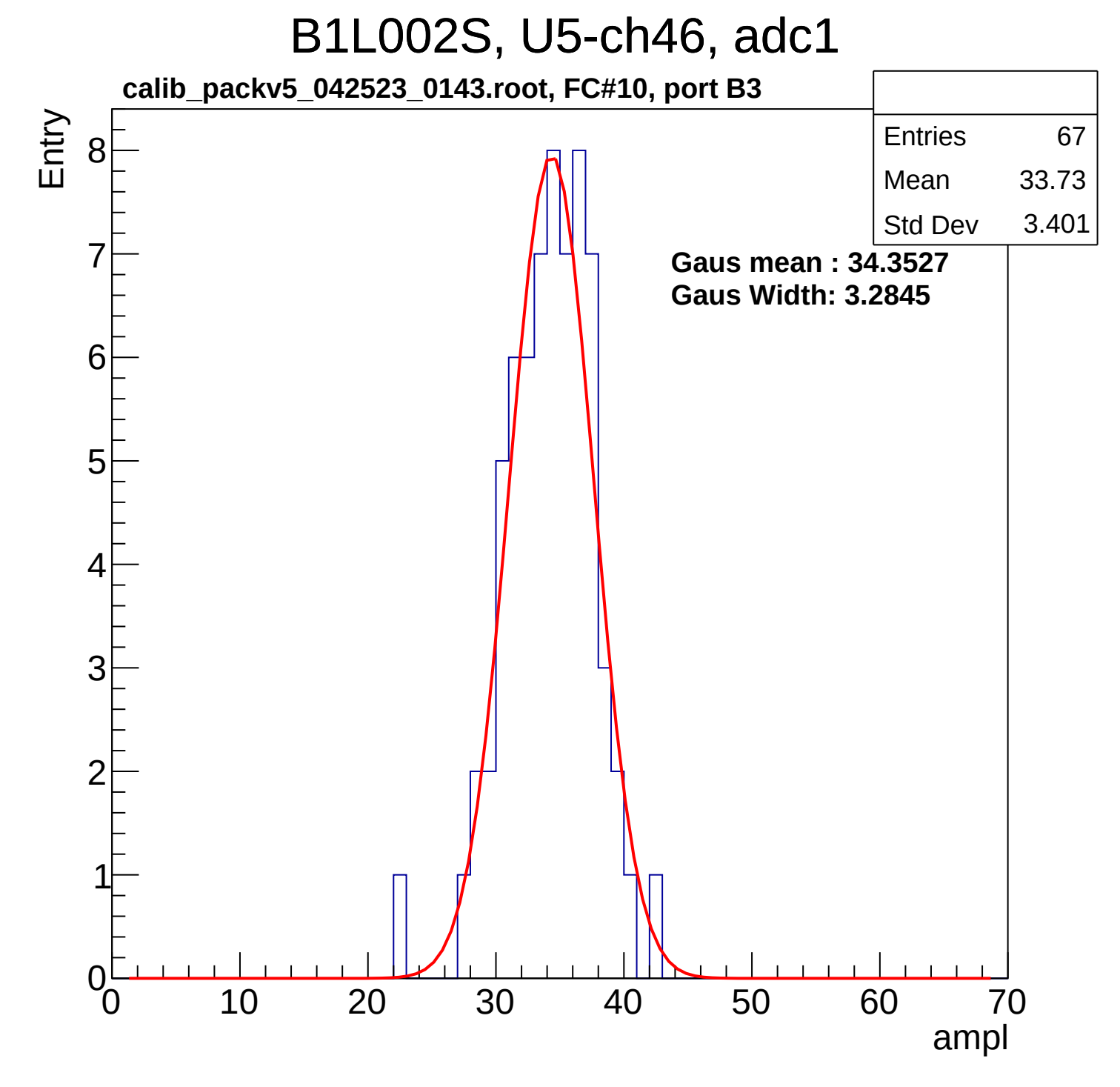
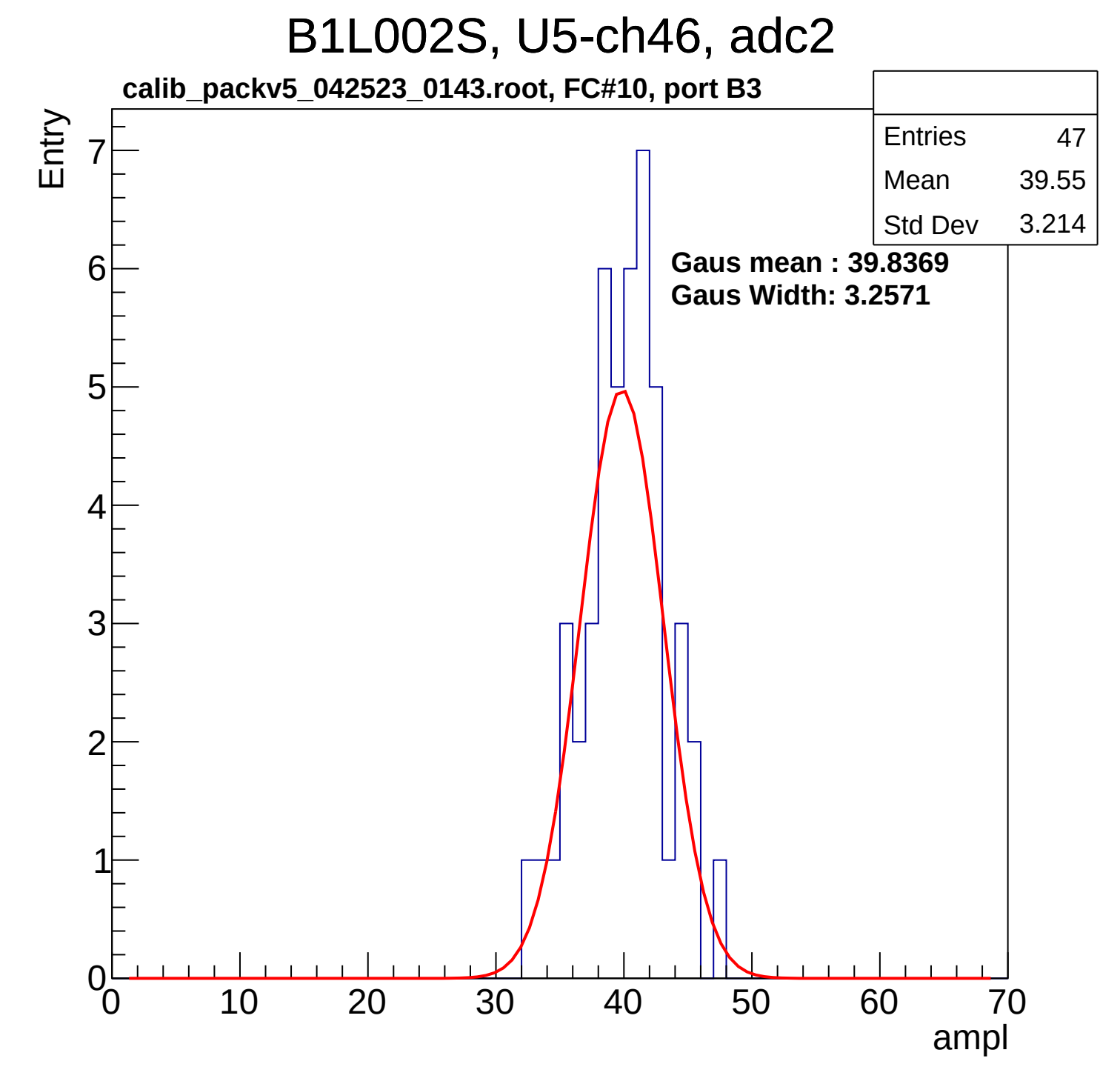

B1L002S, U5-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.73
Std Dev 3.401
Gaus mean : 34.3527
Gaus Width: 3.2845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 39.55
Std Dev 3.214
Gaus mean : 39.8369
Gaus Width: 3.2571
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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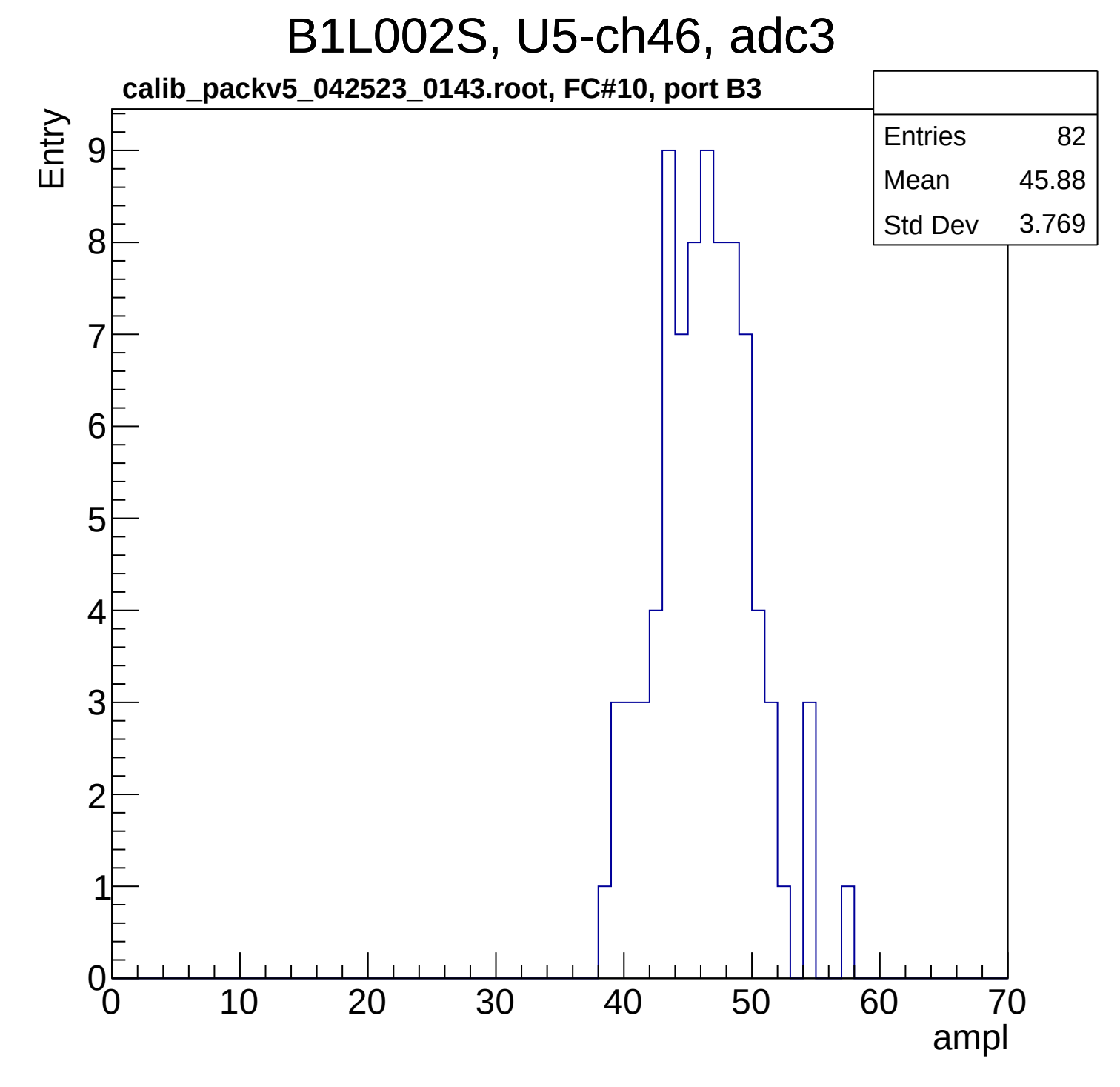
B1L002S, U5-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 45.88
Std Dev 3.769
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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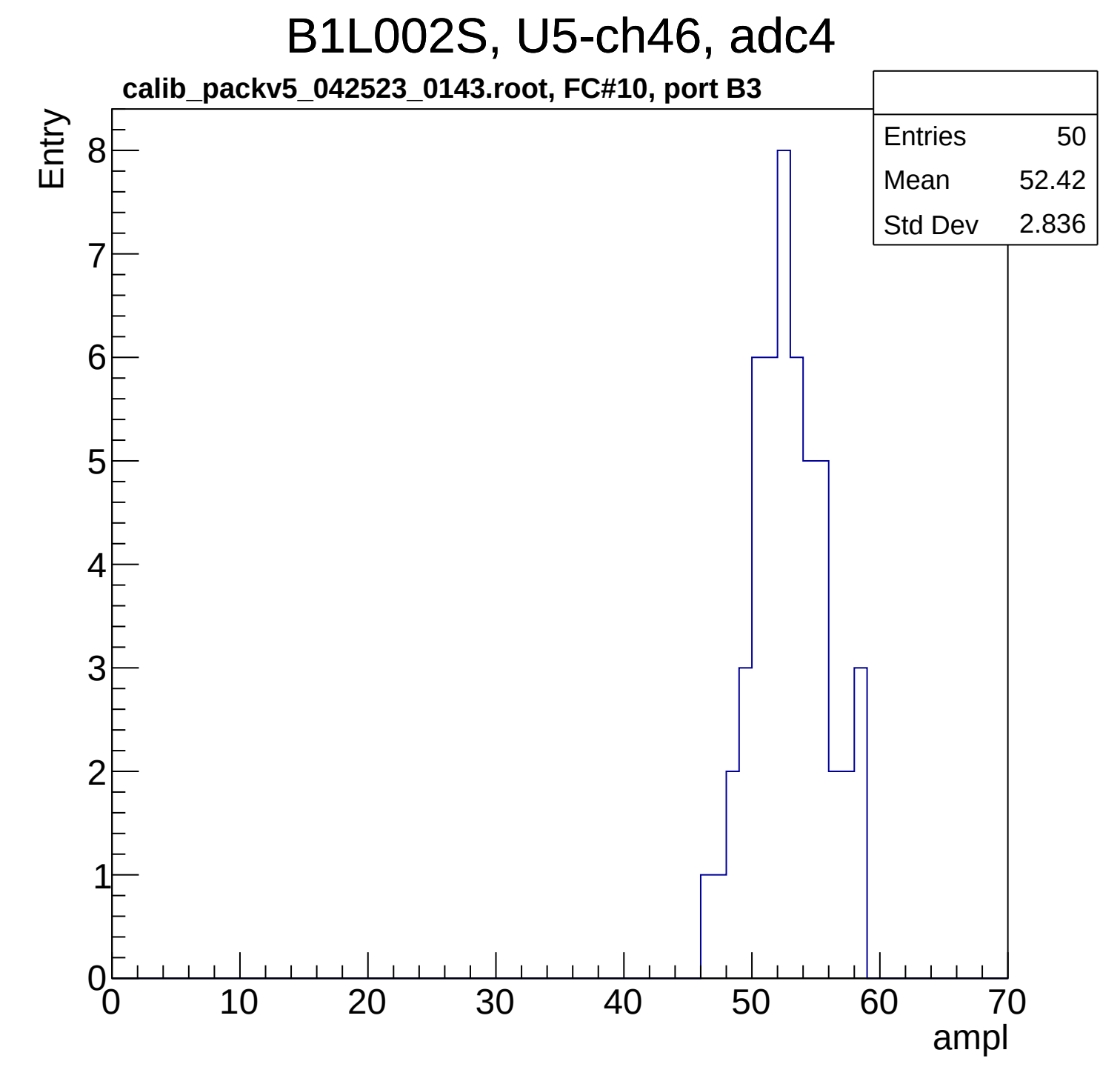
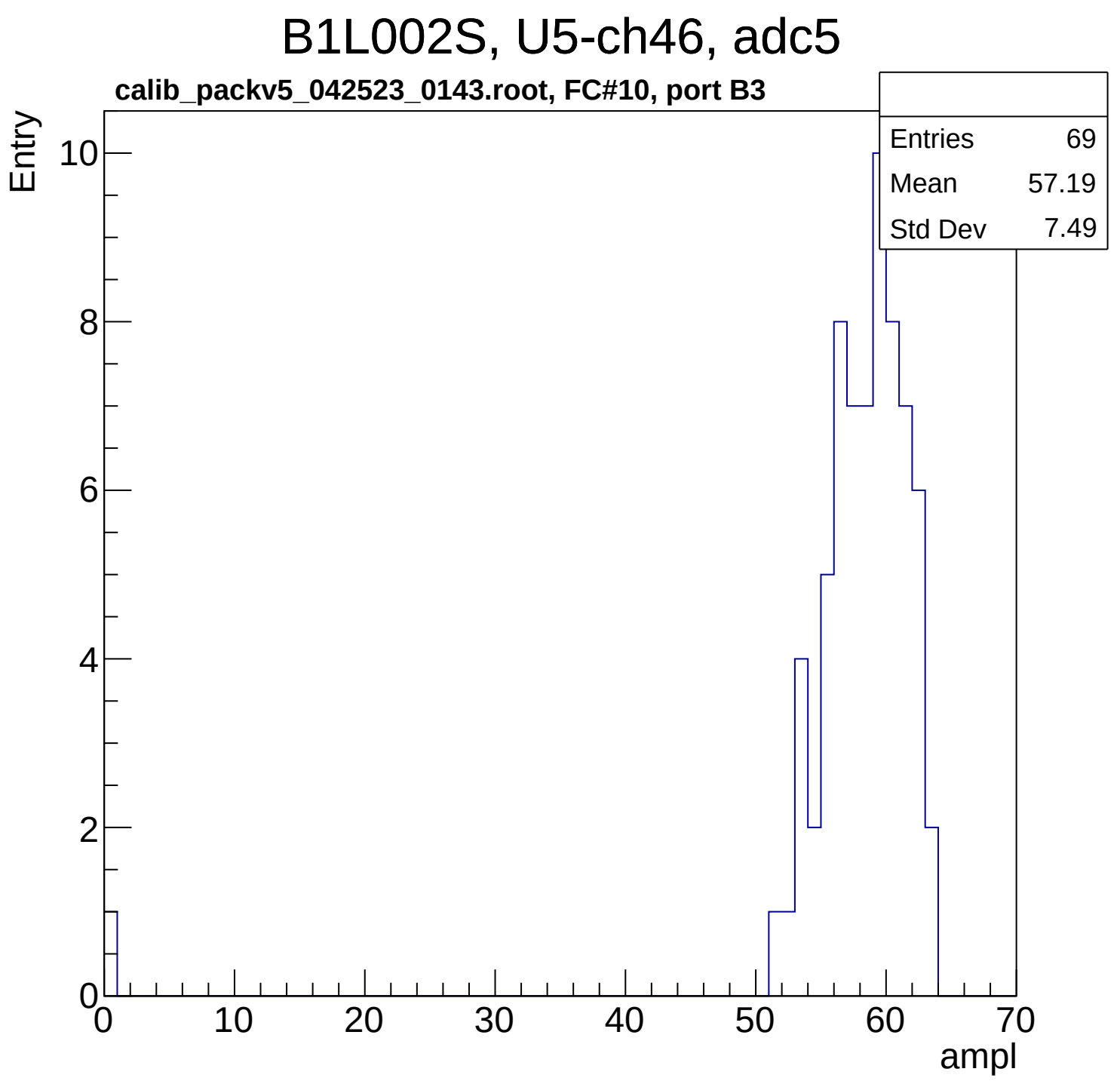
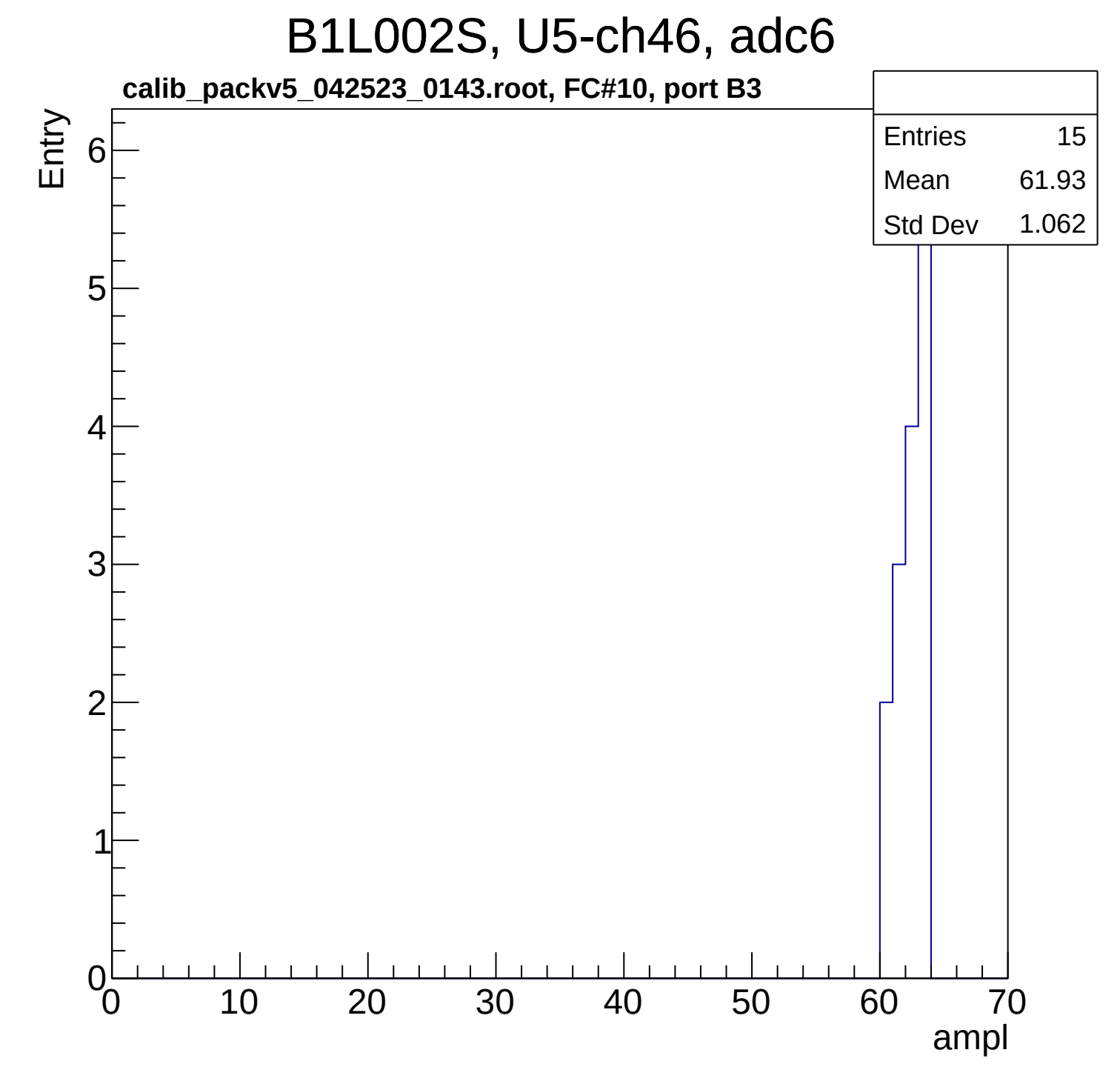
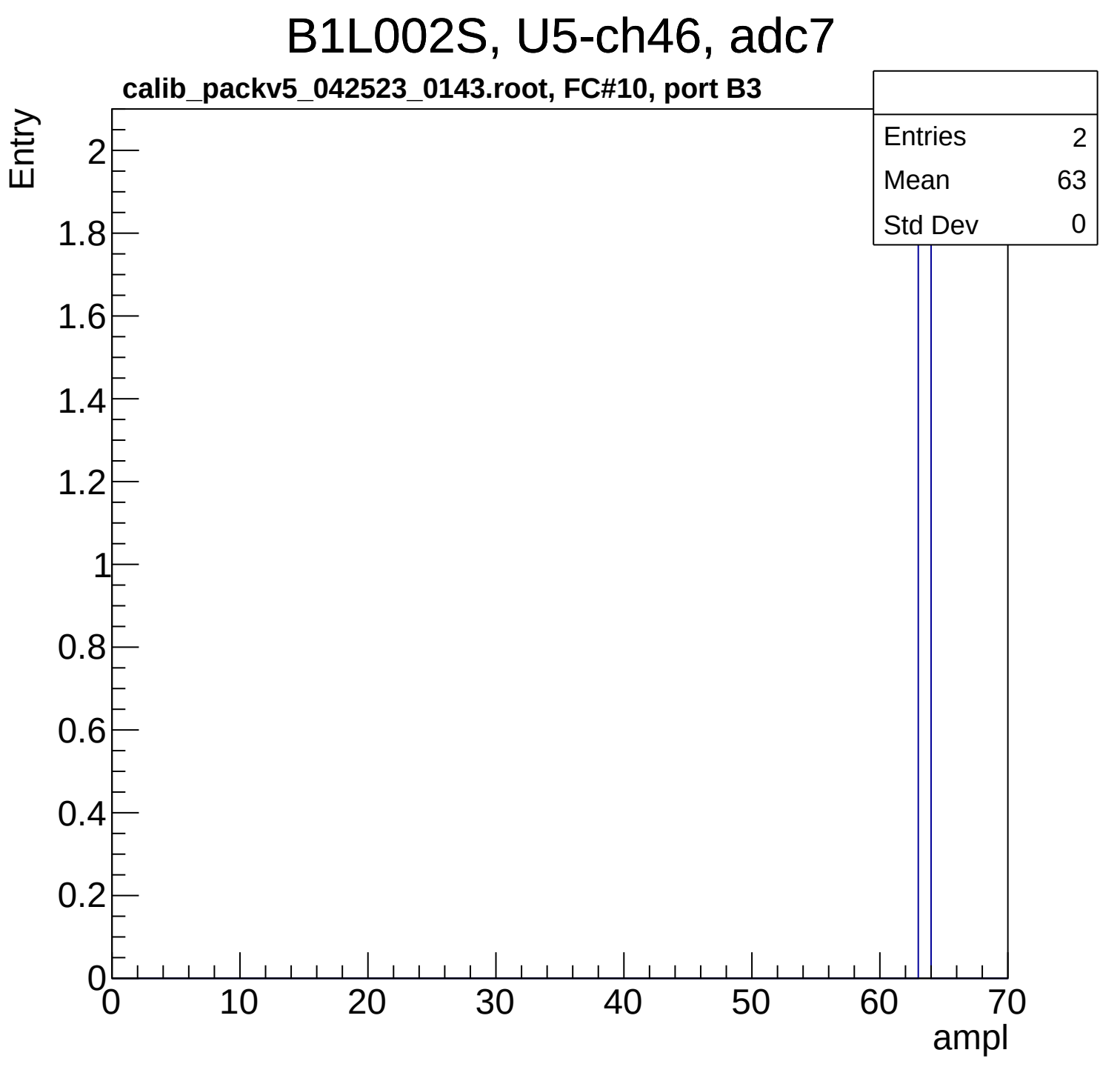

B1L002S, U5-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.42
Std Dev 2.836
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.19
Std Dev 7.49
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.93
Std Dev 1.062
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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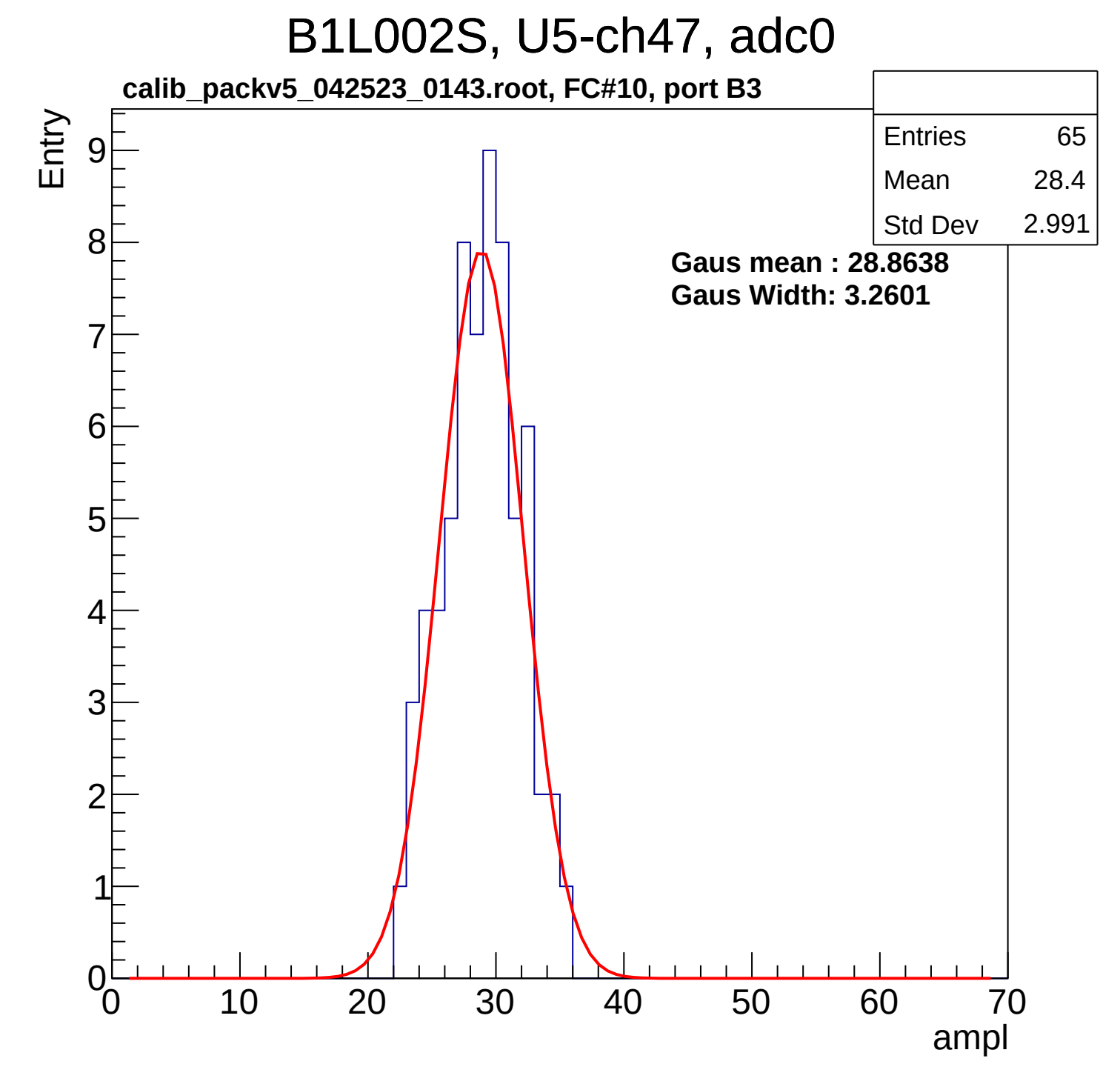
B1L002S, U5-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.4
Std Dev 2.991
Gaus mean : 28.8638
Gaus Width: 3.2601
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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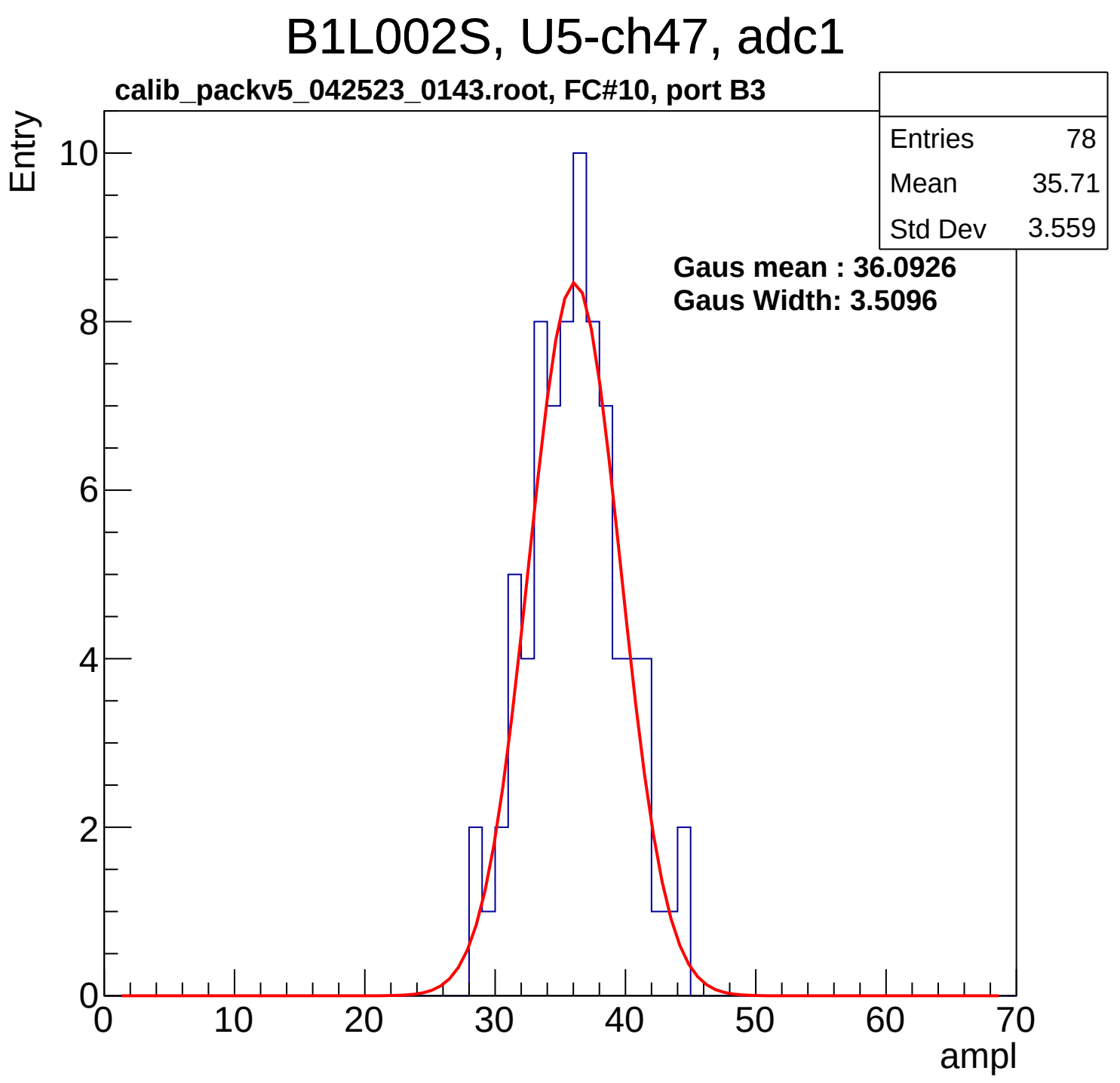
B1L002S, U5-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 35.71
Std Dev 3.559
Gaus mean : 36.0926
Gaus Width: 3.5096
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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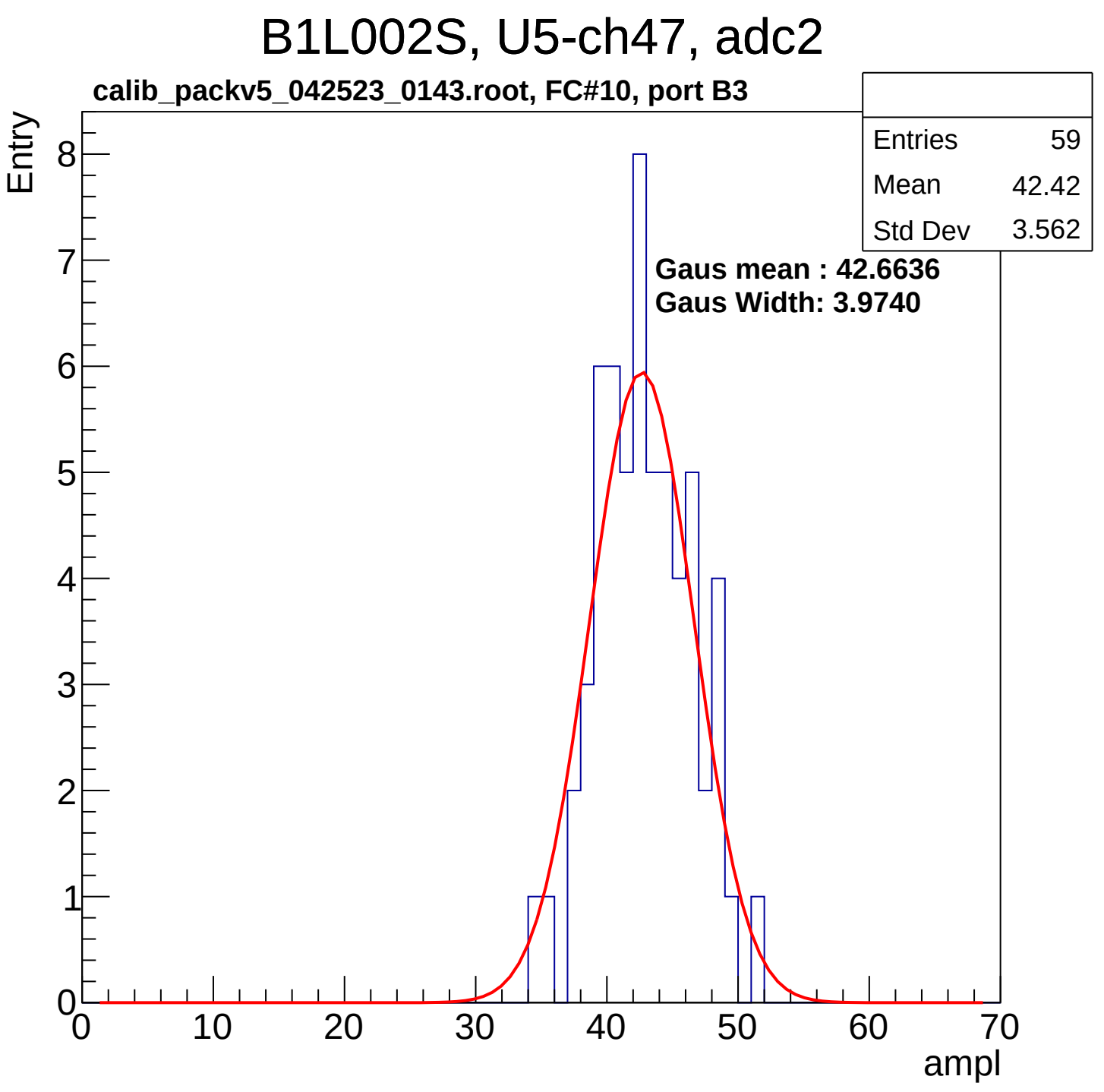
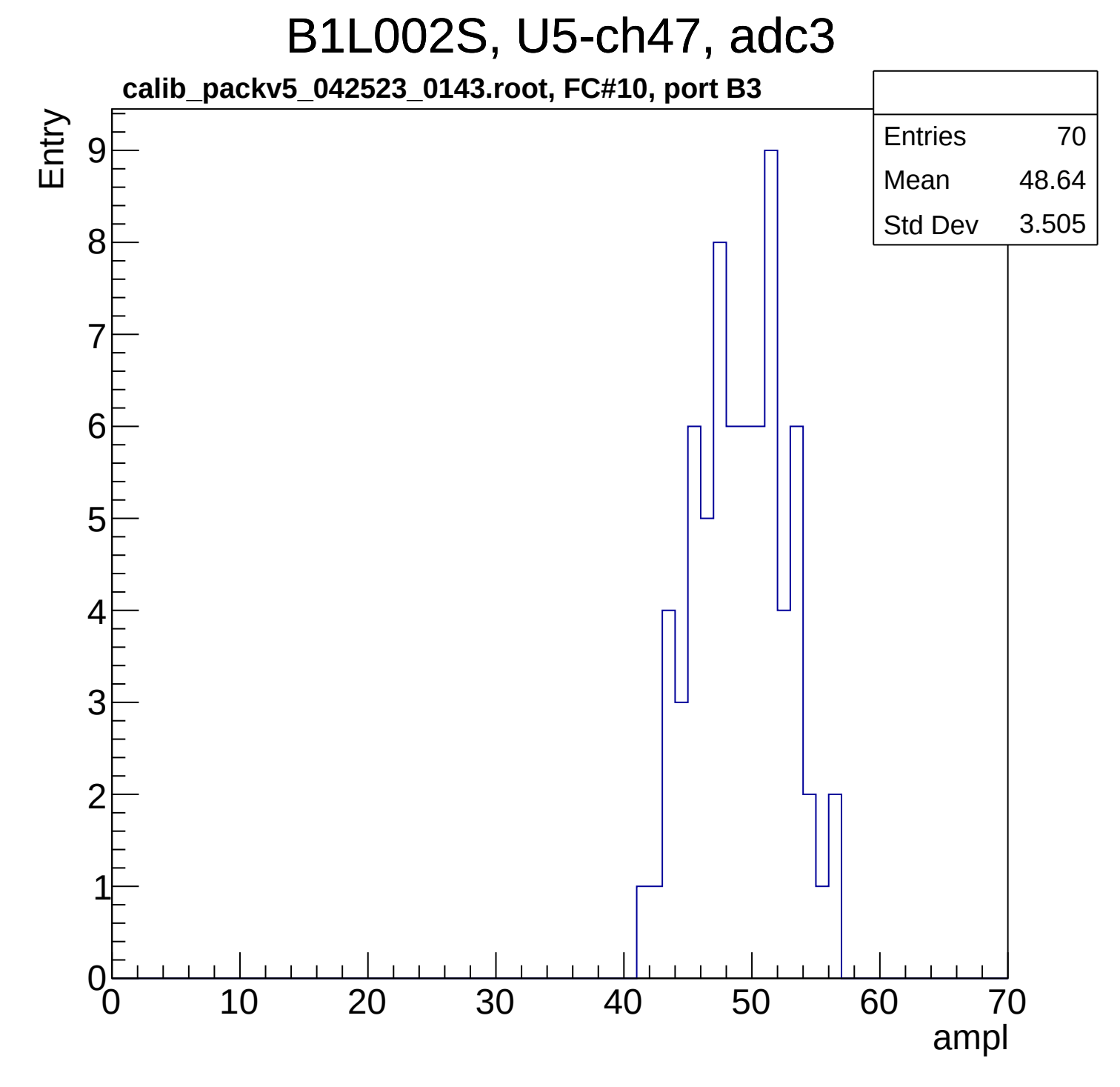

B1L002S, U5-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 42.42
Std Dev 3.562
Gaus mean : 42.6636
Gaus Width: 3.9740
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.64
Std Dev 3.505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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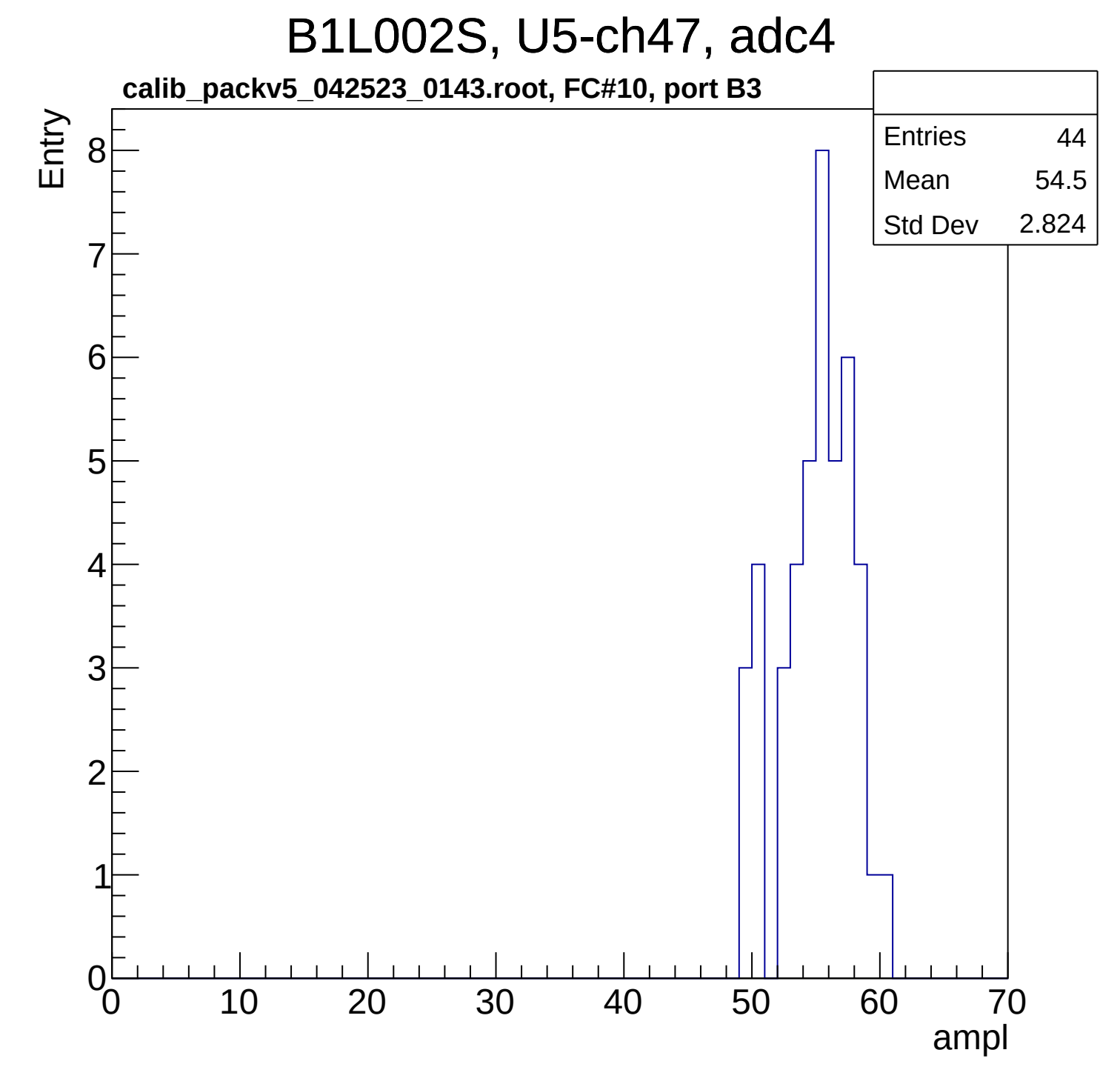
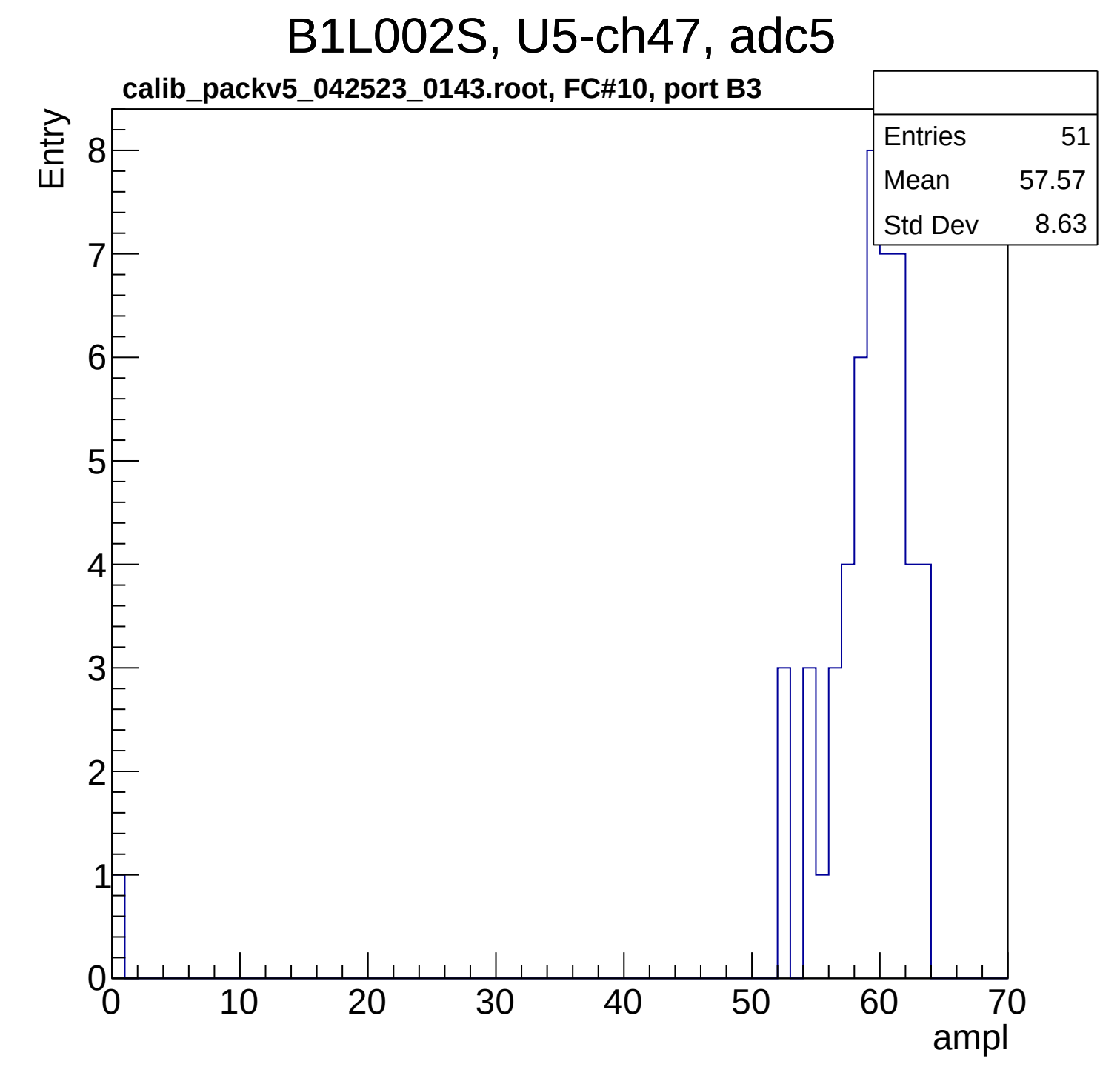

B1L002S, U5-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 54.5
Std Dev 2.824
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.57
Std Dev 8.63
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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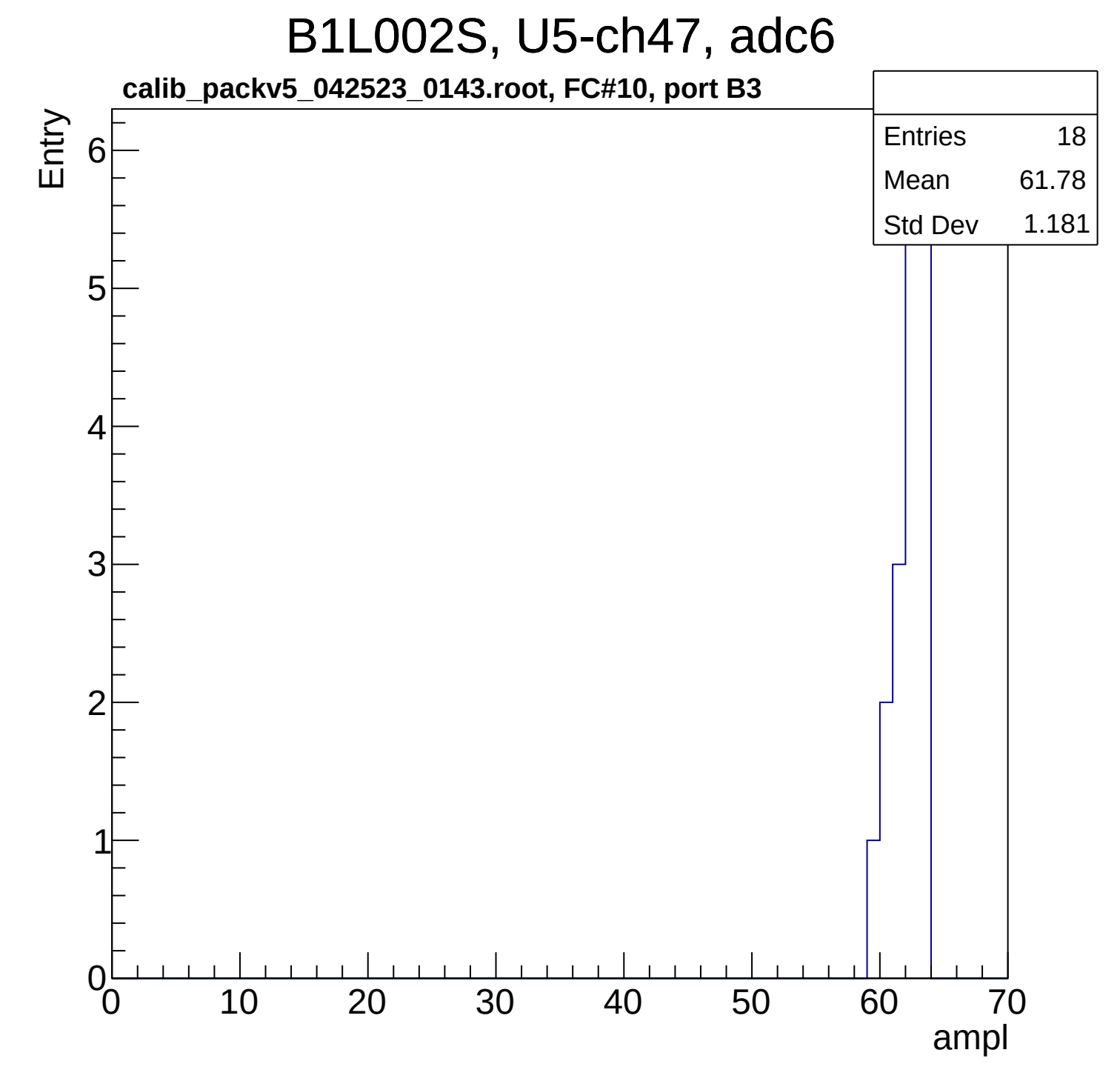
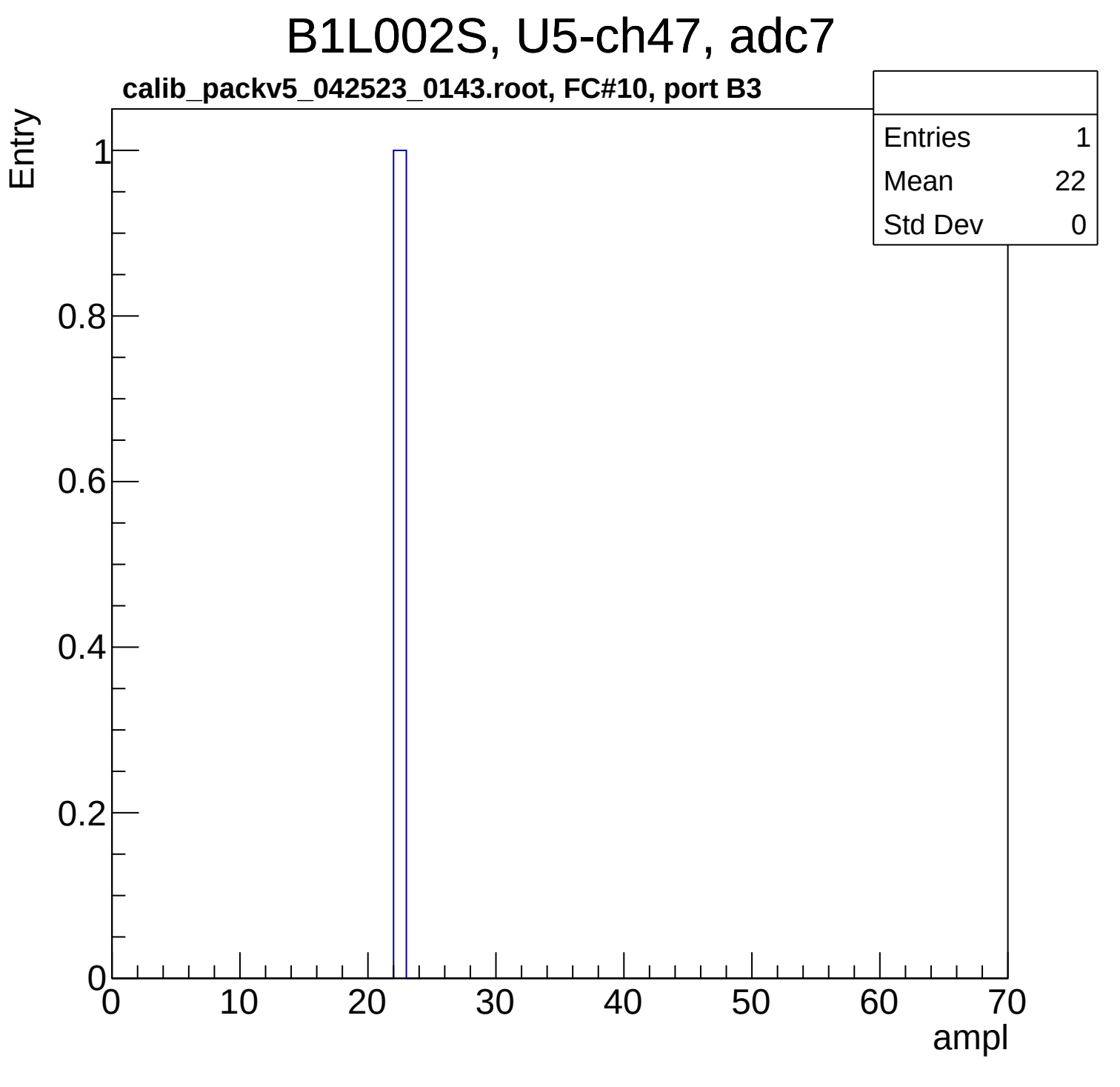
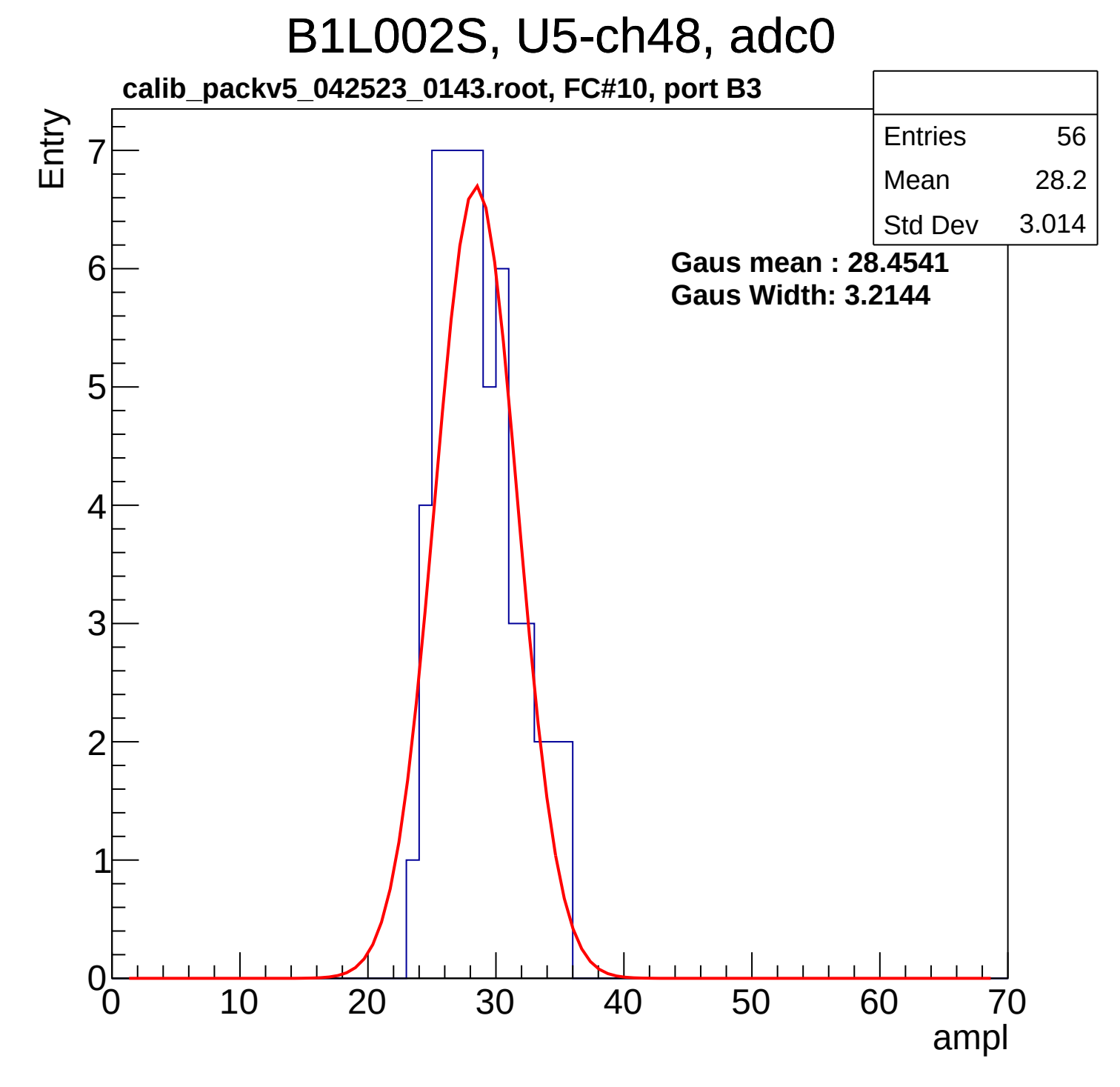

B1L002S, U5-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.78
Std Dev 1.181
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.2
Std Dev 3.014
Gaus mean : 28.4541
Gaus Width: 3.2144
Entry
ampl
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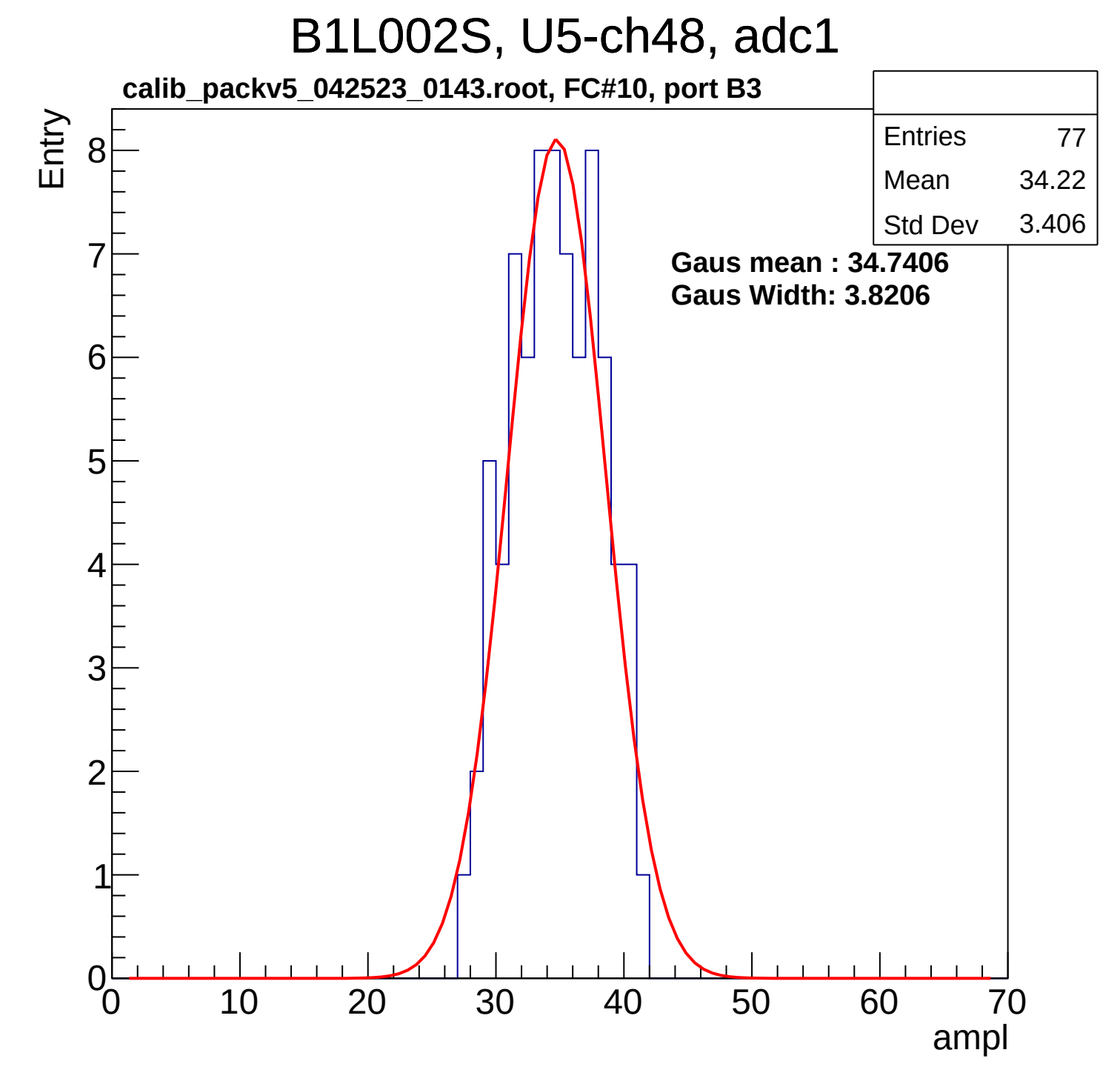
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.22
Std Dev 3.406
Gaus mean : 34.7406
Gaus Width: 3.8206
Entry
ampl
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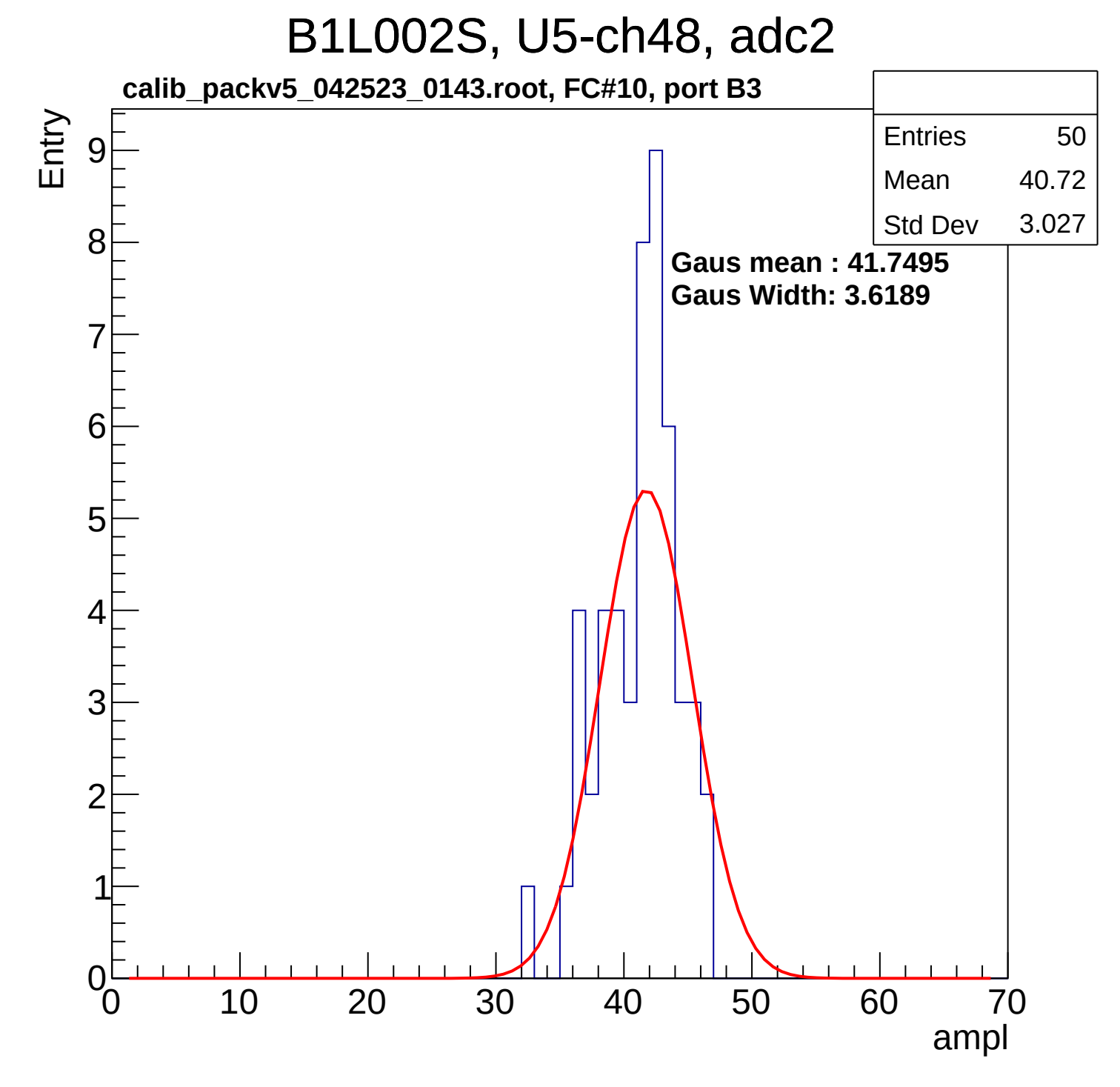
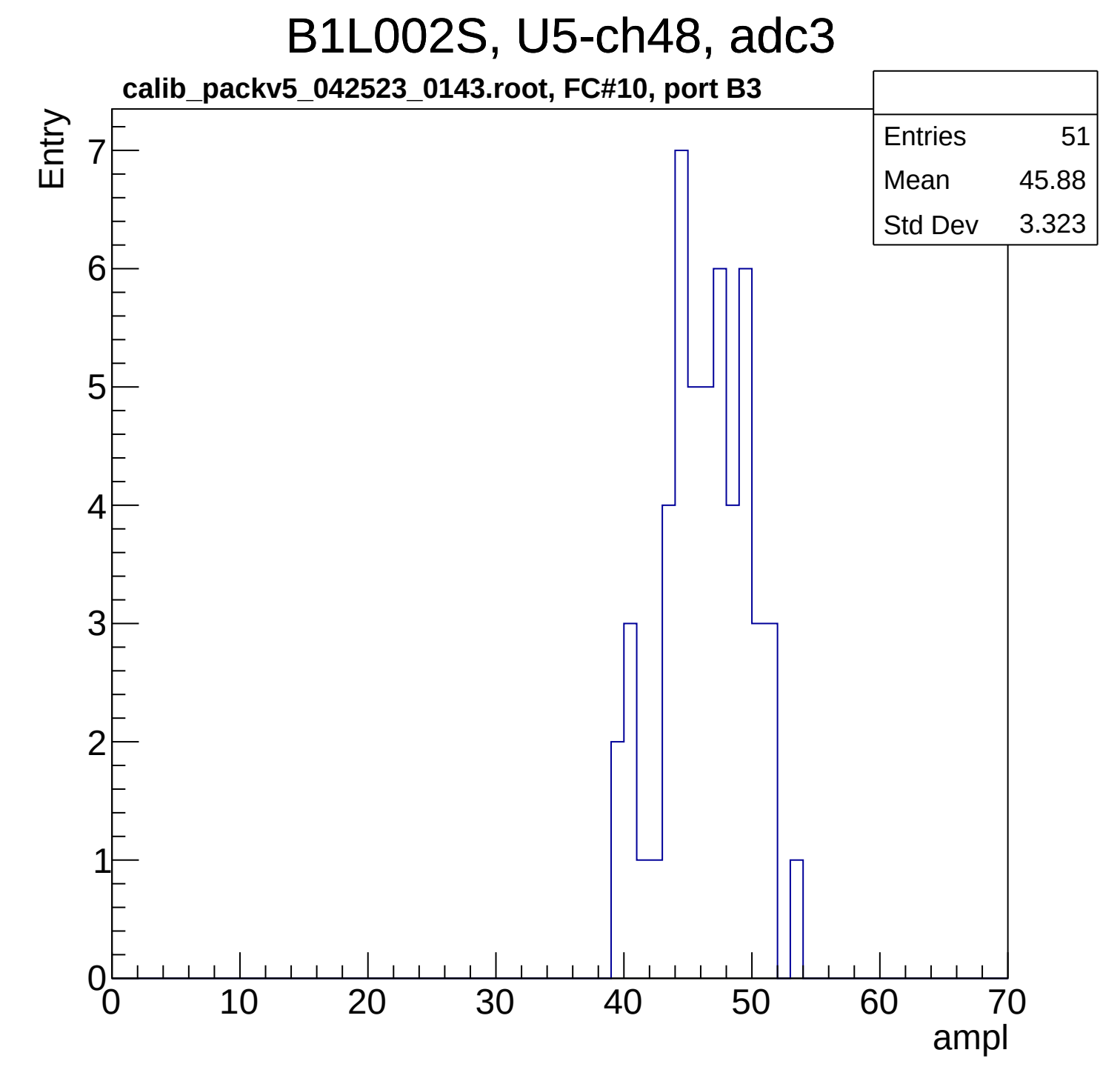
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.72
Std Dev 3.027
Gaus mean : 41.7495
Gaus Width: 3.6189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 45.88
Std Dev 3.323
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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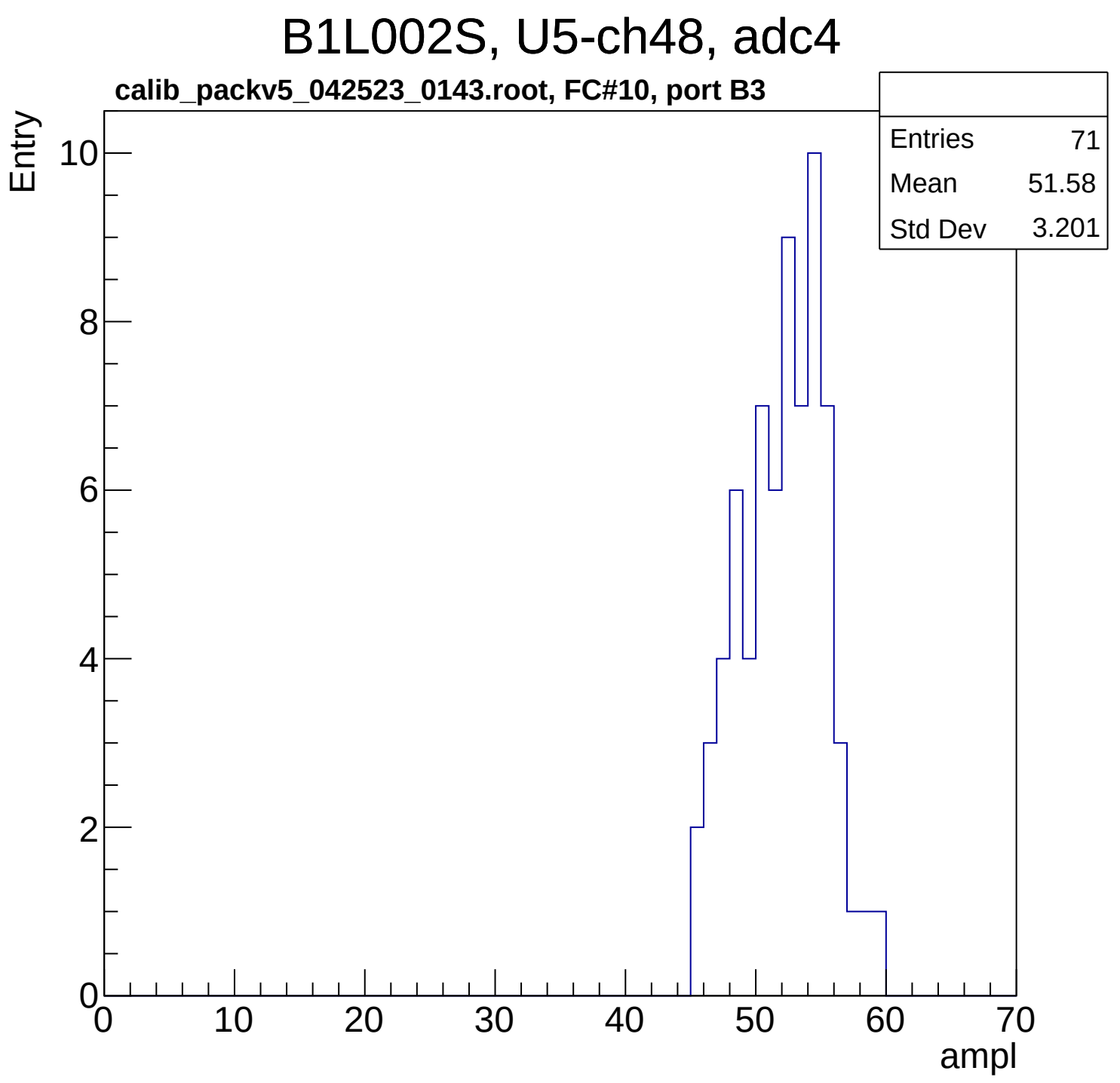
B1L002S, U5-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 51.58
Std Dev 3.201
Entry
ampl
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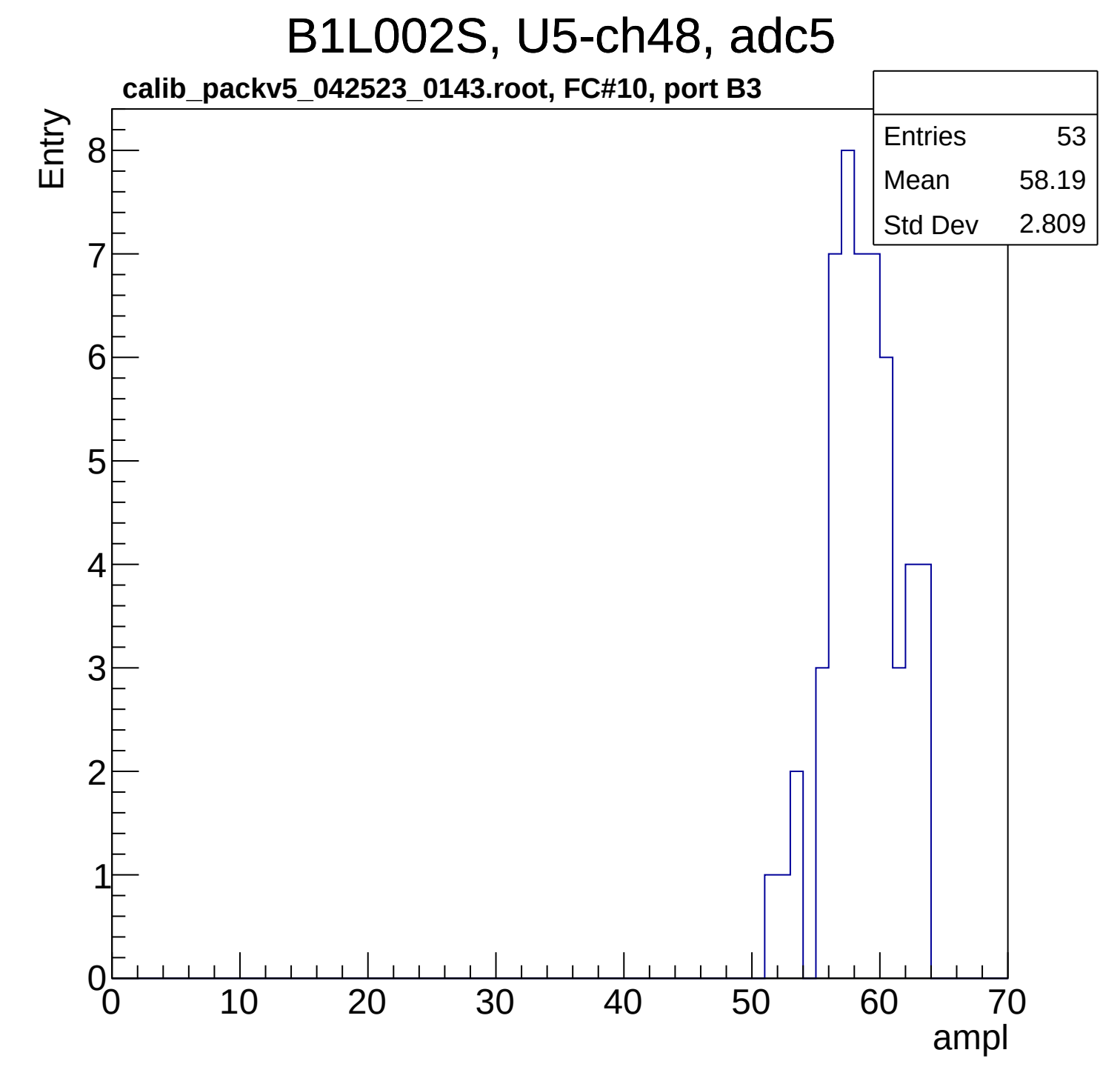
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.19
Std Dev 2.809
Entry
ampl
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1
2
3
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7
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0
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20
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40
50
60
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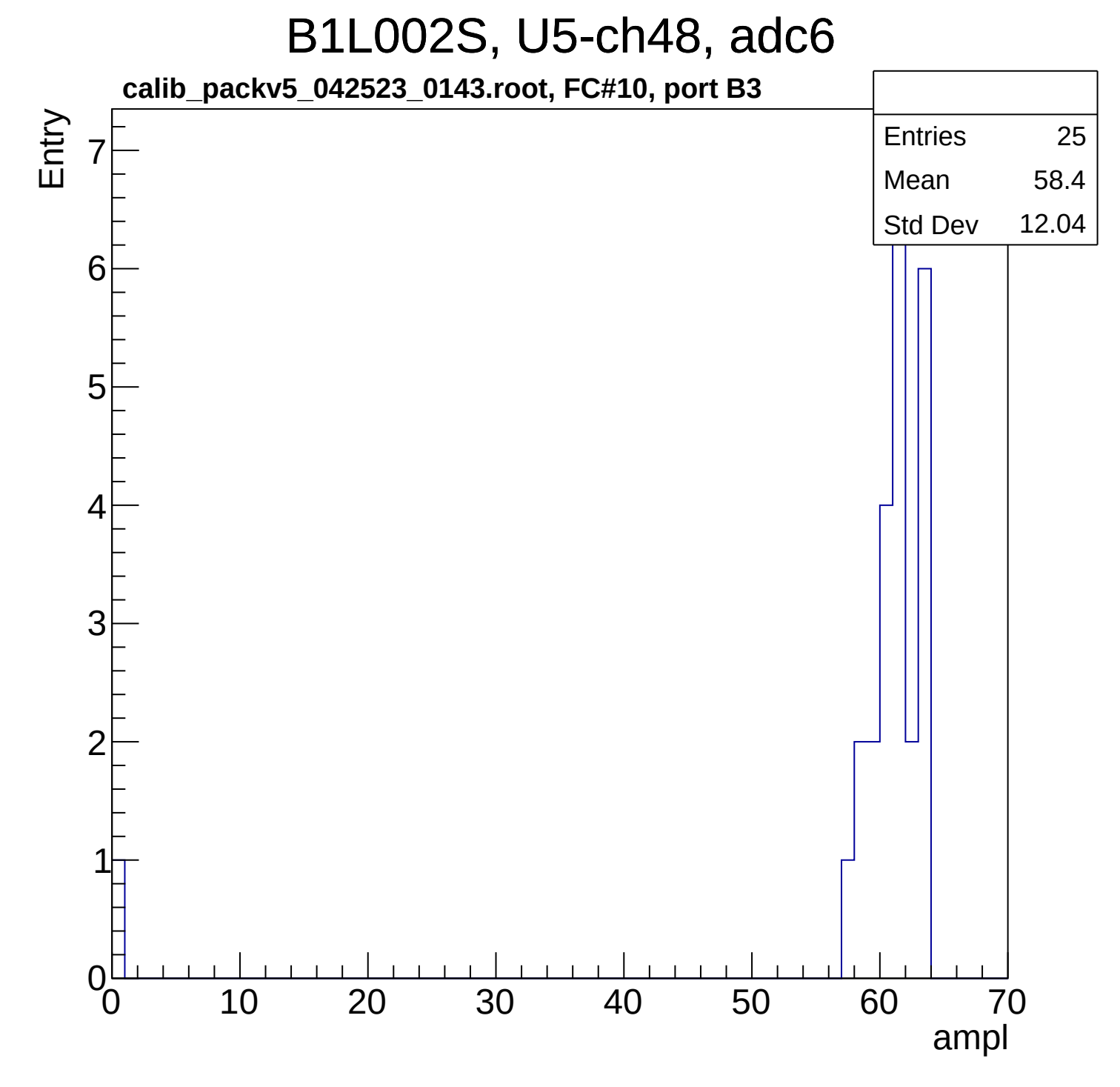
B1L002S, U5-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.4
Std Dev 12.04
Entry
ampl
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1
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3
4
5
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60
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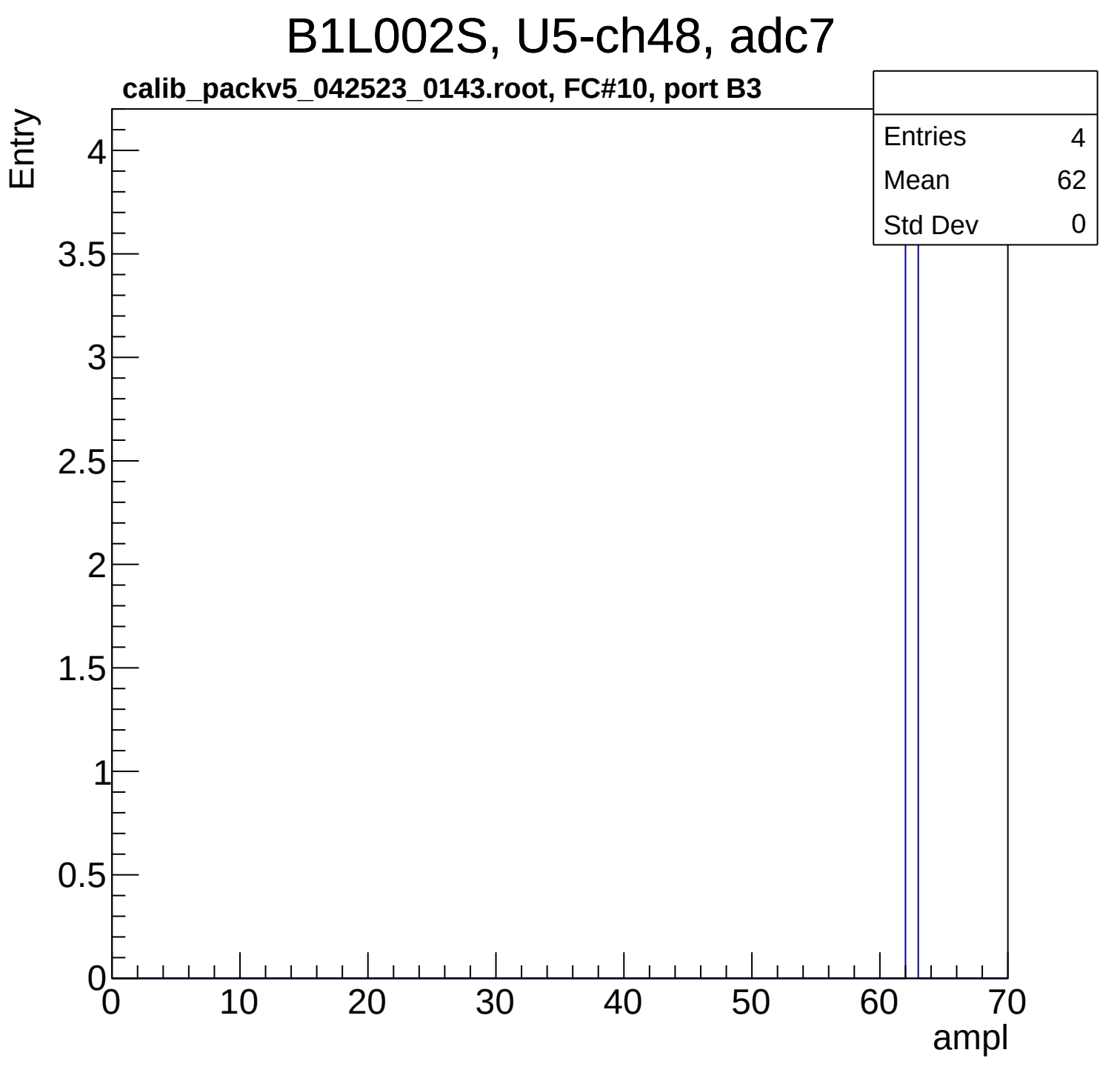
B1L002S, U5-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62
Std Dev 0
Entry
ampl
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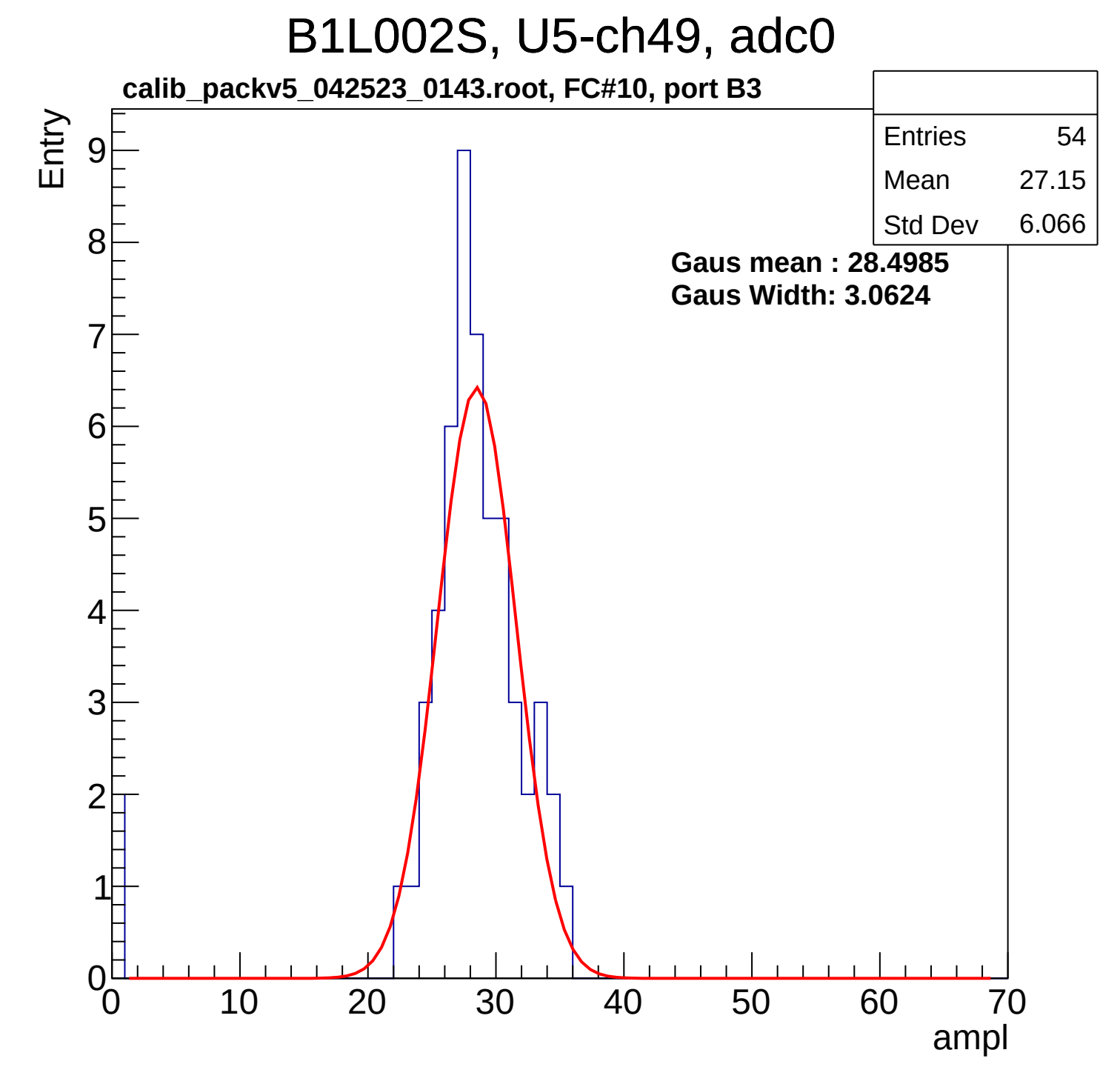
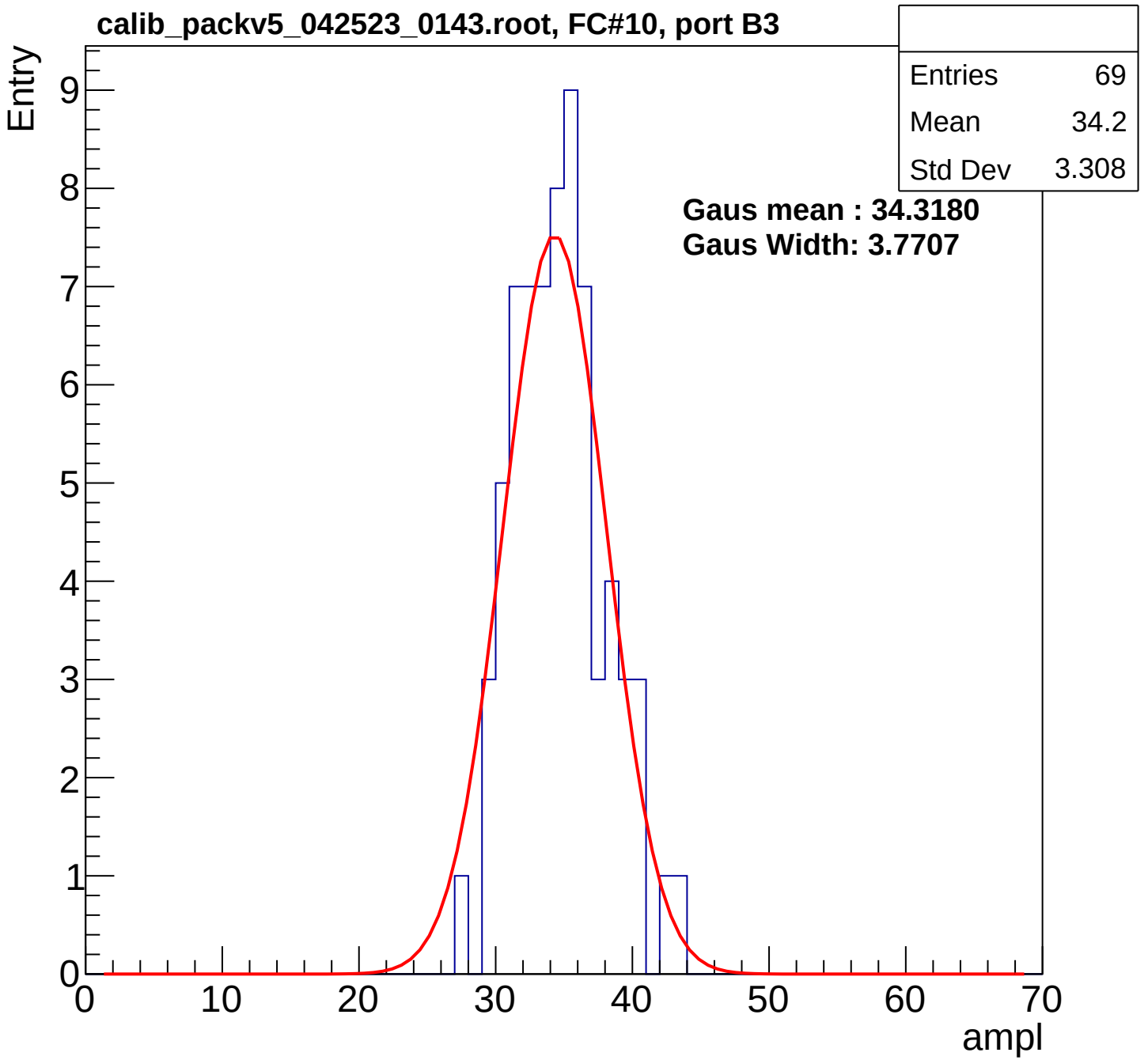
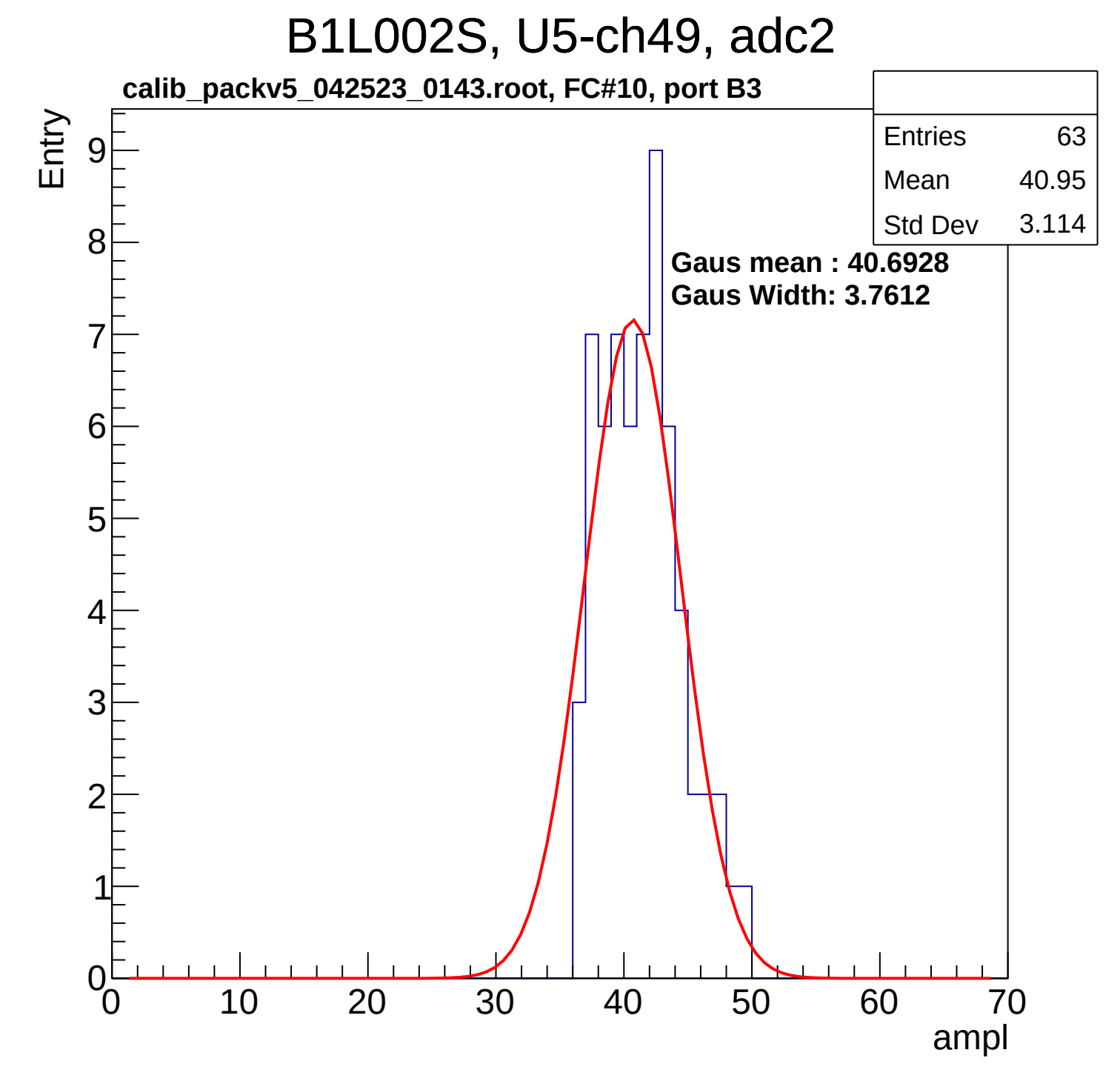
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U5-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.15
Std Dev 6.066
Gaus mean : 28.4985
Gaus Width: 3.0624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.2
Std Dev 3.308
Gaus mean : 34.3180
Gaus Width: 3.7707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.95
Std Dev 3.114
Gaus mean : 40.6928
Gaus Width: 3.7612
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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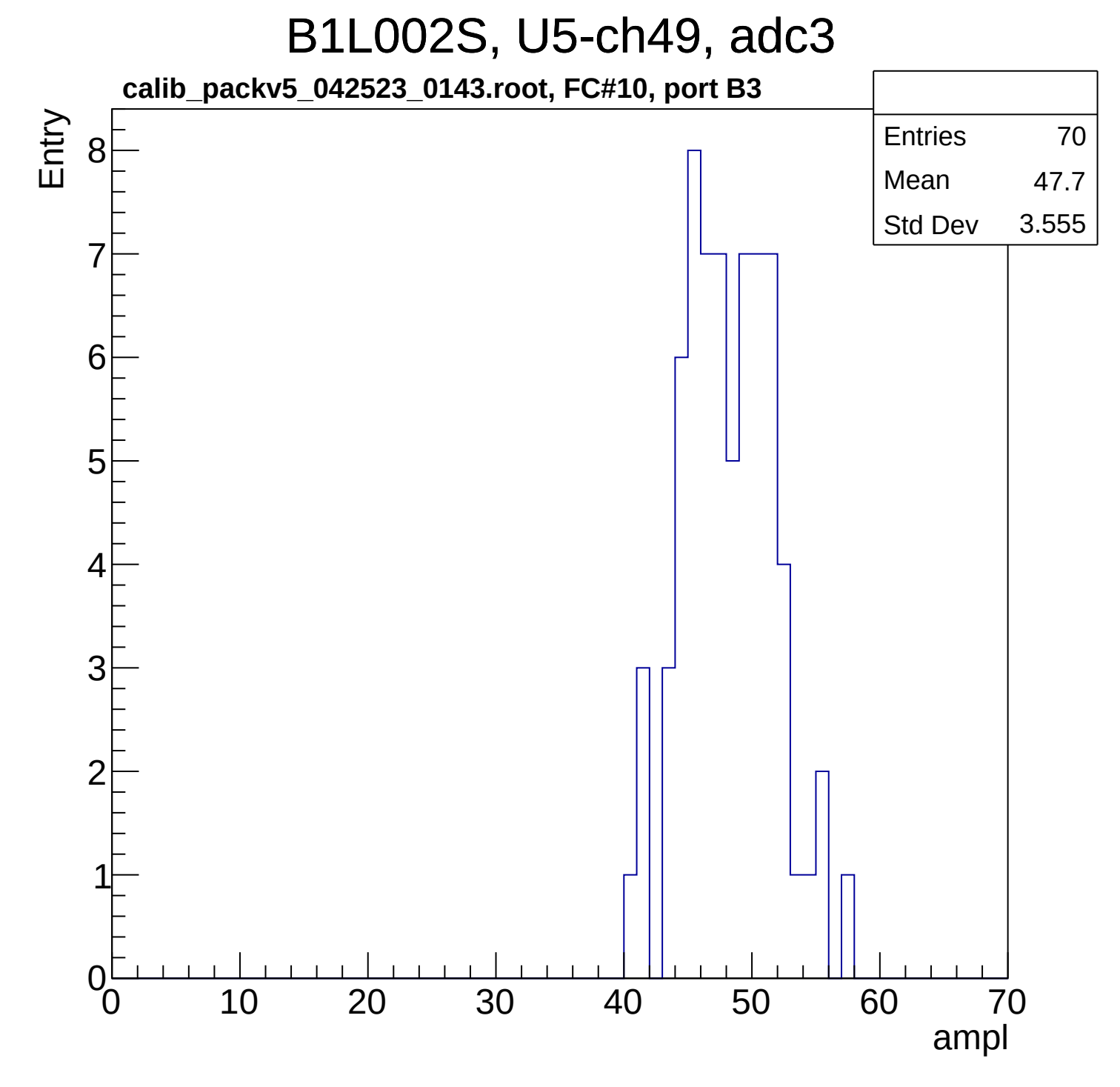
B1L002S, U5-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.7
Std Dev 3.555
Entry
ampl
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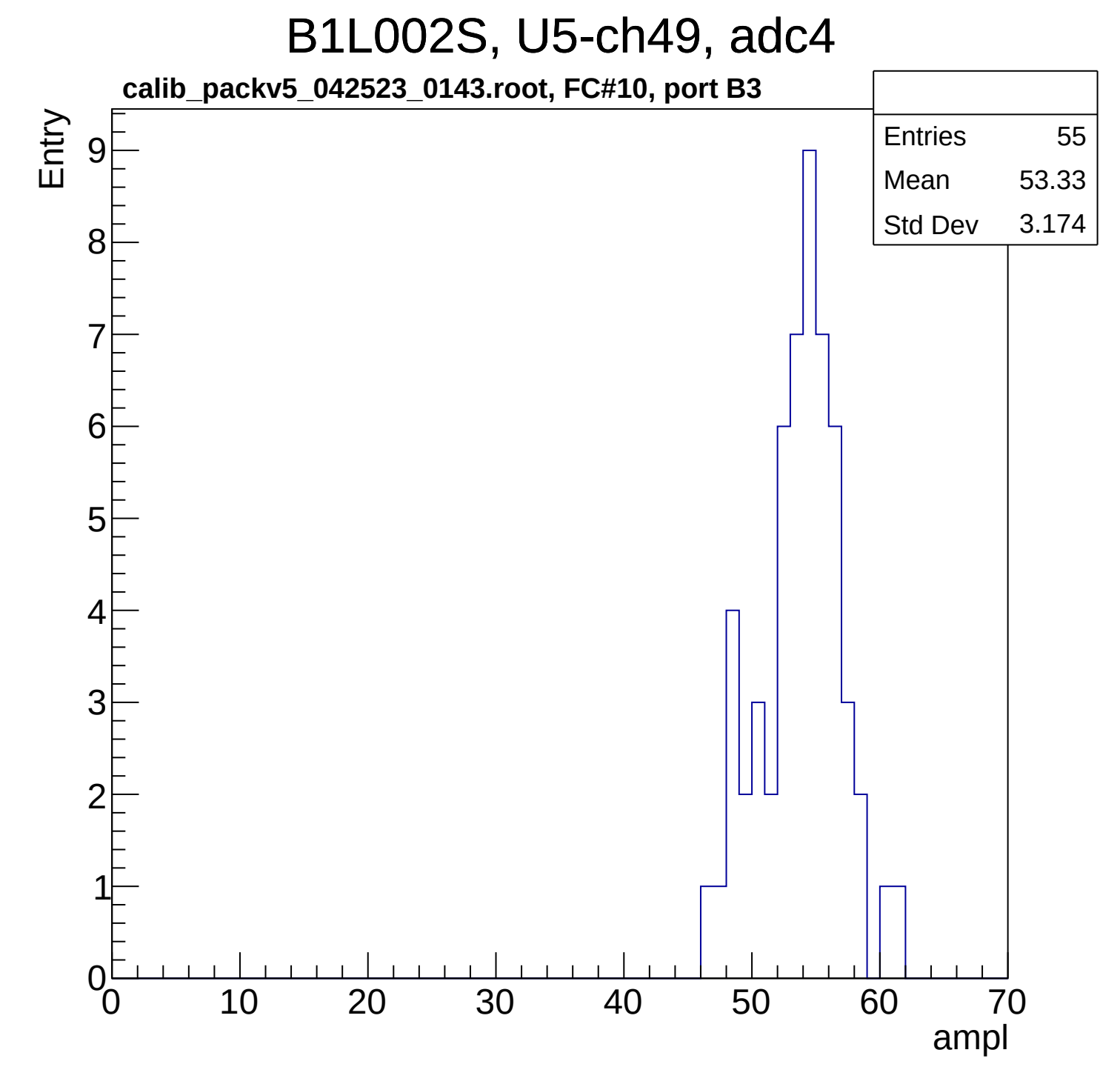
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.33
Std Dev 3.174
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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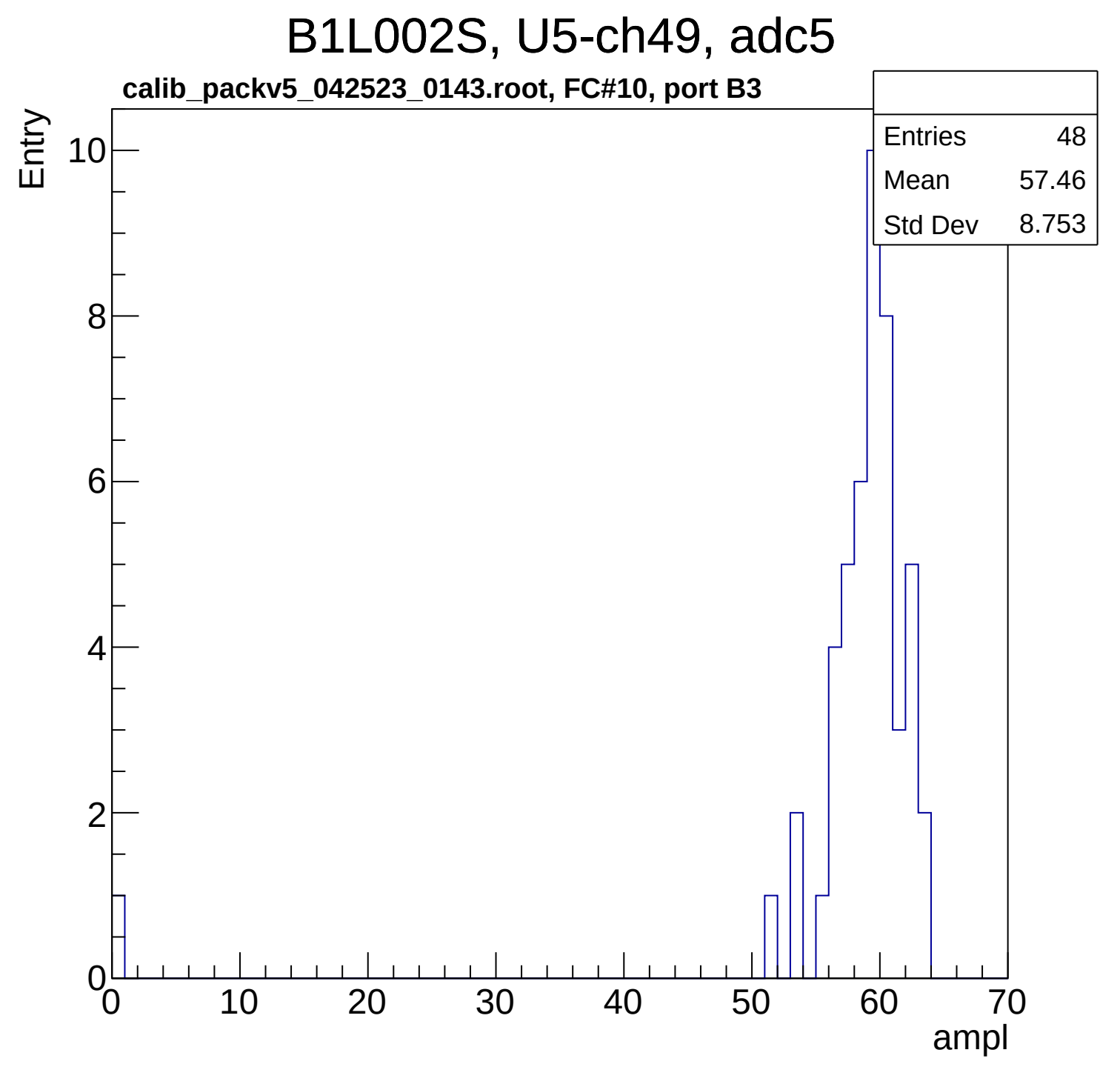
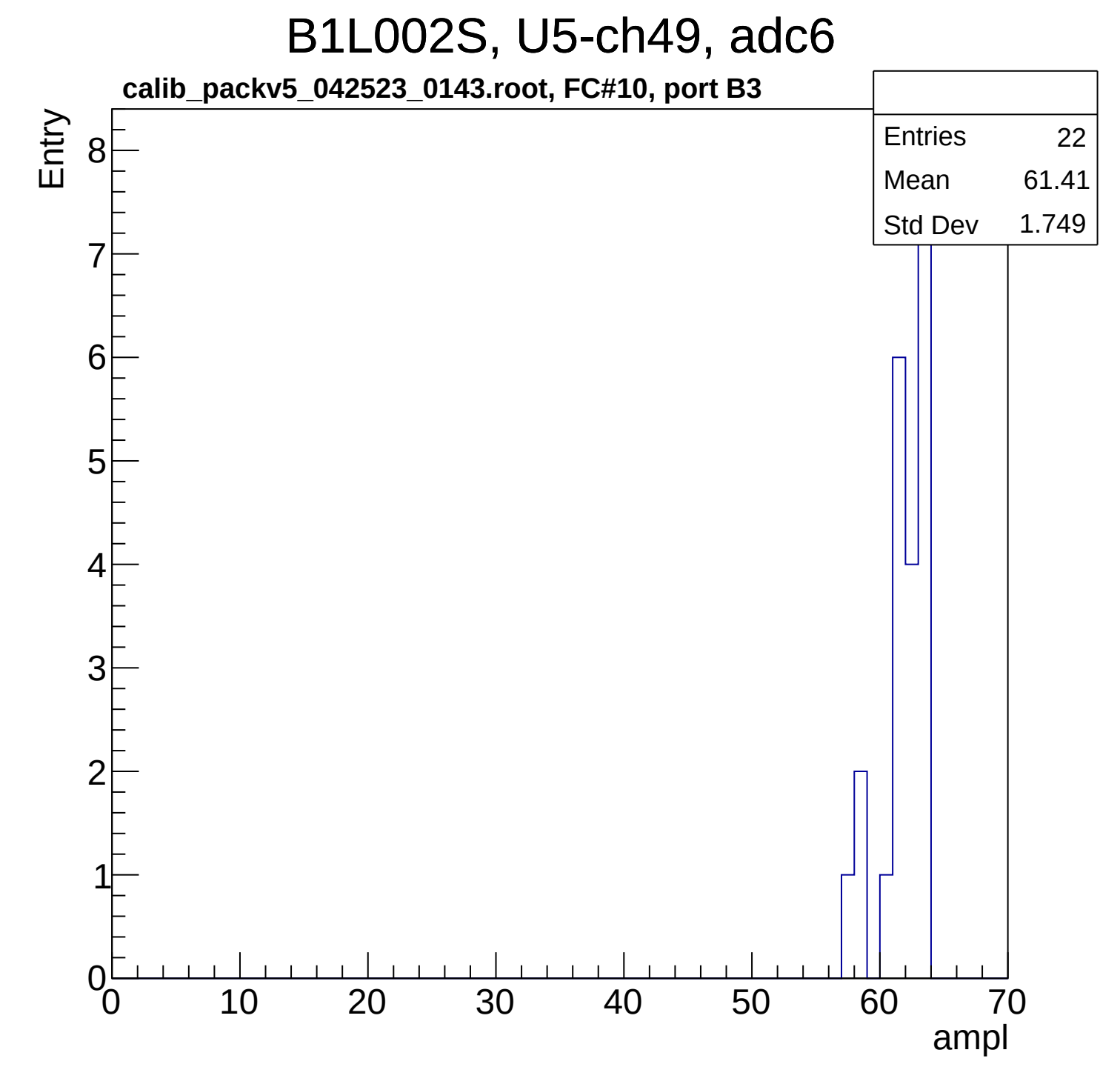
40
50
60
70


B1L002S, U5-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.46
Std Dev 8.753
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.41
Std Dev 1.749
Entry
ampl
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1
2
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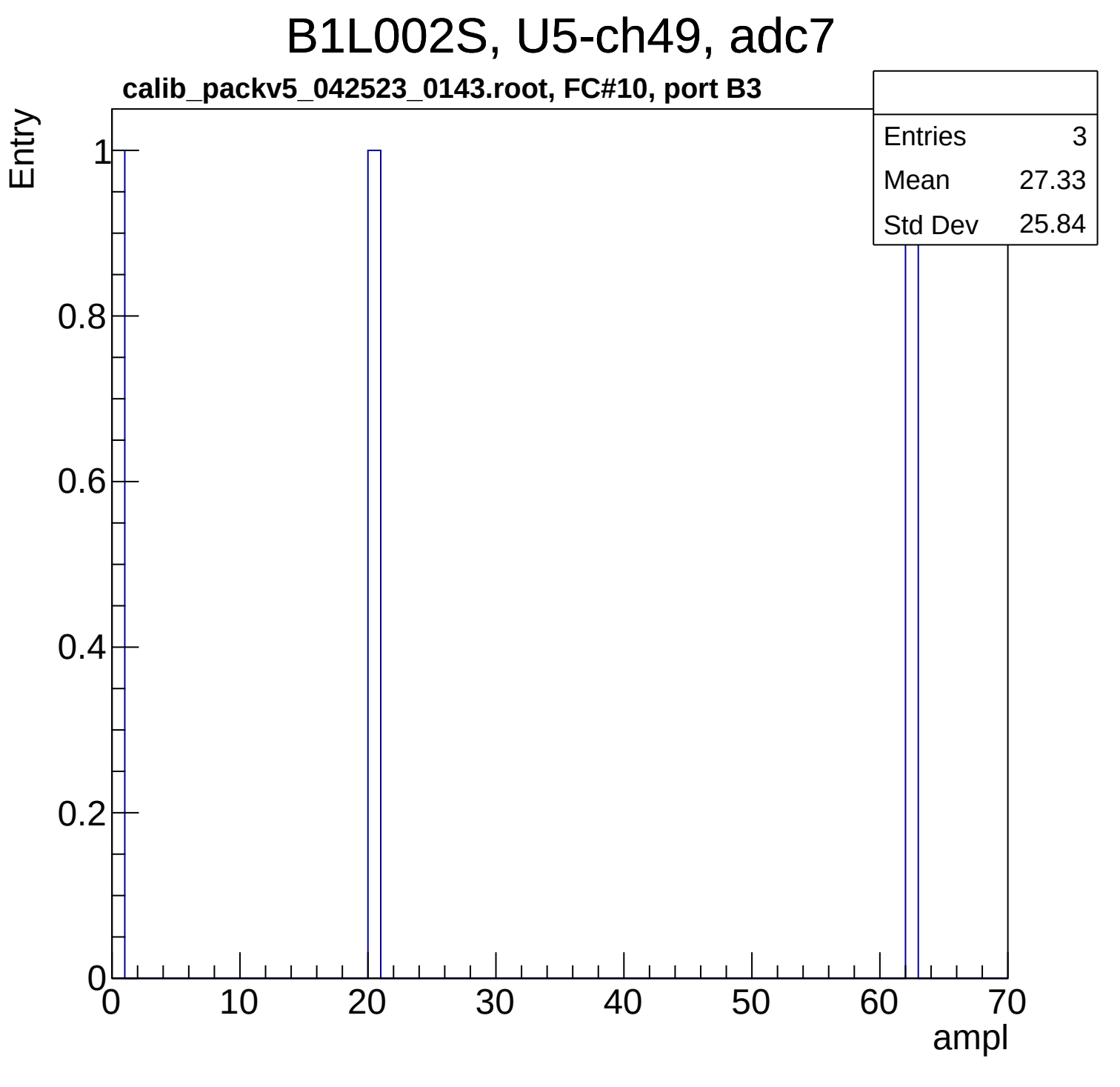
B1L002S, U5-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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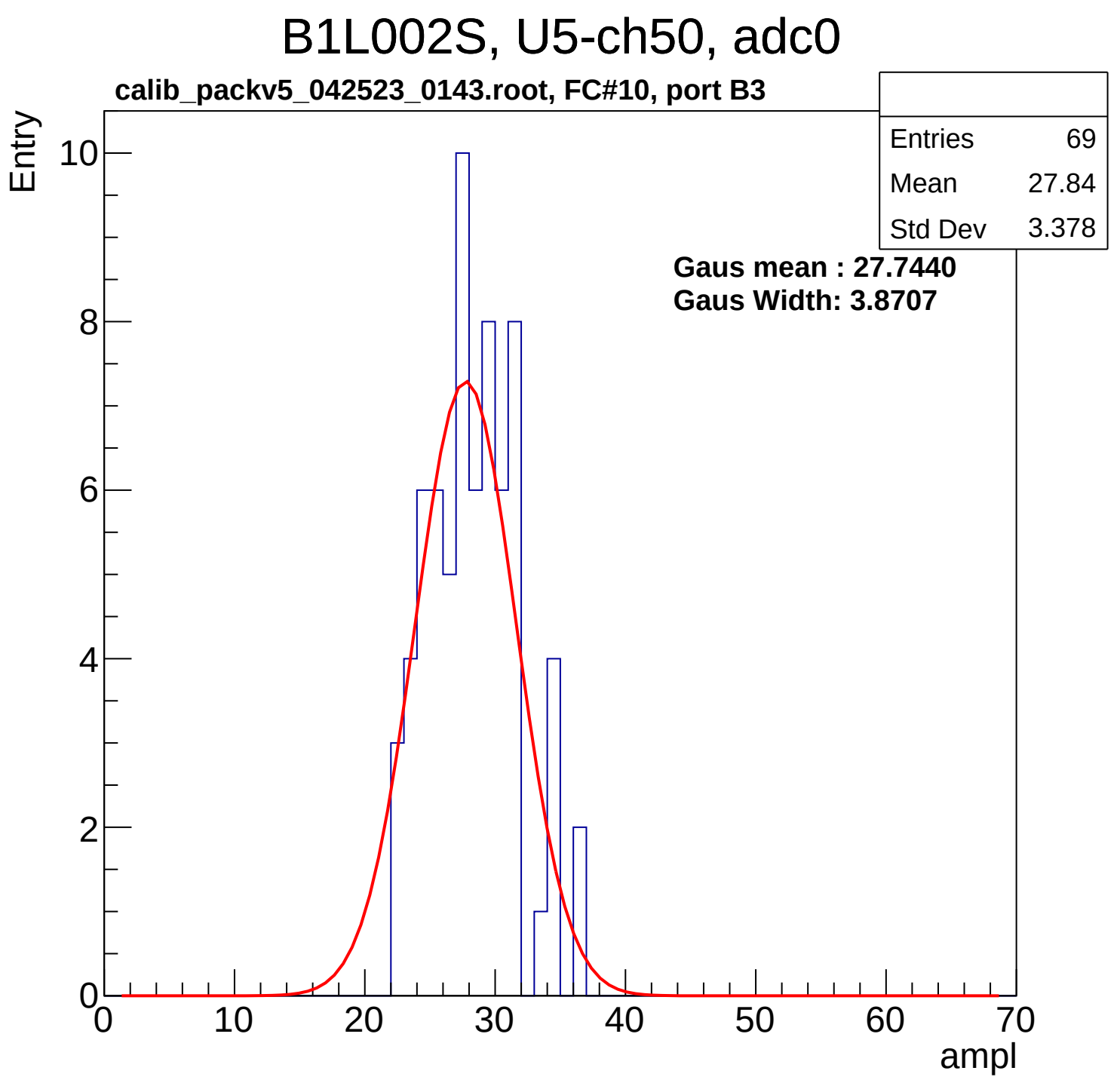
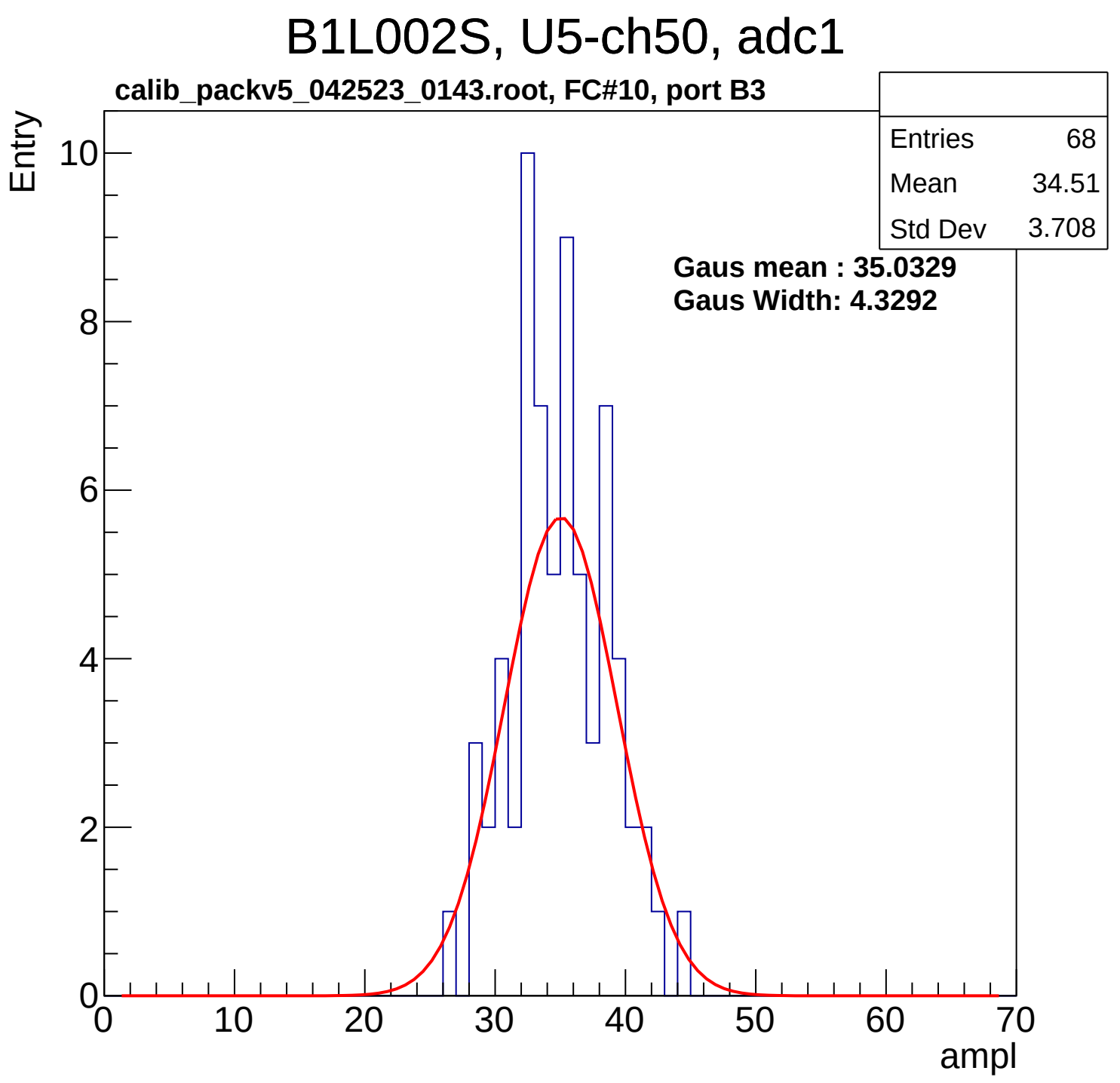
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U5-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.84
Std Dev 3.378
Gaus mean : 27.7440
Gaus Width: 3.8707
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.51
Std Dev 3.708
Gaus mean : 35.0329
Gaus Width: 4.3292
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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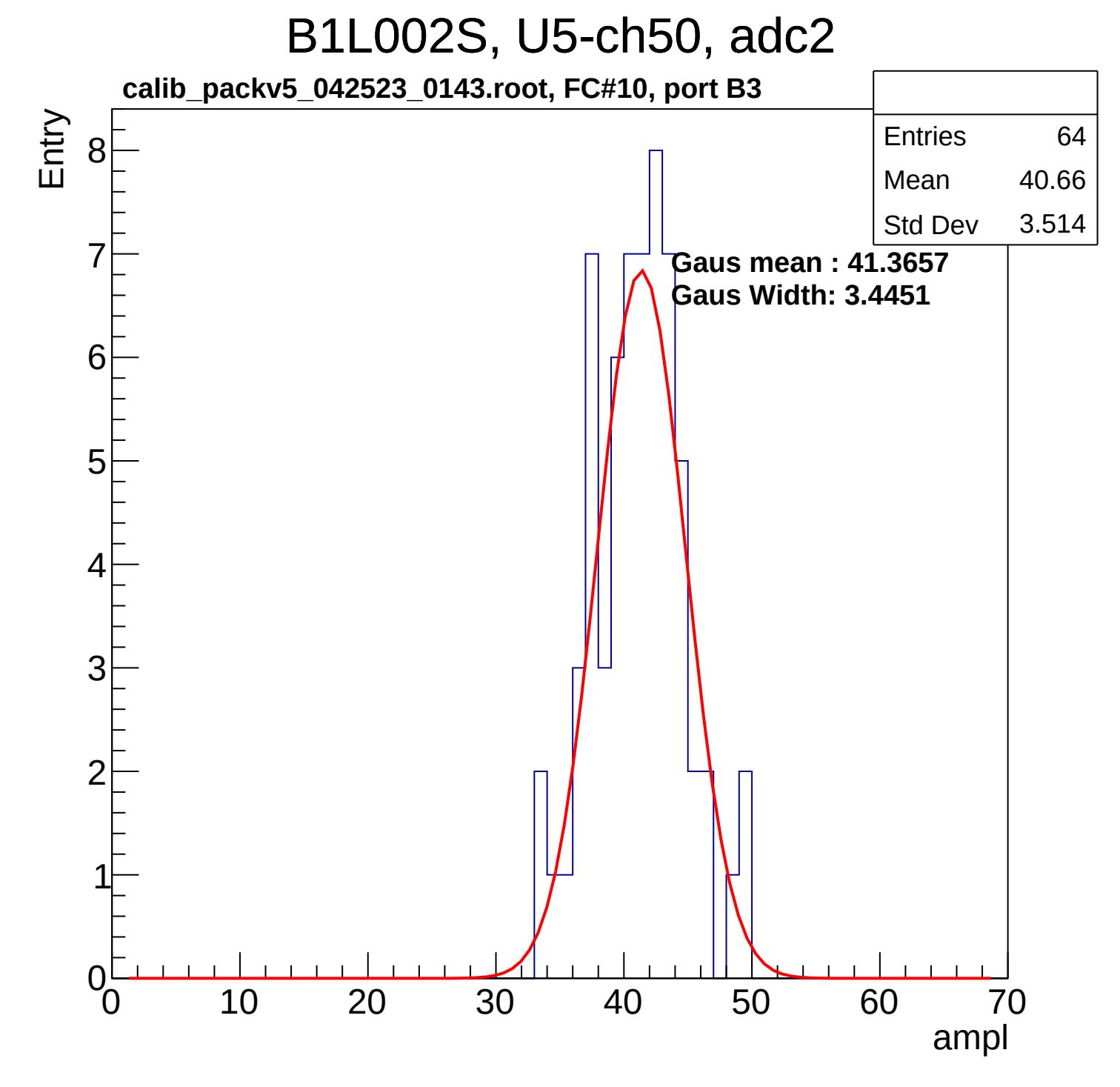
B1L002S, U5-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.66
Std Dev 3.514
Gaus mean : 41.3657
Gaus Width: 3.4451
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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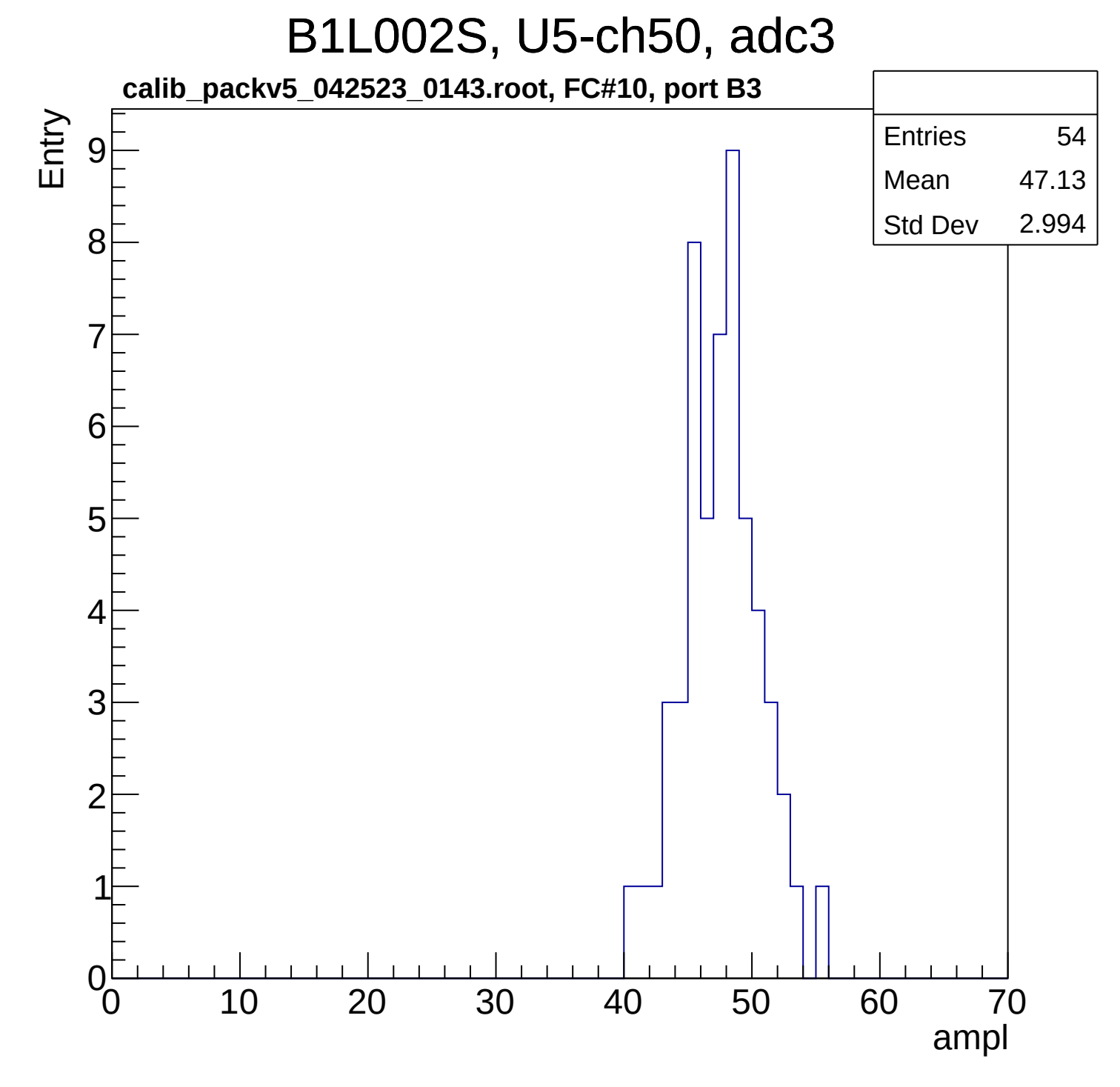
B1L002S, U5-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.13
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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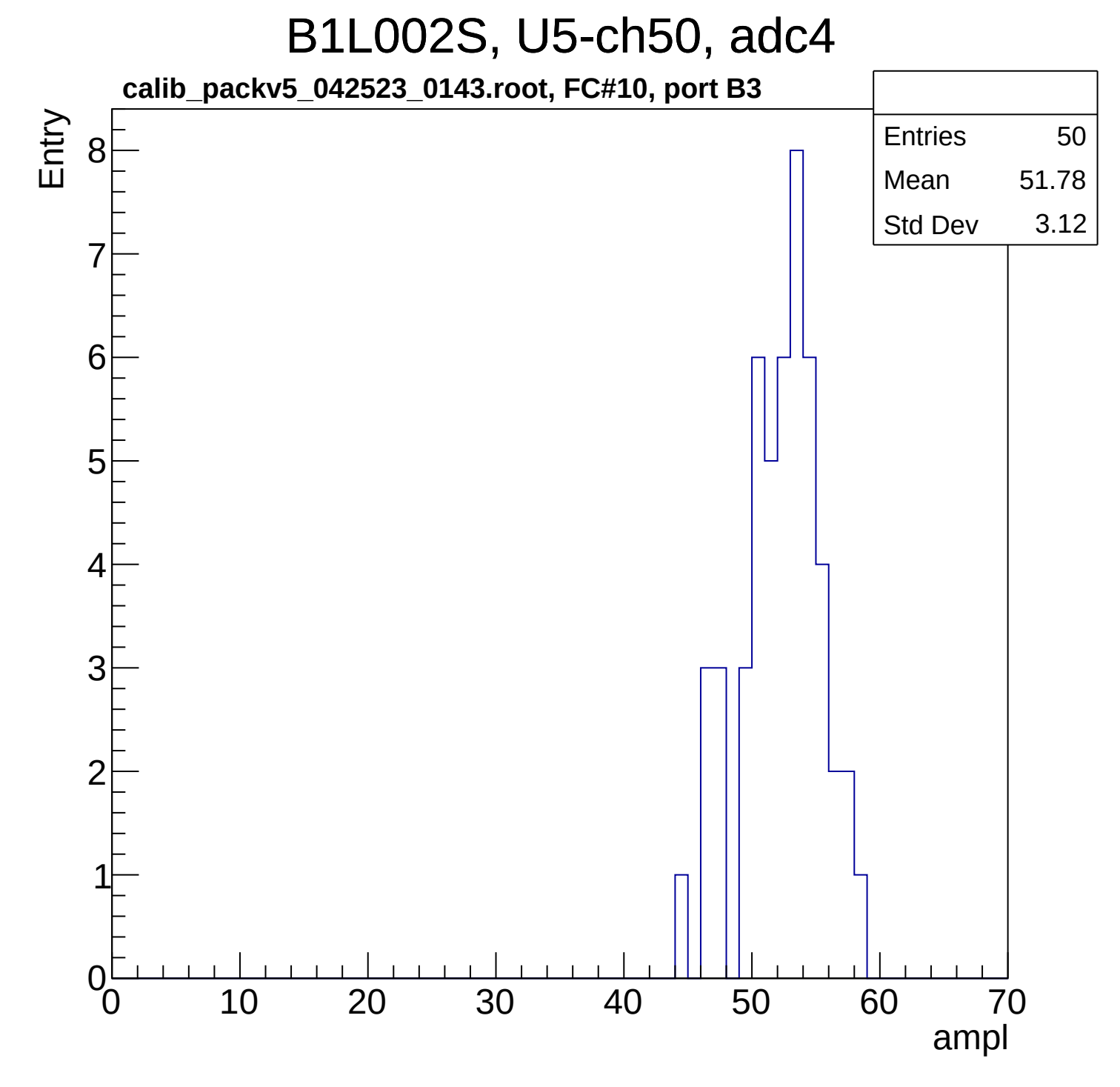
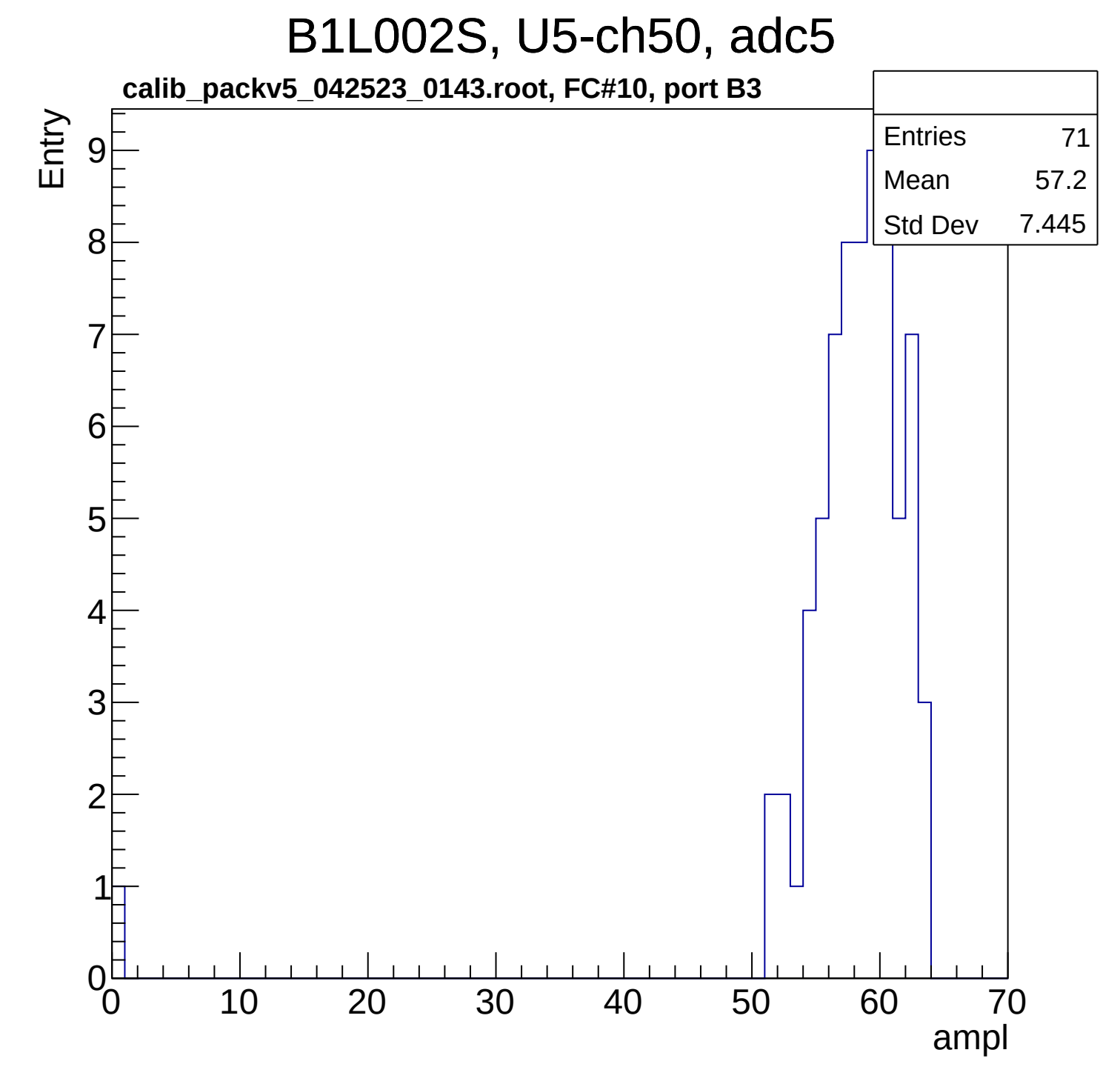

B1L002S, U5-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.78
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.2
Std Dev 7.445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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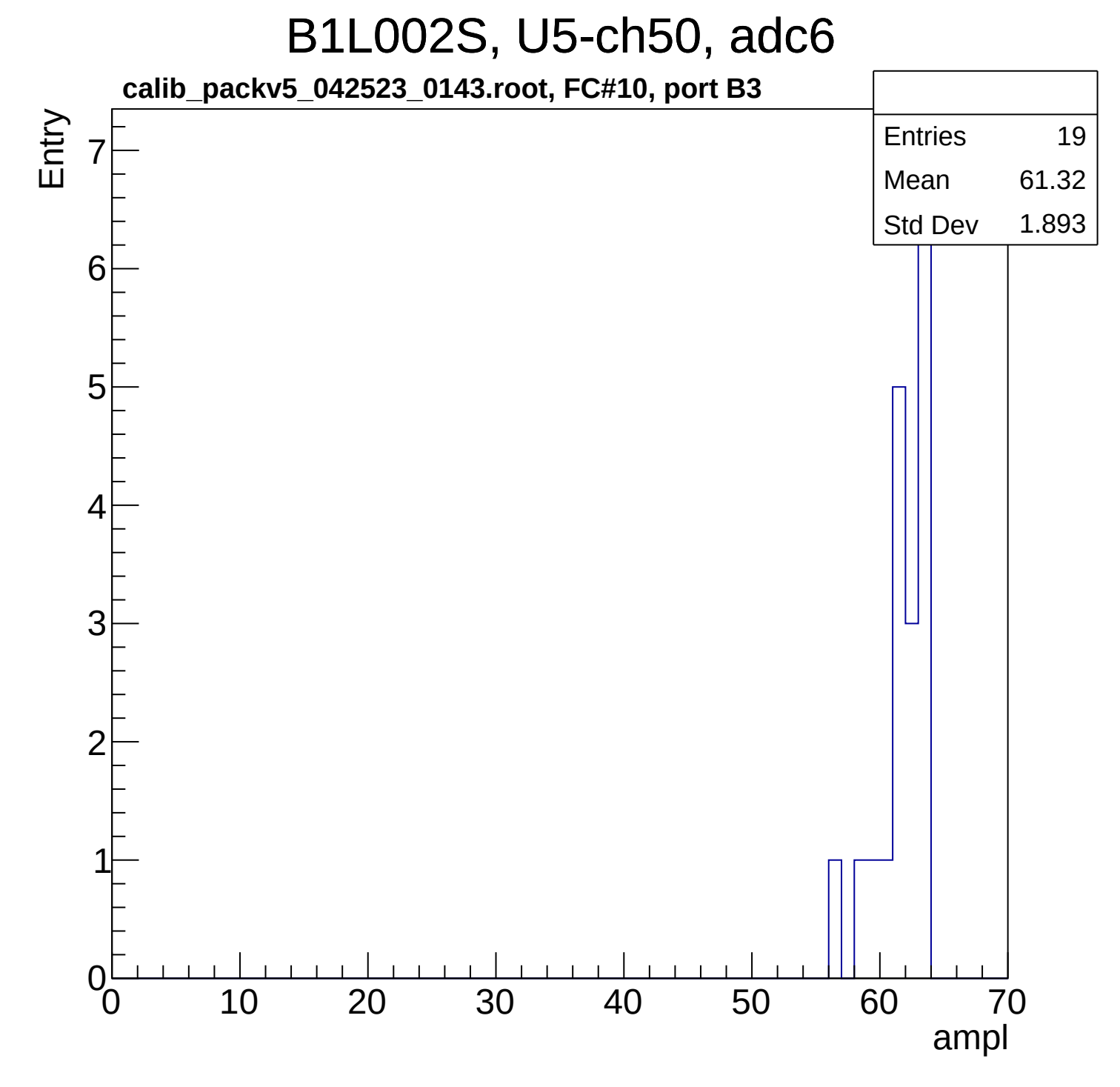
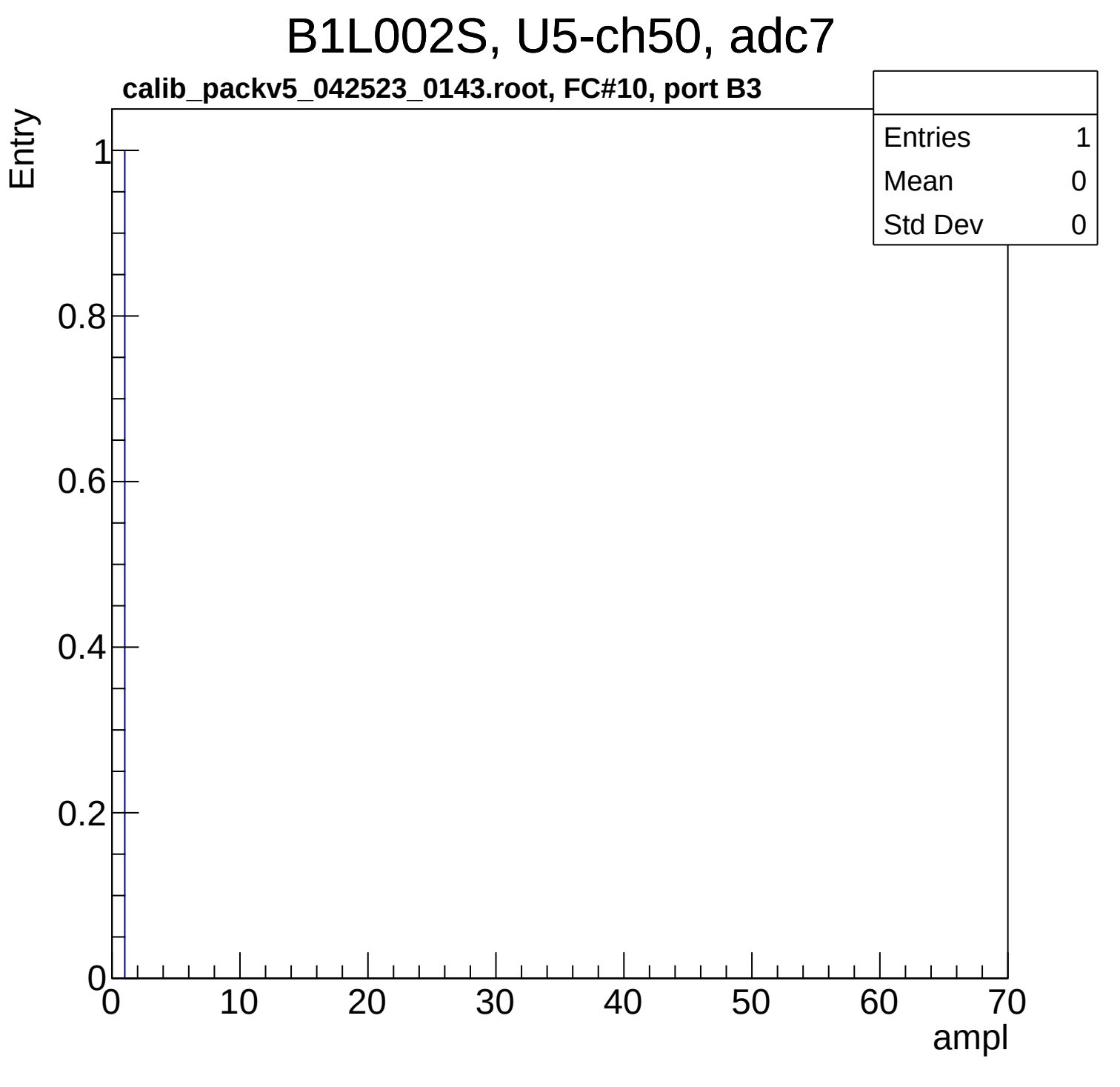
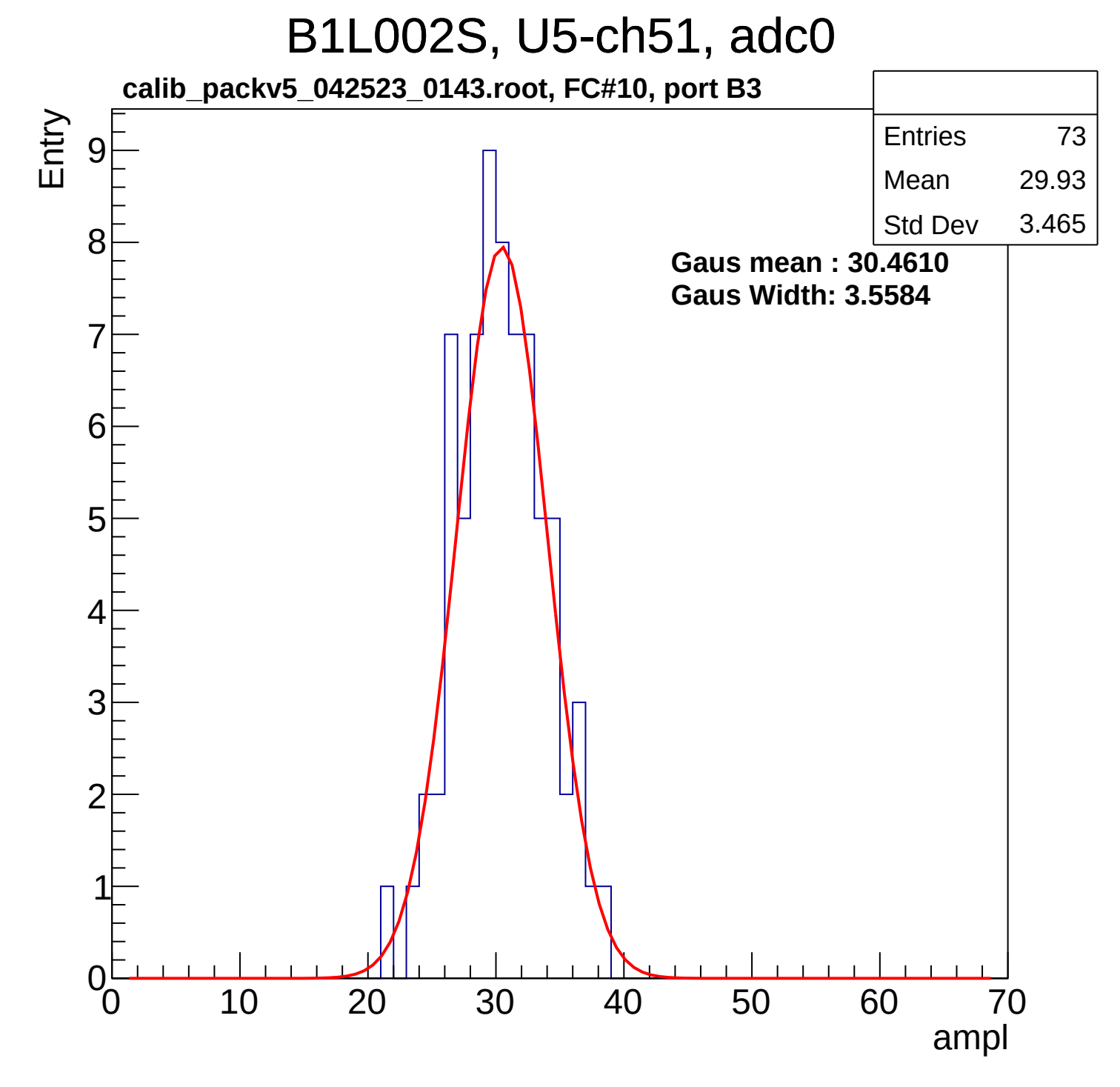
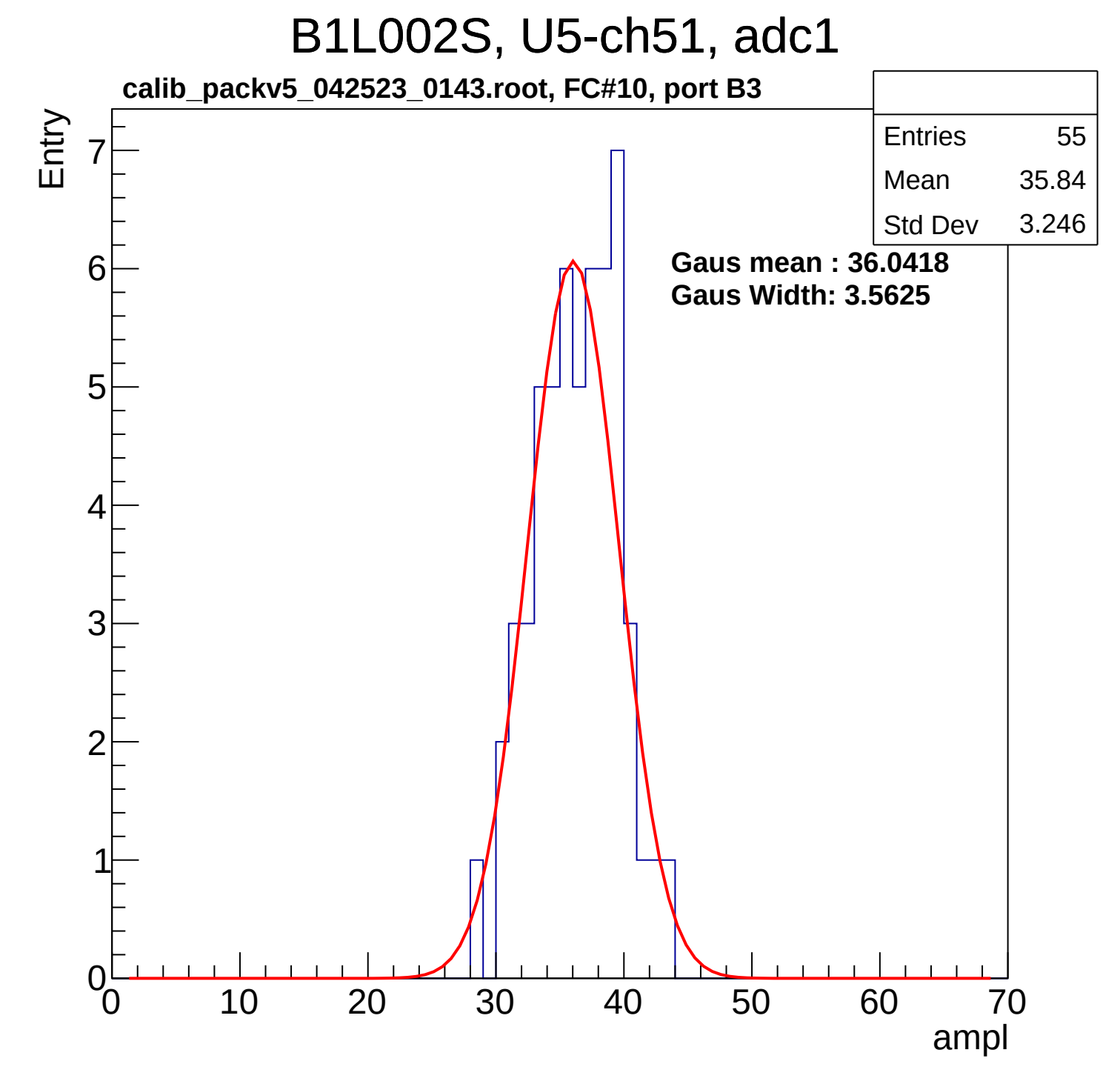
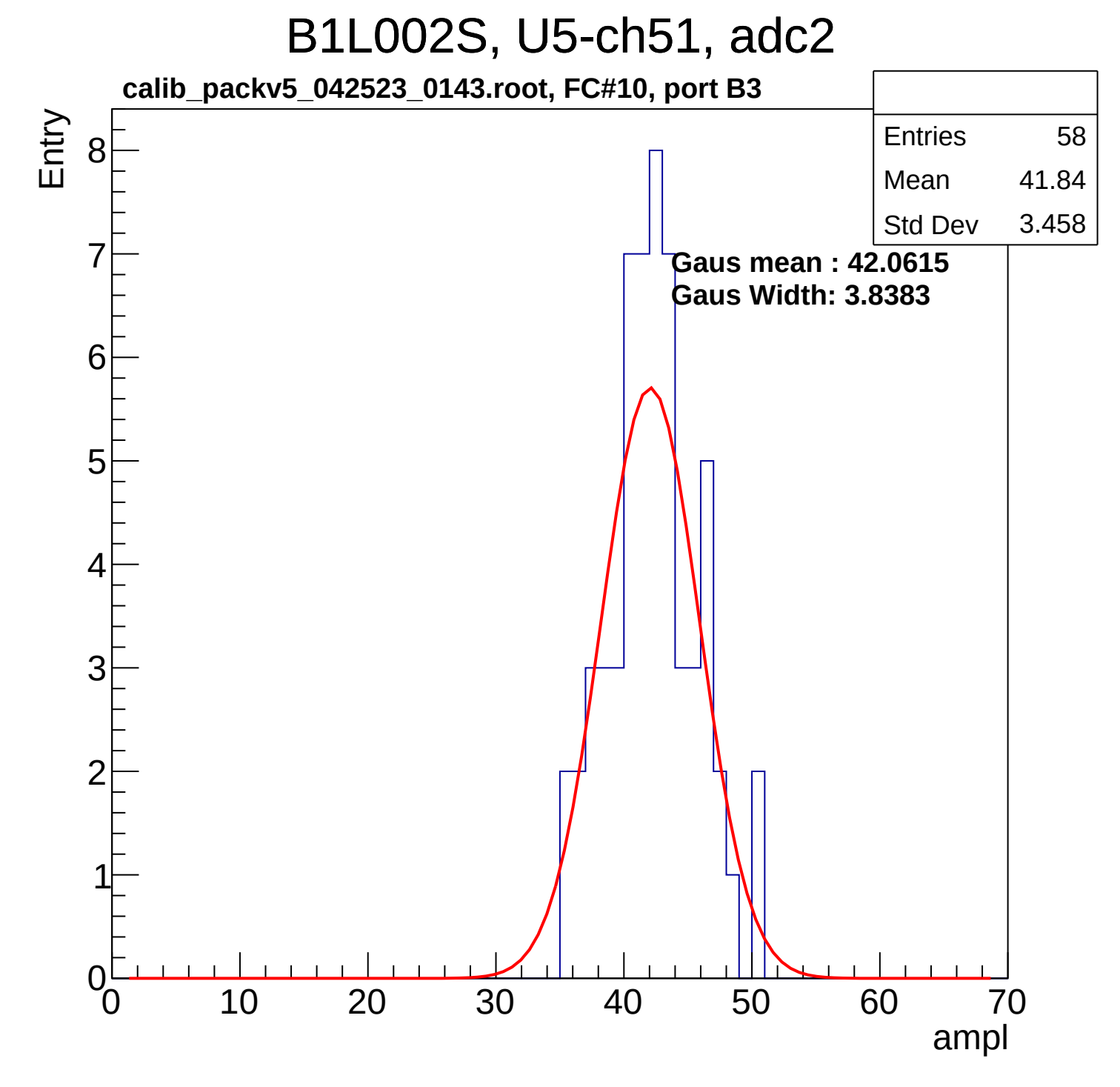
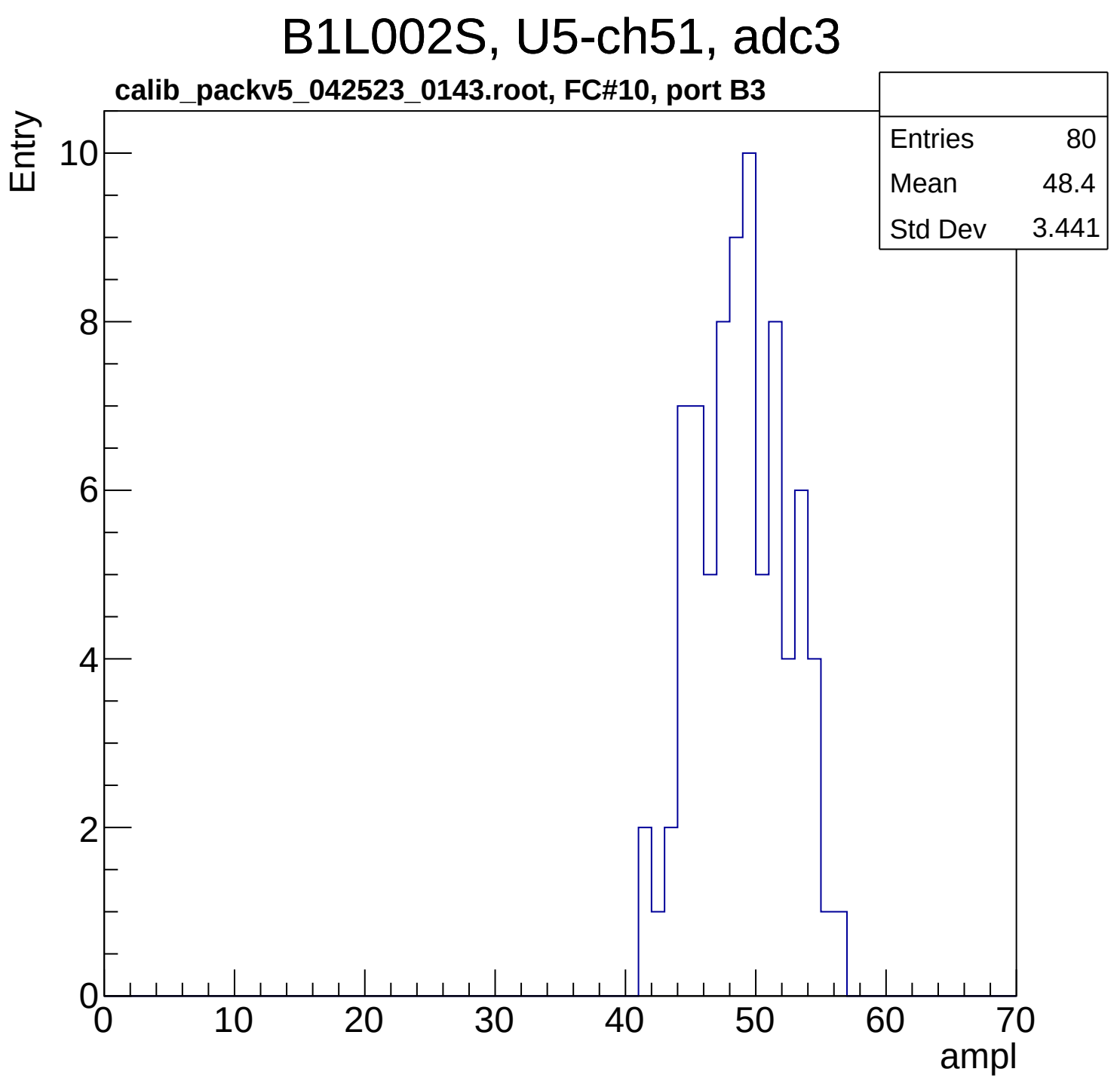

B1L002S, U5-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.32
Std Dev 1.893
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

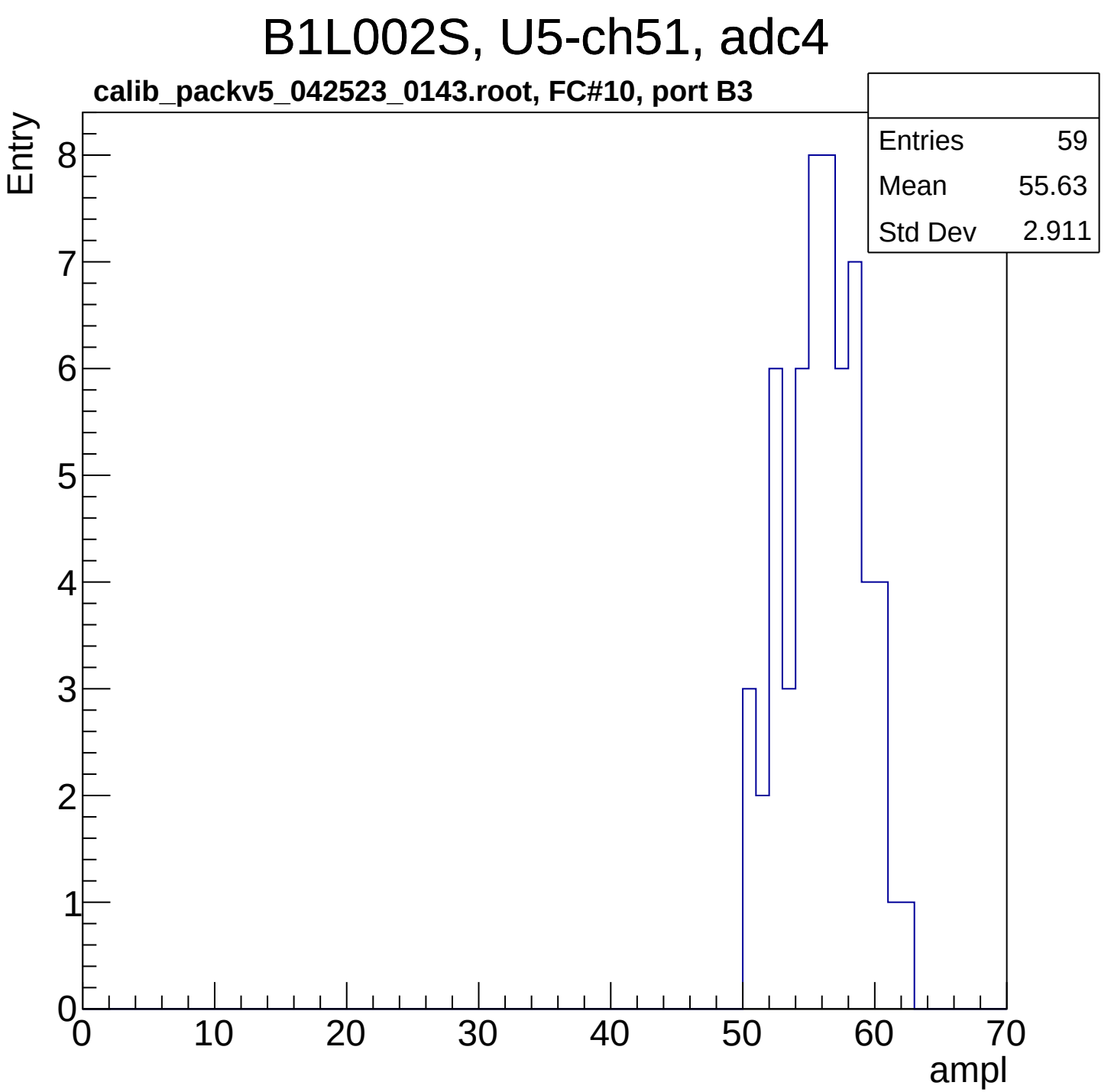

B1L002S, U5-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

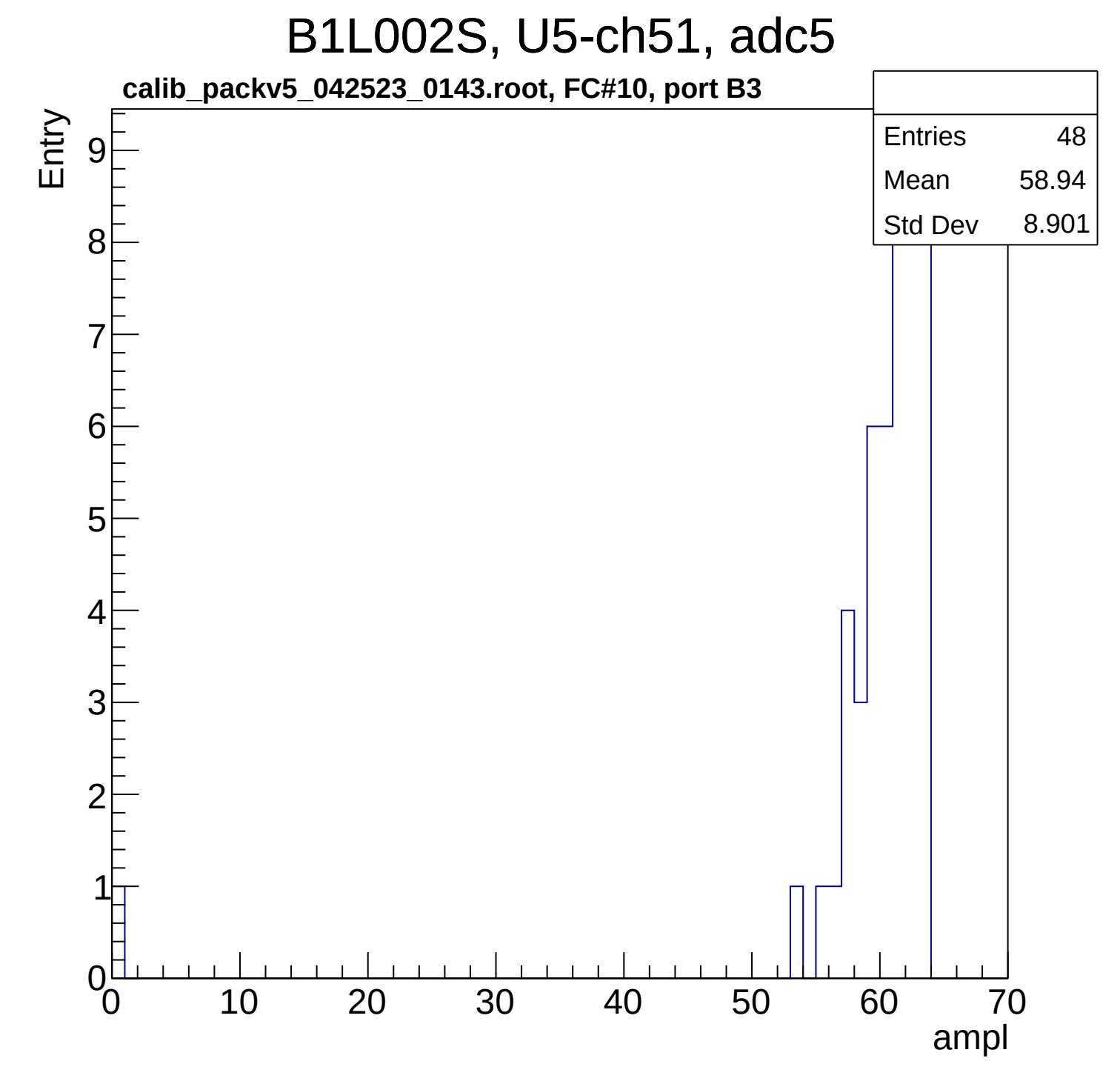

B1L002S, U5-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.93
Std Dev 3.465
Gaus mean : 30.4610
Gaus Width: 3.5584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

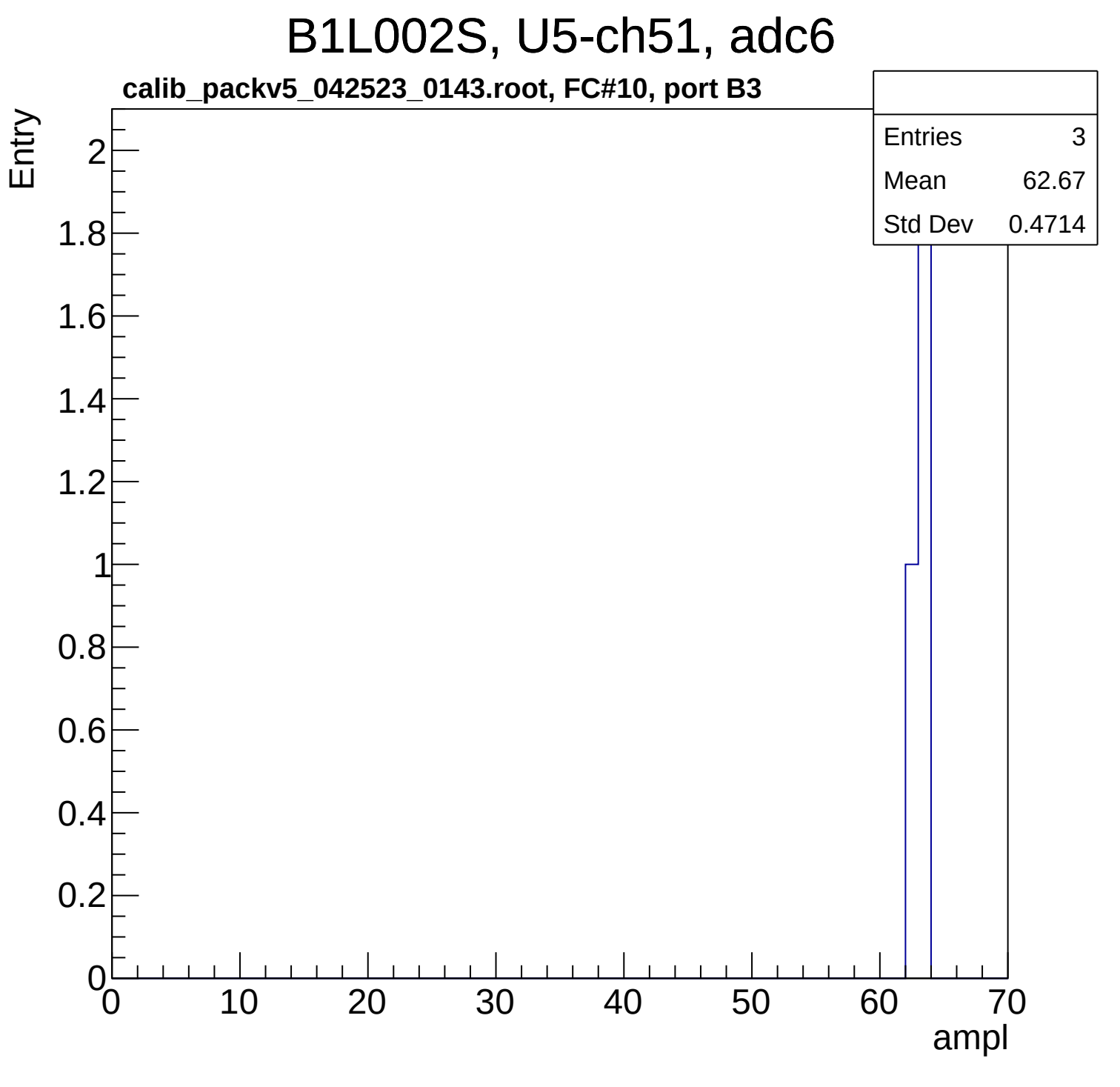

B1L002S, U5-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.84
Std Dev 3.246
Gaus mean : 36.0418
Gaus Width: 3.5625
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

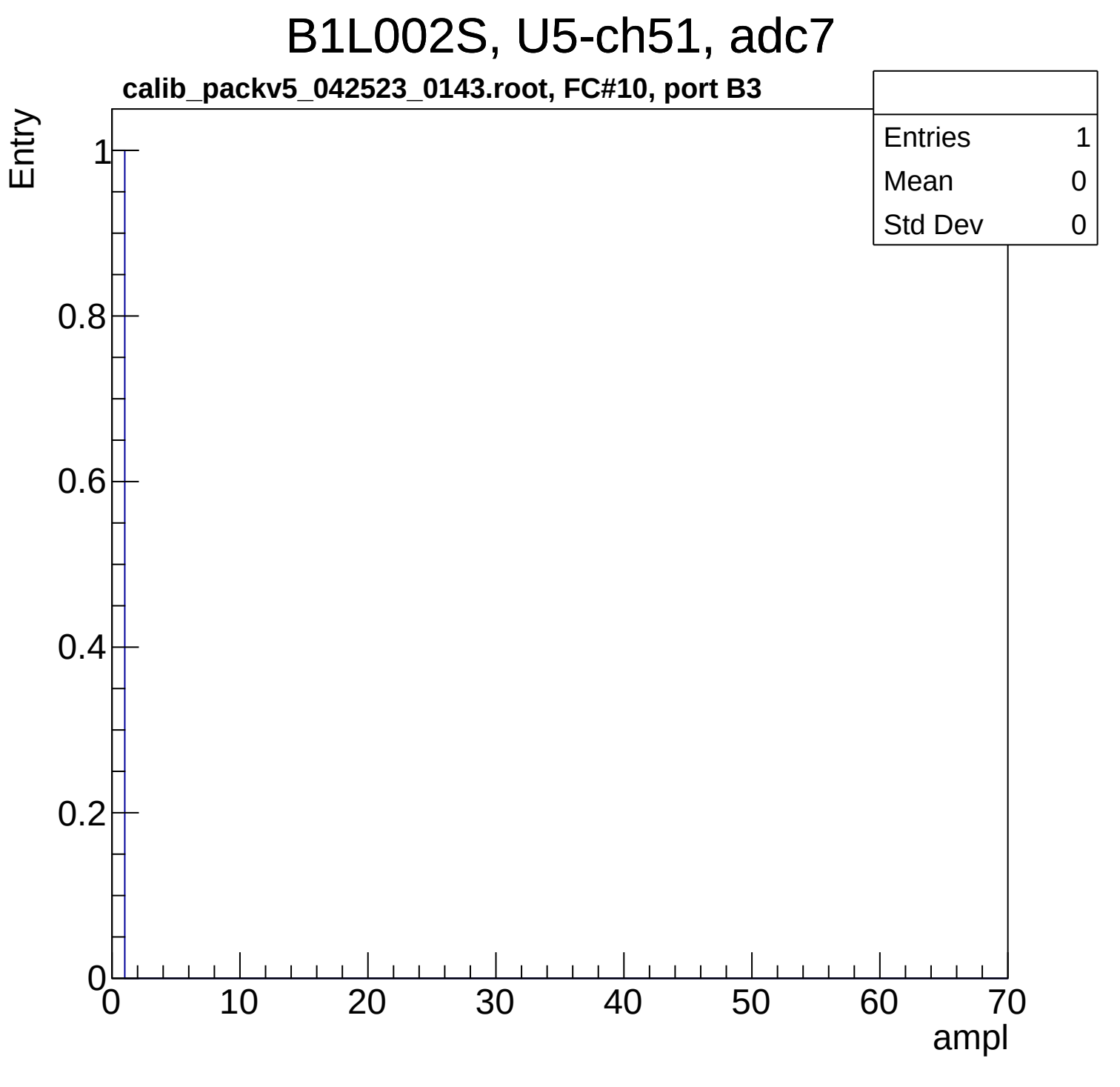

B1L002S, U5-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.84
Std Dev 3.458
Gaus mean : 42.0615
Gaus Width: 3.8383
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

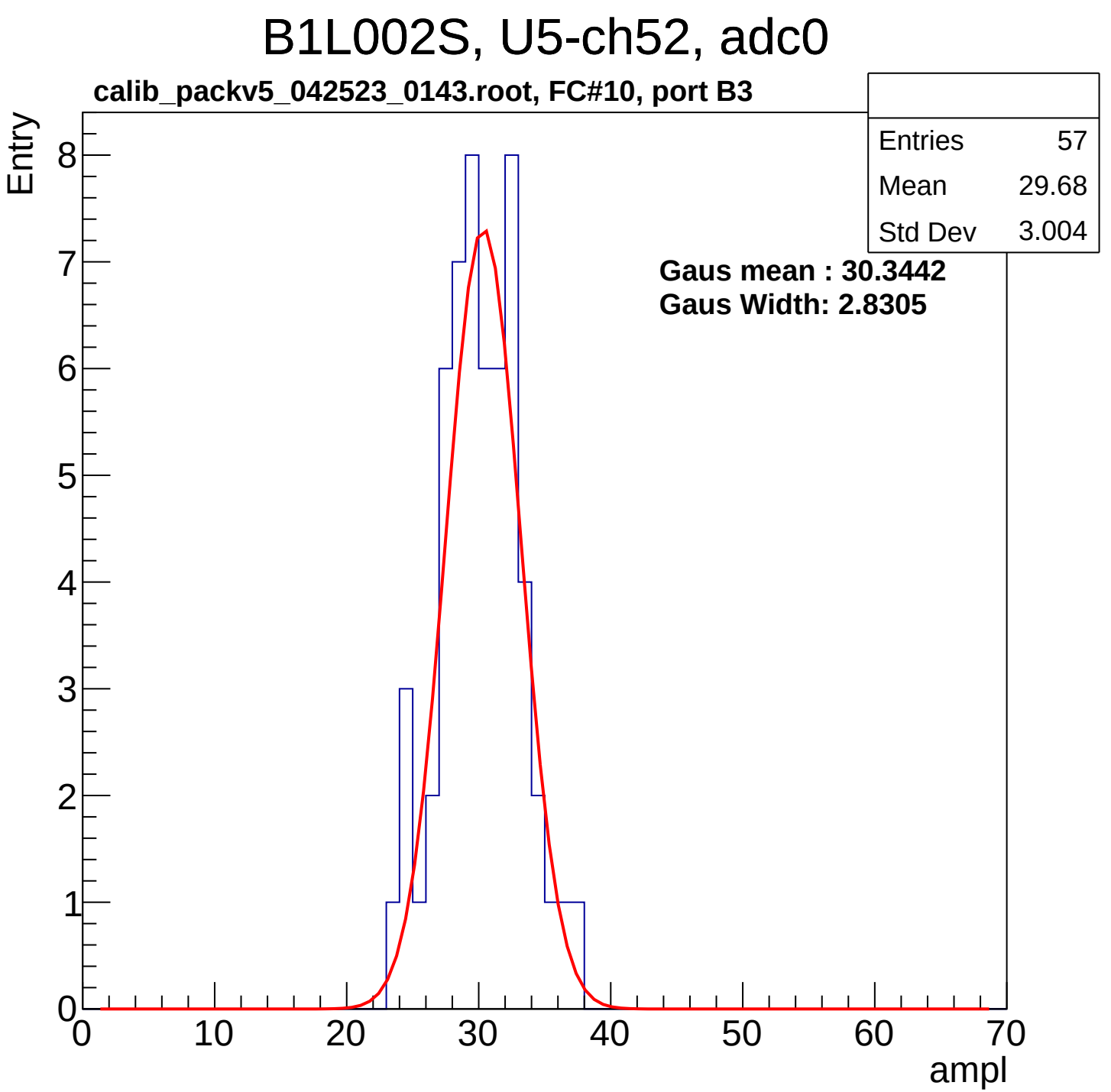

B1L002S, U5-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 48.4
Std Dev 3.441
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

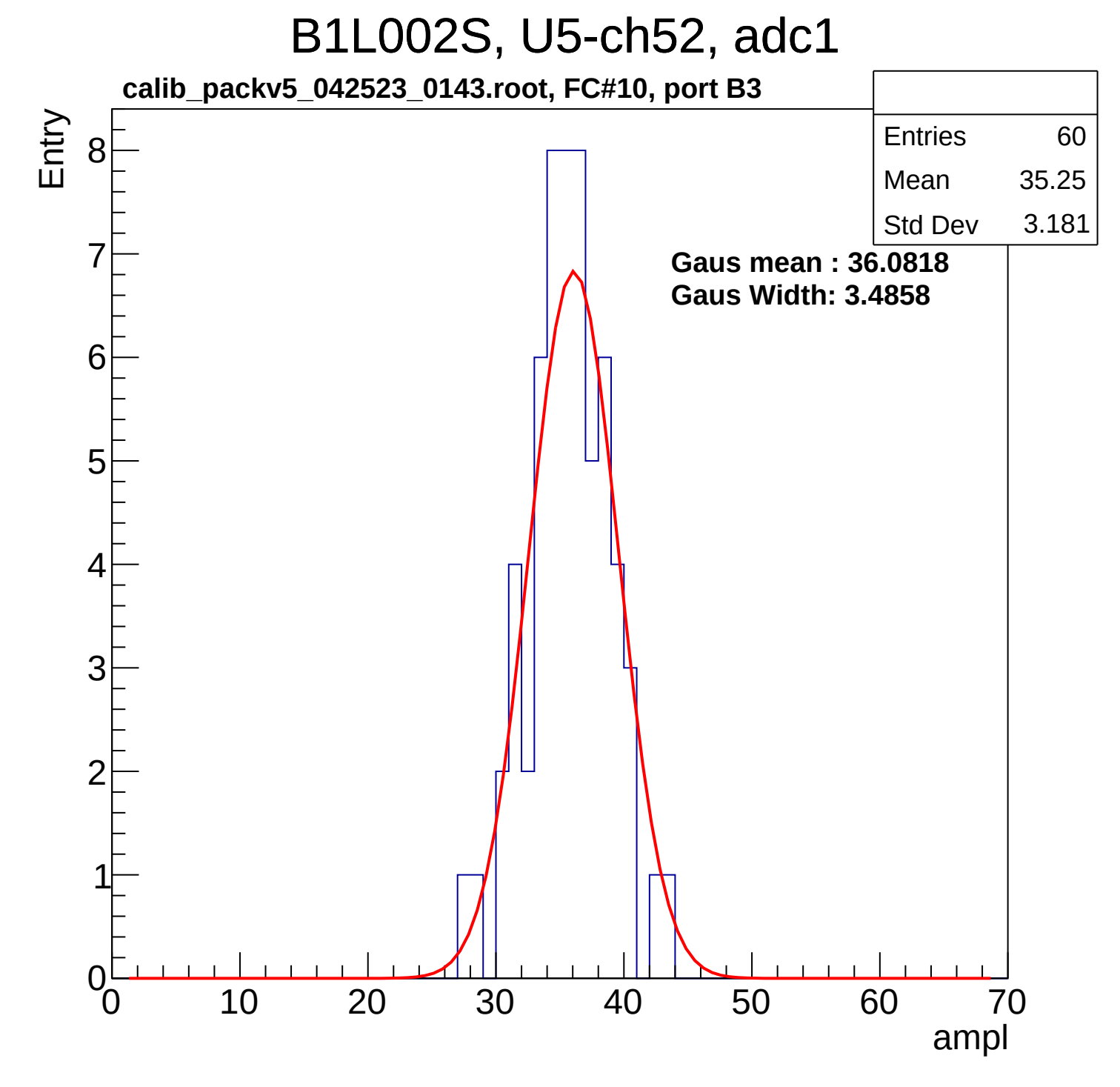

B1L002S, U5-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 55.63
Std Dev 2.911
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

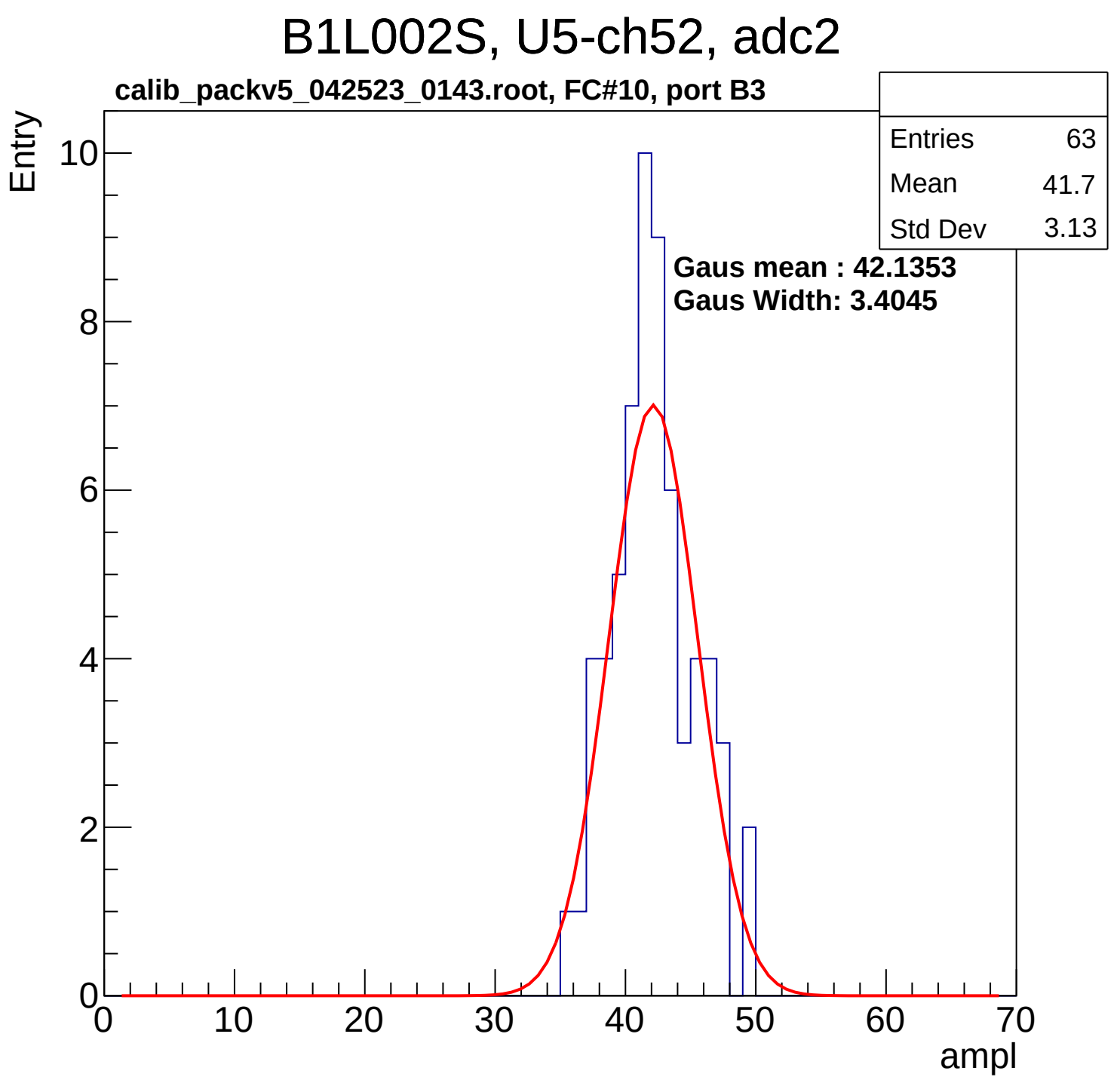

B1L002S, U5-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.94
Std Dev 8.901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

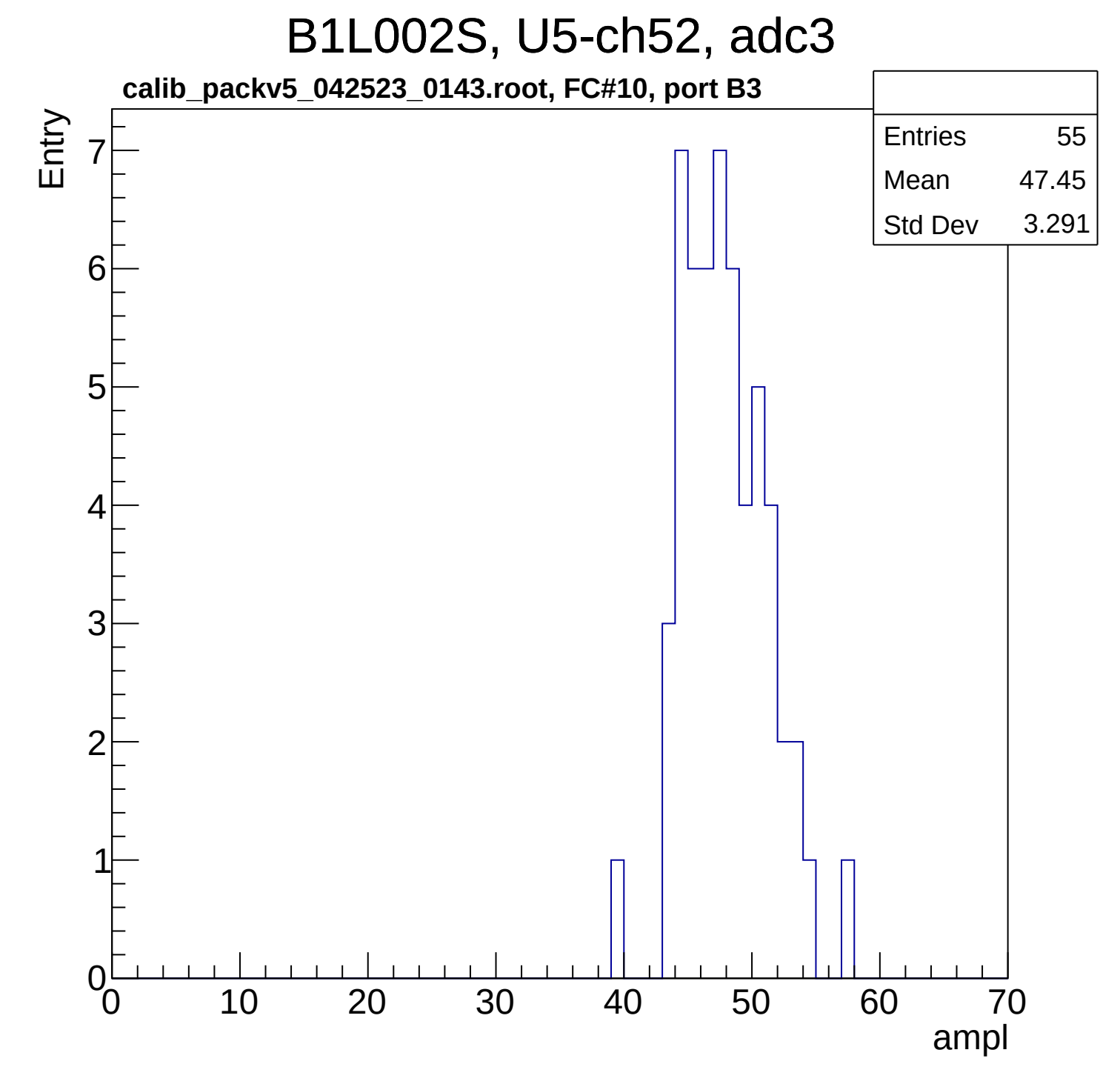

B1L002S, U5-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

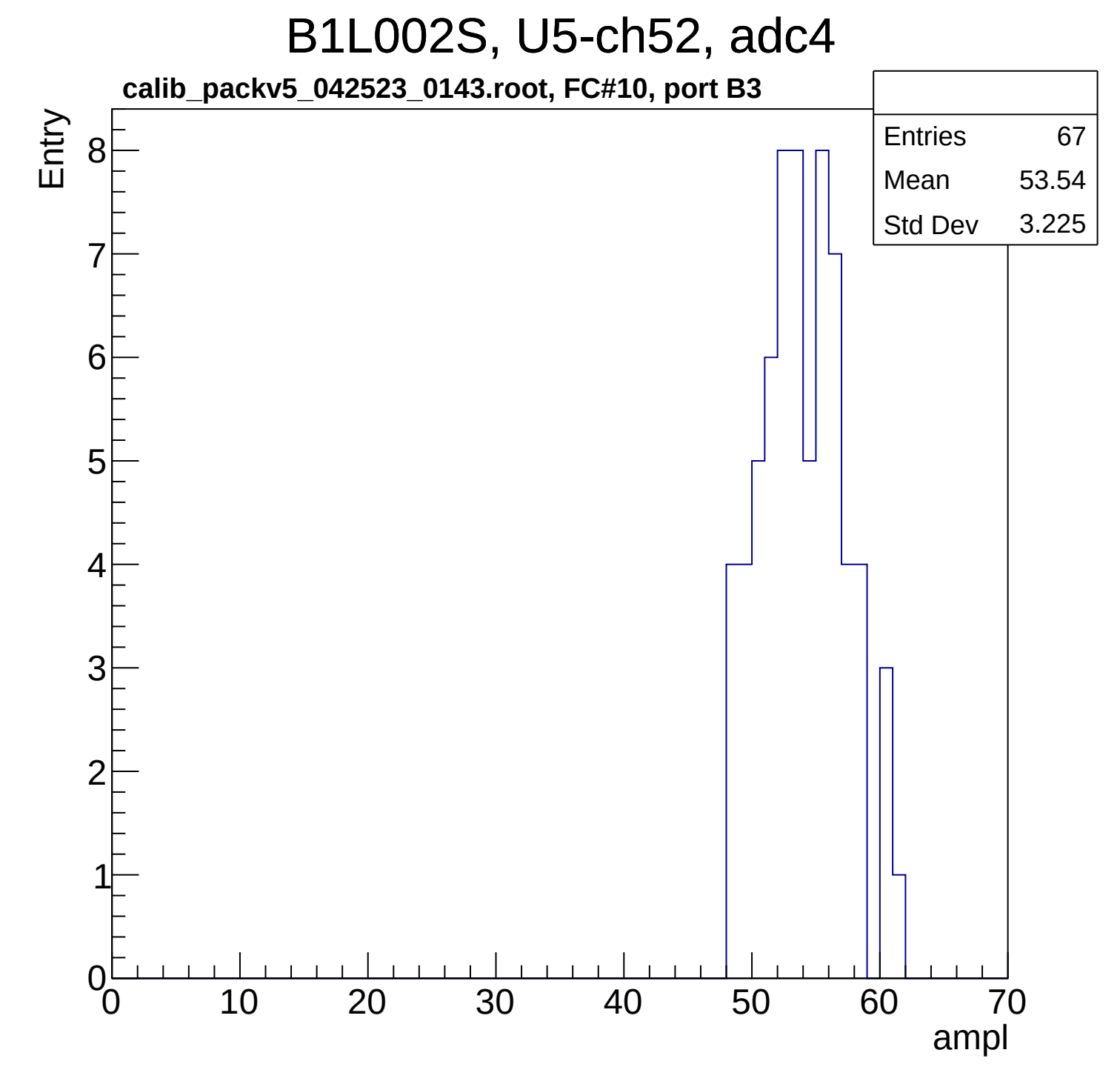

B1L002S, U5-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.68
Std Dev 3.004
Gaus mean : 30.3442
Gaus Width: 2.8305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.25
Std Dev 3.181
Gaus mean : 36.0818
Gaus Width: 3.4858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.7
Std Dev 3.13
Gaus mean : 42.1353
Gaus Width: 3.4045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.45
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.54
Std Dev 3.225
Entry
ampl
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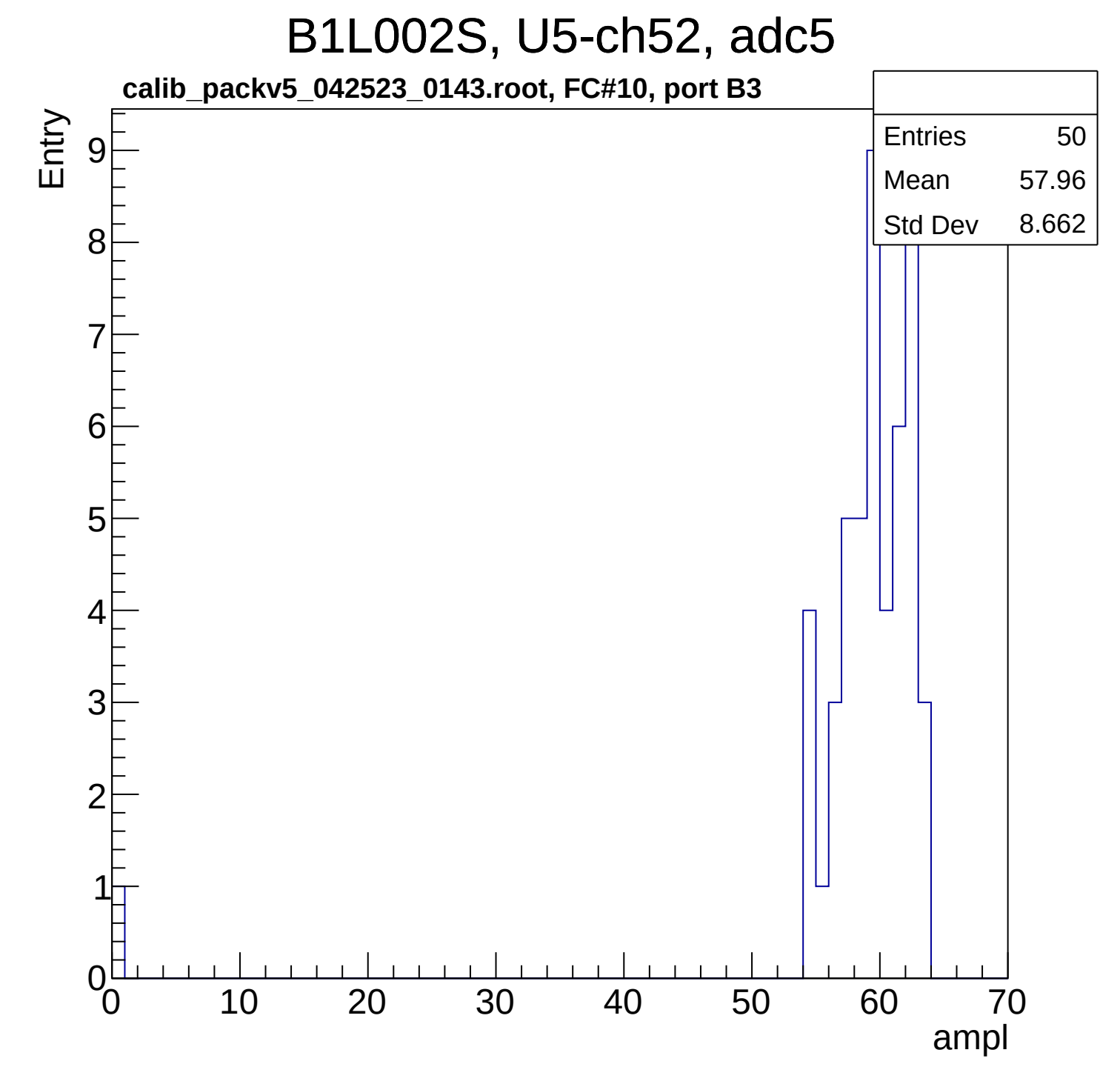
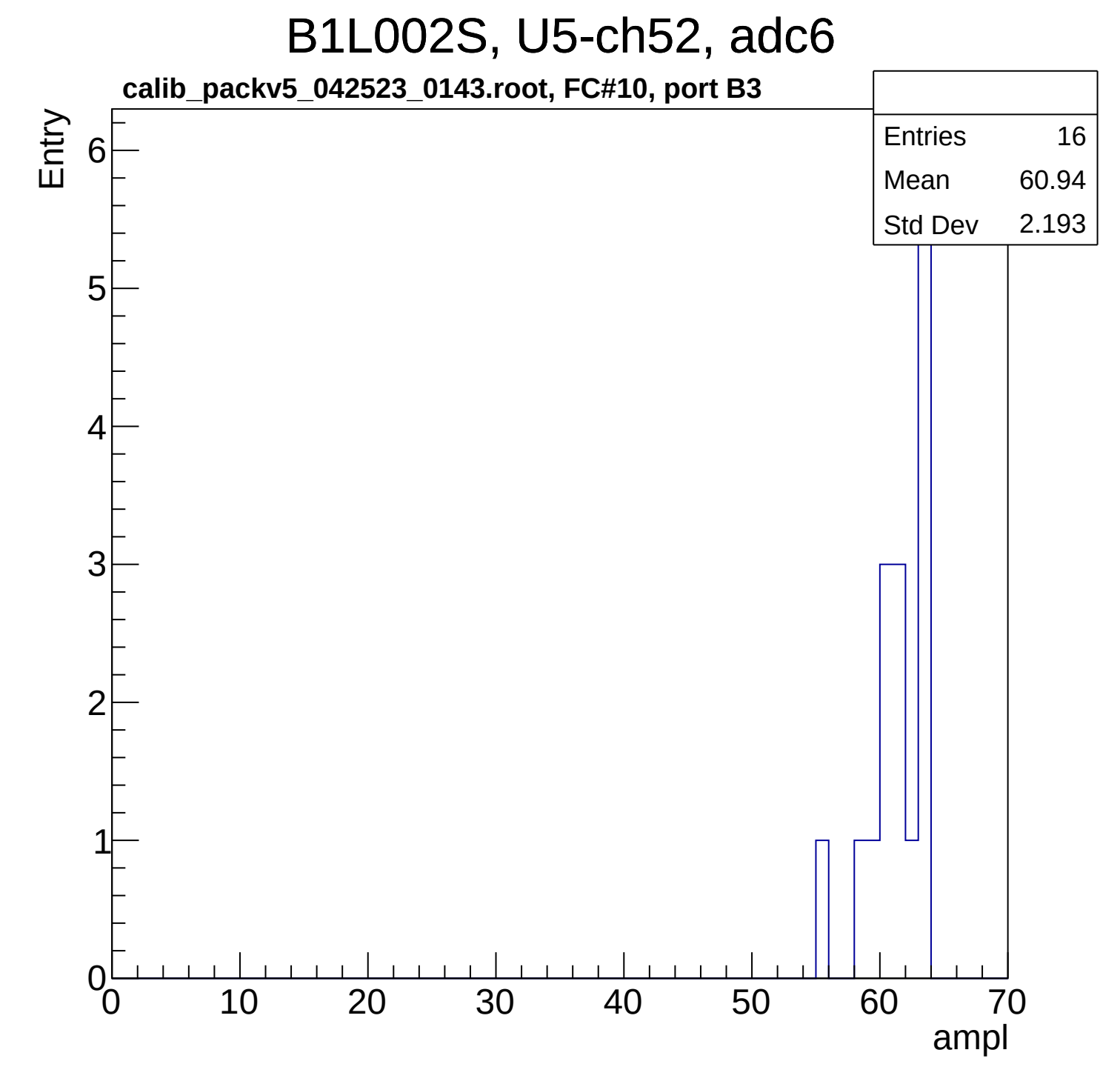
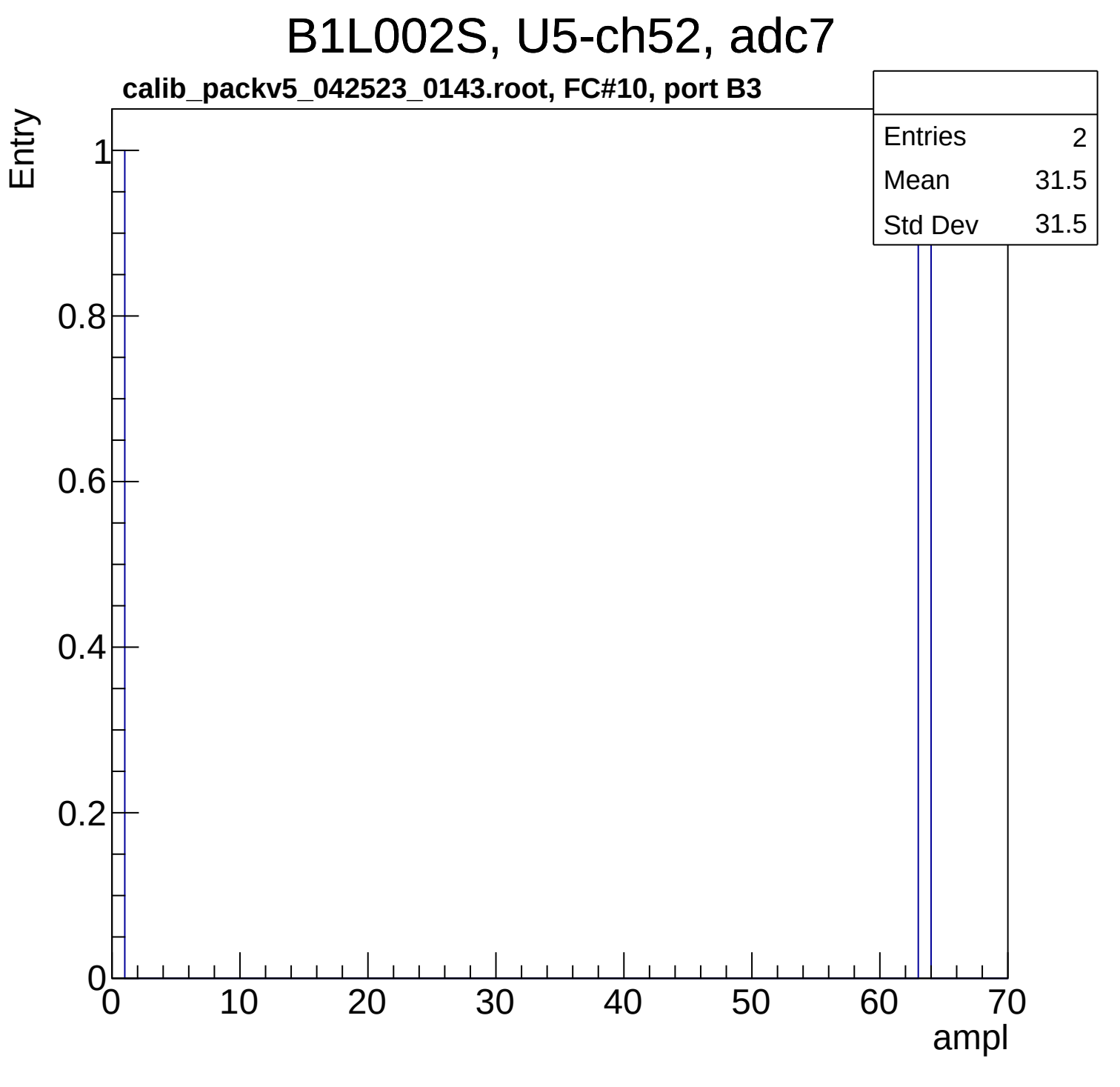
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.96
Std Dev 8.662
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 60.94
Std Dev 2.193
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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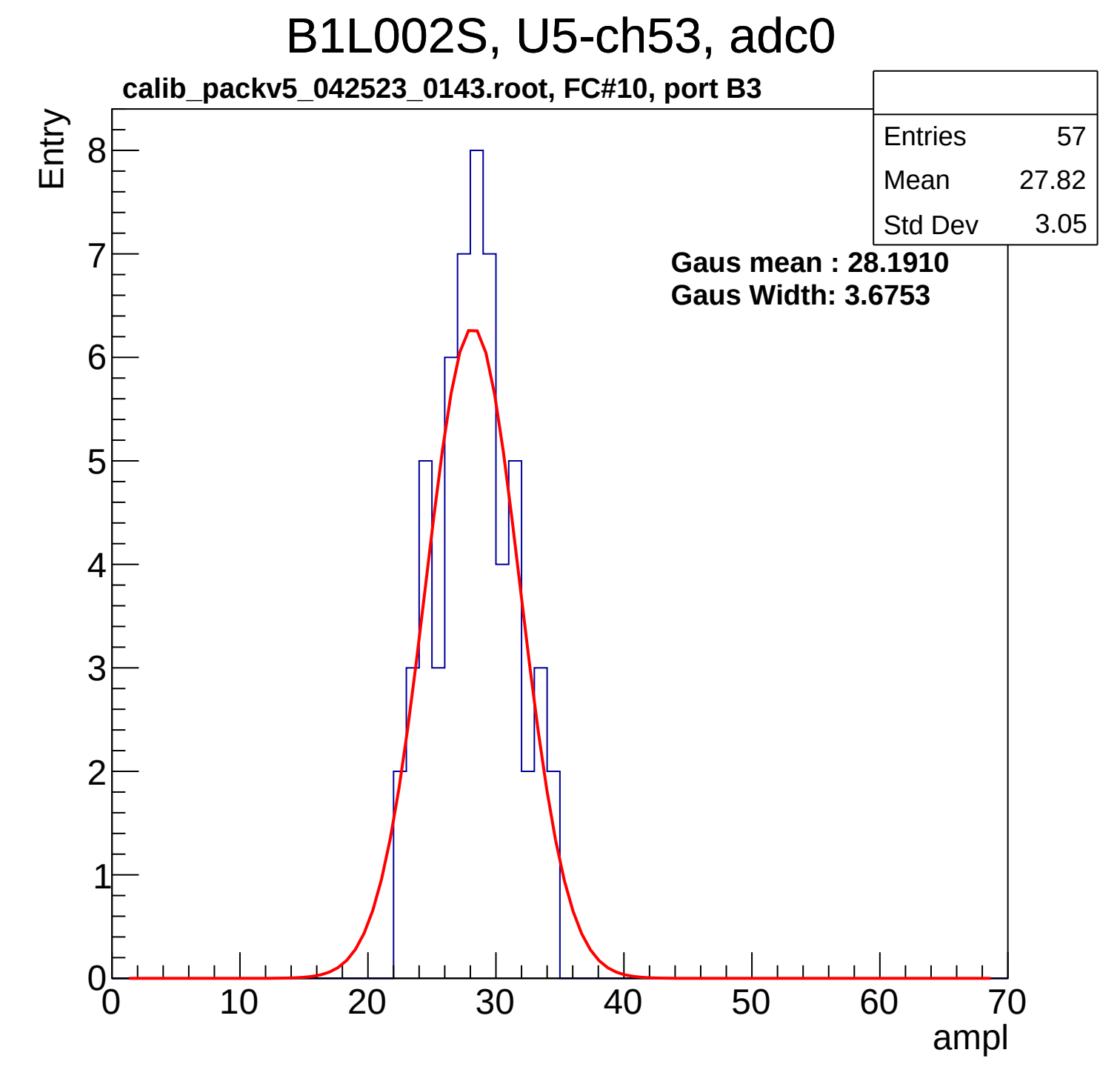
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.82
Std Dev 3.05
Gaus mean : 28.1910
Gaus Width: 3.6753
Entry
ampl
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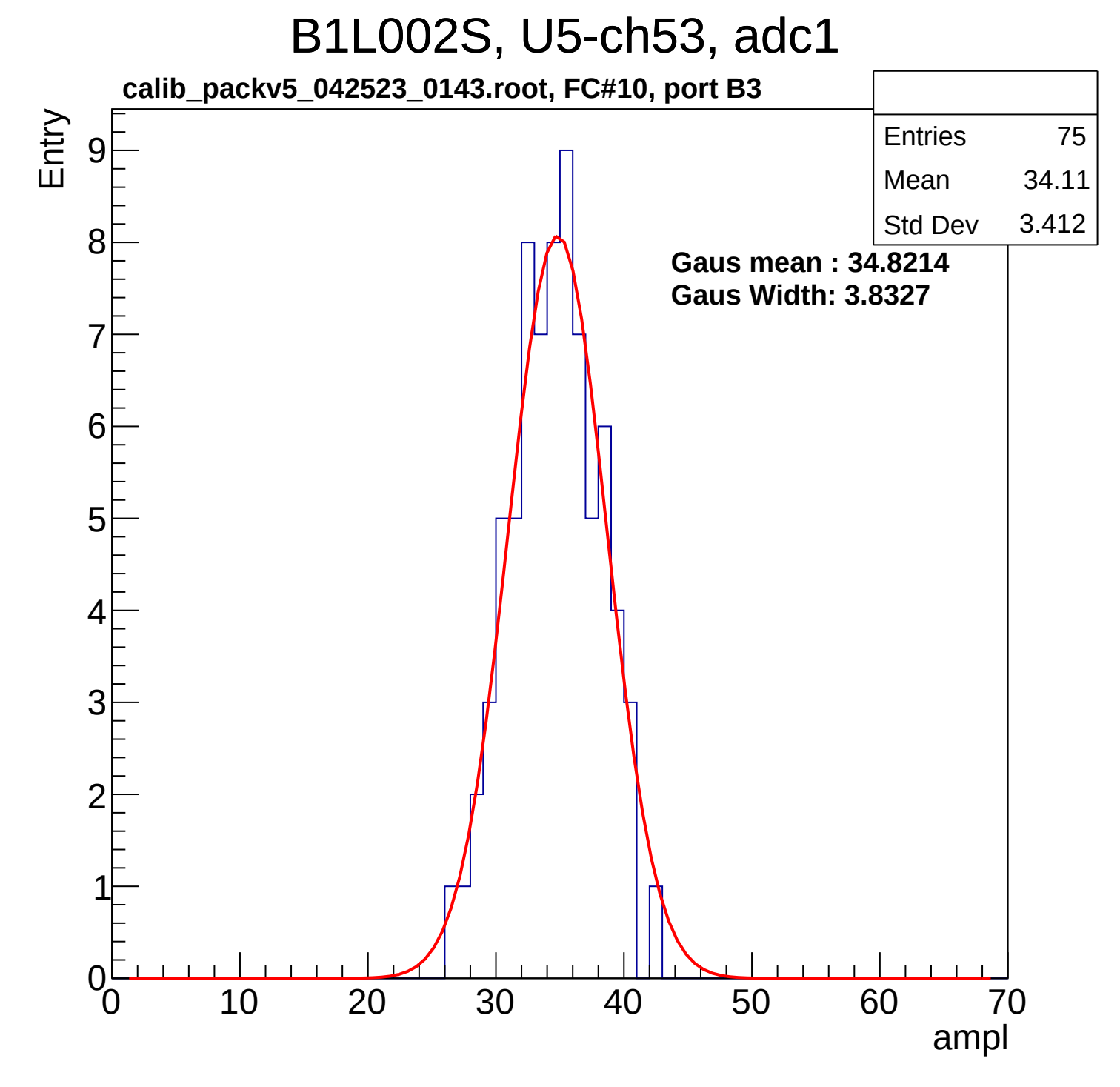
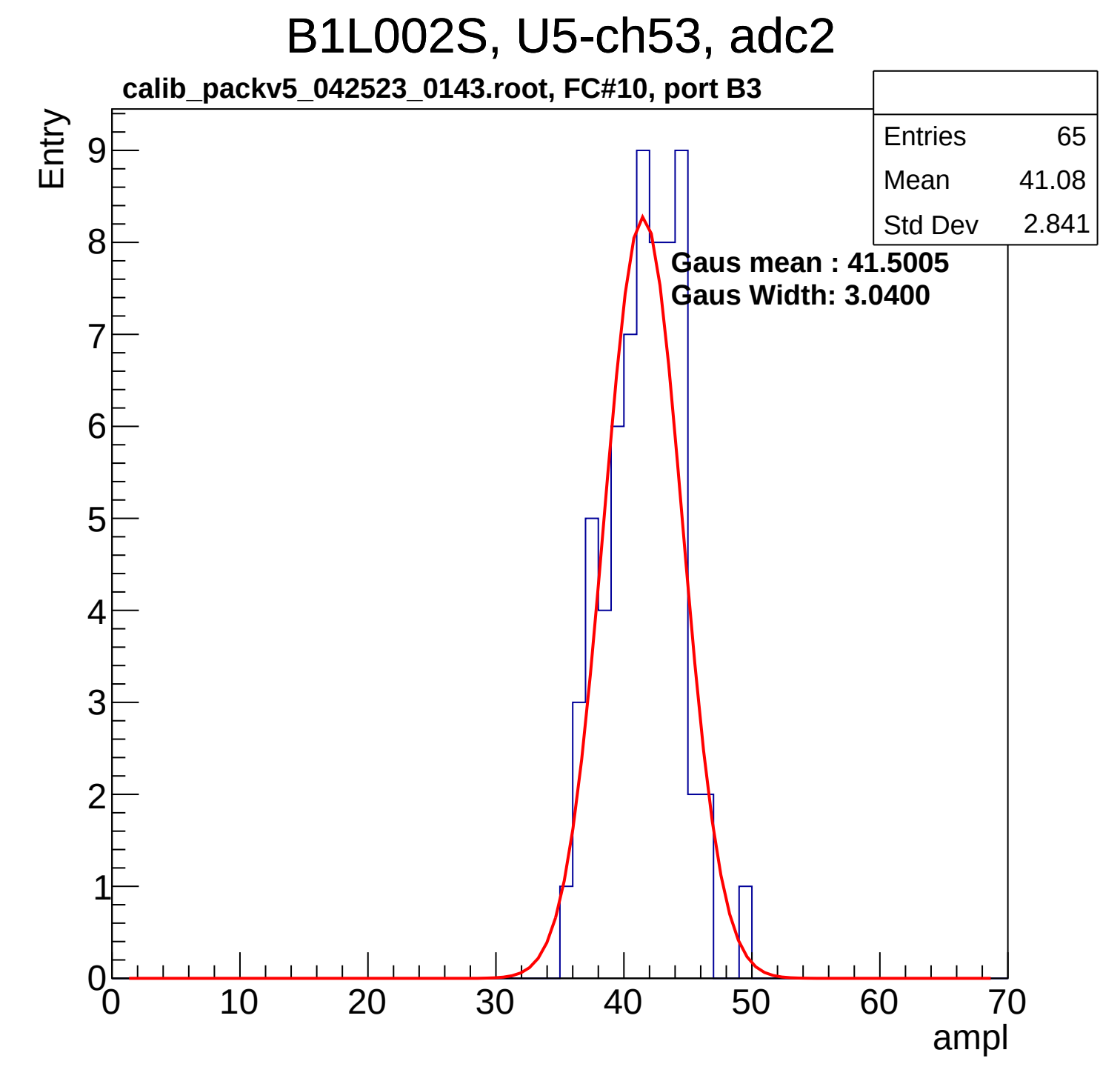
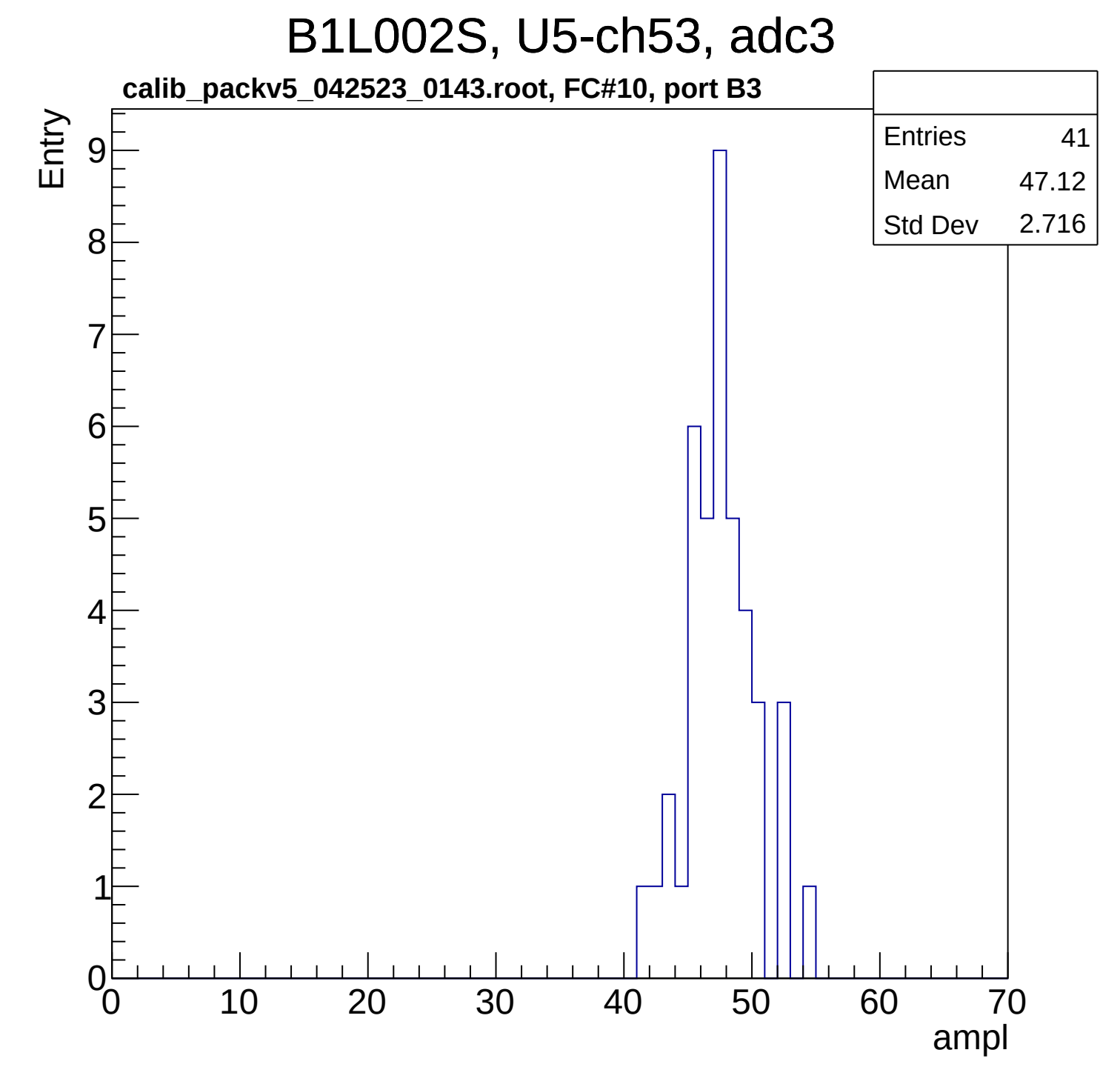
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.11
Std Dev 3.412
Gaus mean : 34.8214
Gaus Width: 3.8327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.08
Std Dev 2.841
Gaus mean : 41.5005
Gaus Width: 3.0400
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 47.12
Std Dev 2.716
Entry
ampl
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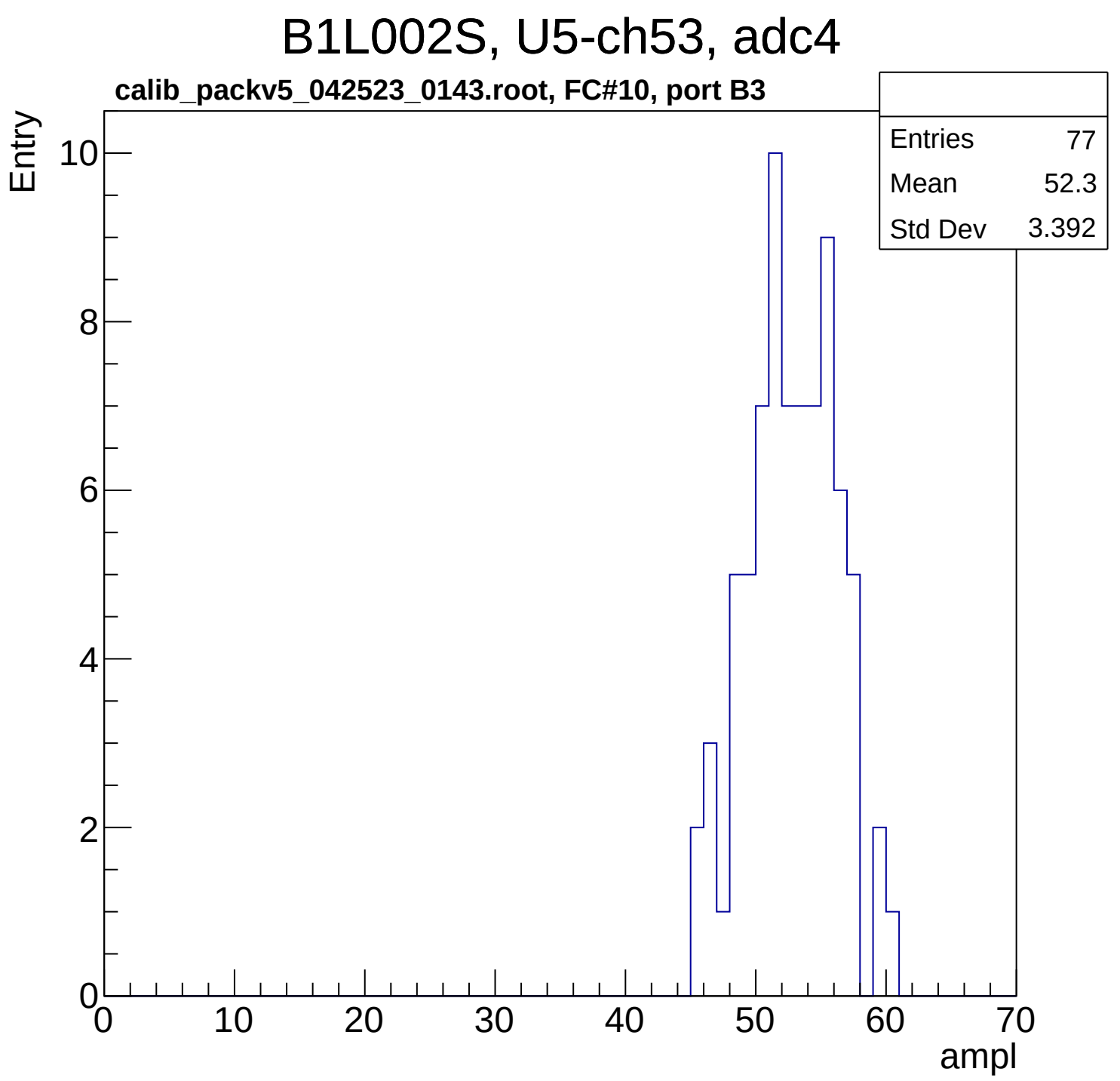
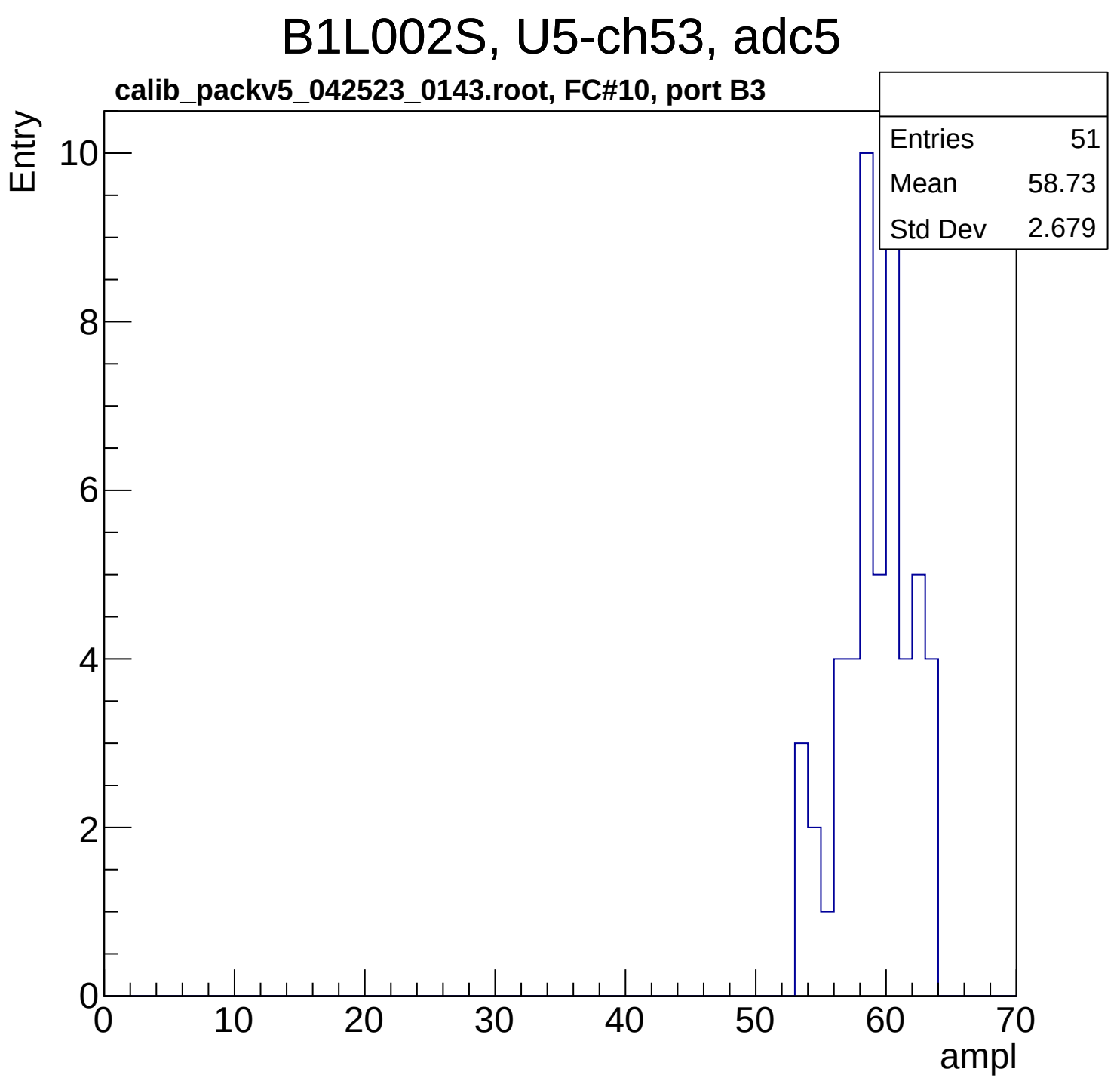
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 52.3
Std Dev 3.392
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.73
Std Dev 2.679
Entry
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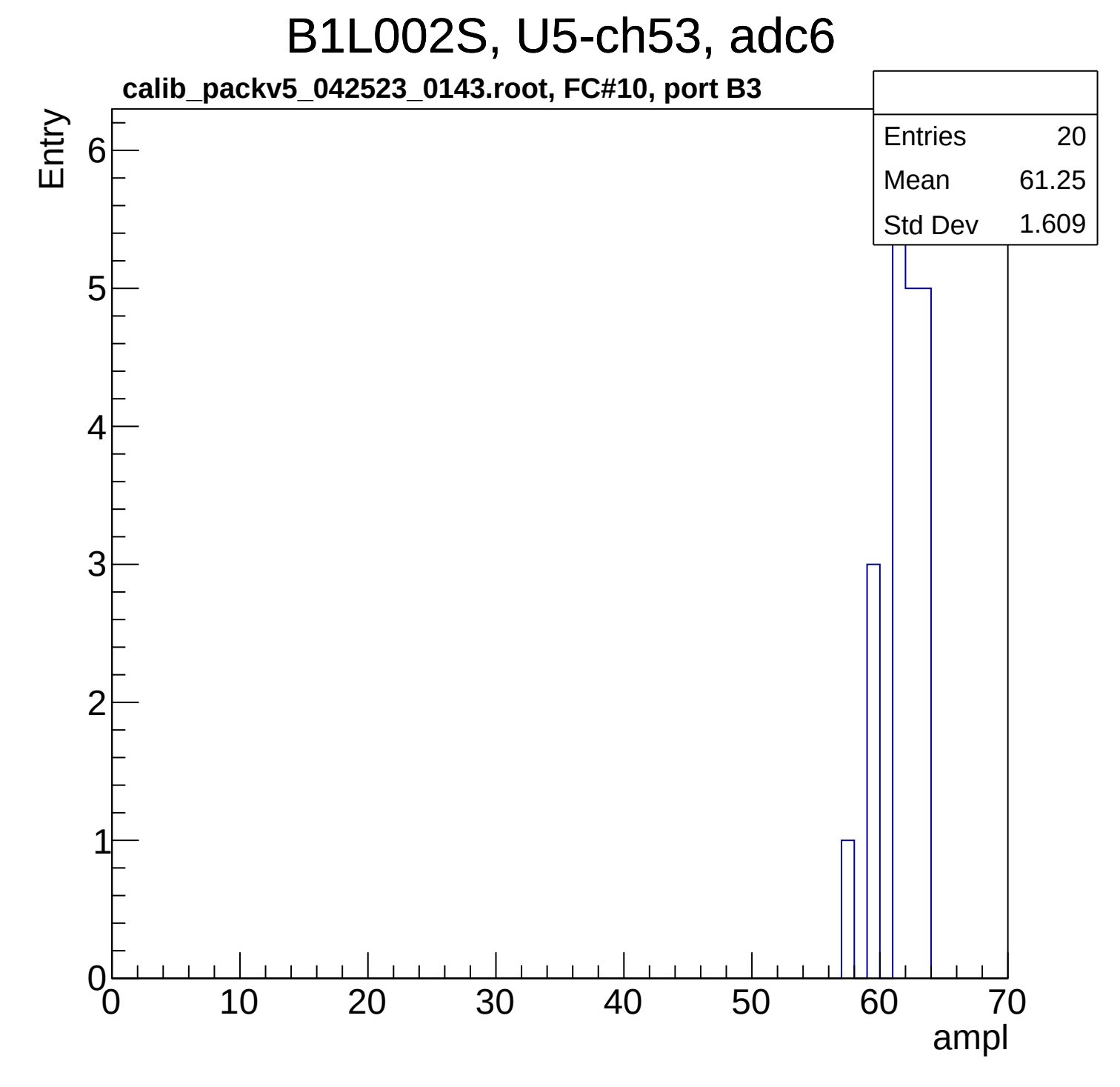
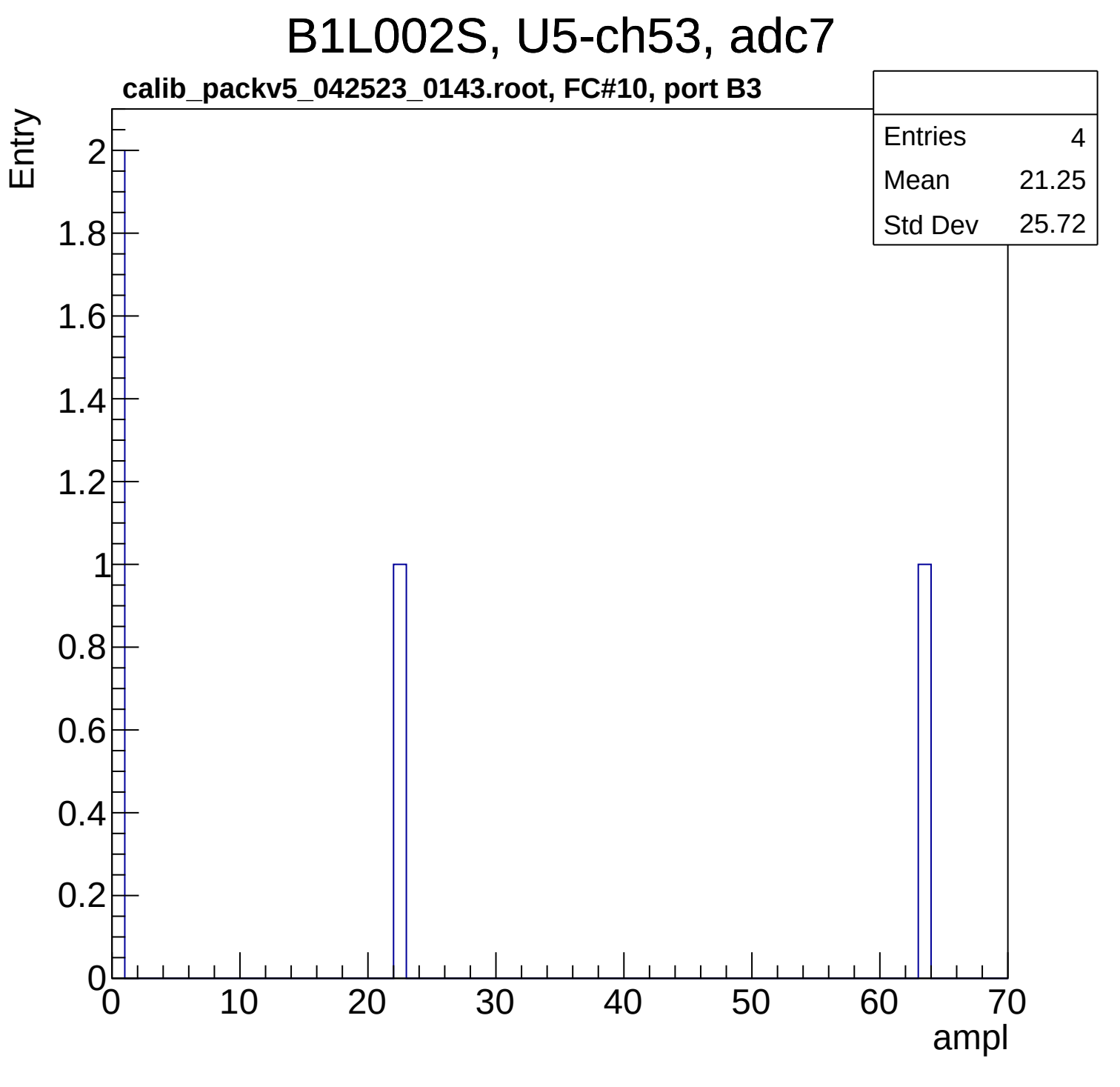
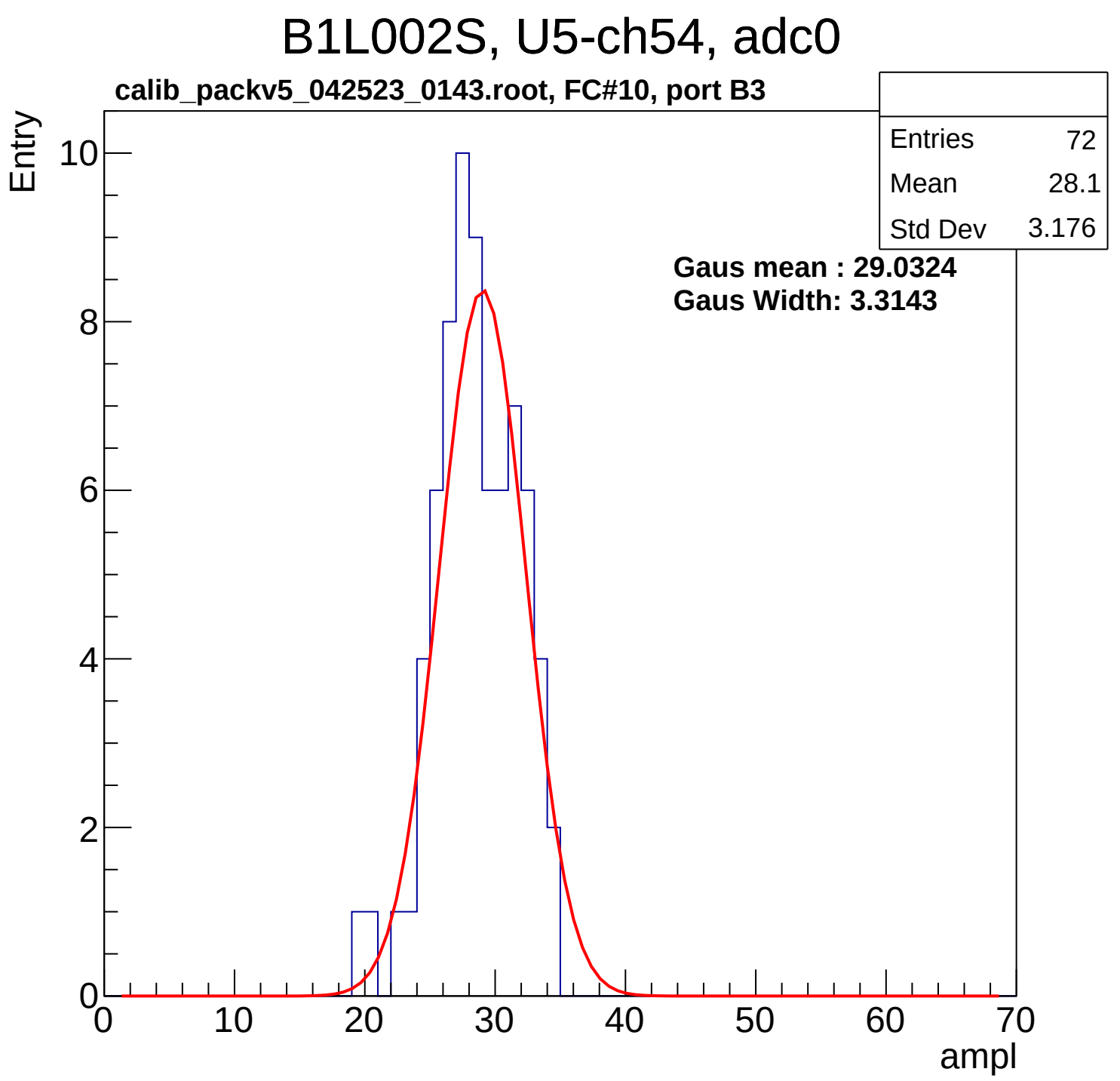
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.25
Std Dev 1.609
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21.25
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.1
Std Dev 3.176
Gaus mean : 29.0324
Gaus Width: 3.3143
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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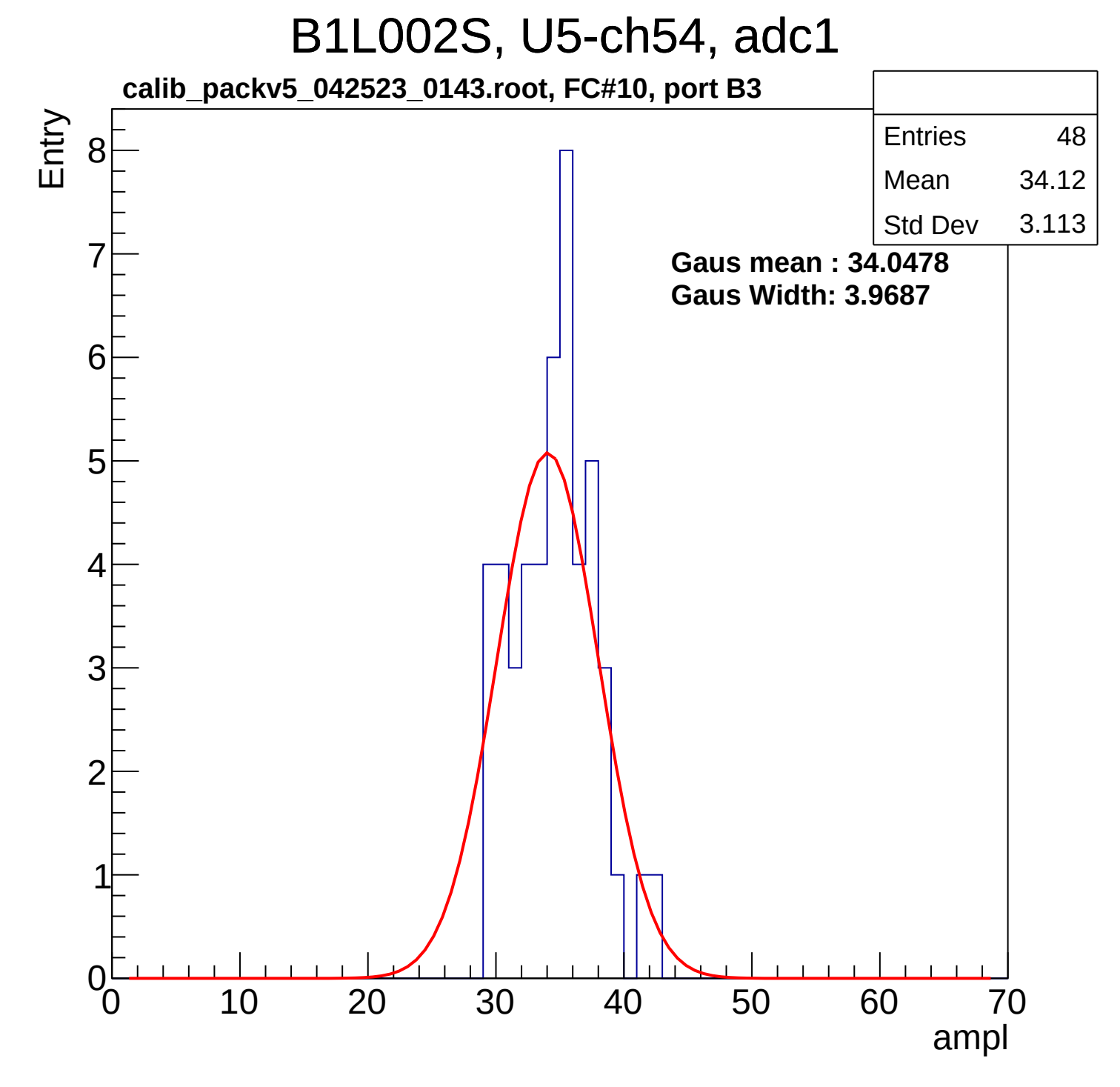
B1L002S, U5-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 34.12
Std Dev 3.113
Gaus mean : 34.0478
Gaus Width: 3.9687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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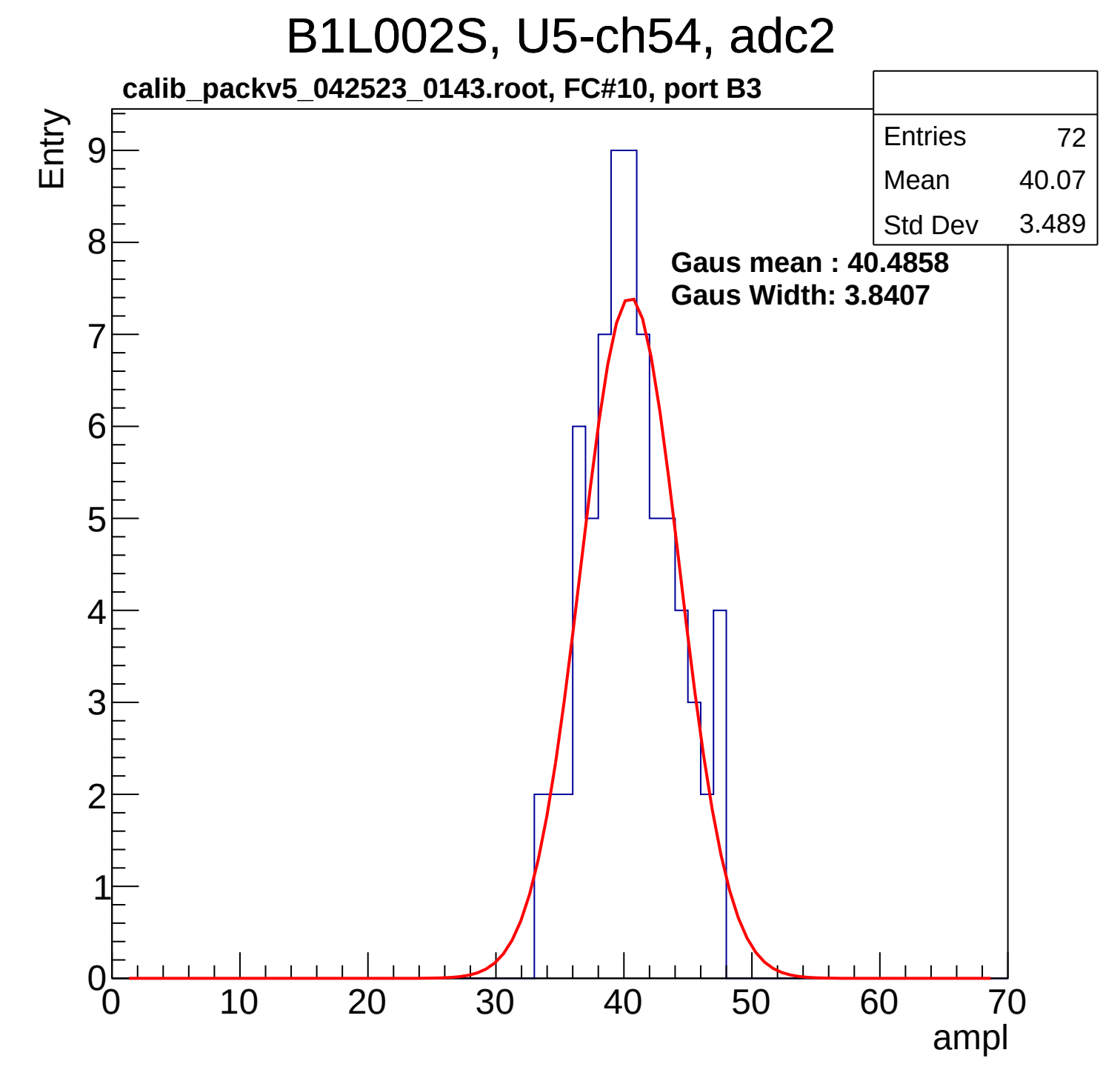
B1L002S, U5-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 40.07
Std Dev 3.489
Gaus mean : 40.4858
Gaus Width: 3.8407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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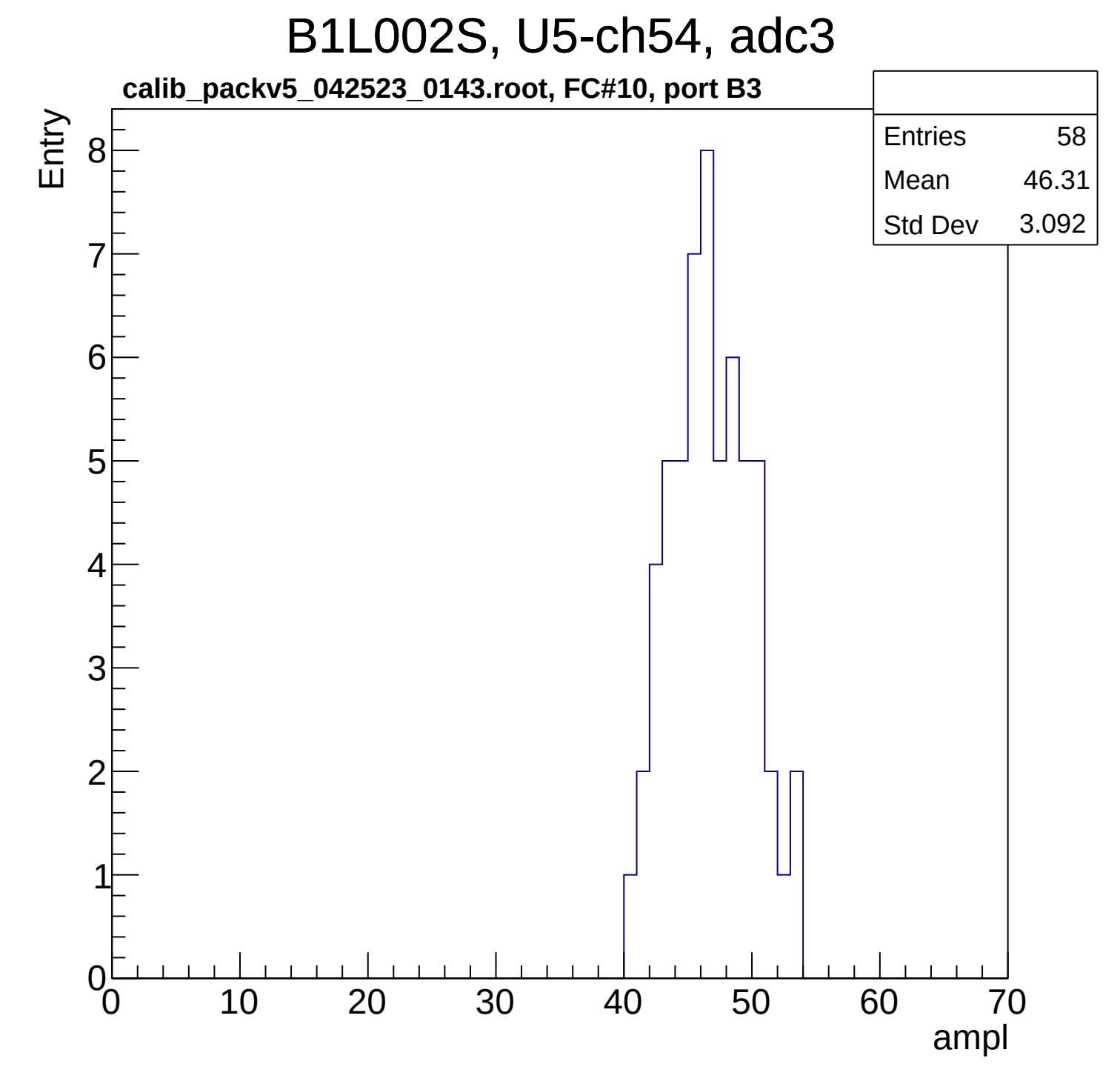
B1L002S, U5-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.31
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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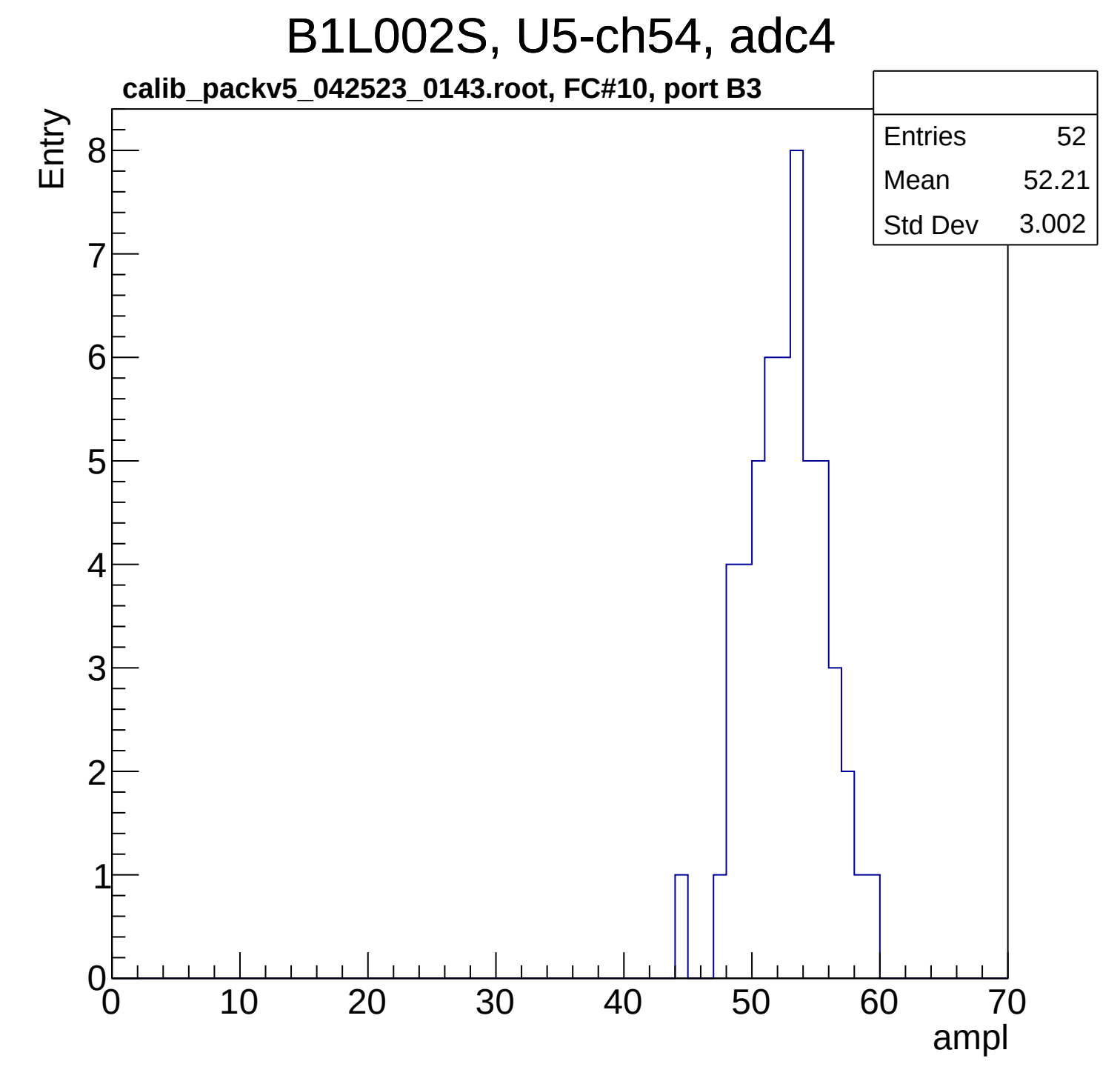
B1L002S, U5-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.21
Std Dev 3.002
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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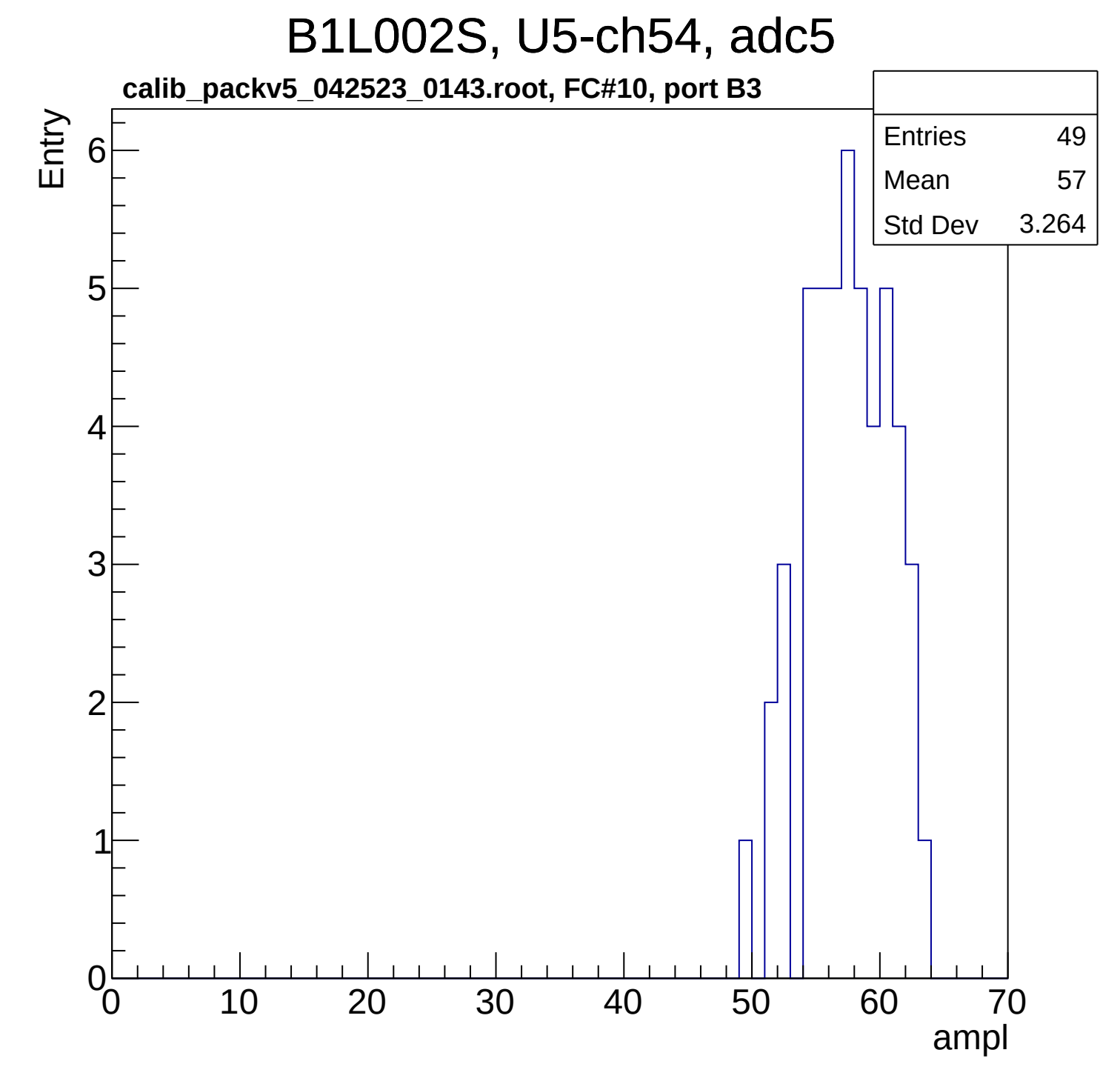
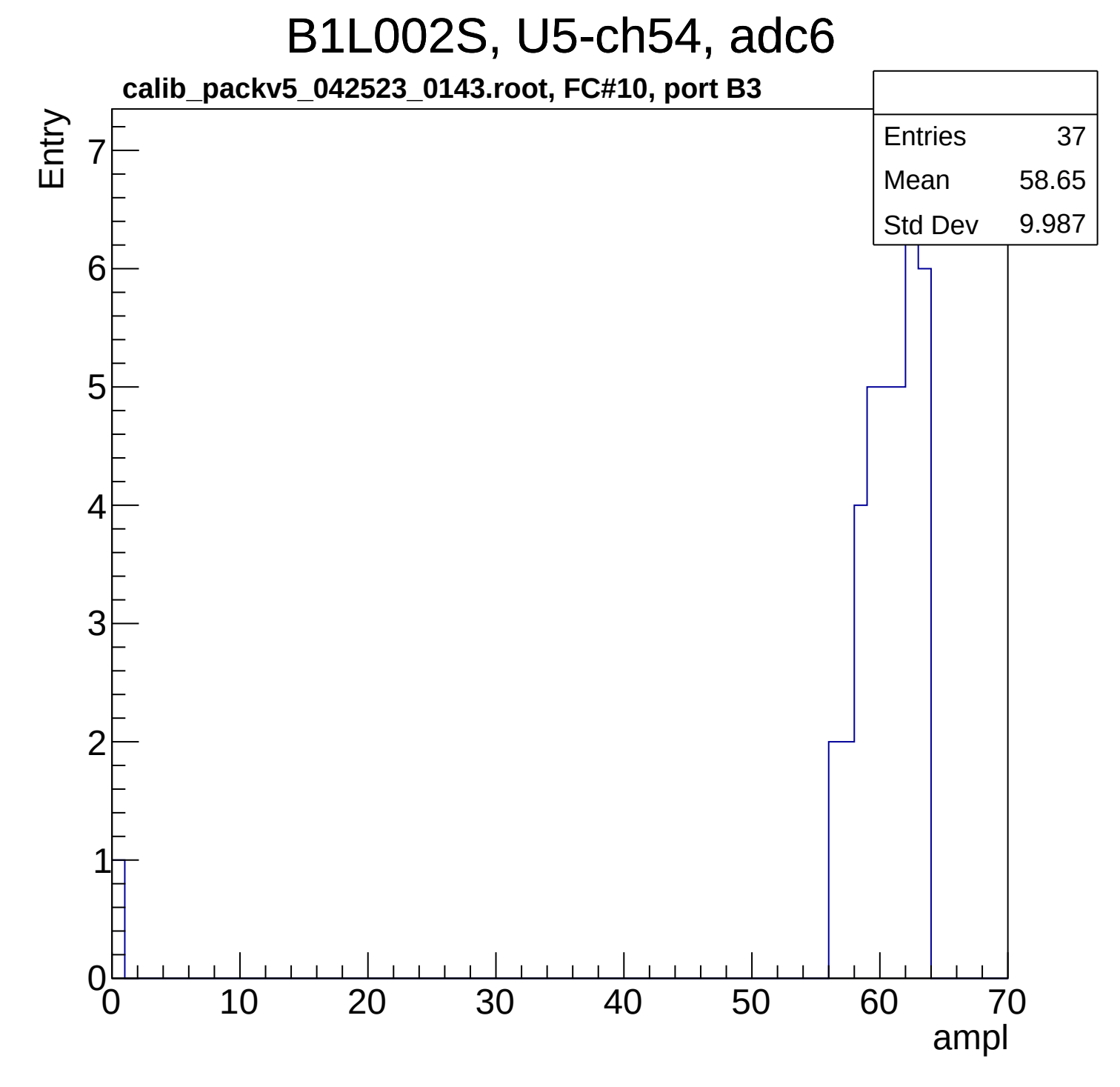

B1L002S, U5-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57
Std Dev 3.264
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.65
Std Dev 9.987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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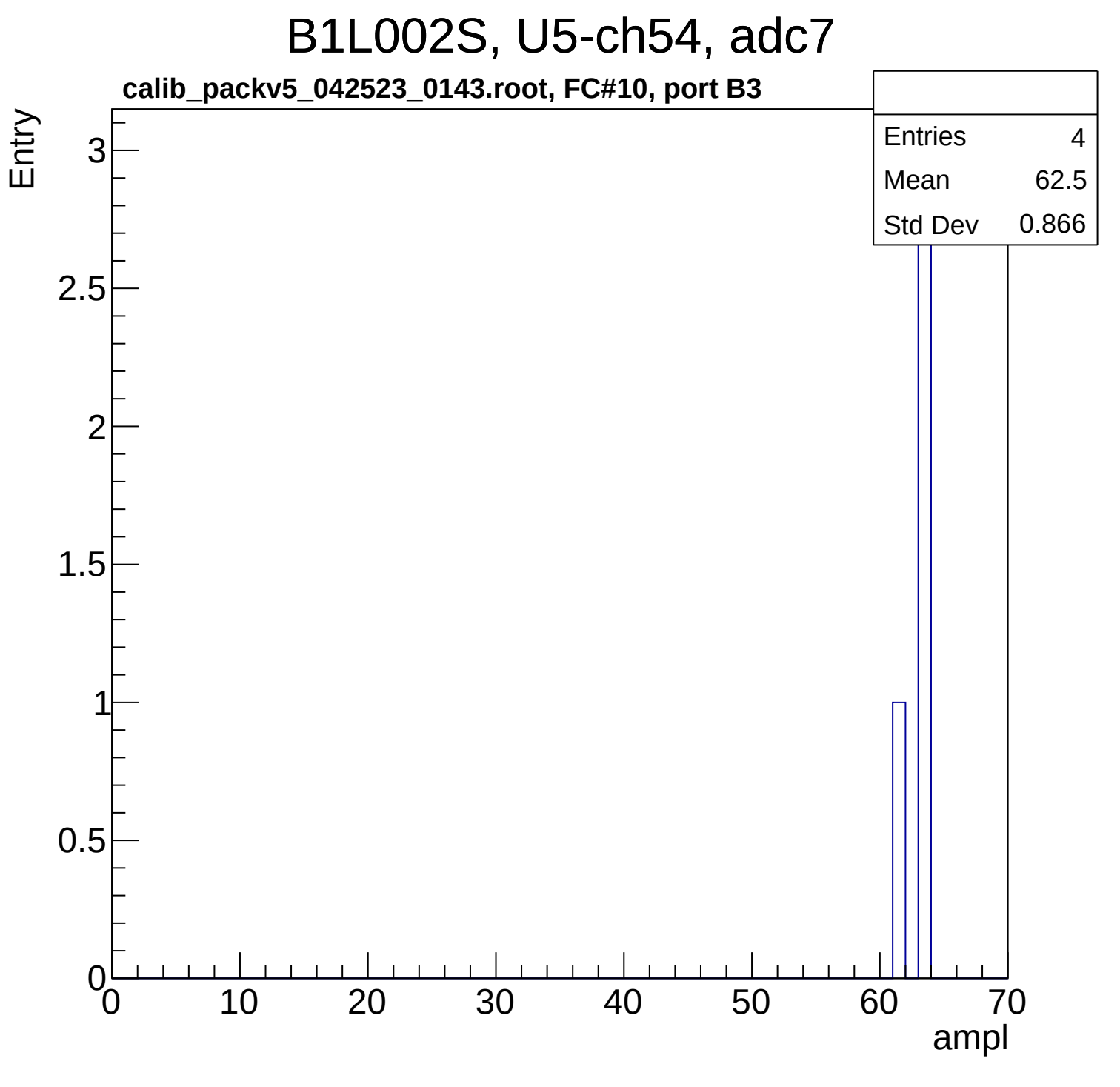
B1L002S, U5-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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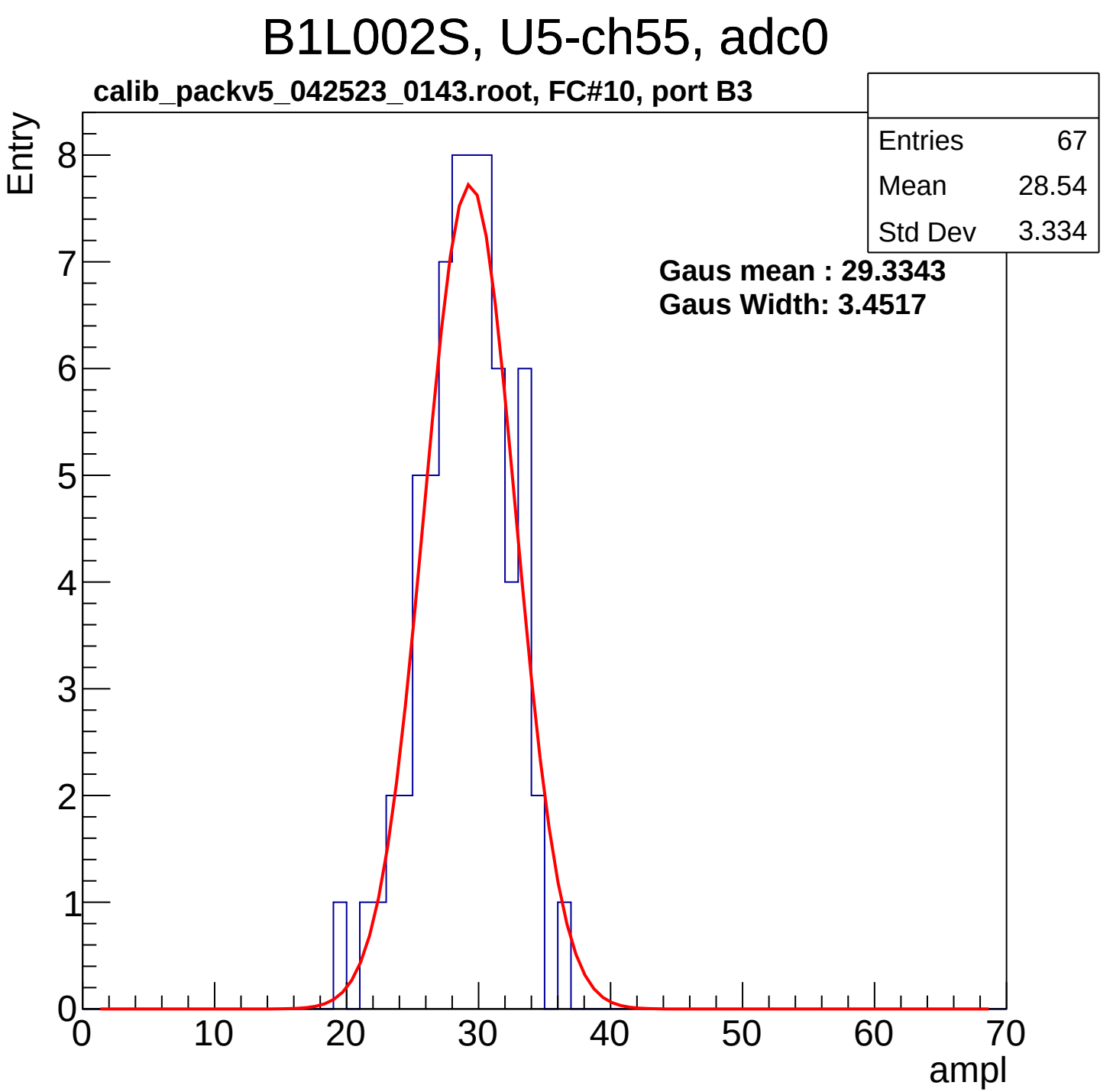
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U5-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.54
Std Dev 3.334
Gaus mean : 29.3343
Gaus Width: 3.4517
Entry
ampl
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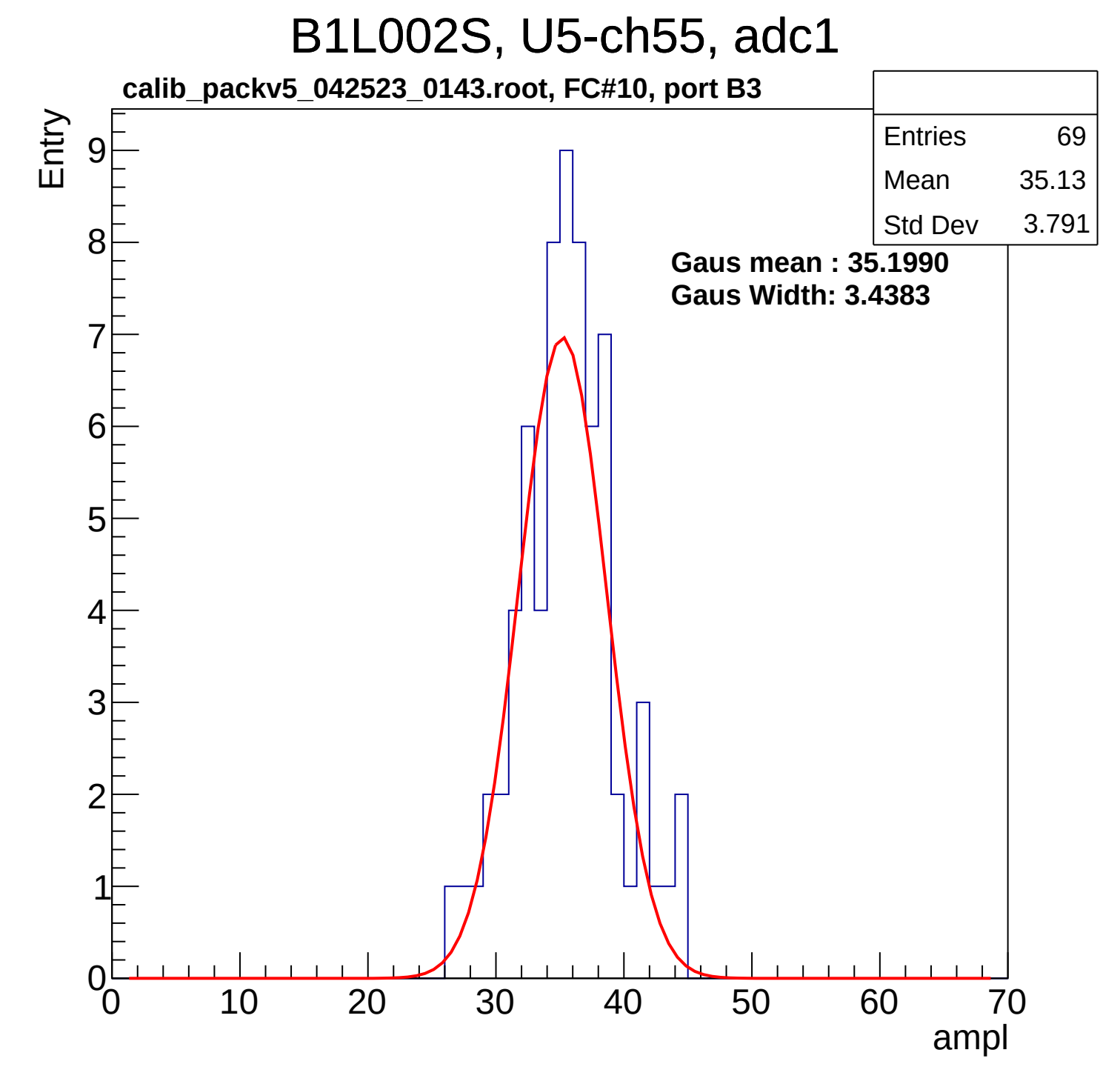
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.13
Std Dev 3.791
Gaus mean : 35.1990
Gaus Width: 3.4383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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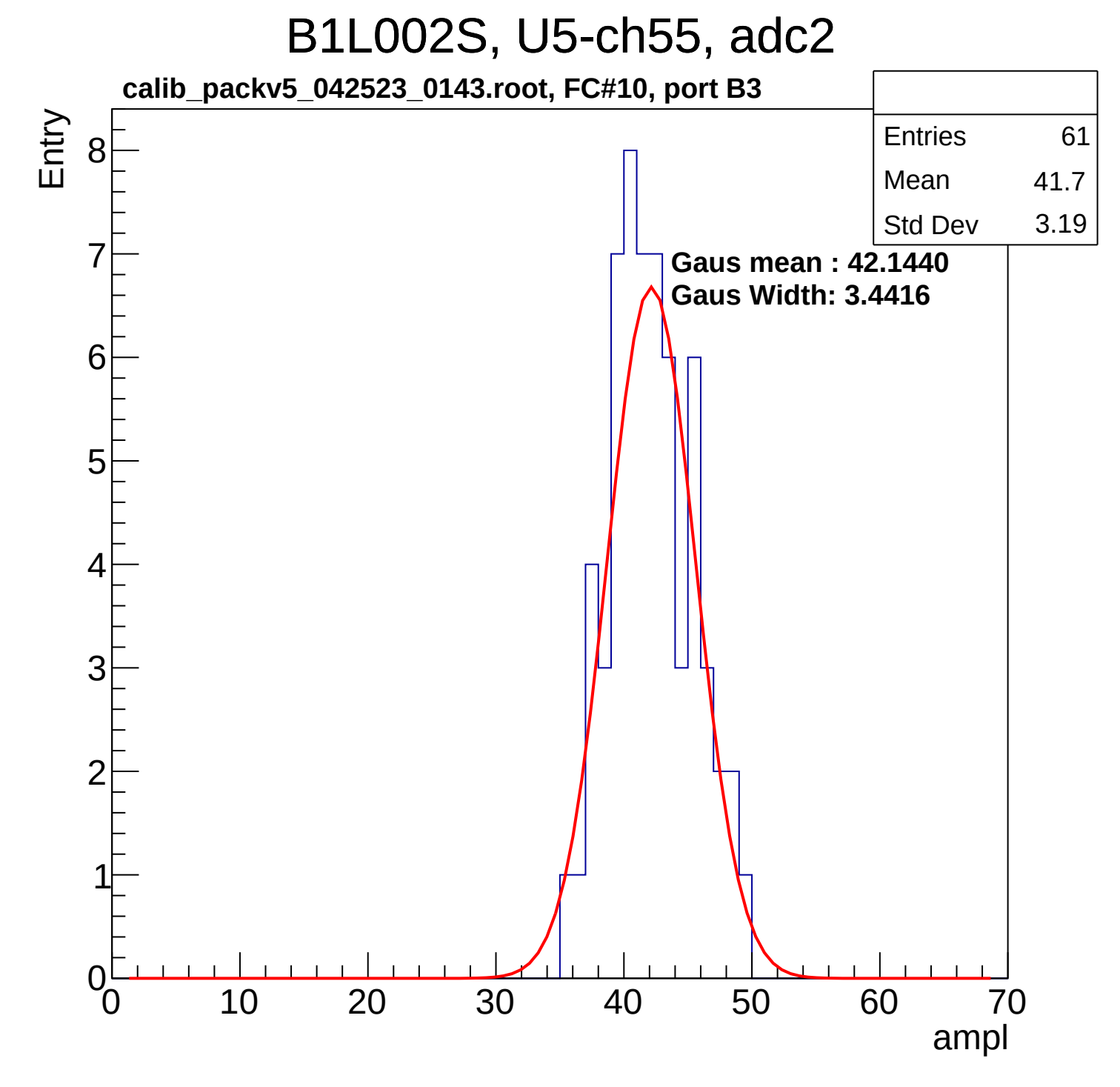
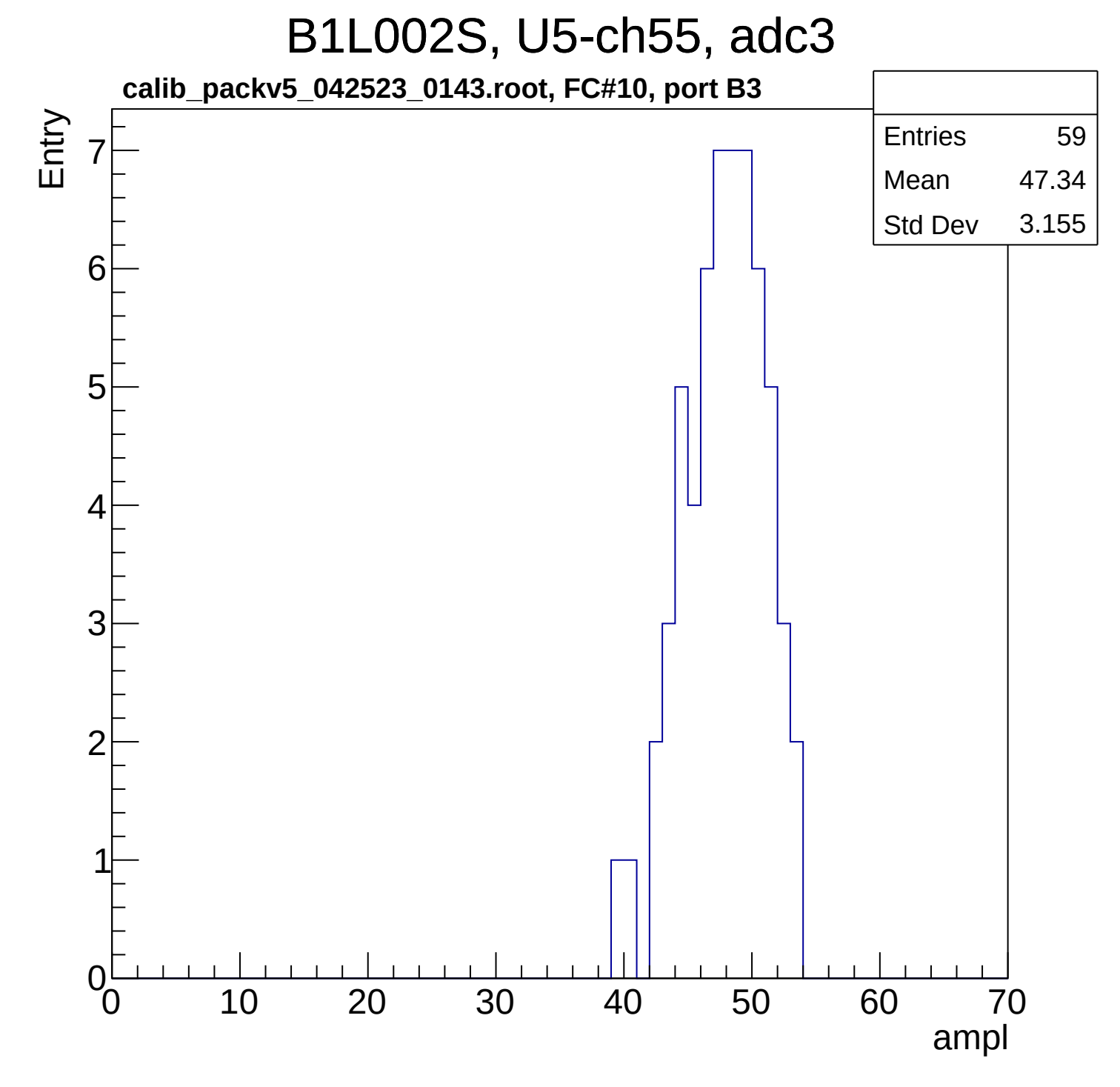

B1L002S, U5-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.7
Std Dev 3.19
Gaus mean : 42.1440
Gaus Width: 3.4416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.34
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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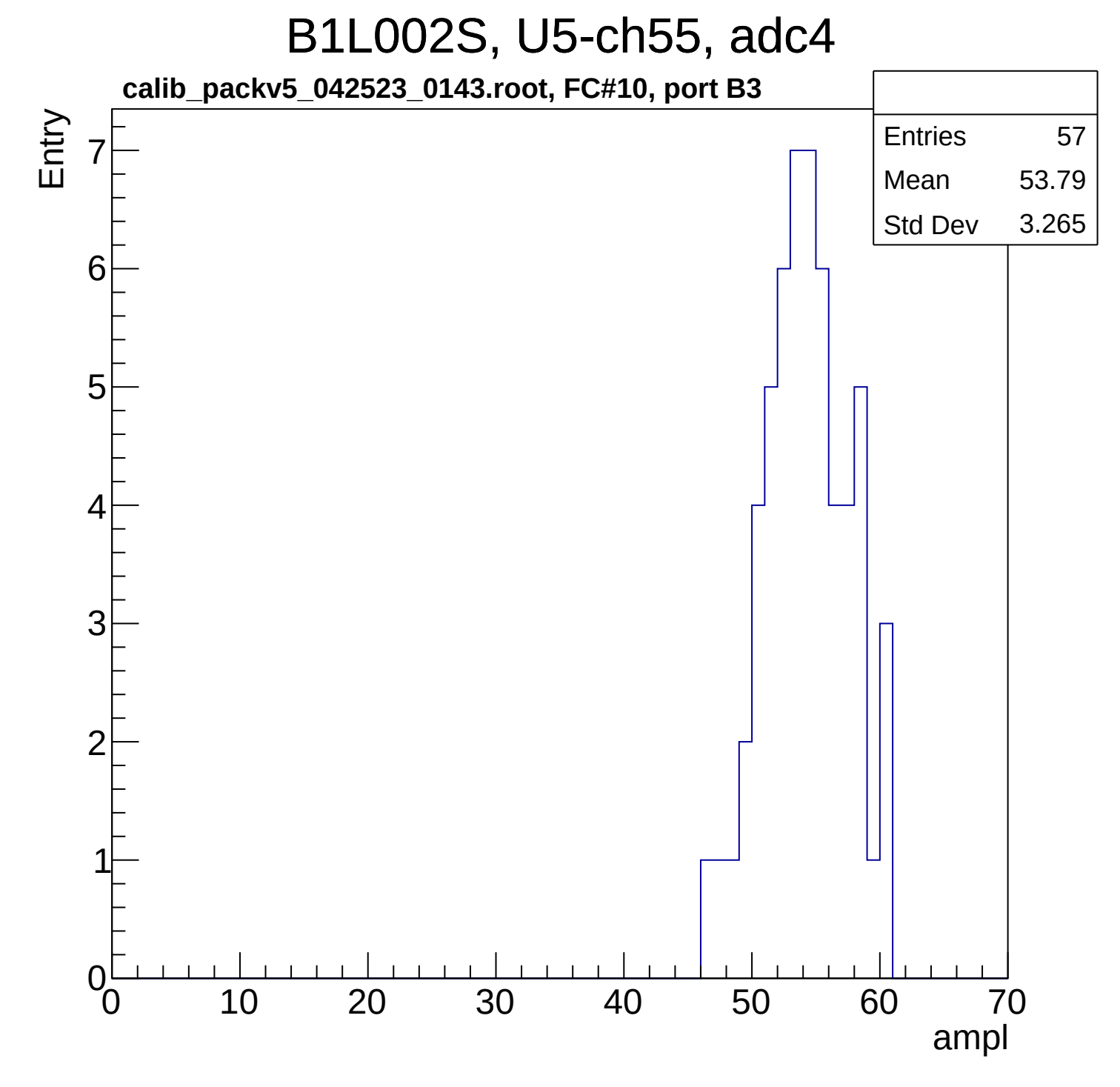
B1L002S, U5-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.79
Std Dev 3.265
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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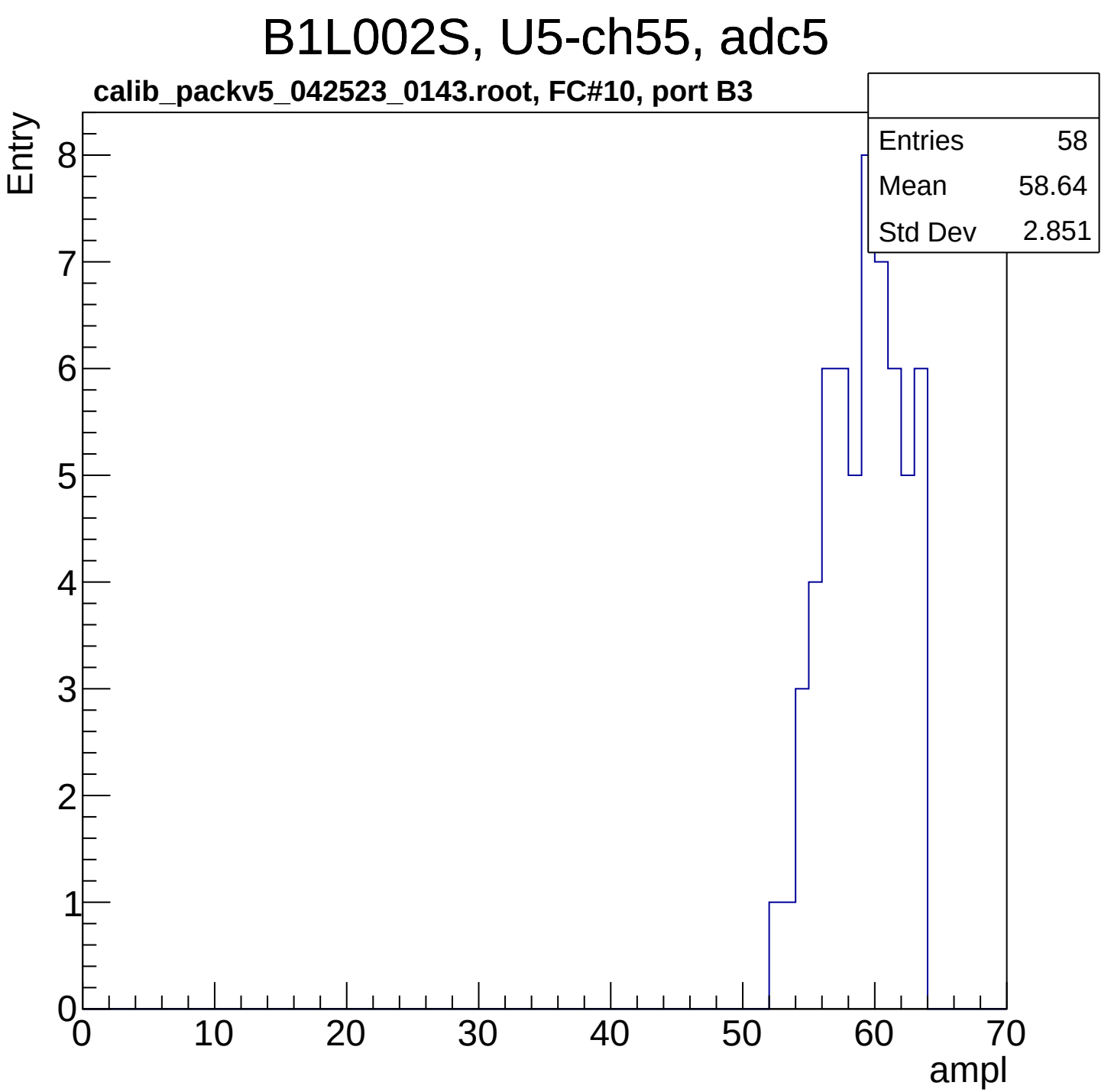
B1L002S, U5-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.64
Std Dev 2.851
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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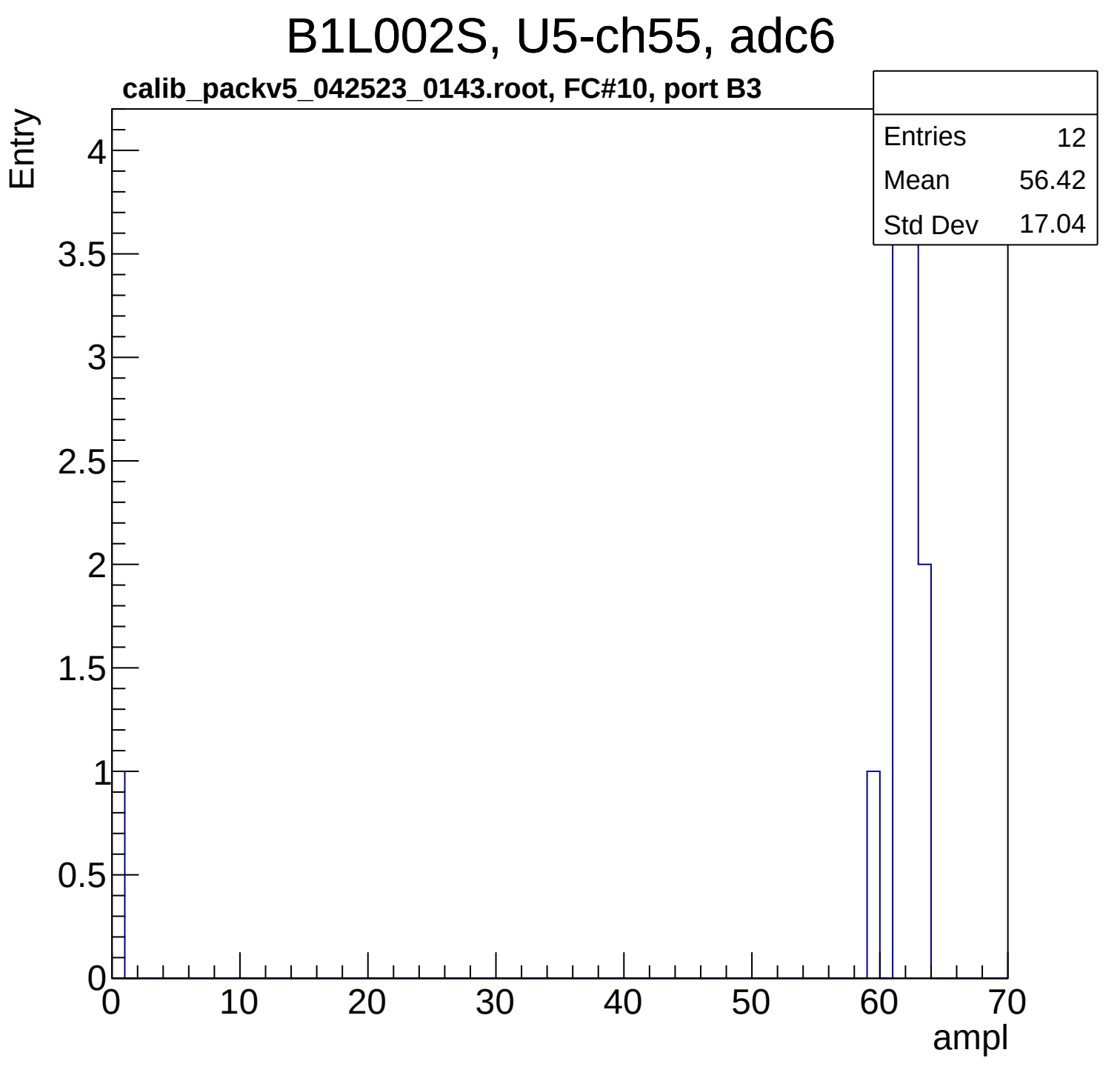
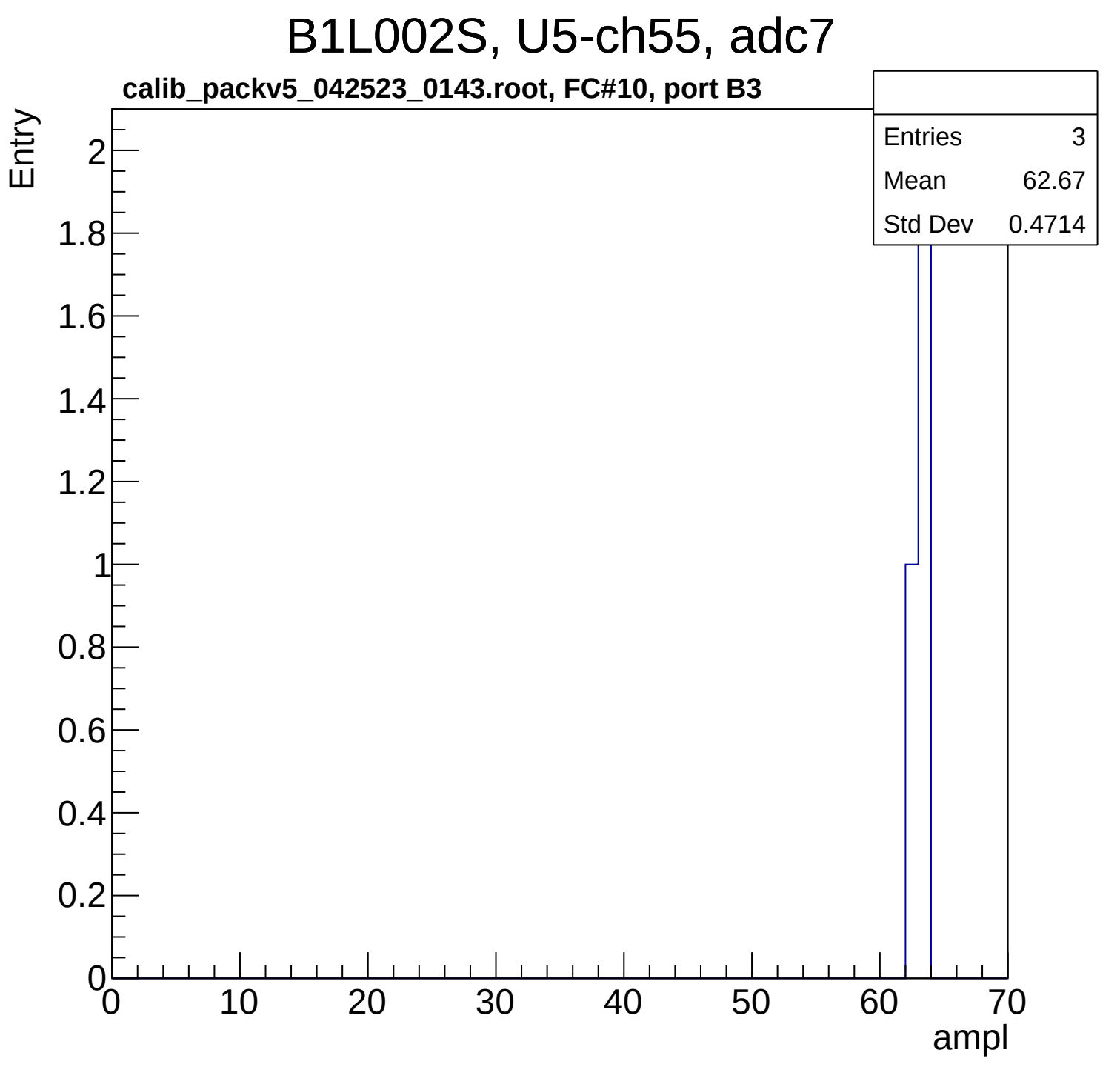
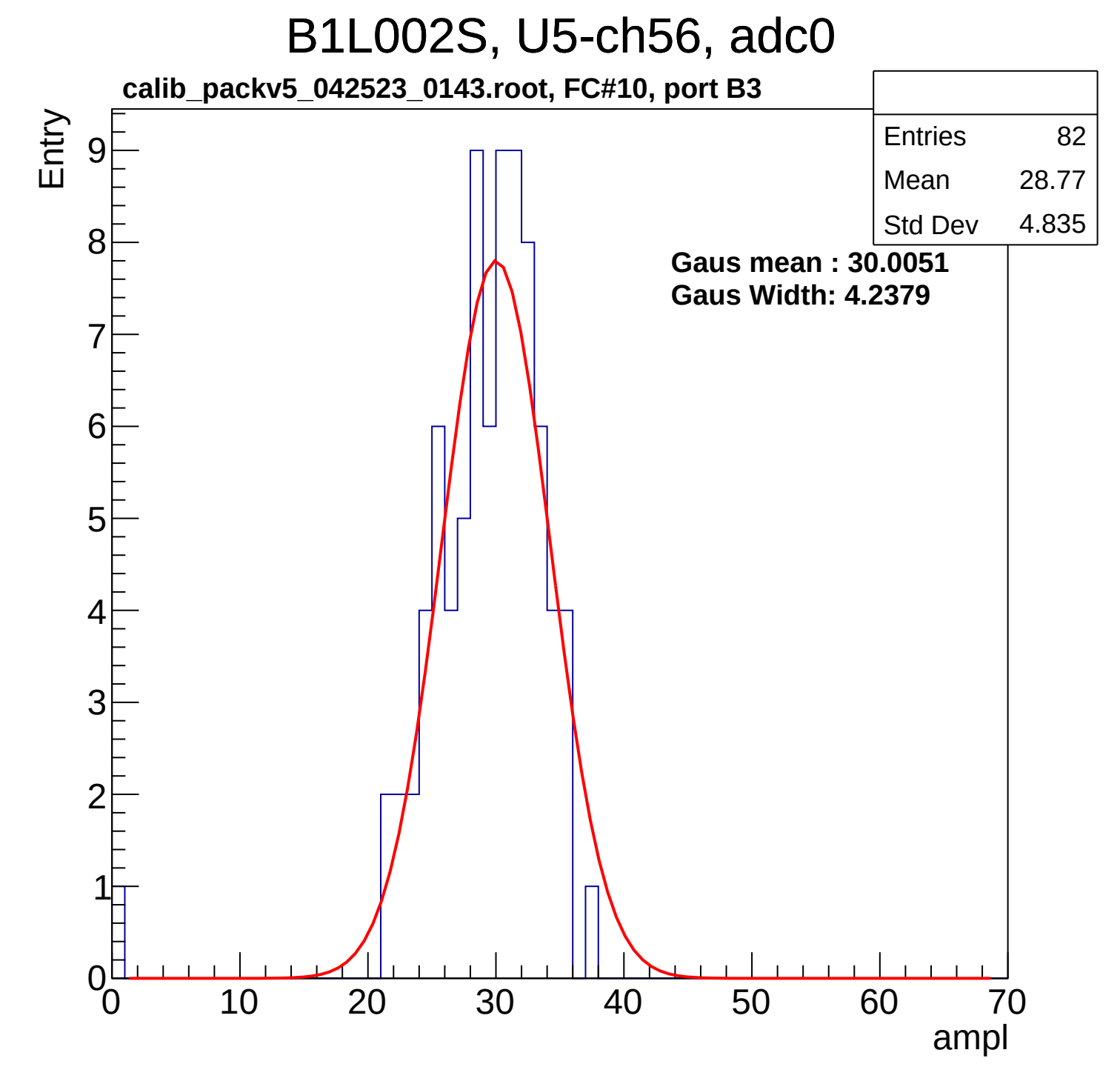

B1L002S, U5-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.42
Std Dev 17.04
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U5-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.77
Std Dev 4.835
Gaus mean : 30.0051
Gaus Width: 4.2379
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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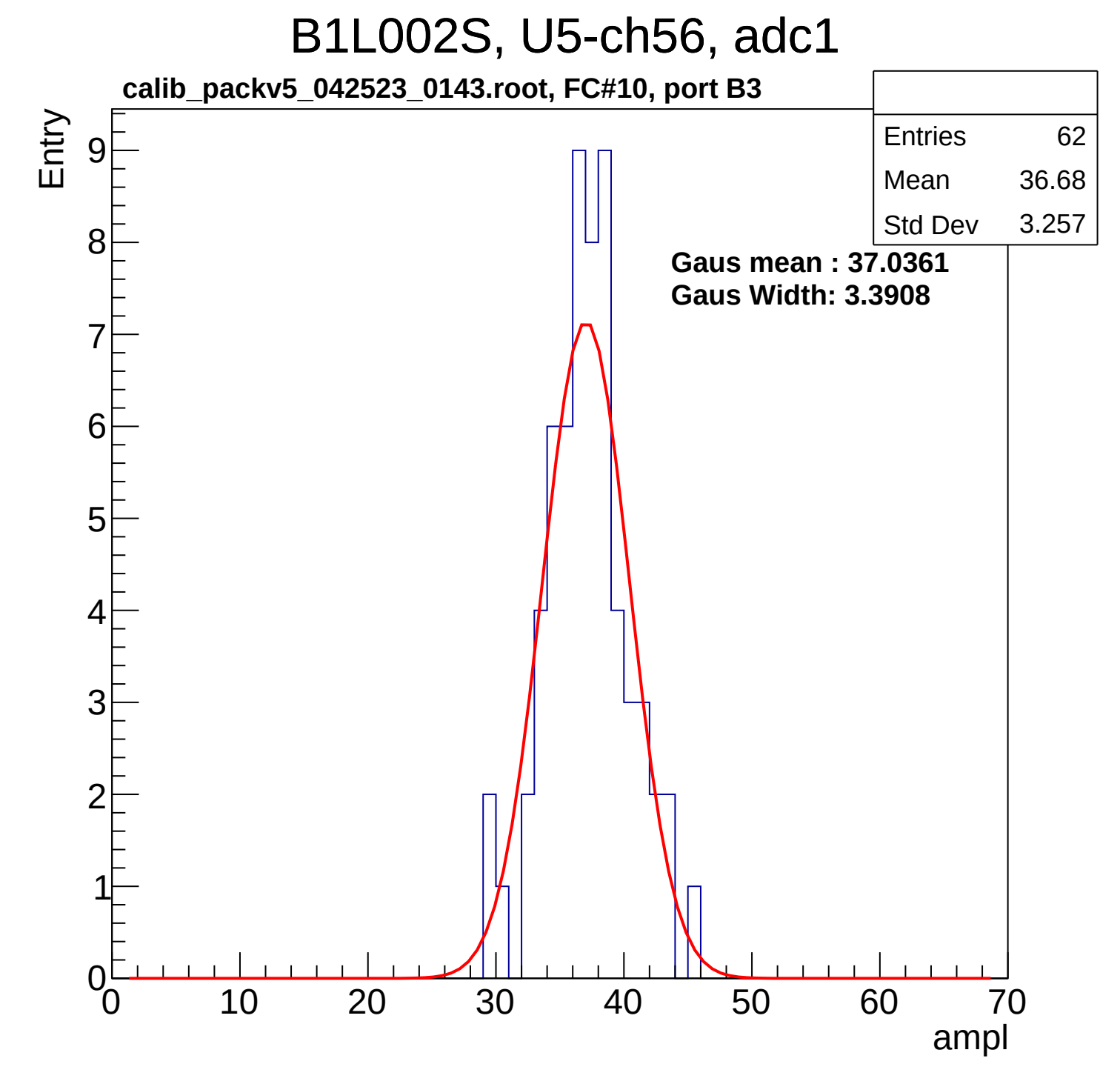
60
70


B1L002S, U5-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 36.68
Std Dev 3.257
Gaus mean : 37.0361
Gaus Width: 3.3908
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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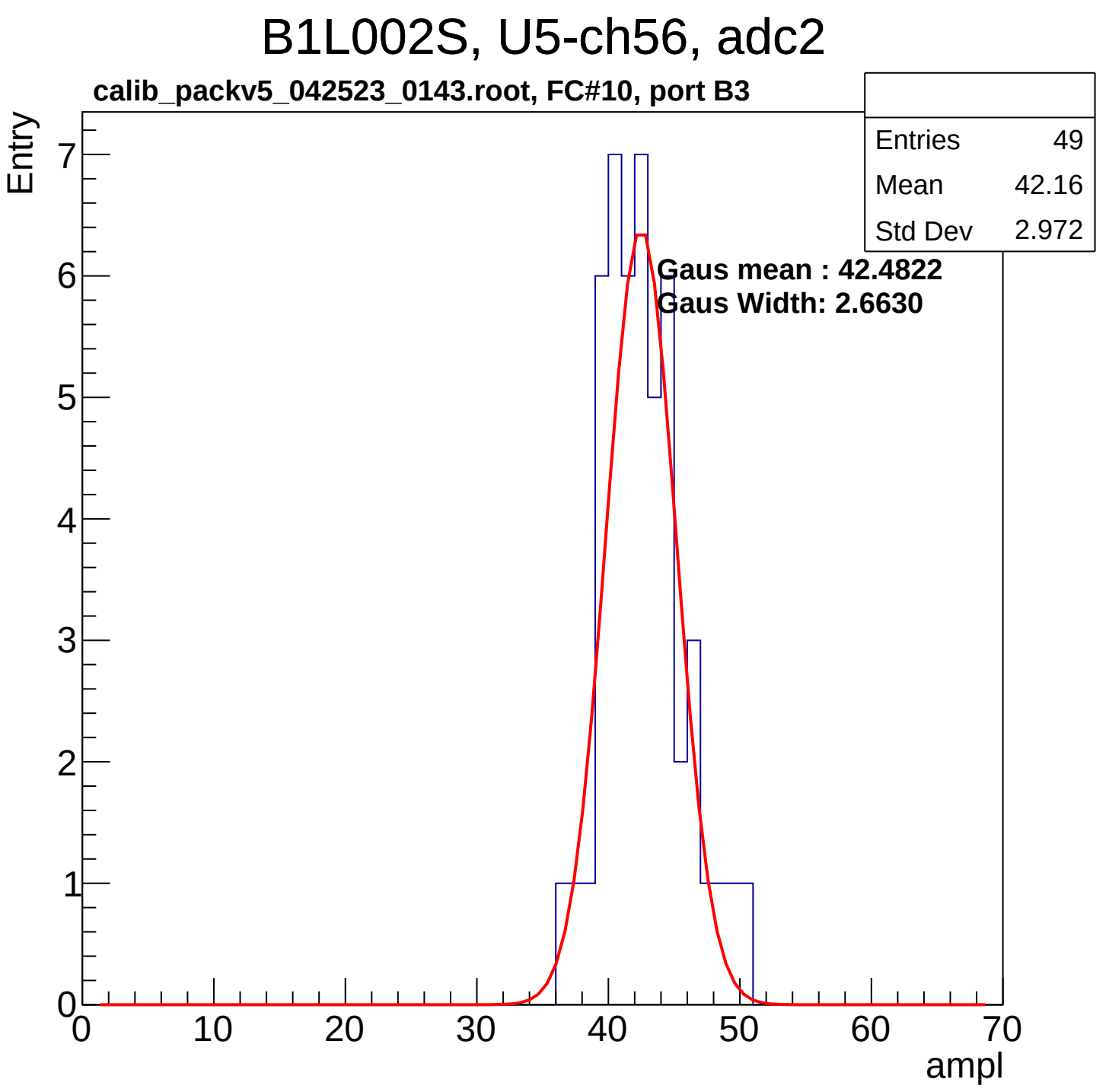
B1L002S, U5-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 42.16
Std Dev 2.972
Gaus mean : 42.4822
Gaus Width: 2.6630
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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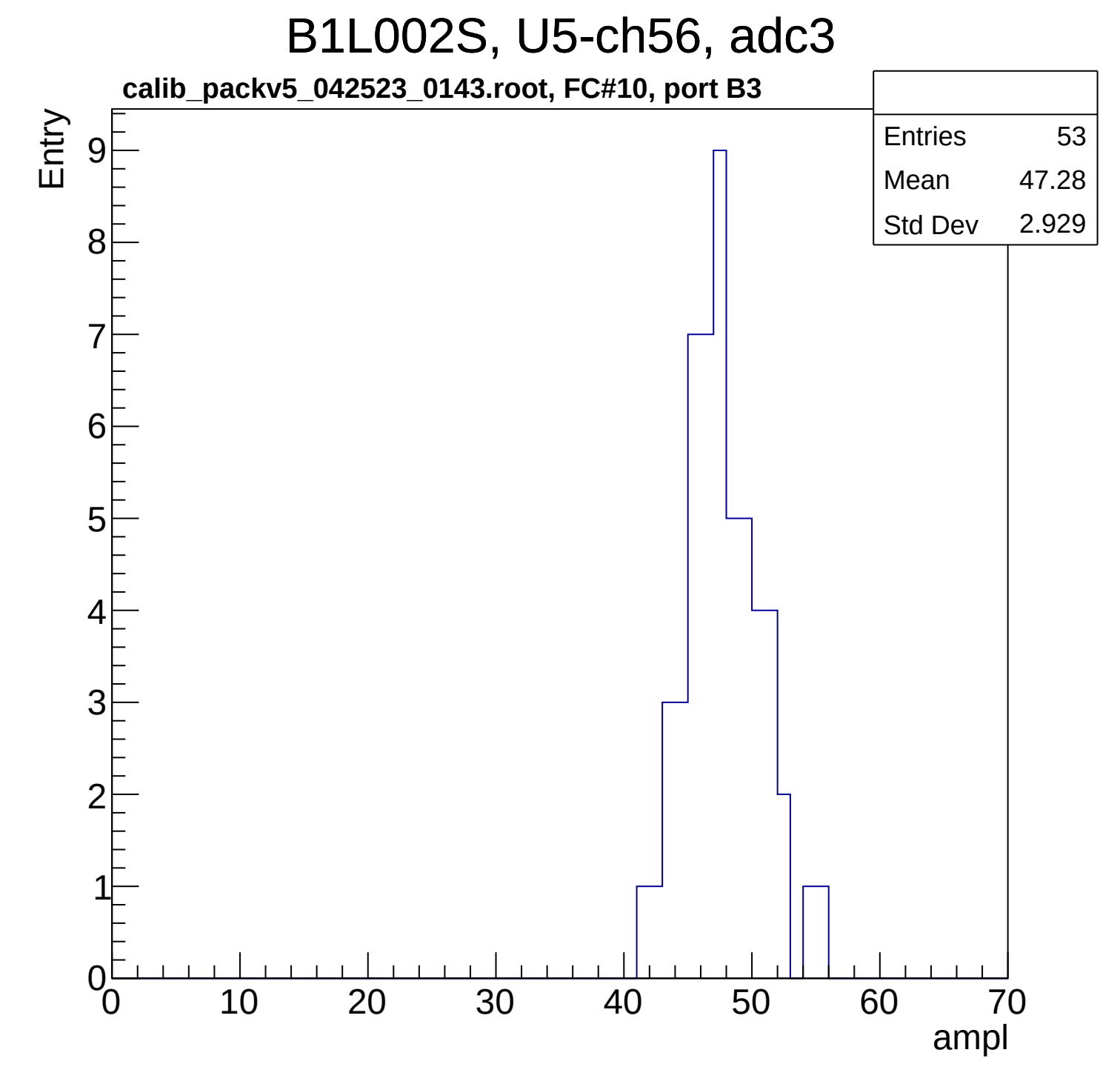
B1L002S, U5-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.28
Std Dev 2.929
Entry
ampl
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3
4
5
6
7
8
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0
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20
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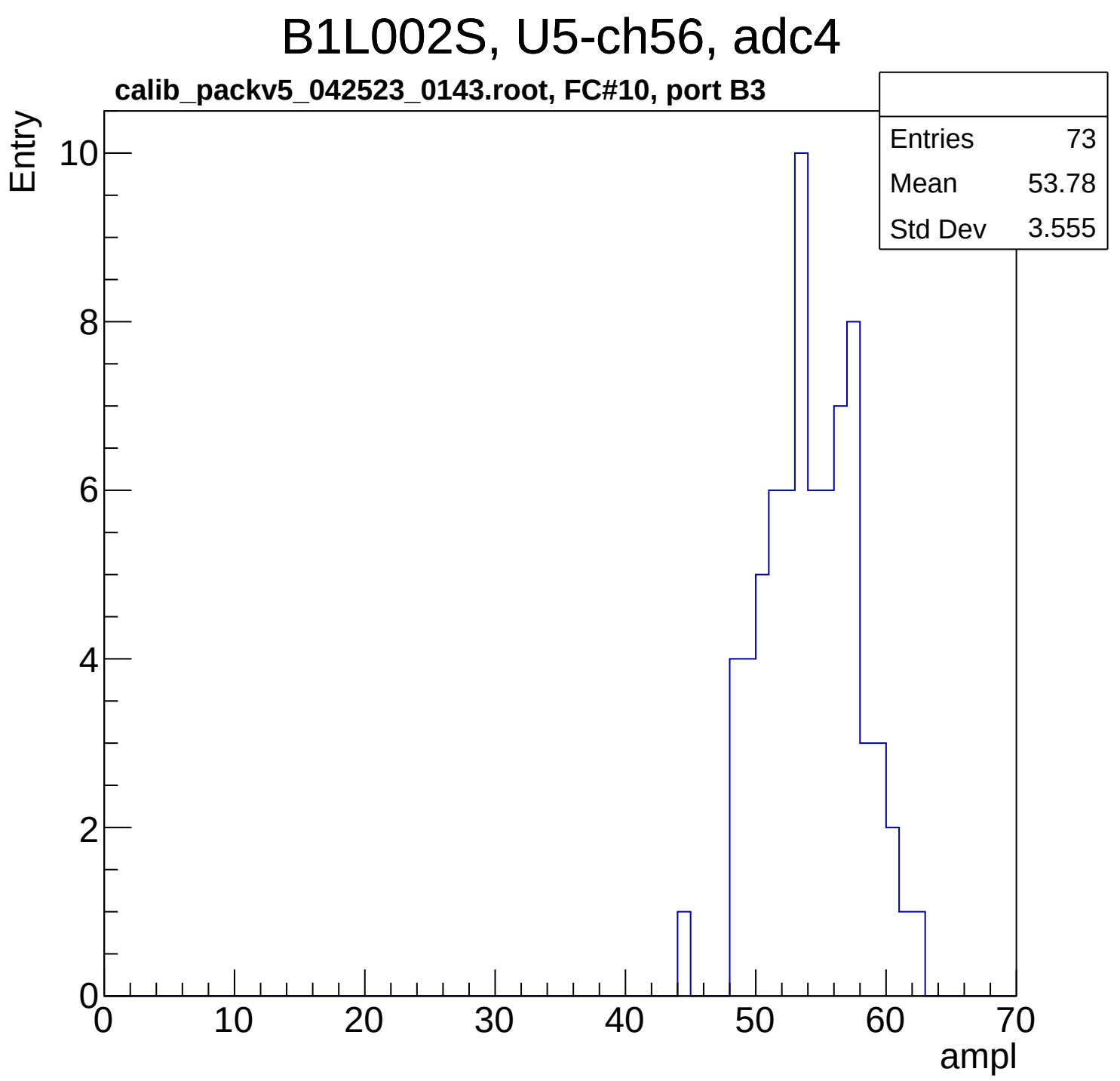
40
50
60
70


B1L002S, U5-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 53.78
Std Dev 3.555
Entry
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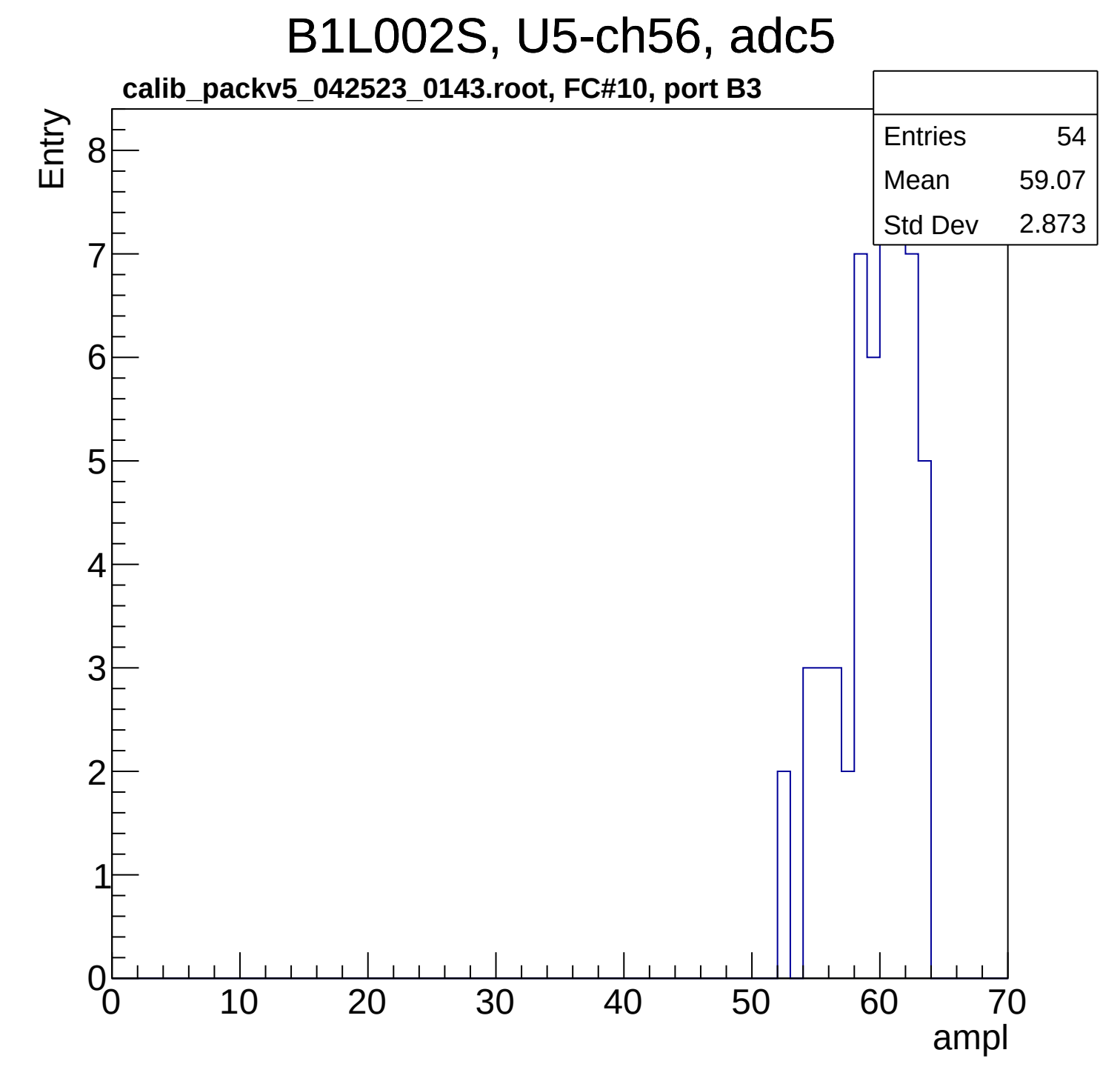
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.07
Std Dev 2.873
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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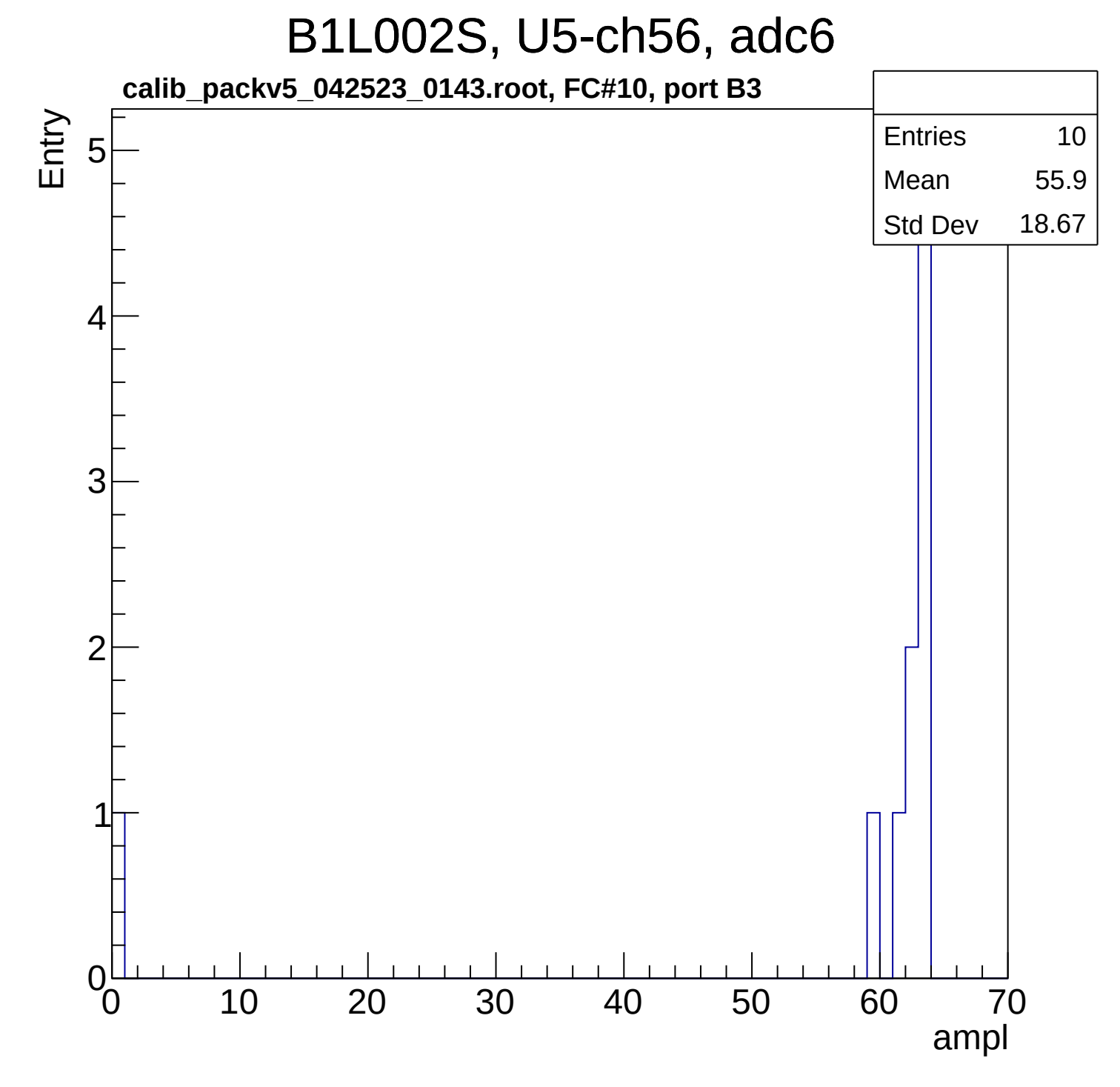
B1L002S, U5-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.9
Std Dev 18.67
Entry
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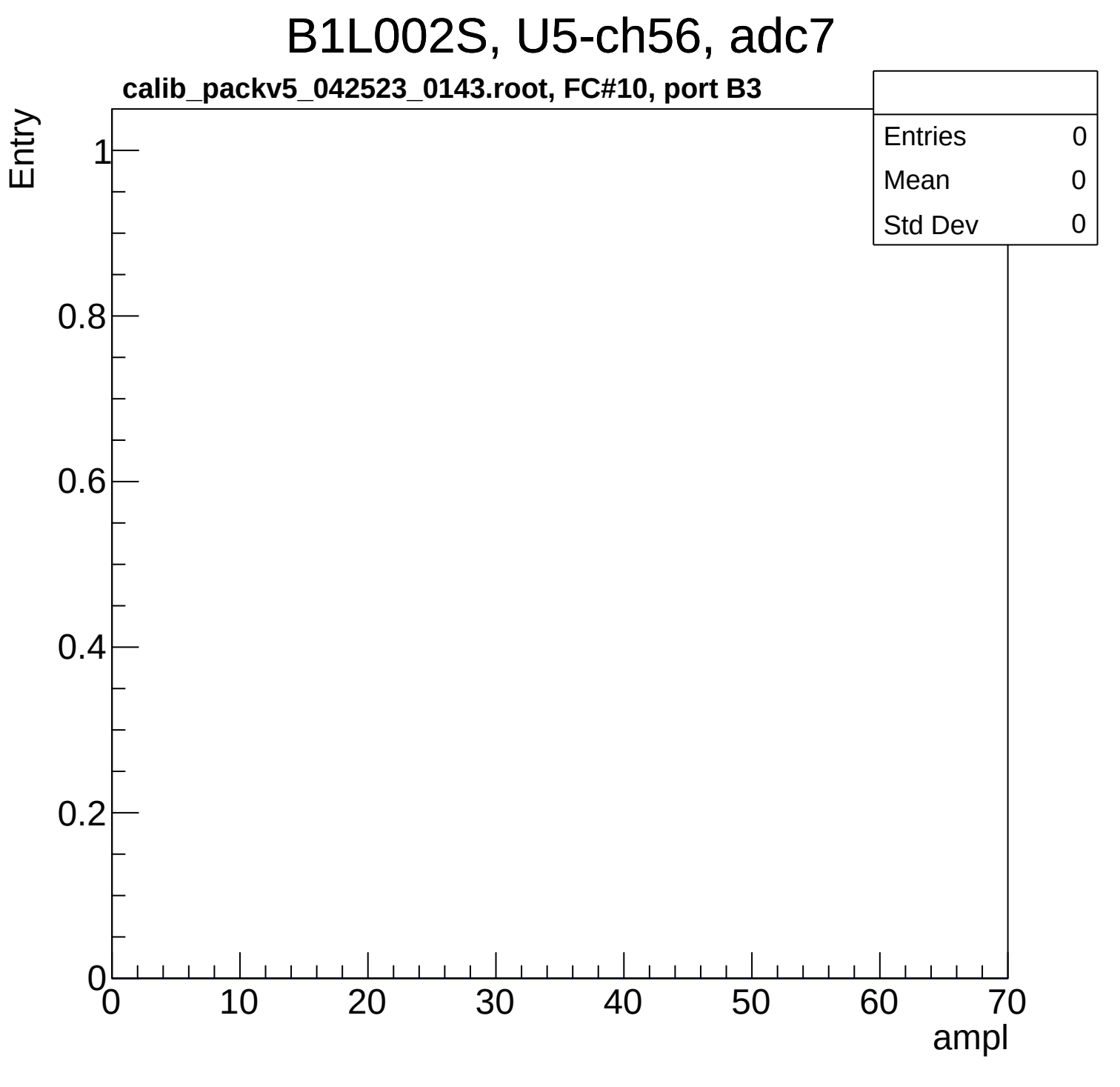
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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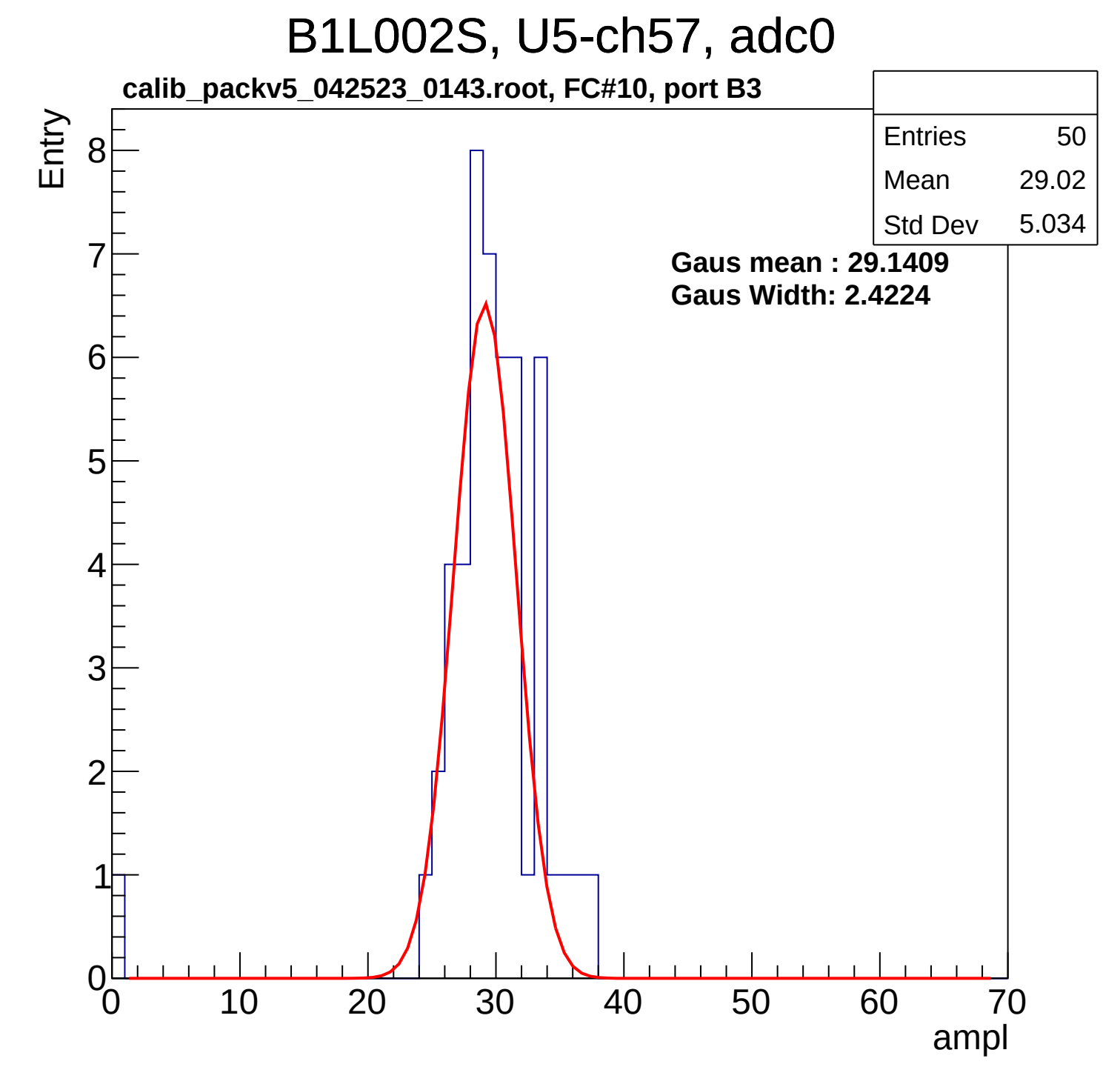
40
50
60
70
ampl


B1L002S, U5-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 29.02
Std Dev 5.034
Gaus mean : 29.1409
Gaus Width: 2.4224
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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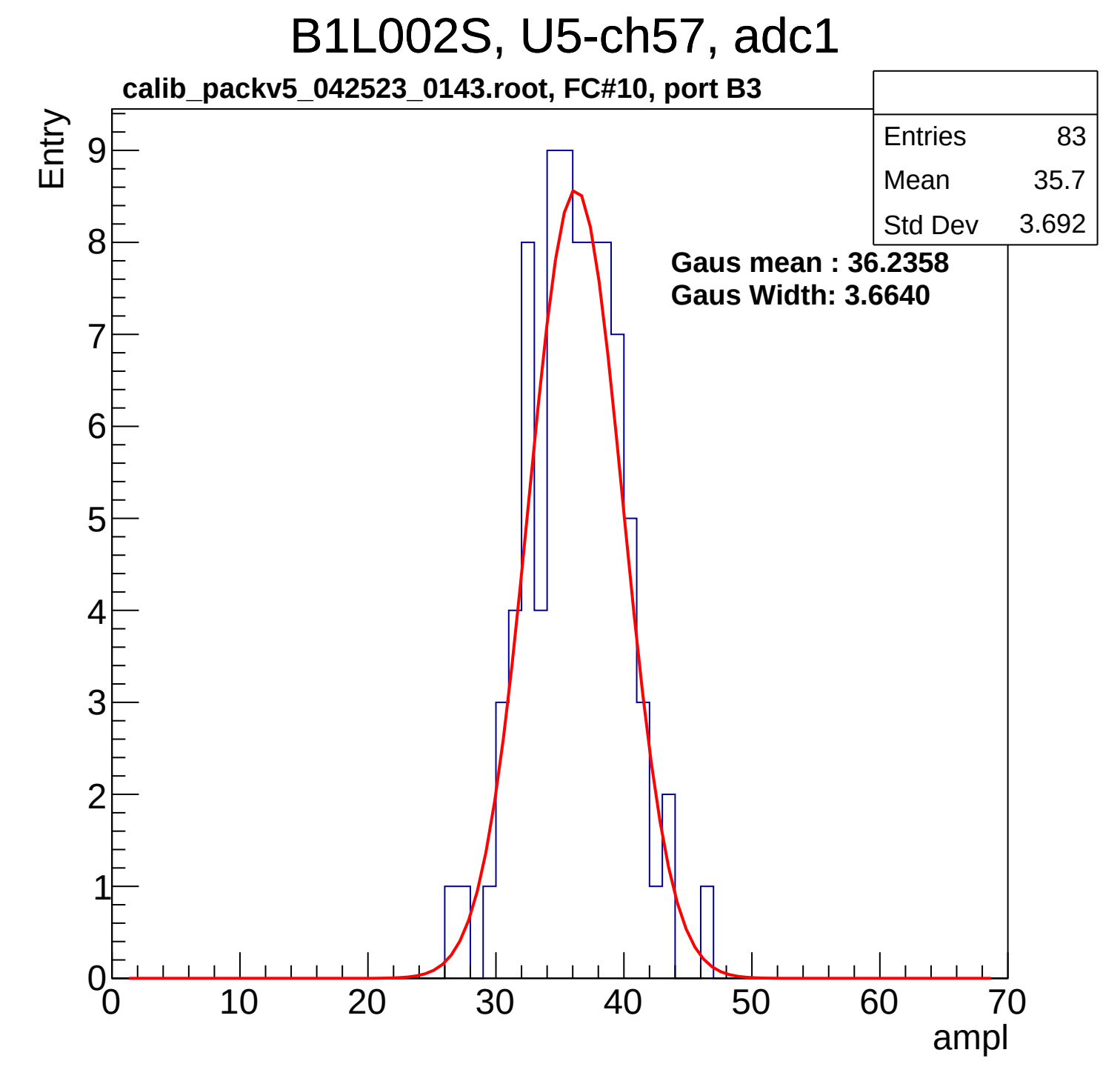
60
70


B1L002S, U5-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 35.7
Std Dev 3.692
Gaus mean : 36.2358
Gaus Width: 3.6640
Entry
ampl
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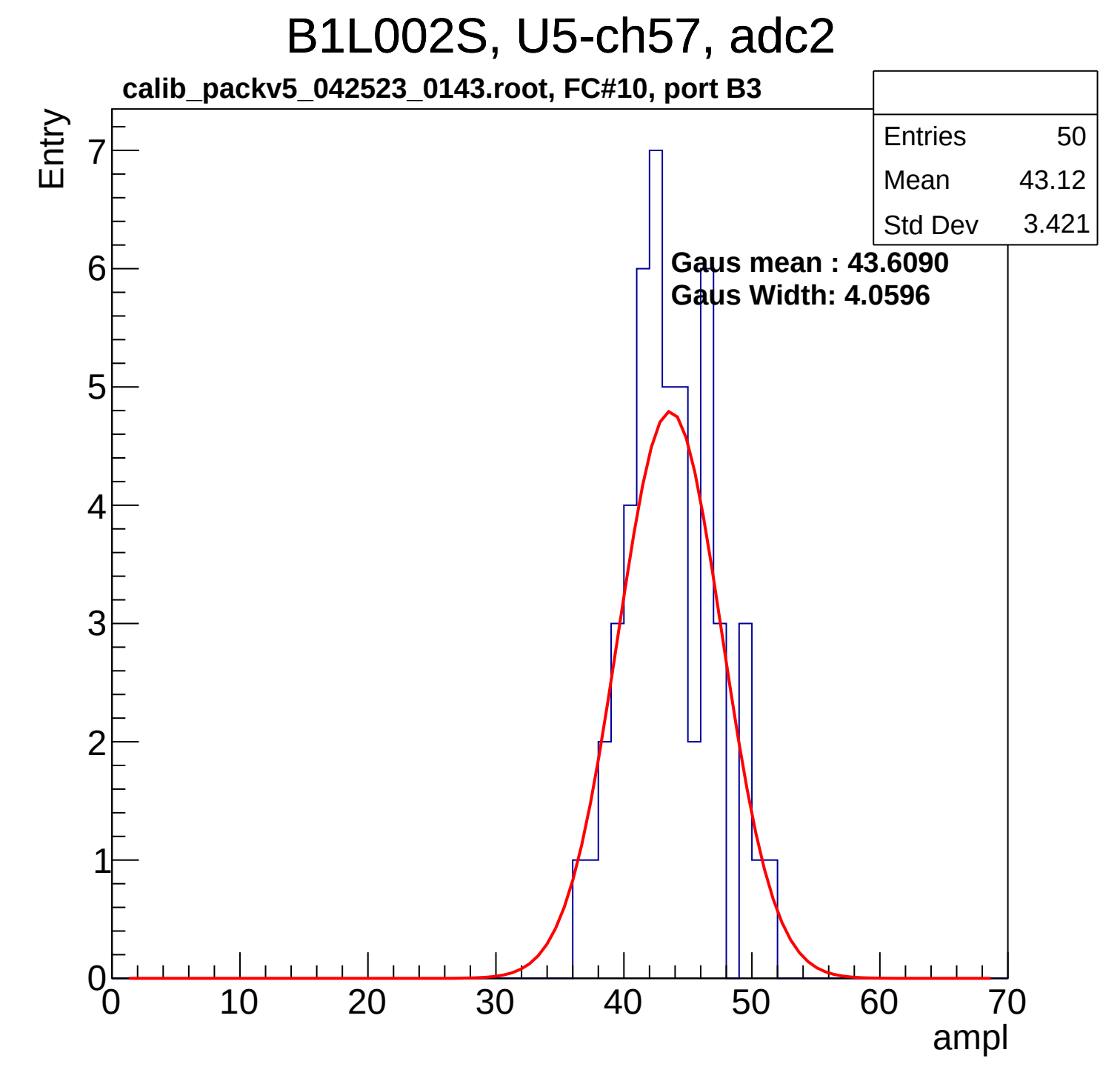
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 43.12
Std Dev 3.421
Gaus mean : 43.6090
Gaus Width: 4.0596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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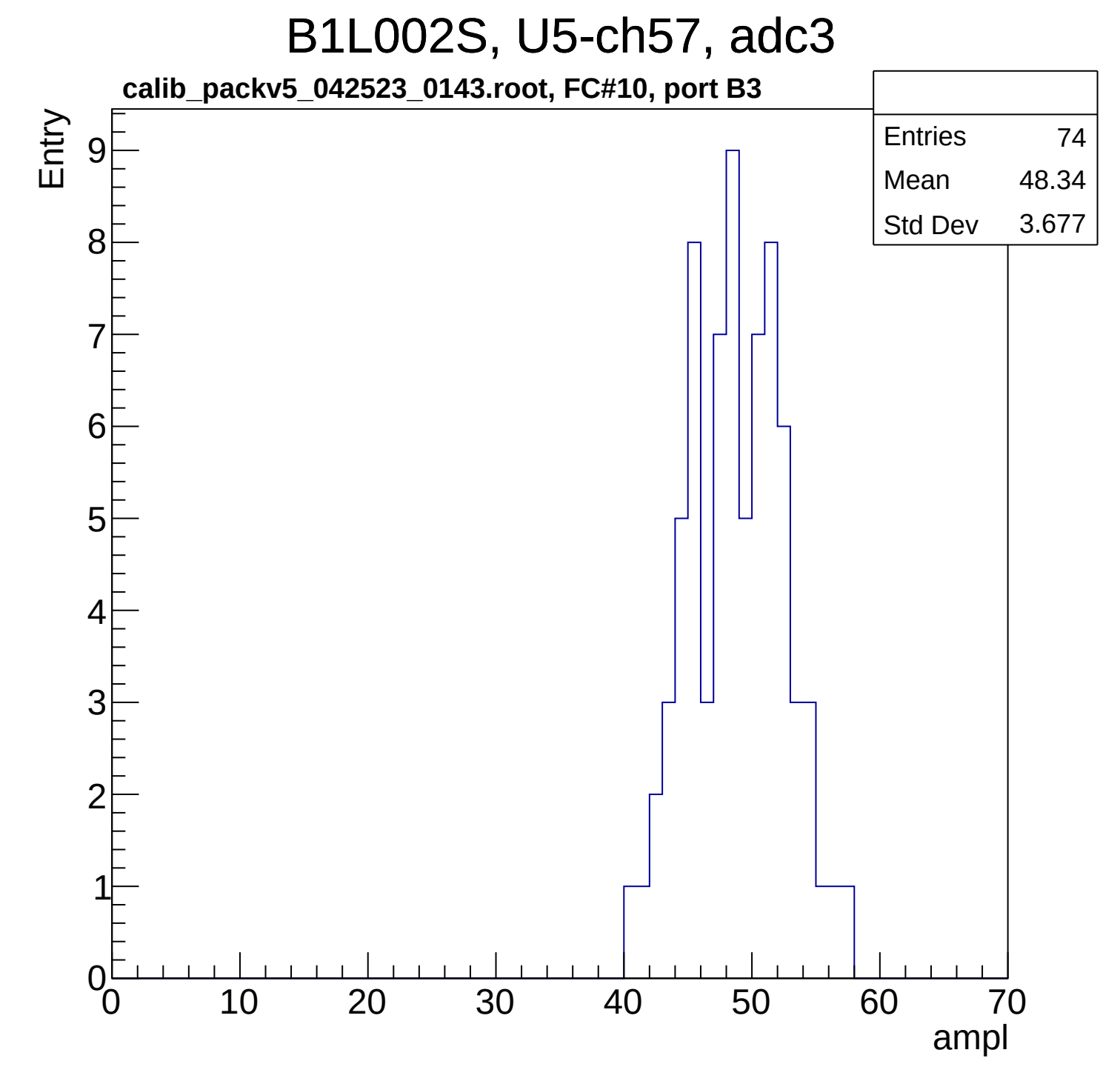
B1L002S, U5-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 48.34
Std Dev 3.677
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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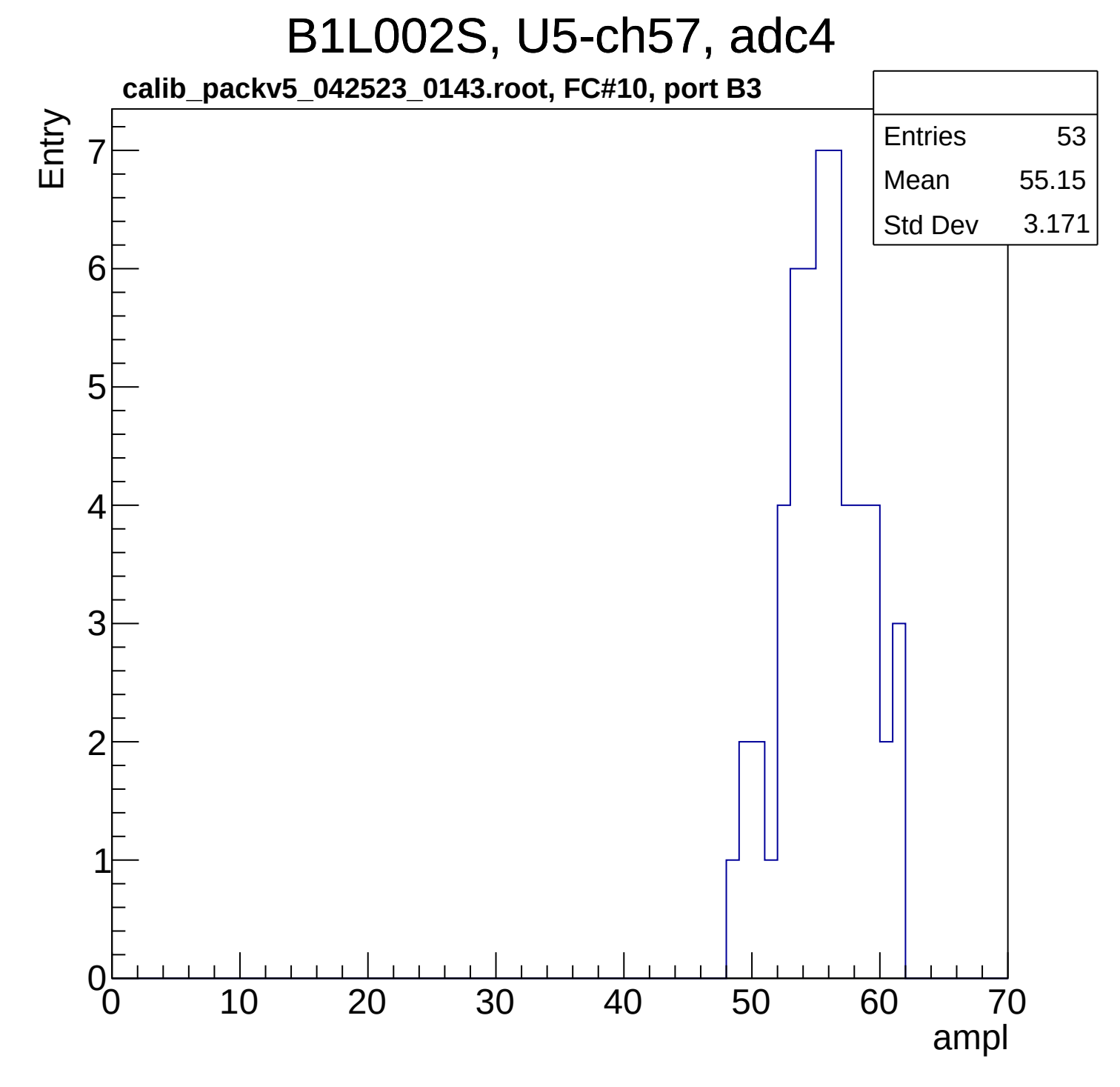
B1L002S, U5-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 55.15
Std Dev 3.171
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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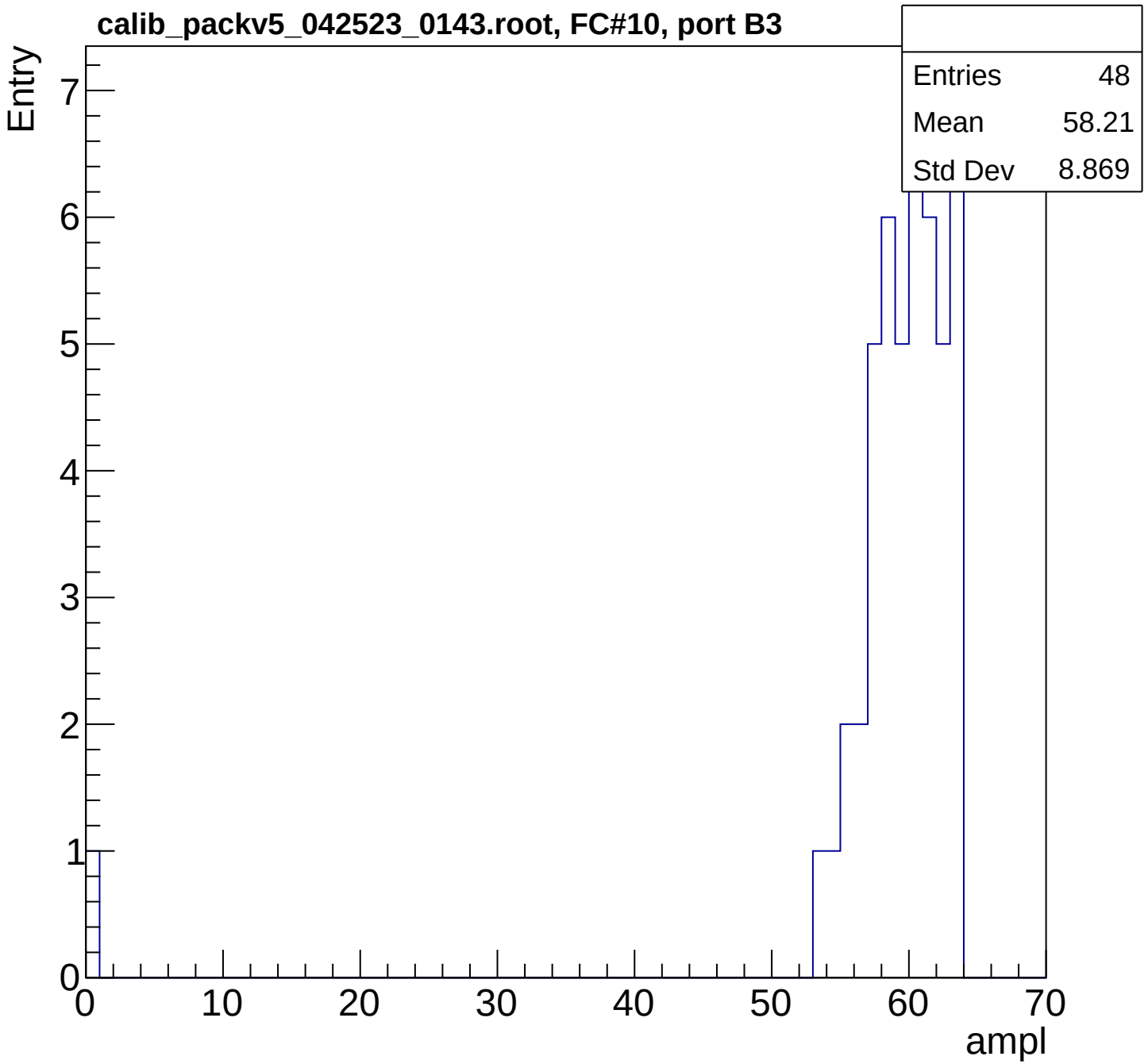
B1L002S, U5-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.21
Std Dev 8.869
Entry
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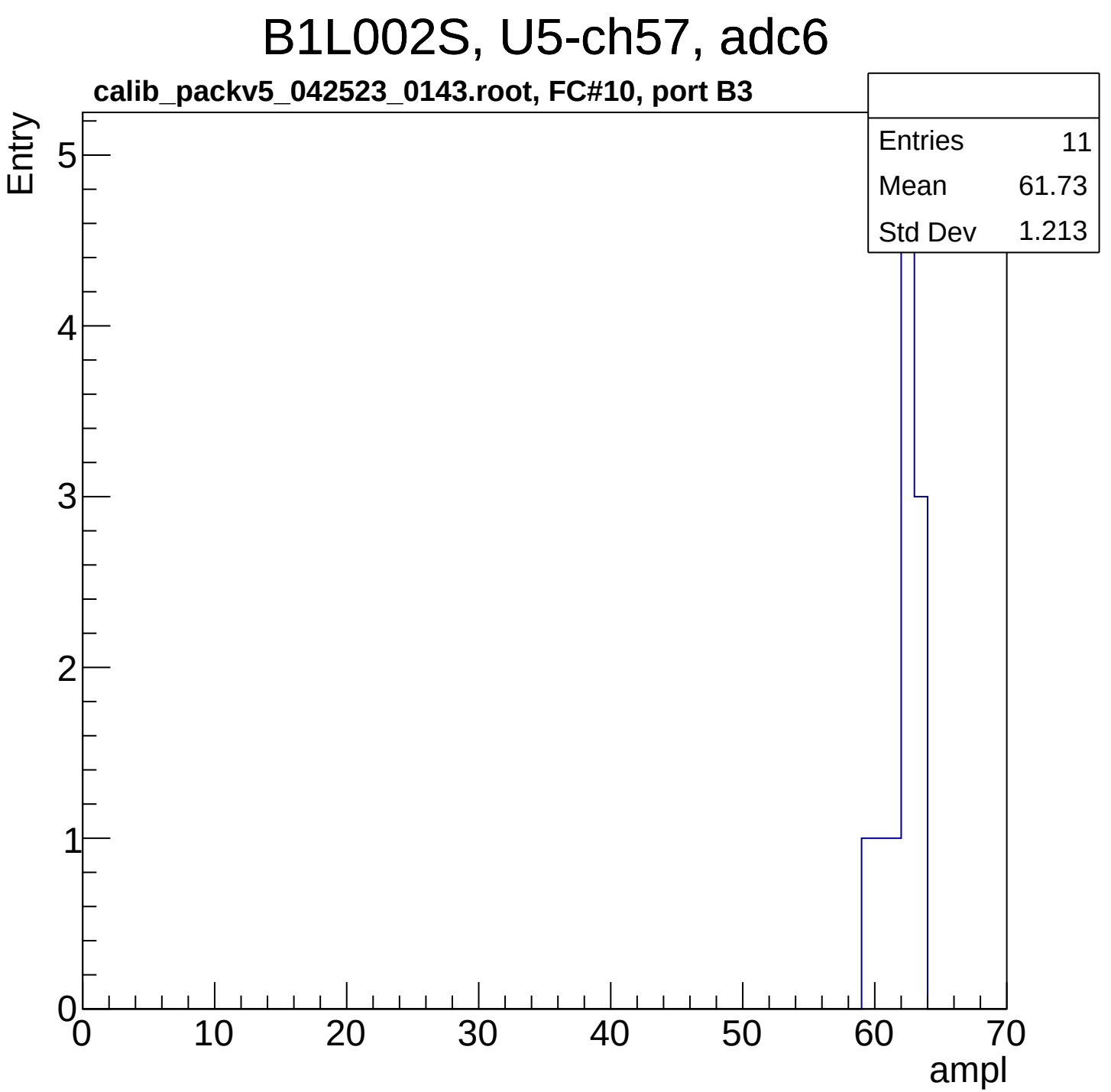
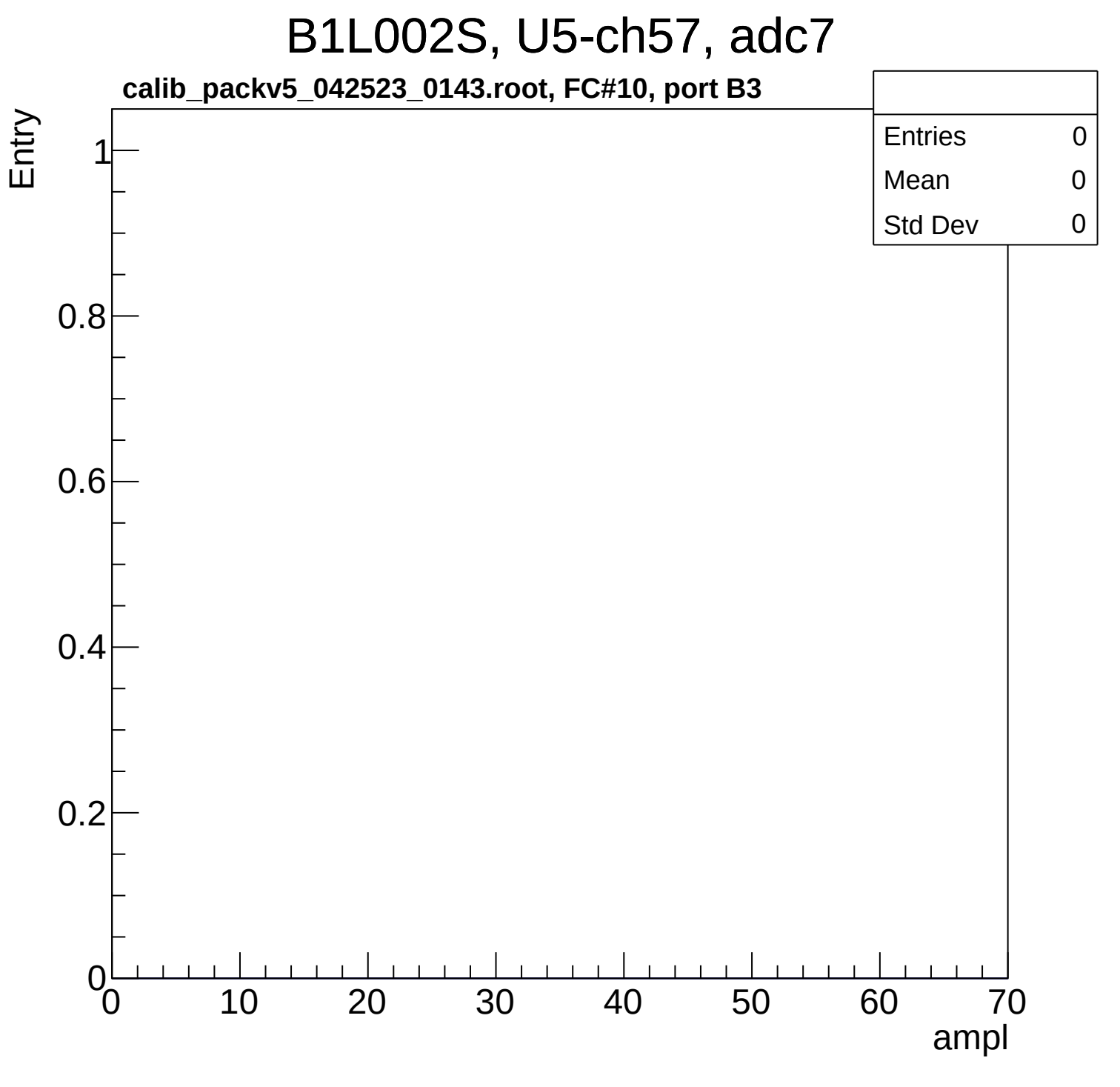
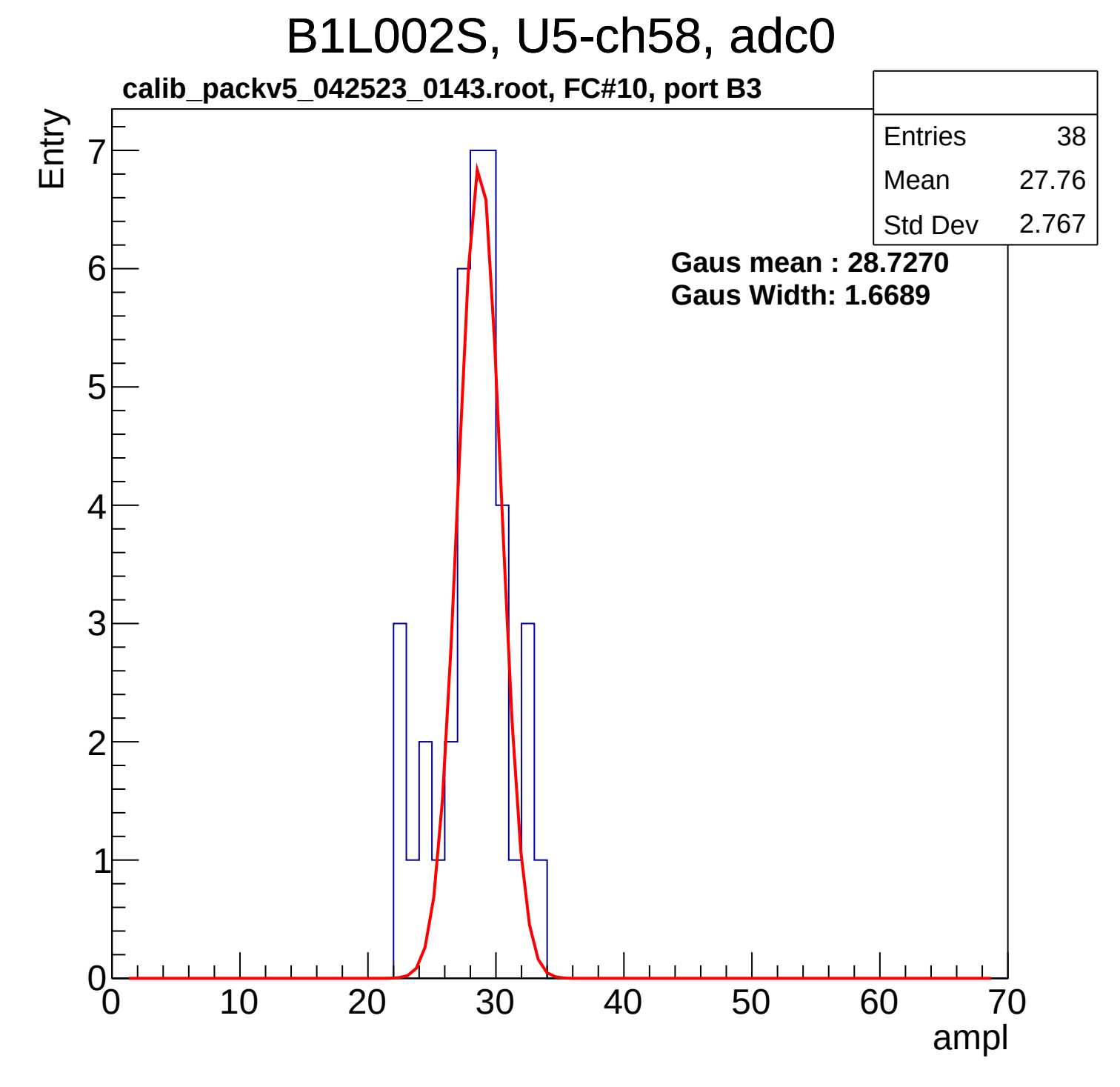
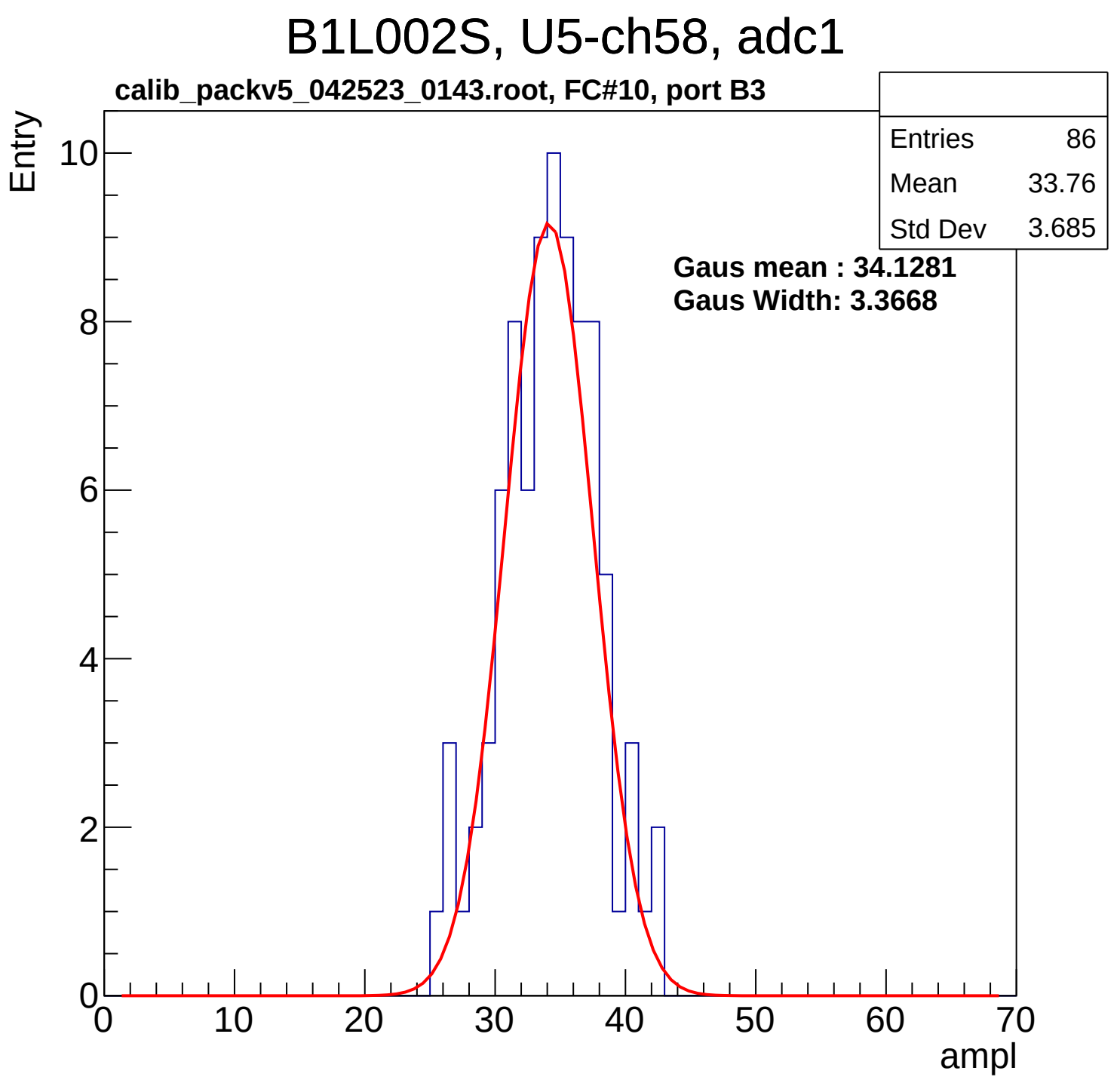
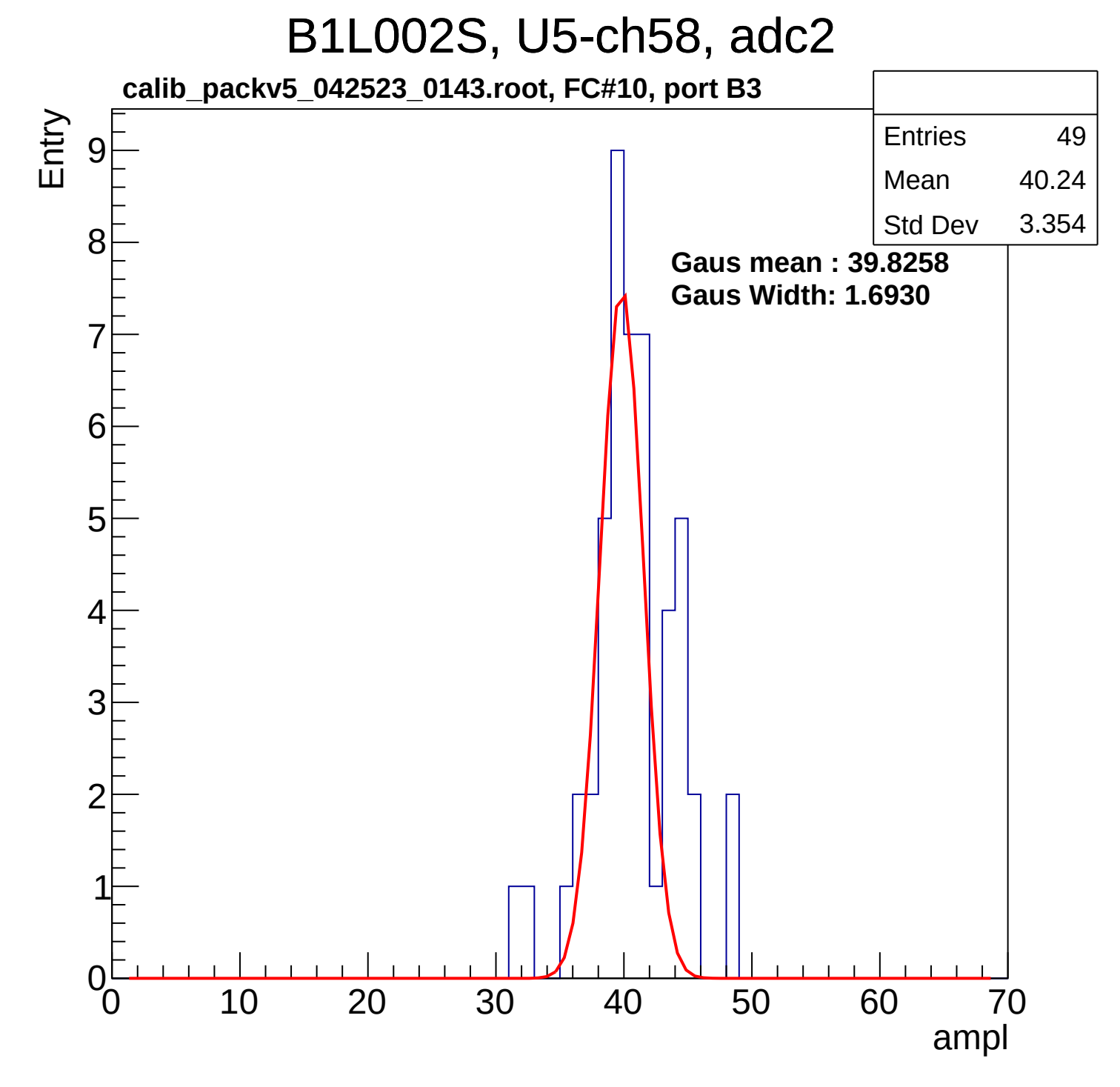
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.73
Std Dev 1.213
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 27.76
Std Dev 2.767
Gaus mean : 28.7270
Gaus Width: 1.6689
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 33.76
Std Dev 3.685
Gaus mean : 34.1281
Gaus Width: 3.3668
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.24
Std Dev 3.354
Gaus mean : 39.8258
Gaus Width: 1.6930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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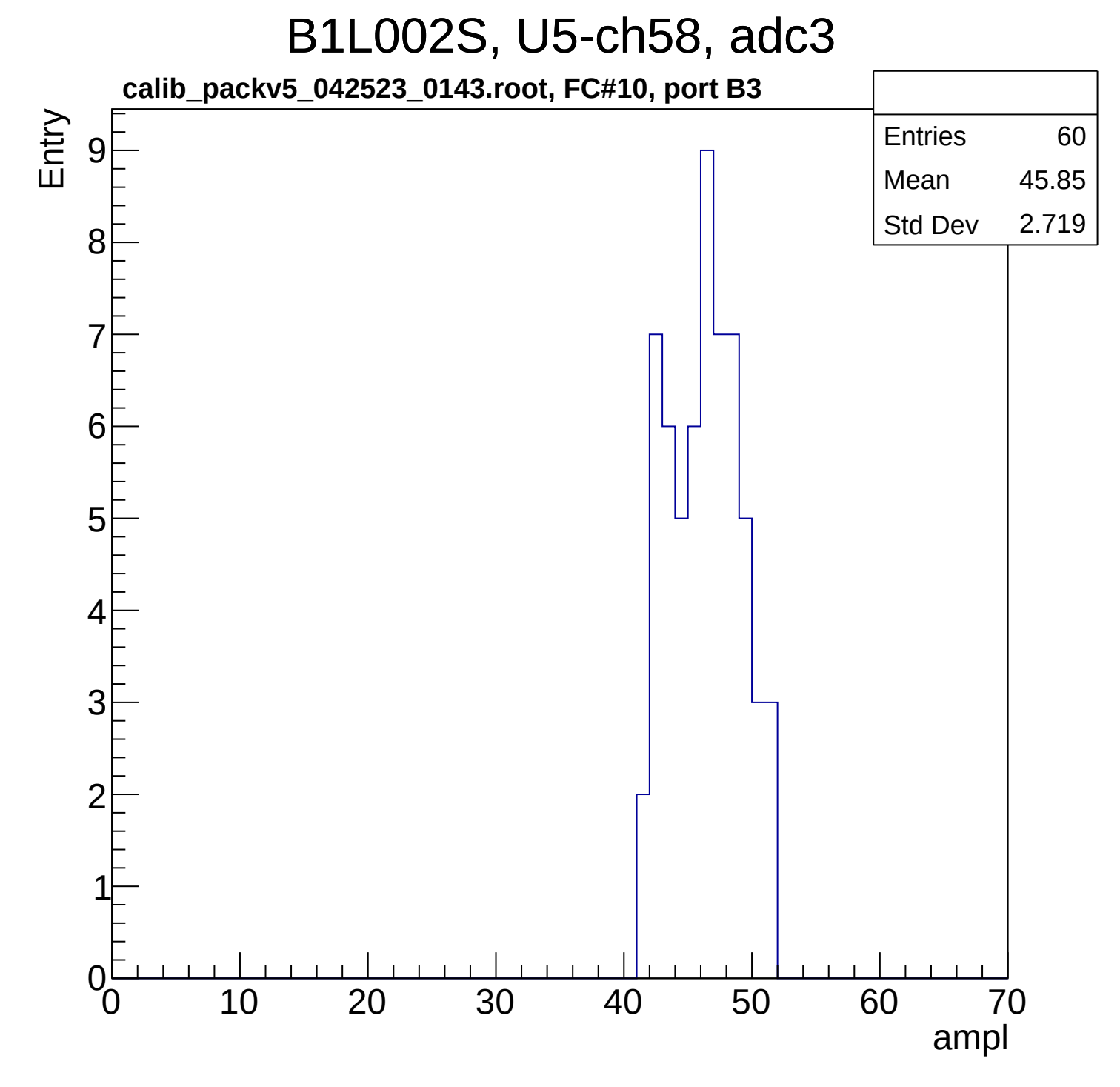
B1L002S, U5-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 45.85
Std Dev 2.719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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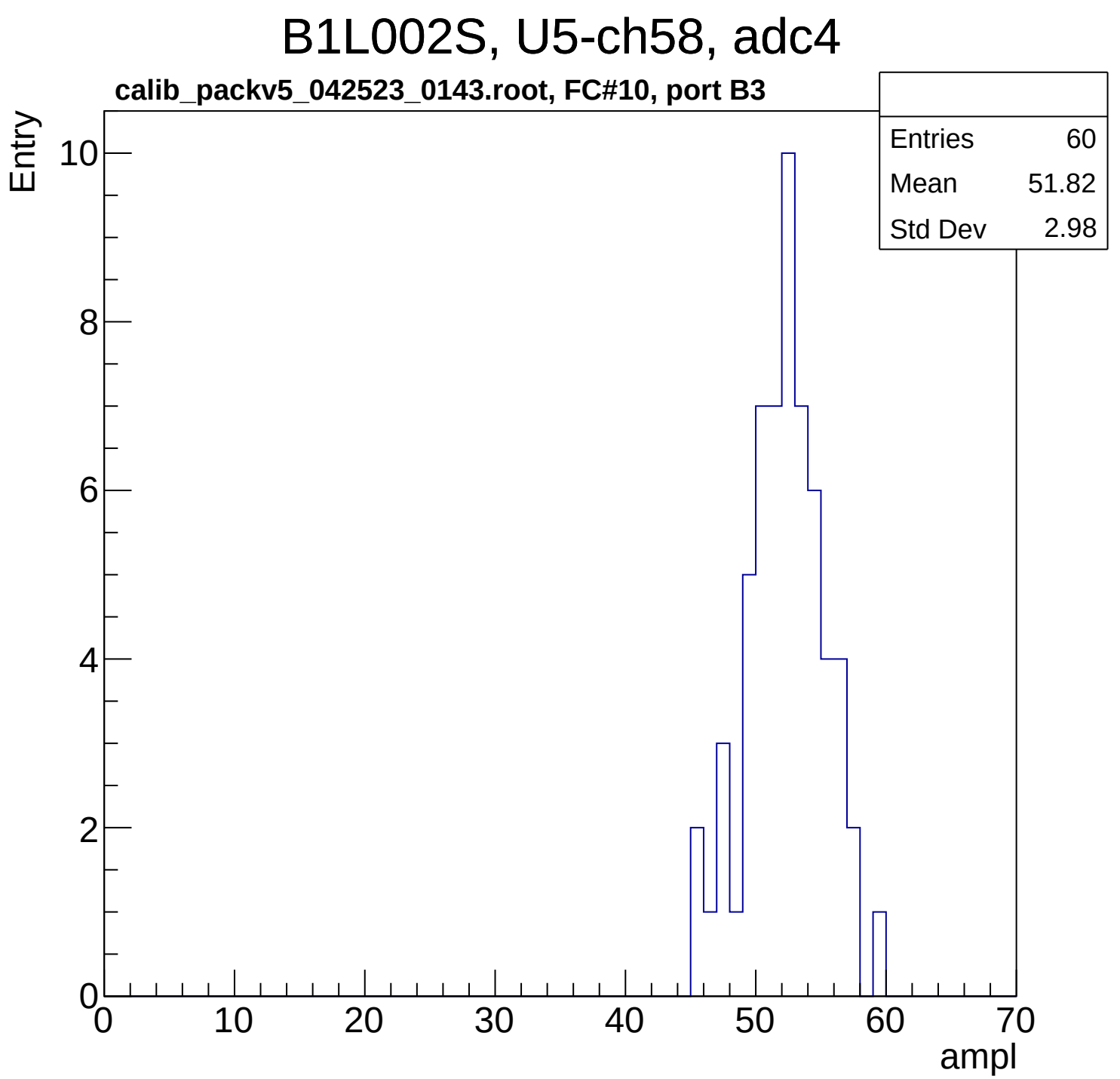
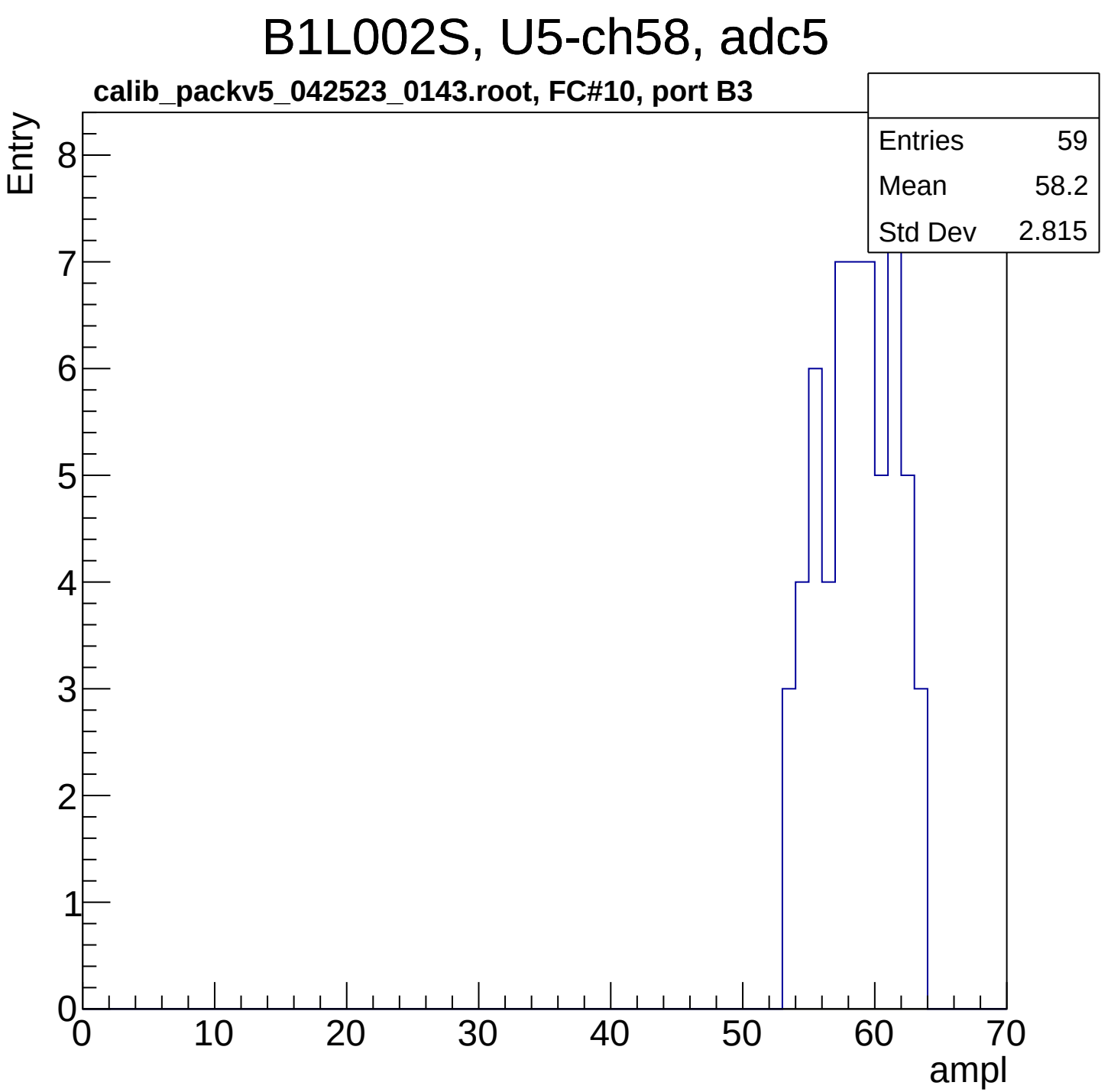
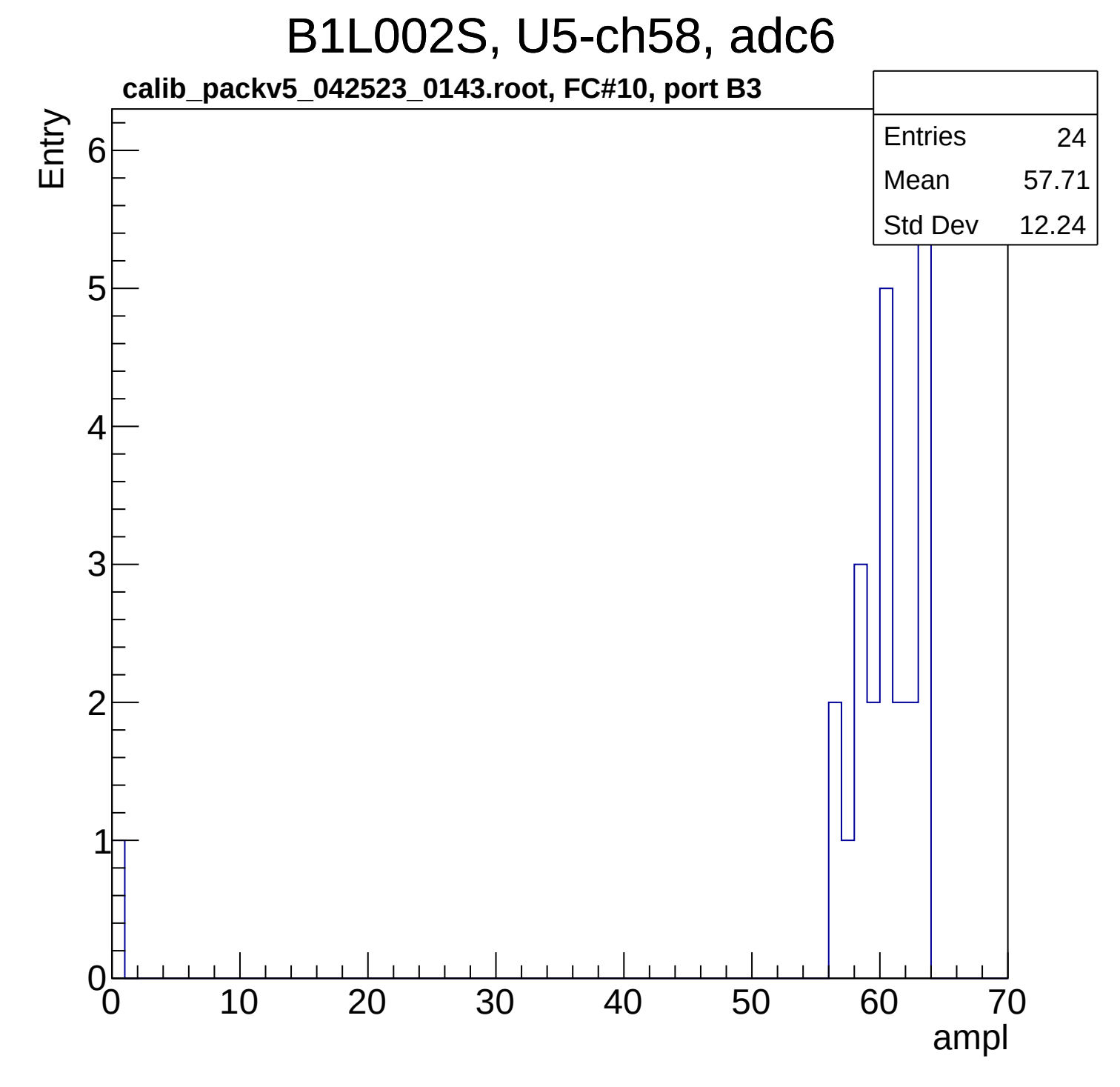
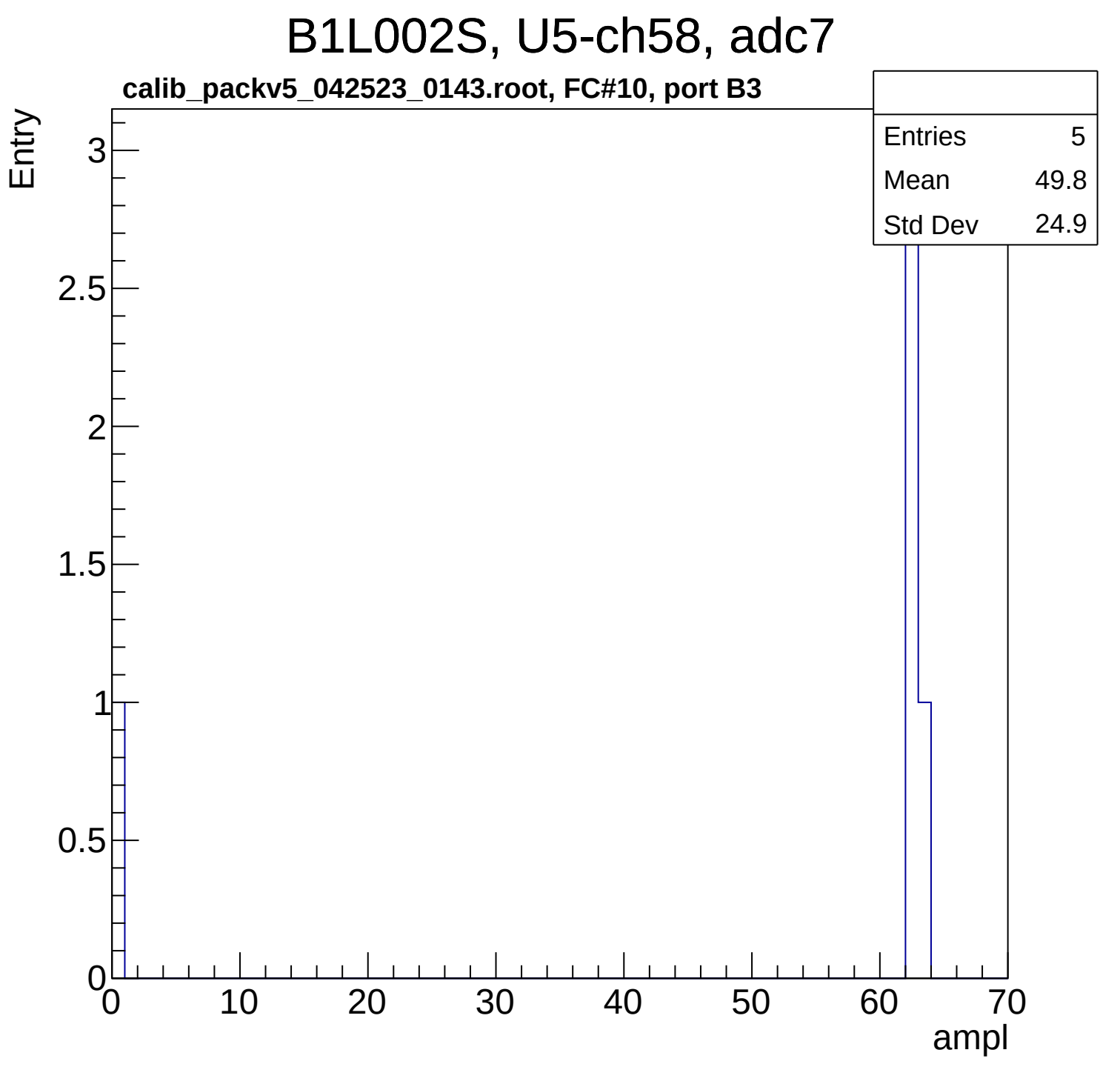
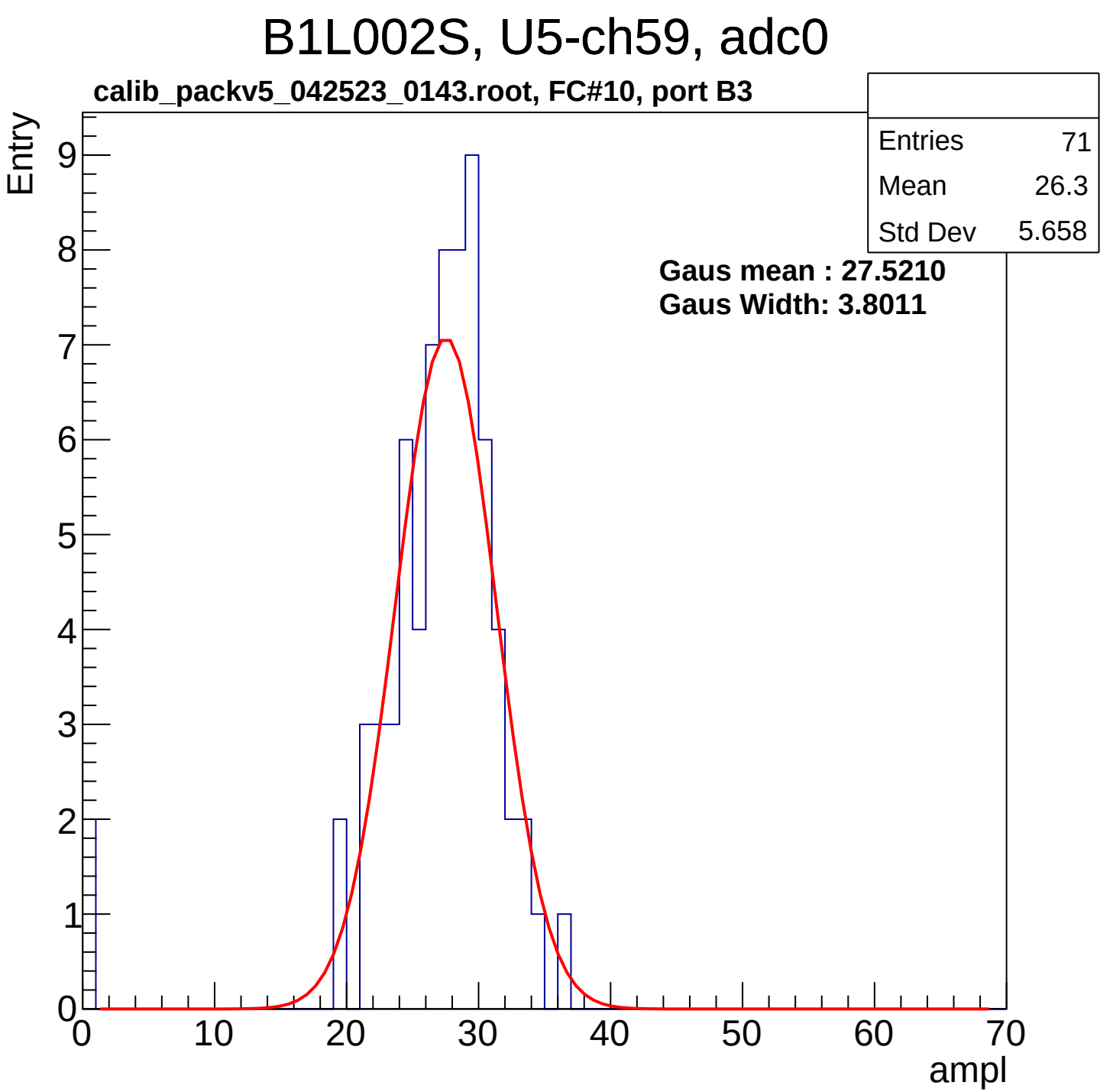
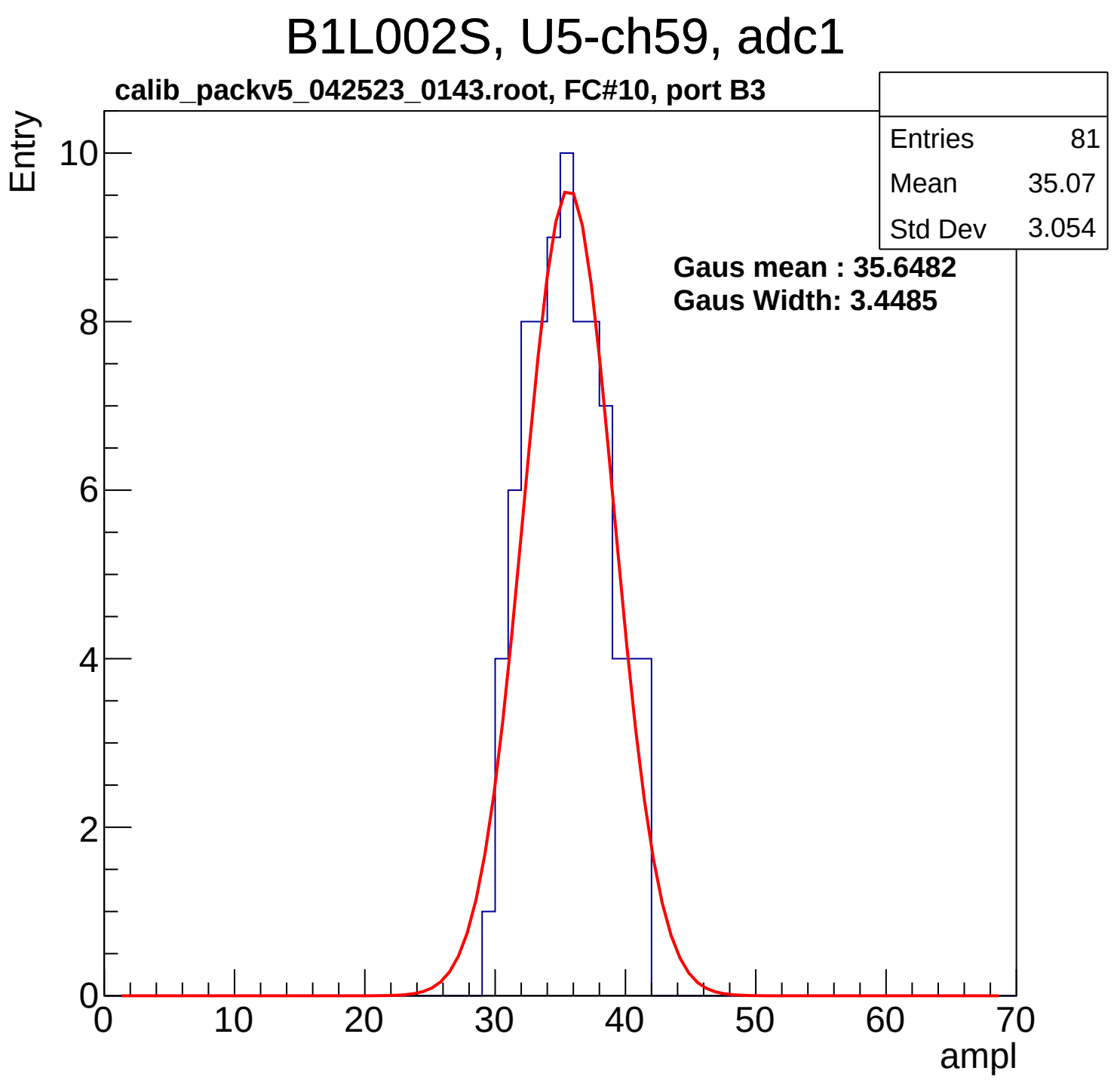

B1L002S, U5-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.82
Std Dev 2.98
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

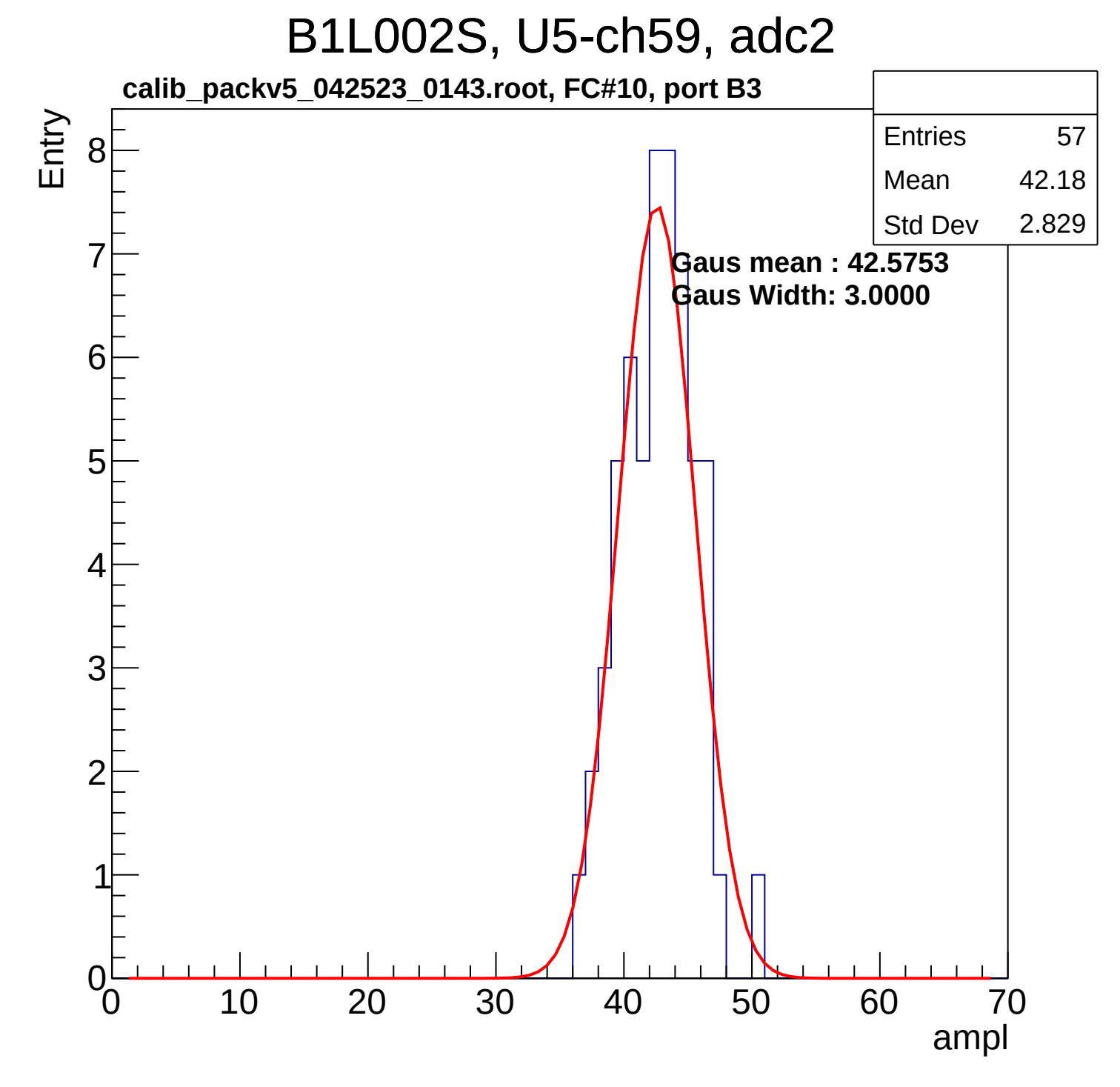

B1L002S, U5-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.2
Std Dev 2.815
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 57.71
Std Dev 12.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.8
Std Dev 24.9
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 26.3
Std Dev 5.658
Gaus mean : 27.5210
Gaus Width: 3.8011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.07
Std Dev 3.054
Gaus mean : 35.6482
Gaus Width: 3.4485
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.18
Std Dev 2.829
Gaus mean : 42.5753
Gaus Width: 3.0000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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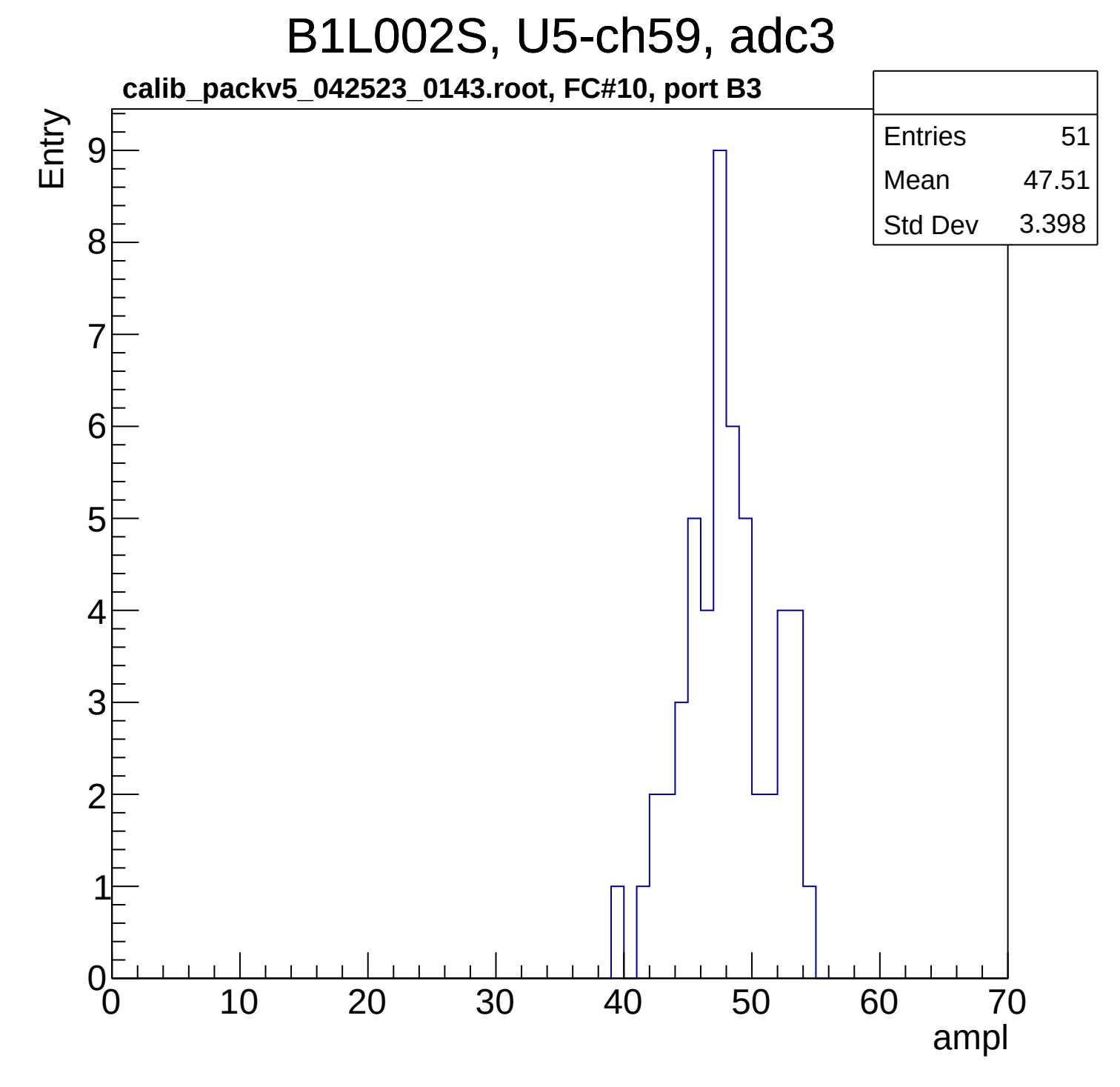
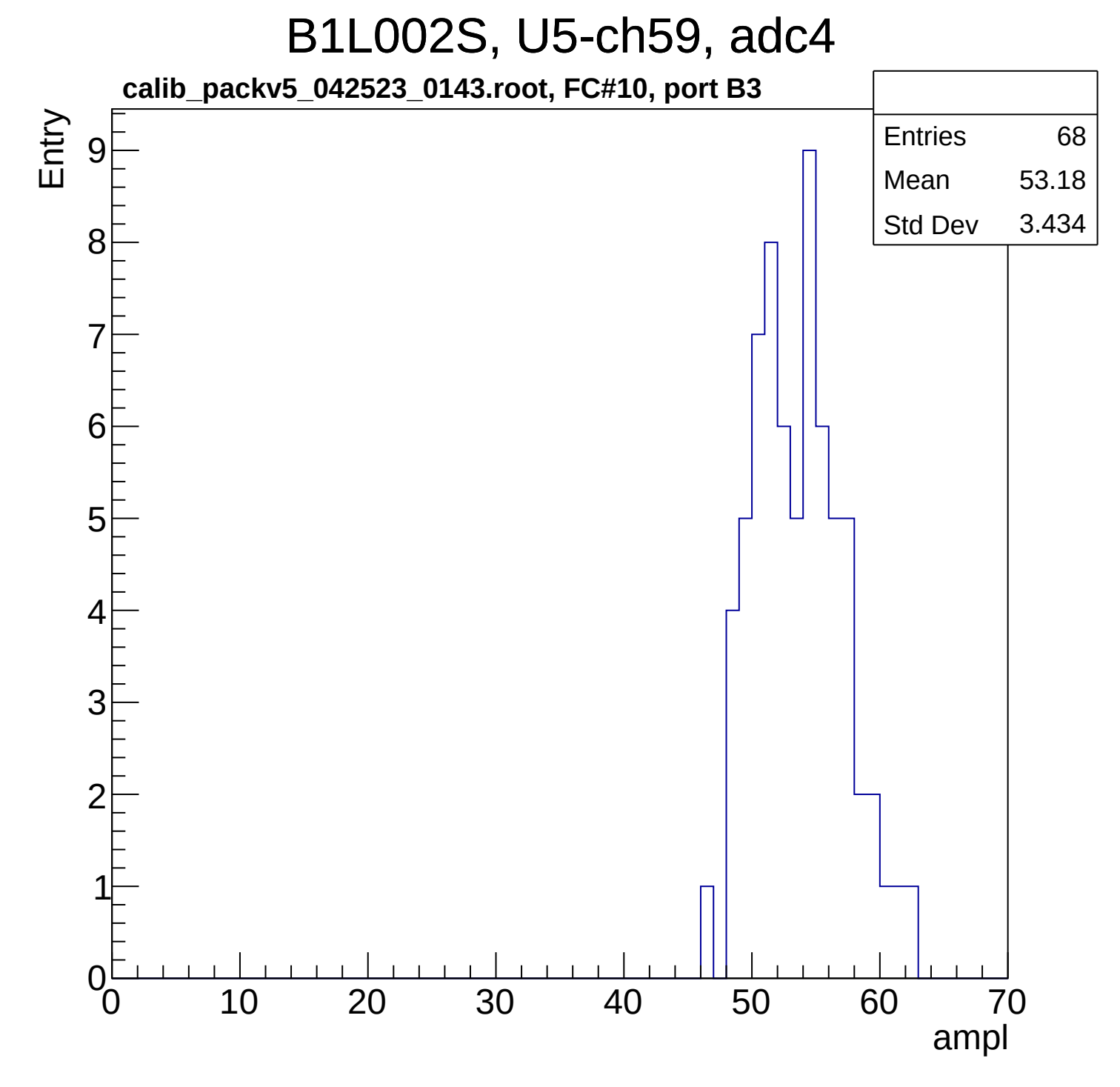
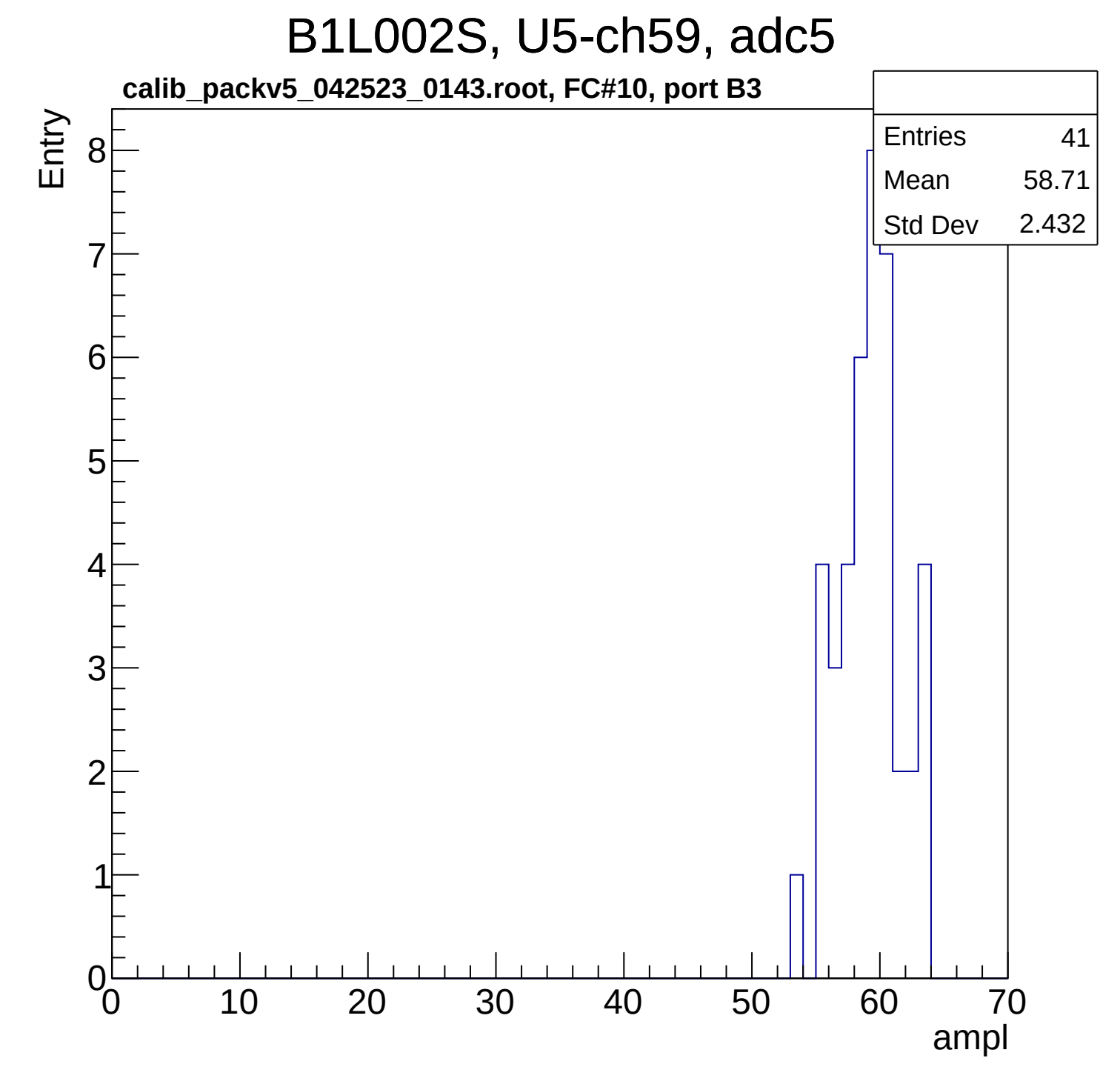
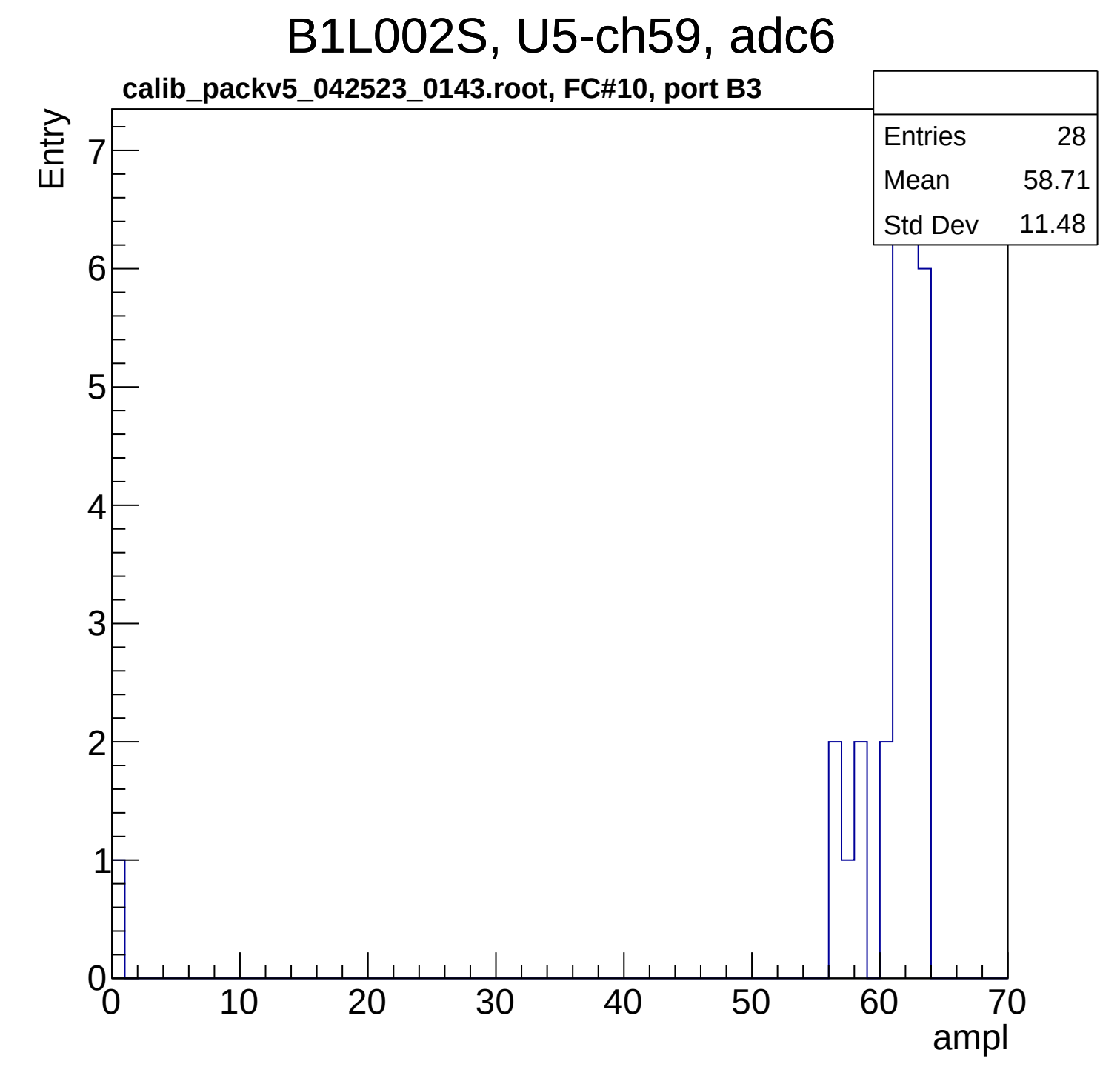
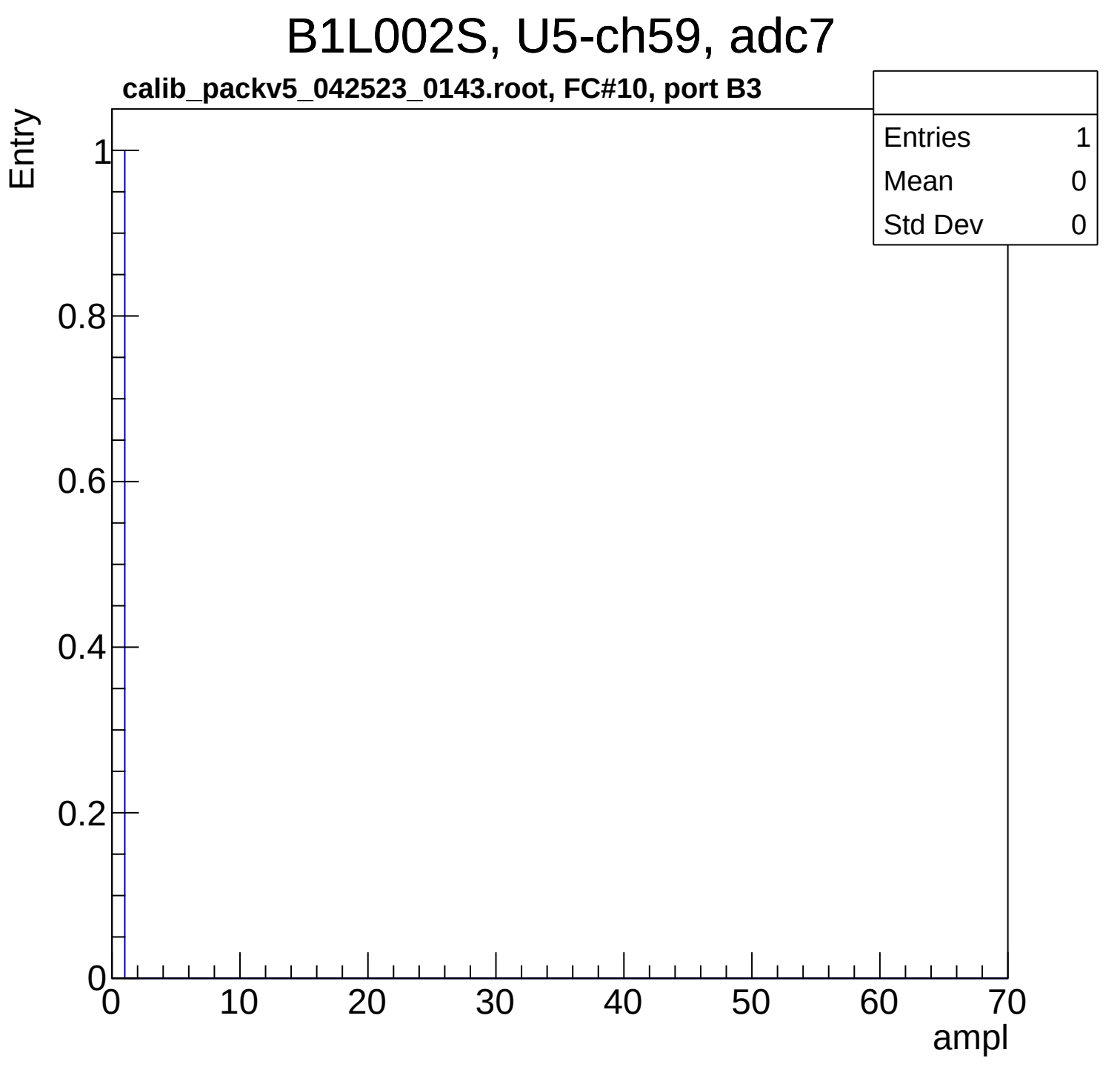
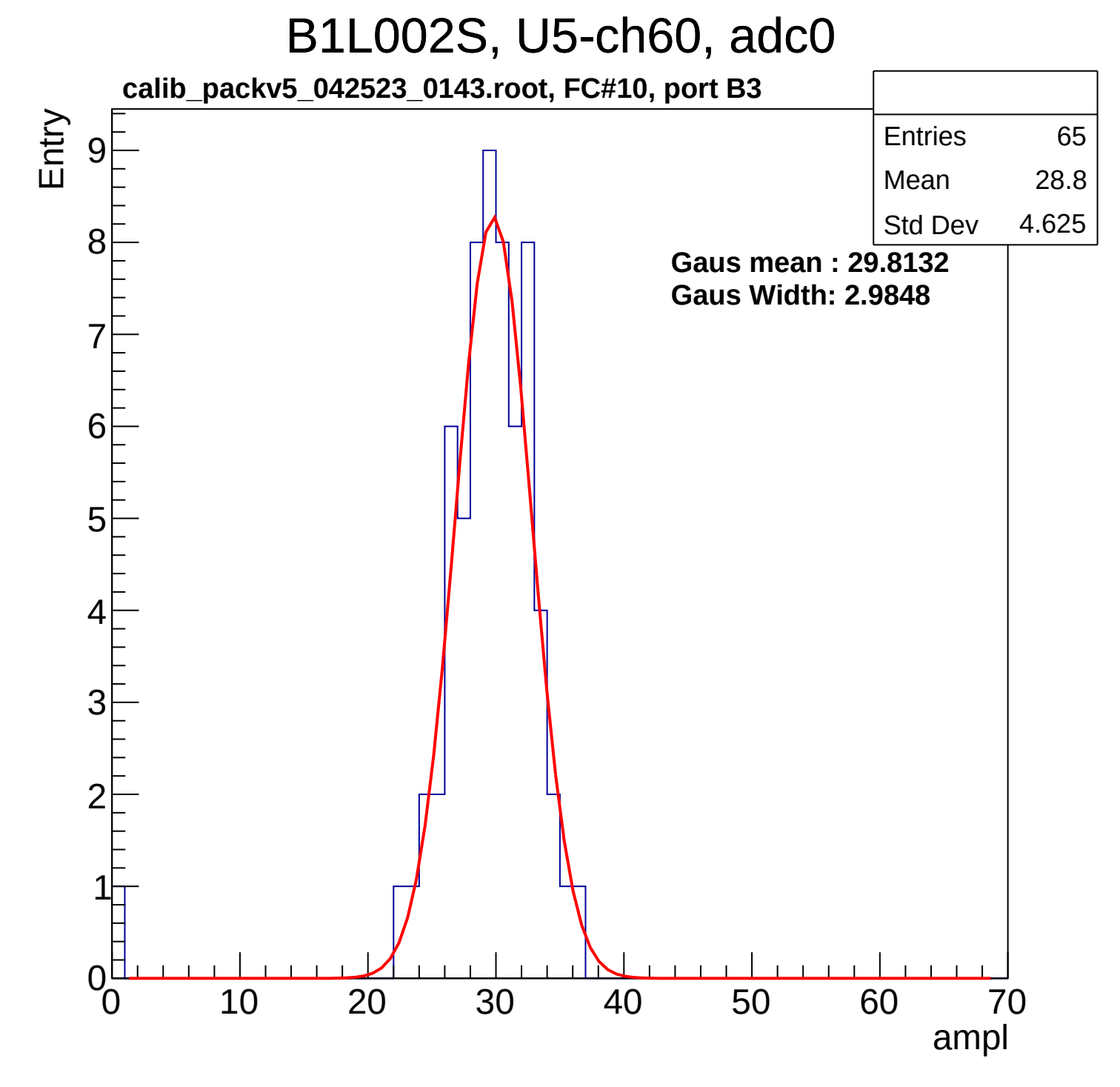

B1L002S, U5-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.51
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

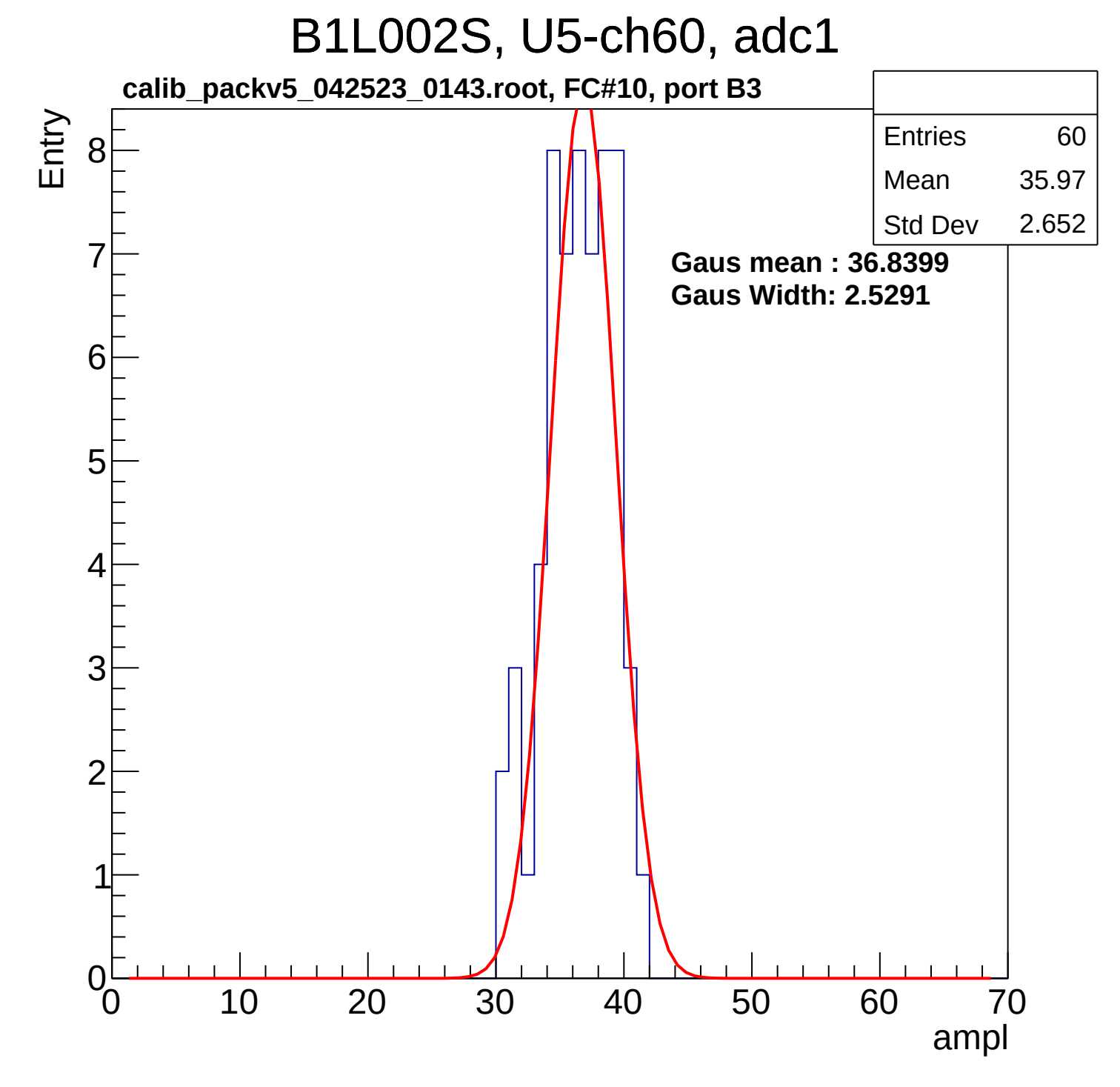

B1L002S, U5-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.18
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

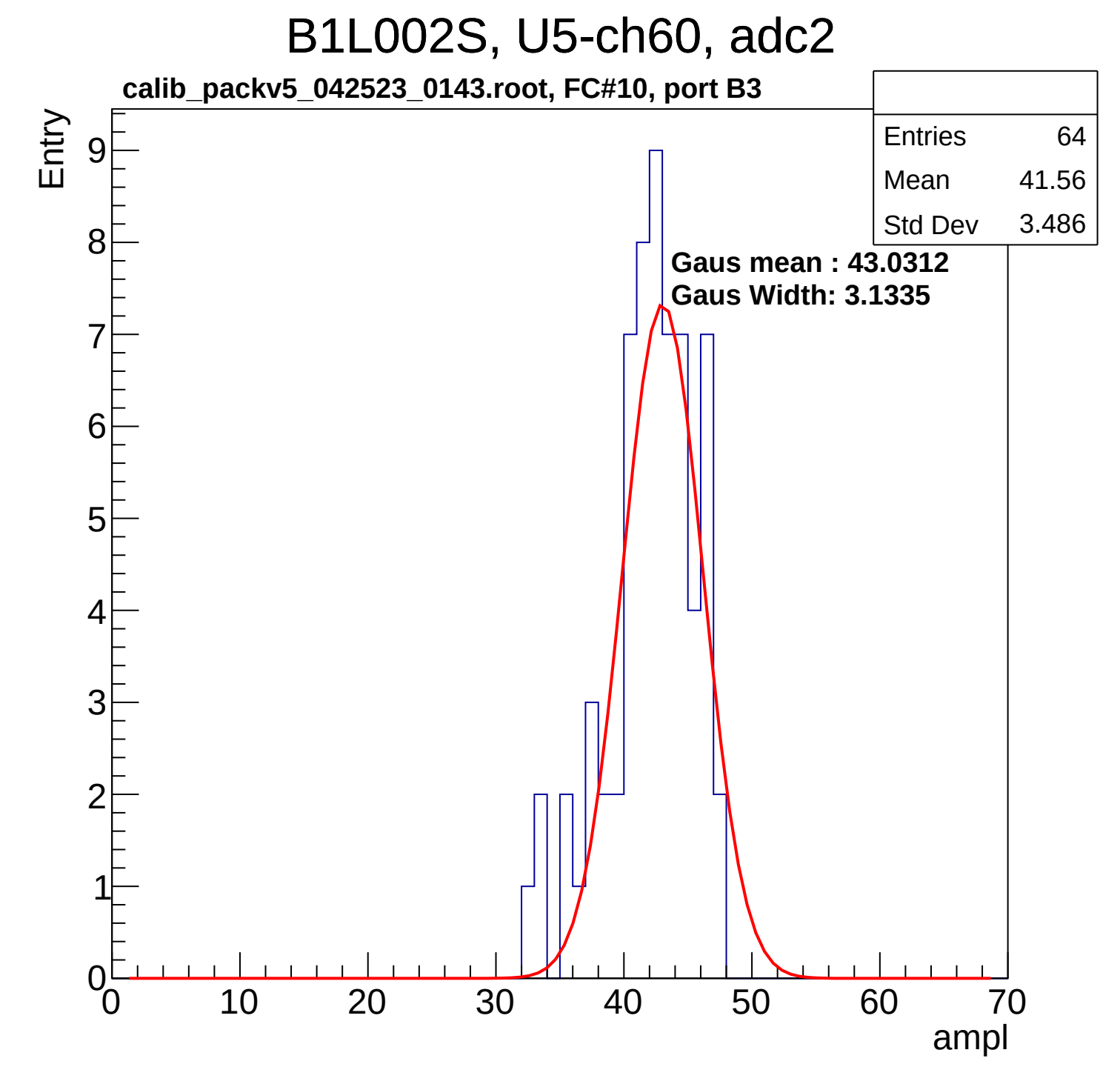

B1L002S, U5-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 58.71
Std Dev 2.432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

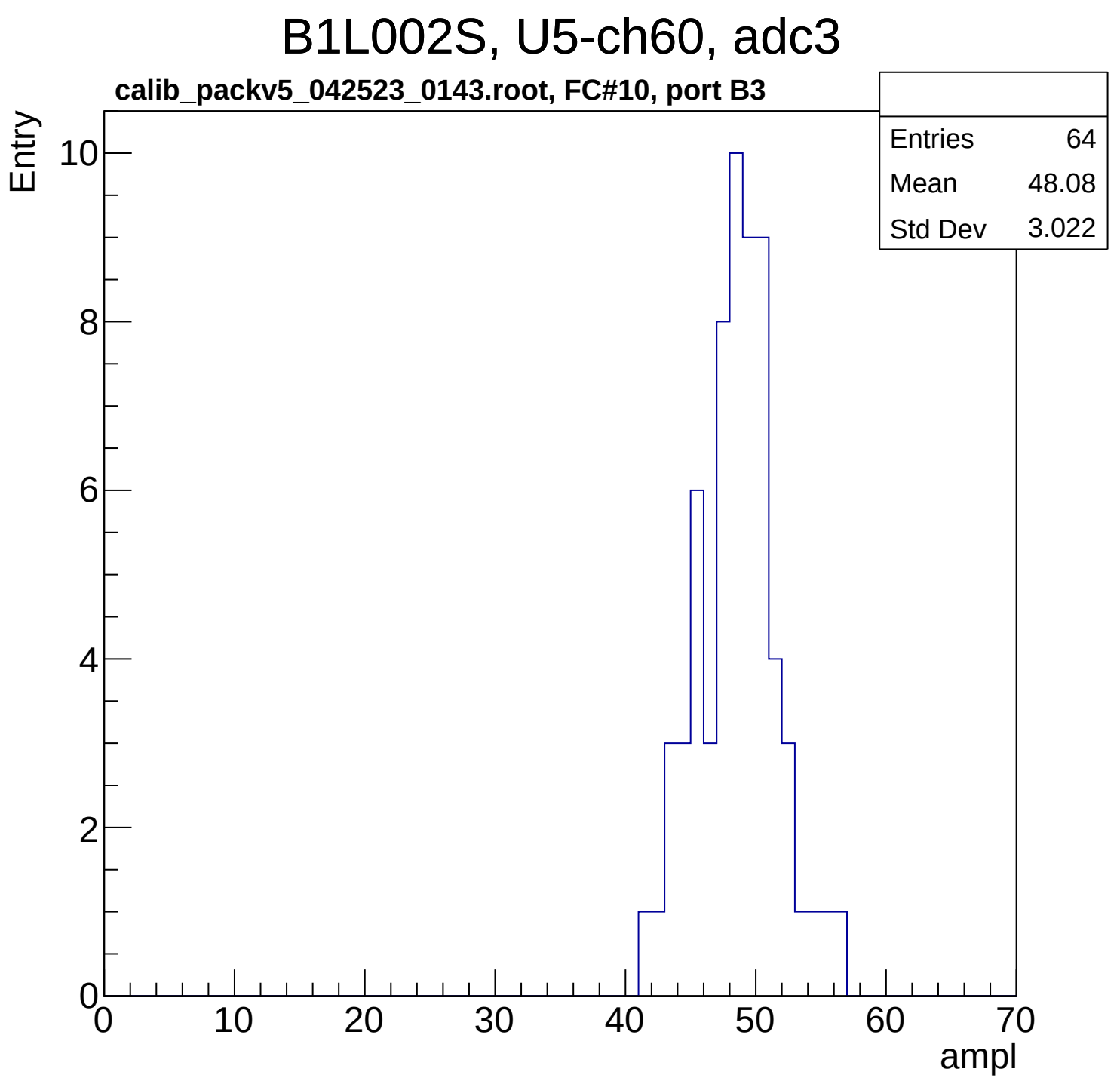

B1L002S, U5-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.71
Std Dev 11.48
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

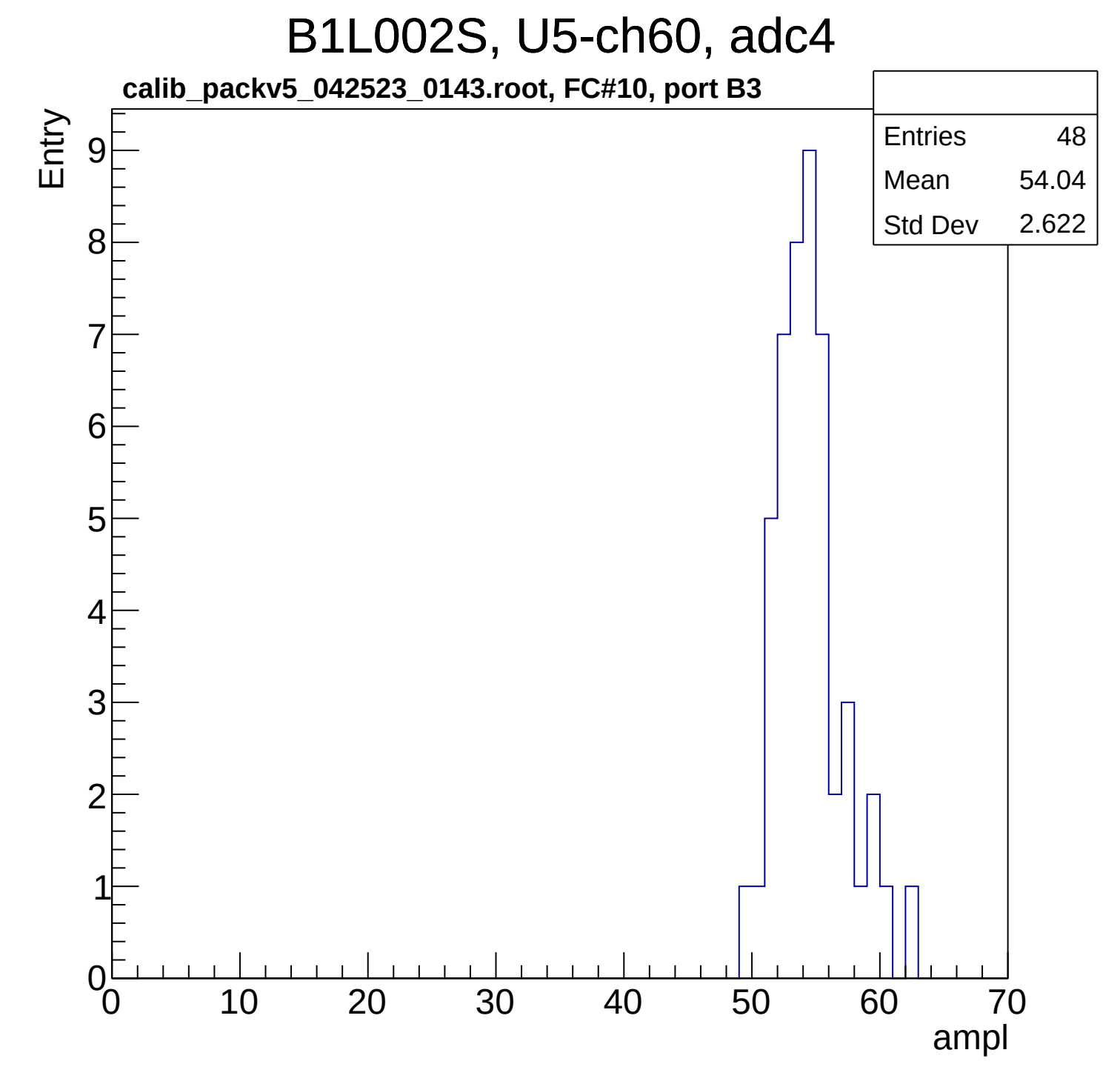

B1L002S, U5-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.8
Std Dev 4.625
Gaus mean : 29.8132
Gaus Width: 2.9848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.97
Std Dev 2.652
Gaus mean : 36.8399
Gaus Width: 2.5291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.56
Std Dev 3.486
Gaus mean : 43.0312
Gaus Width: 3.1335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.08
Std Dev 3.022
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.04
Std Dev 2.622
Entry
ampl
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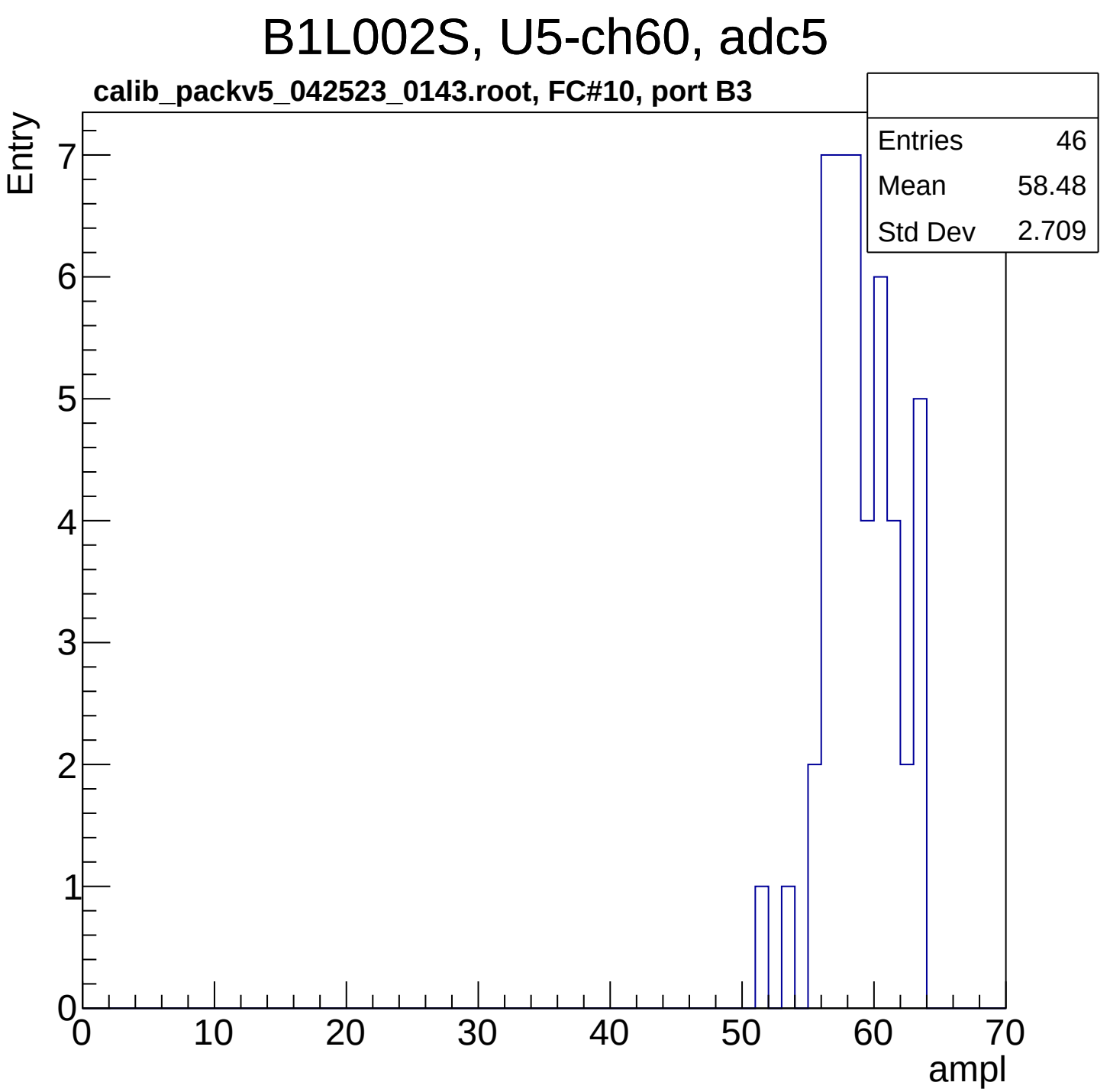
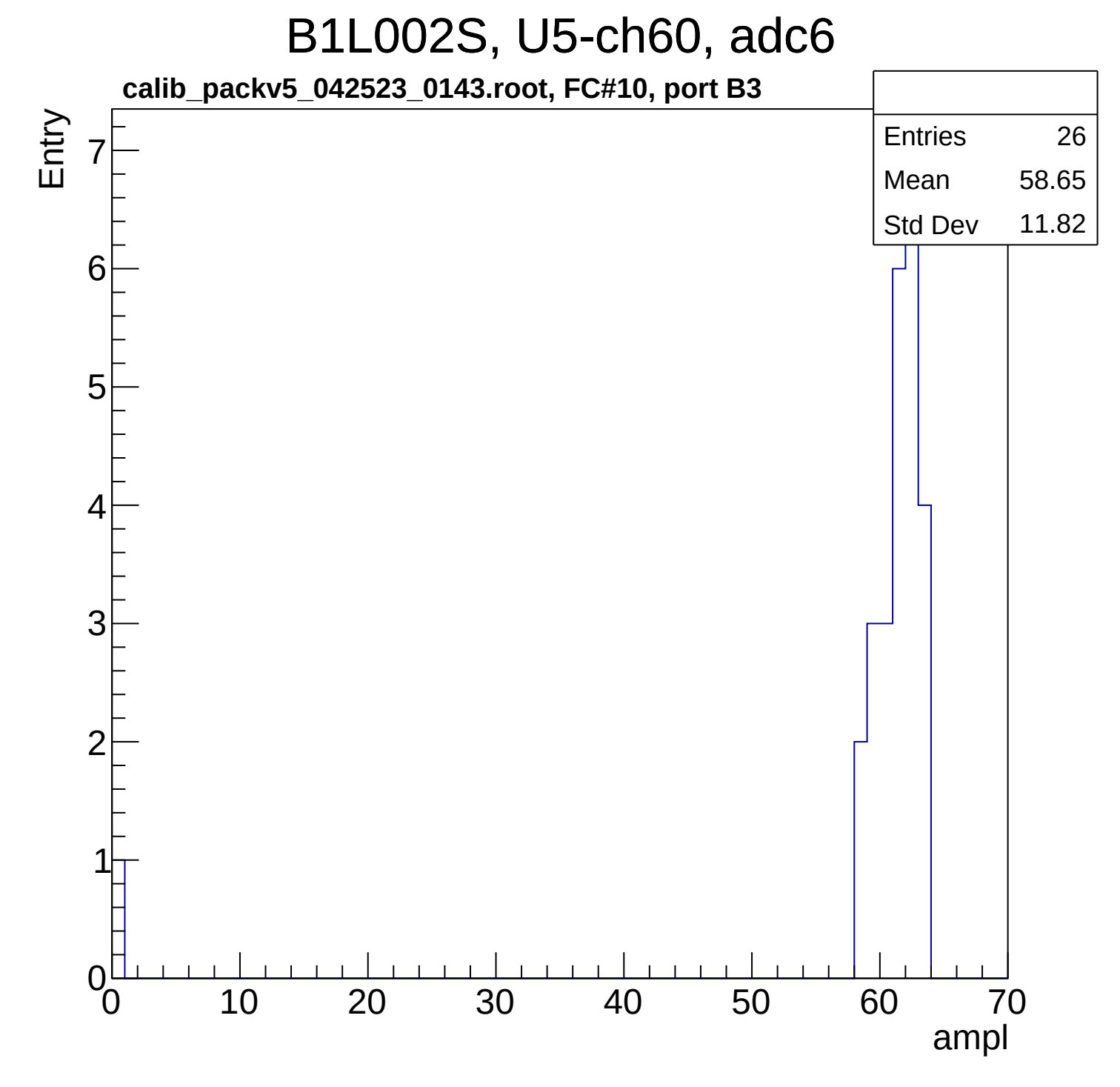
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.48
Std Dev 2.709
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.65
Std Dev 11.82
Entry
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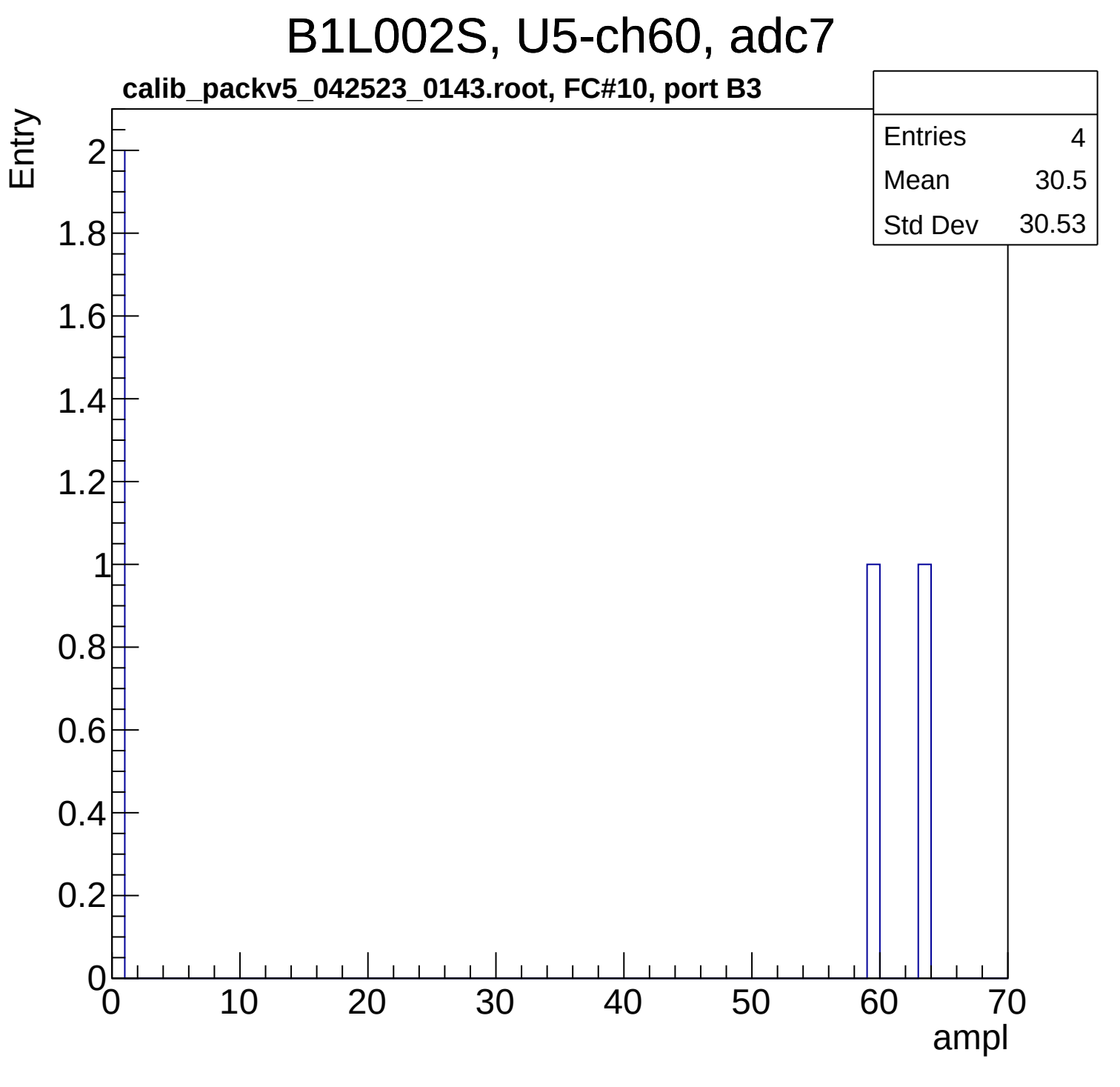
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.5
Std Dev 30.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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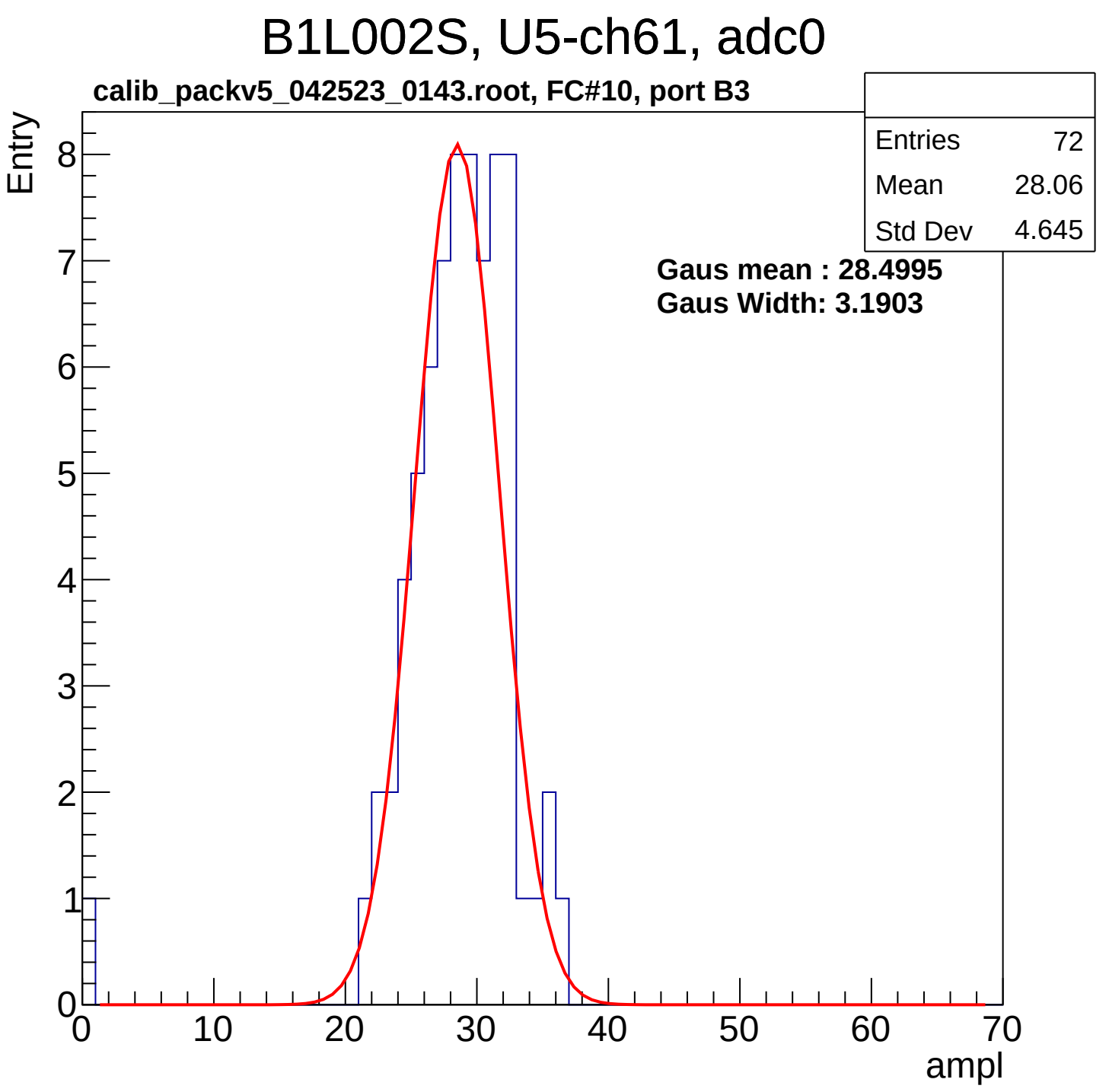
B1L002S, U5-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.06
Std Dev 4.645
Gaus mean : 28.4995
Gaus Width: 3.1903
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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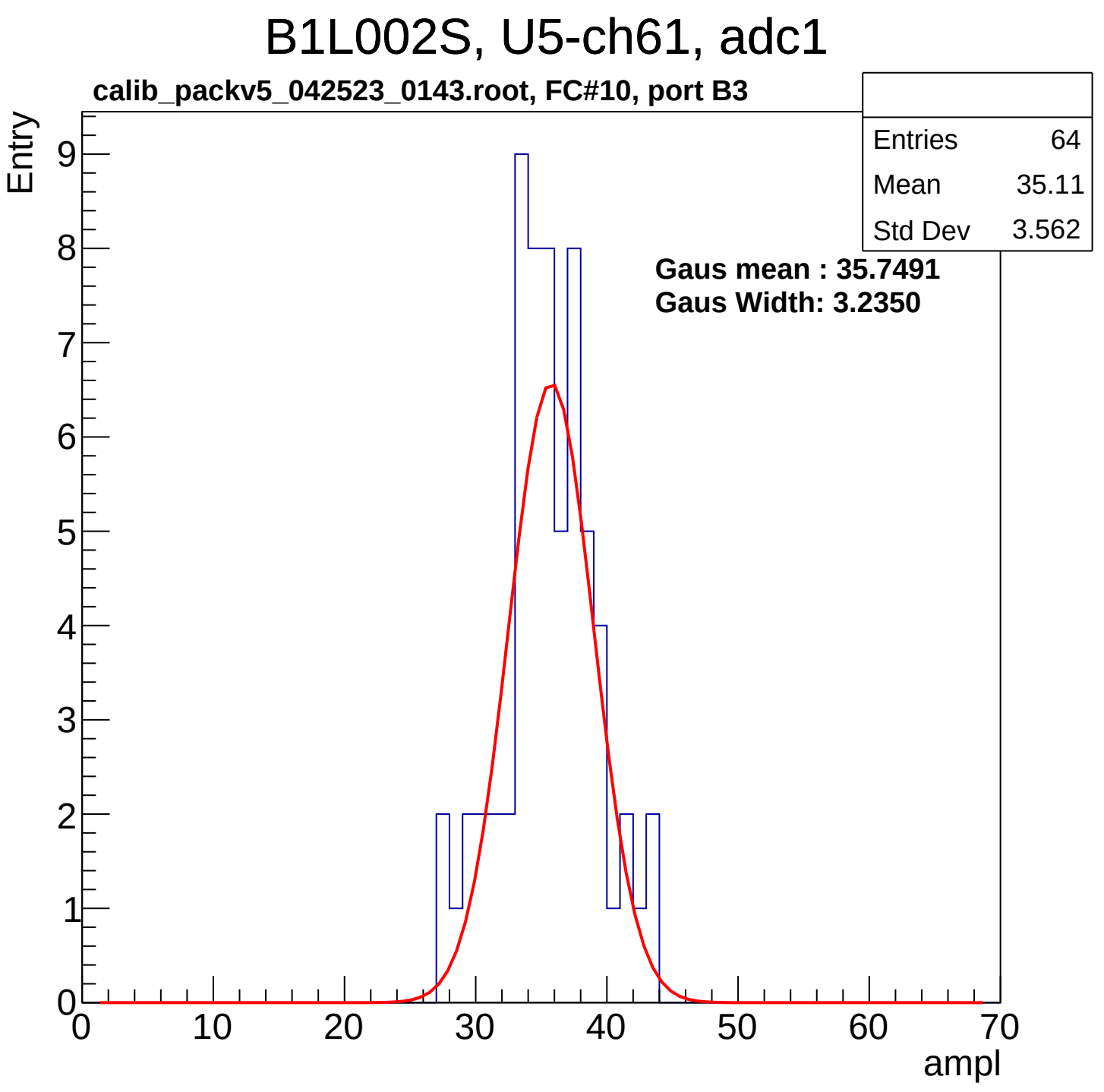
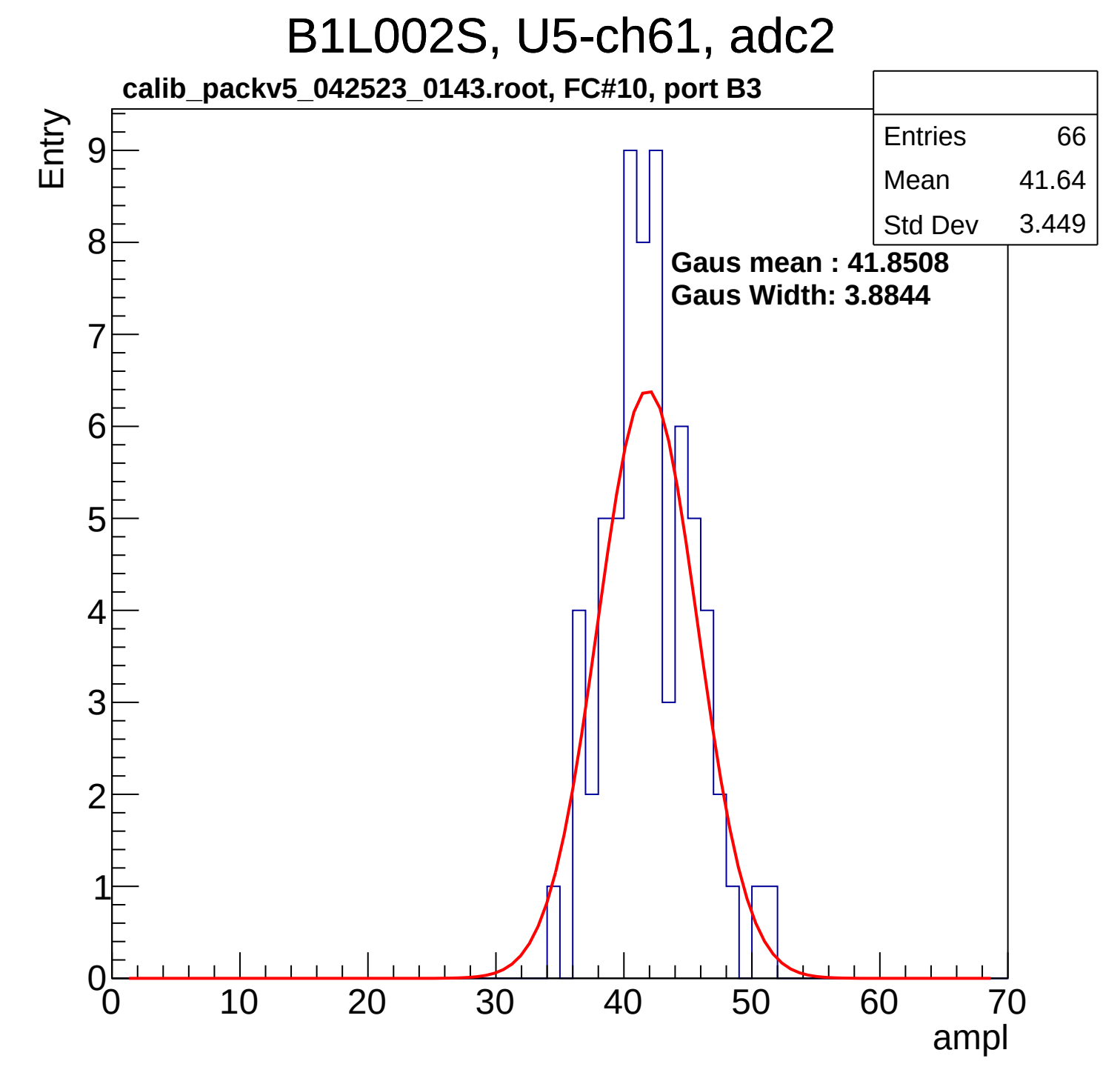
40
50
60
70


B1L002S, U5-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.11
Std Dev 3.562
Gaus mean : 35.7491
Gaus Width: 3.2350
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.64
Std Dev 3.449
Gaus mean : 41.8508
Gaus Width: 3.8844
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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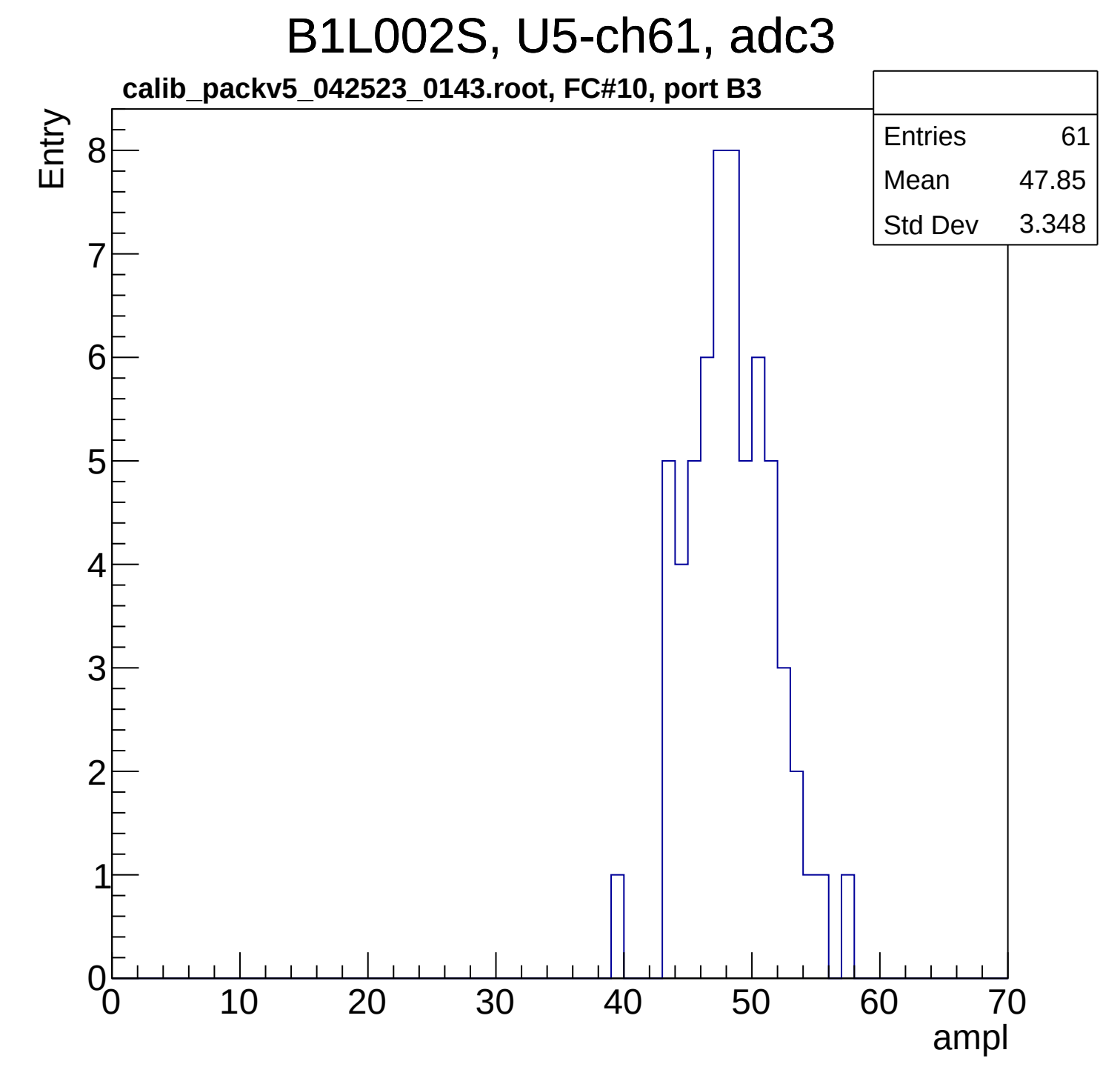
B1L002S, U5-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.85
Std Dev 3.348
Entry
ampl
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4
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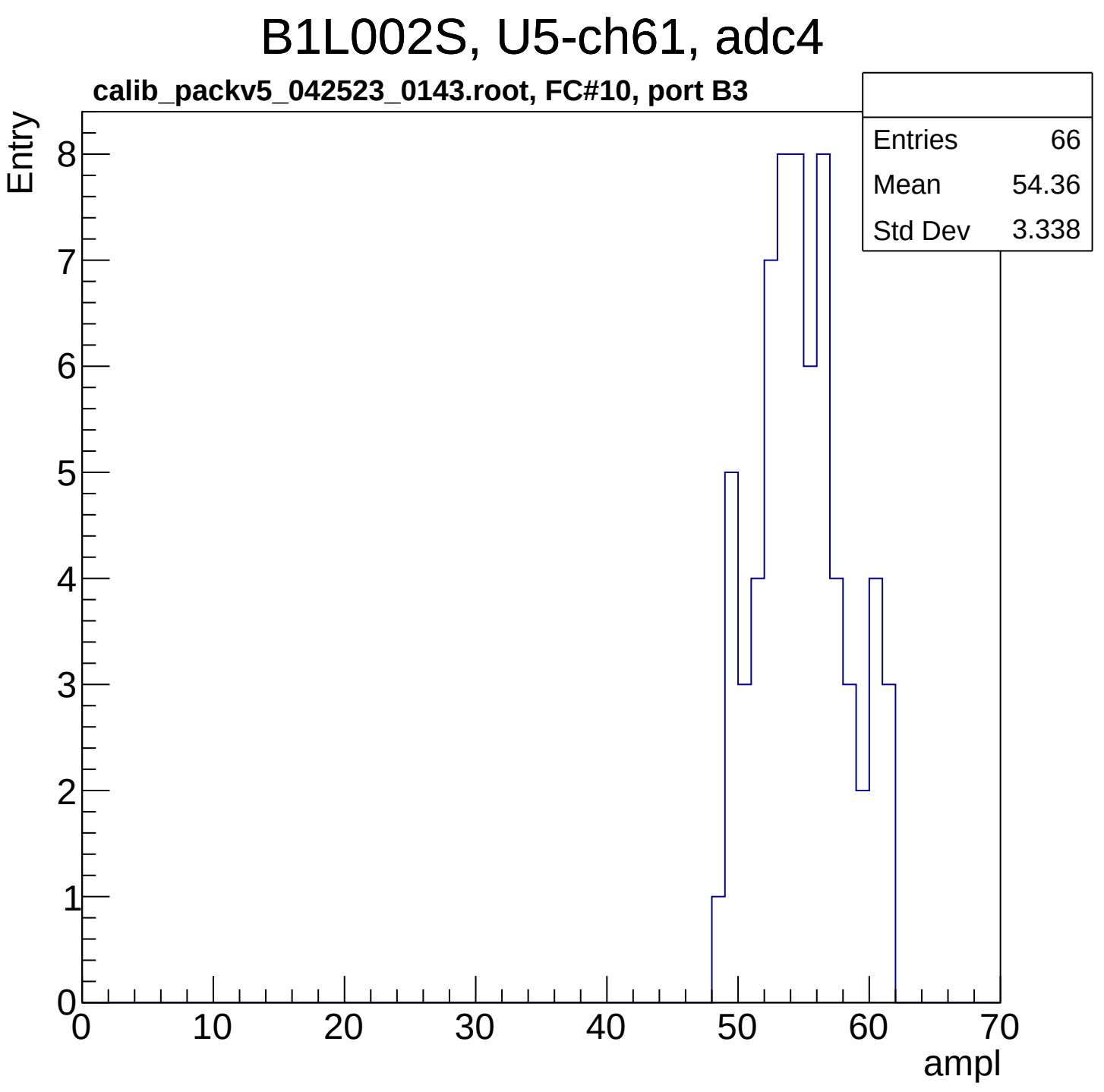
B1L002S, U5-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.36
Std Dev 3.338
Entry
ampl
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4
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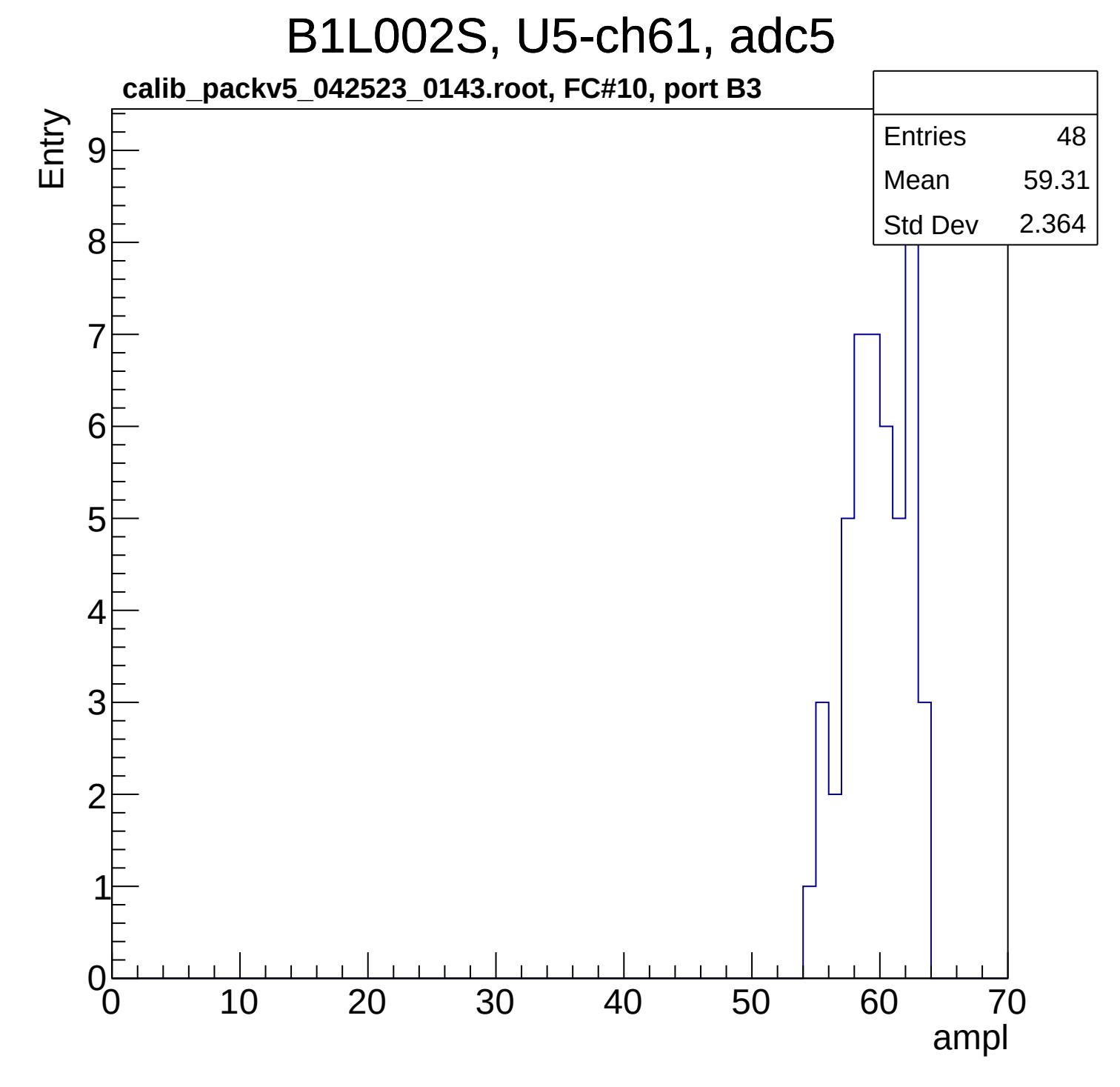
B1L002S, U5-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.31
Std Dev 2.364
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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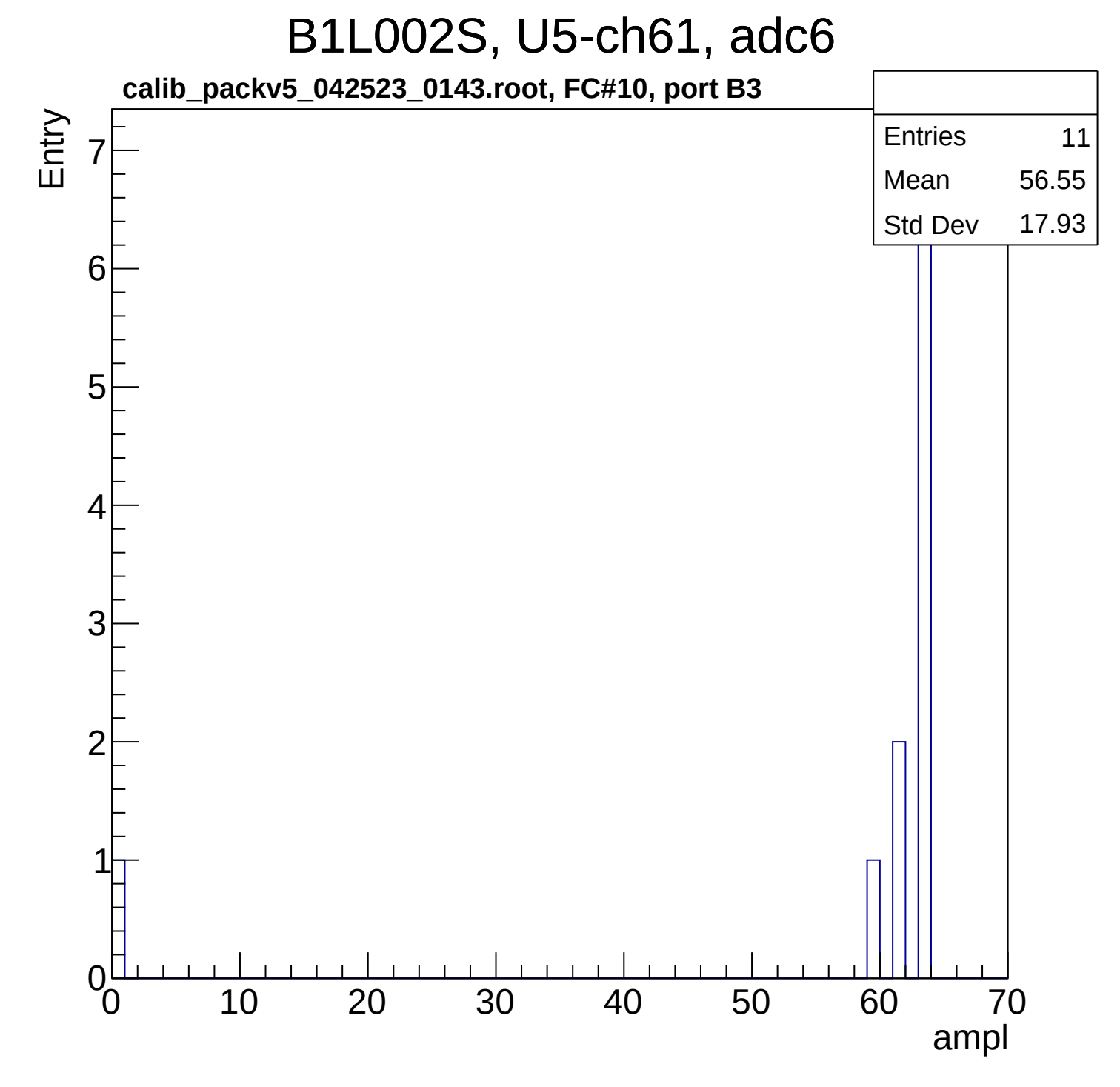
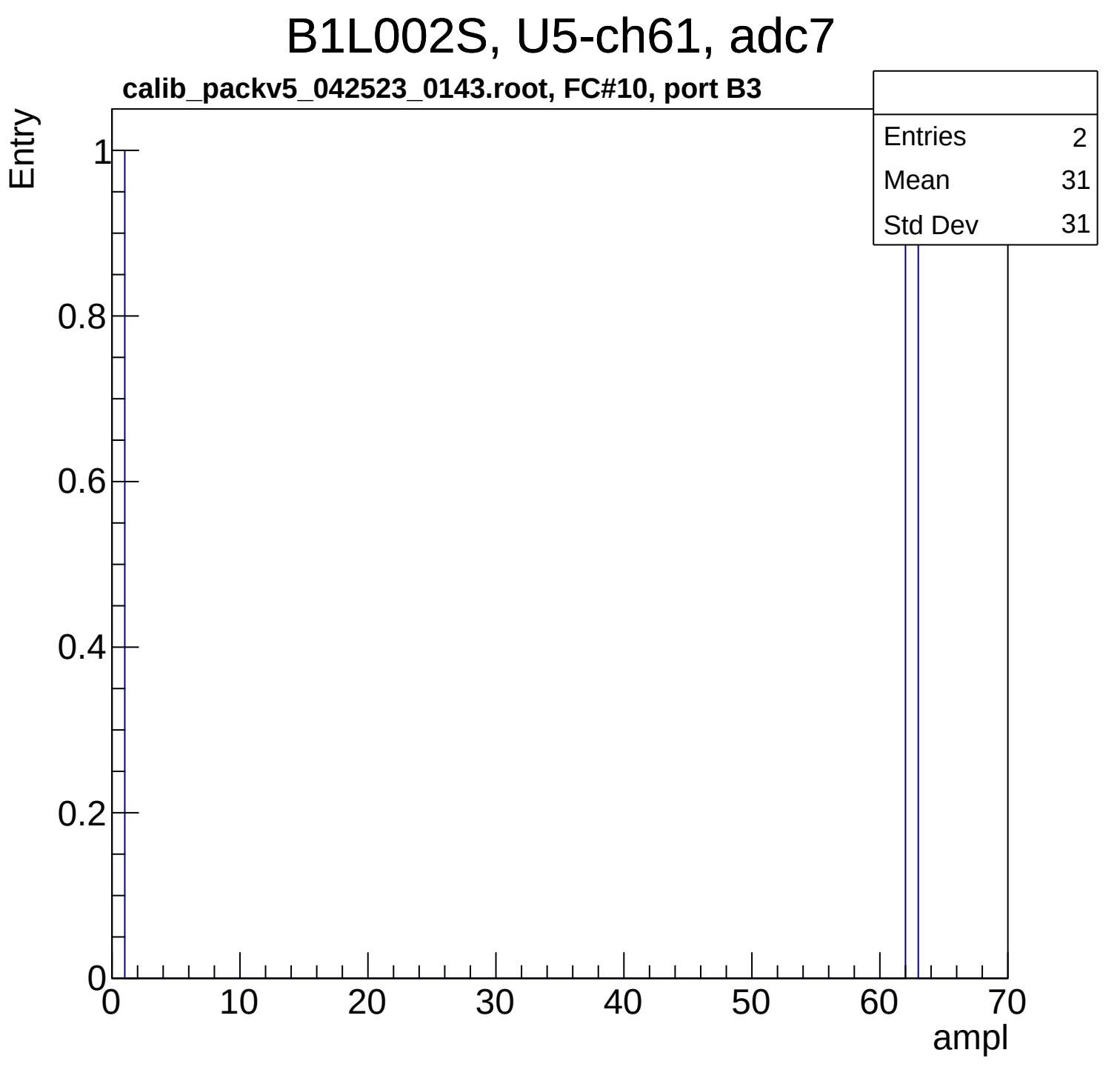
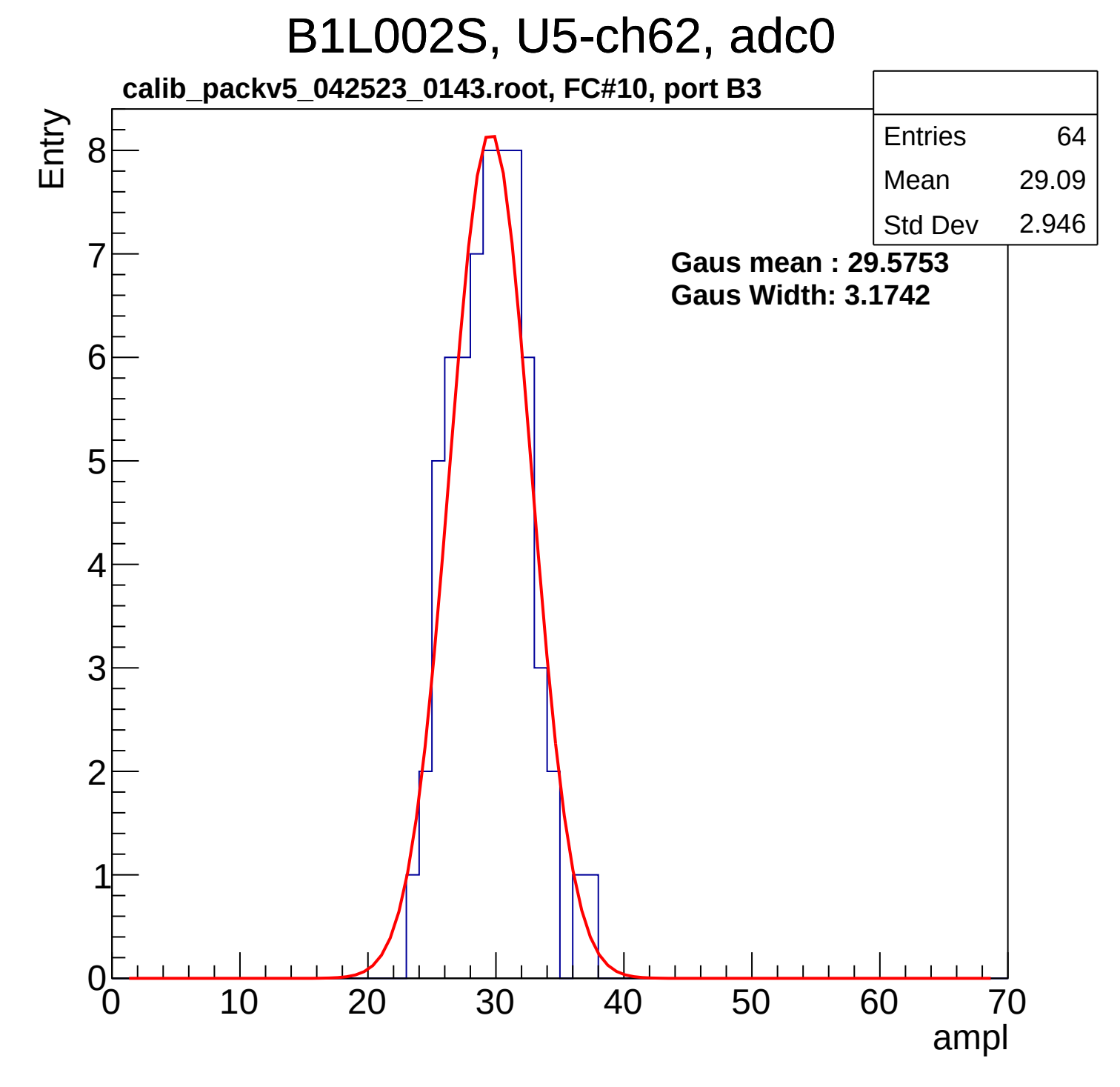
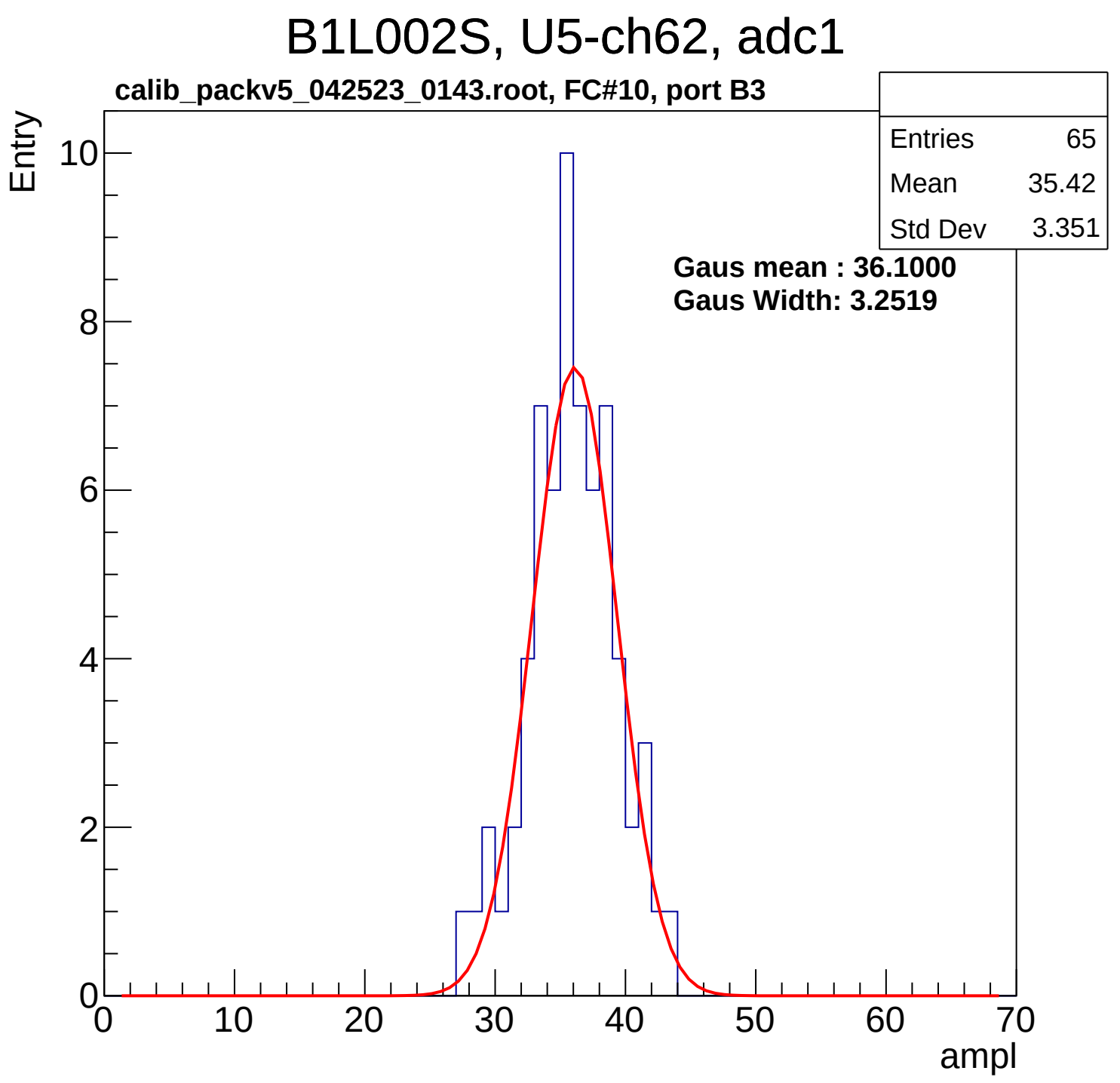
50
60
70
ampl


B1L002S, U5-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.55
Std Dev 17.93
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 29.09
Std Dev 2.946
Gaus mean : 29.5753
Gaus Width: 3.1742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.42
Std Dev 3.351
Gaus mean : 36.1000
Gaus Width: 3.2519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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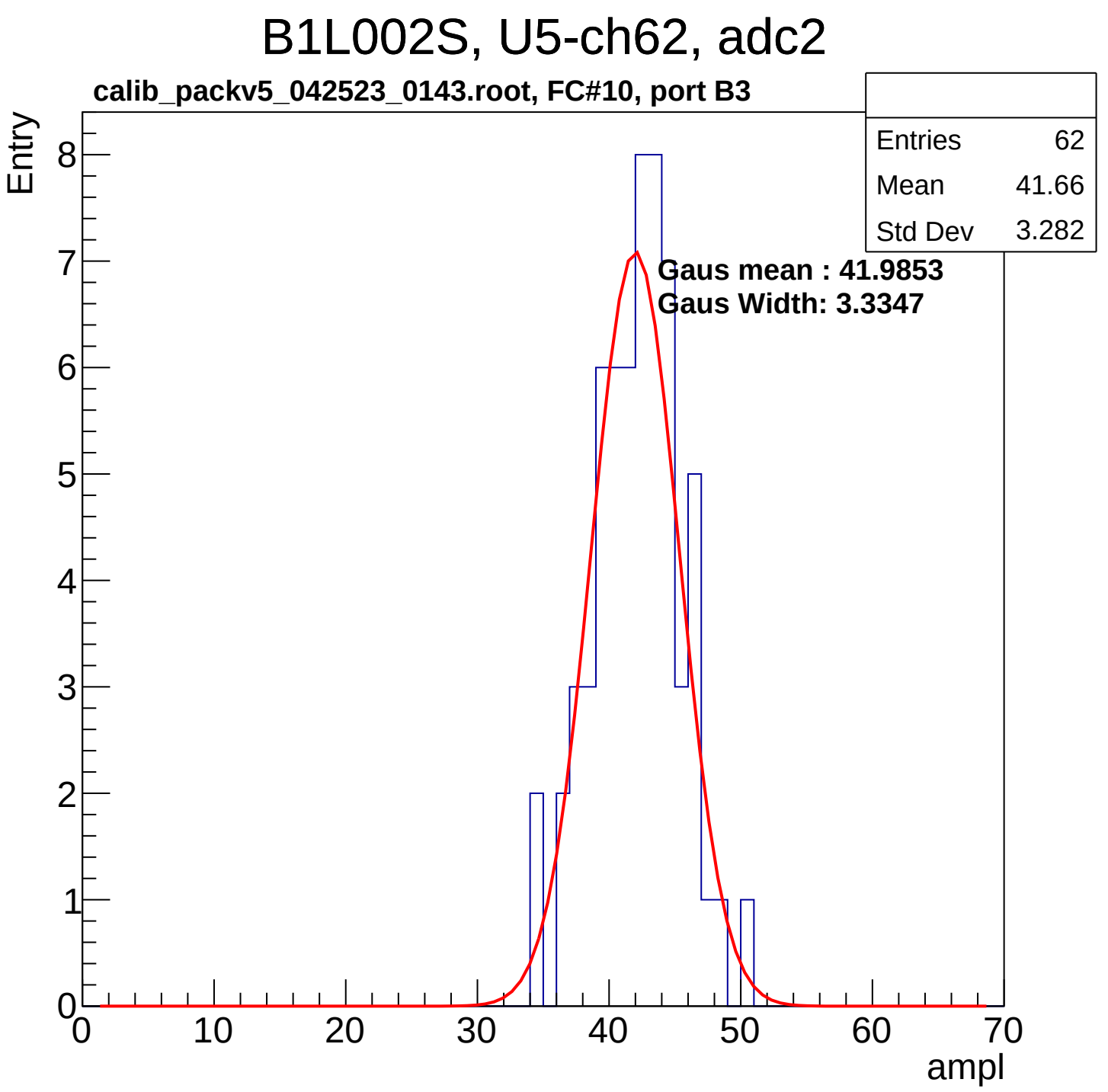
B1L002S, U5-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.66
Std Dev 3.282
Gaus mean : 41.9853
Gaus Width: 3.3347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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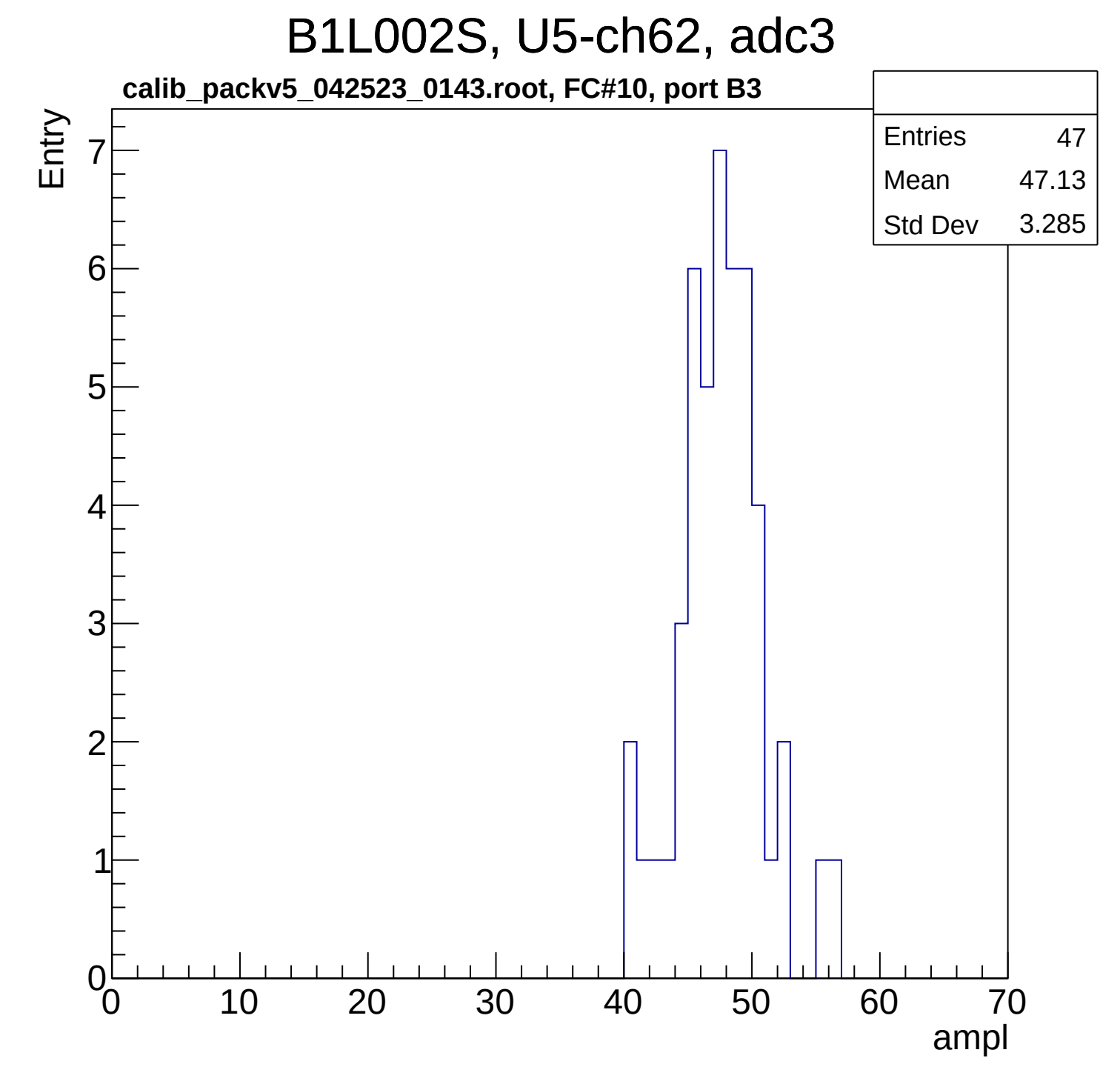
B1L002S, U5-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.13
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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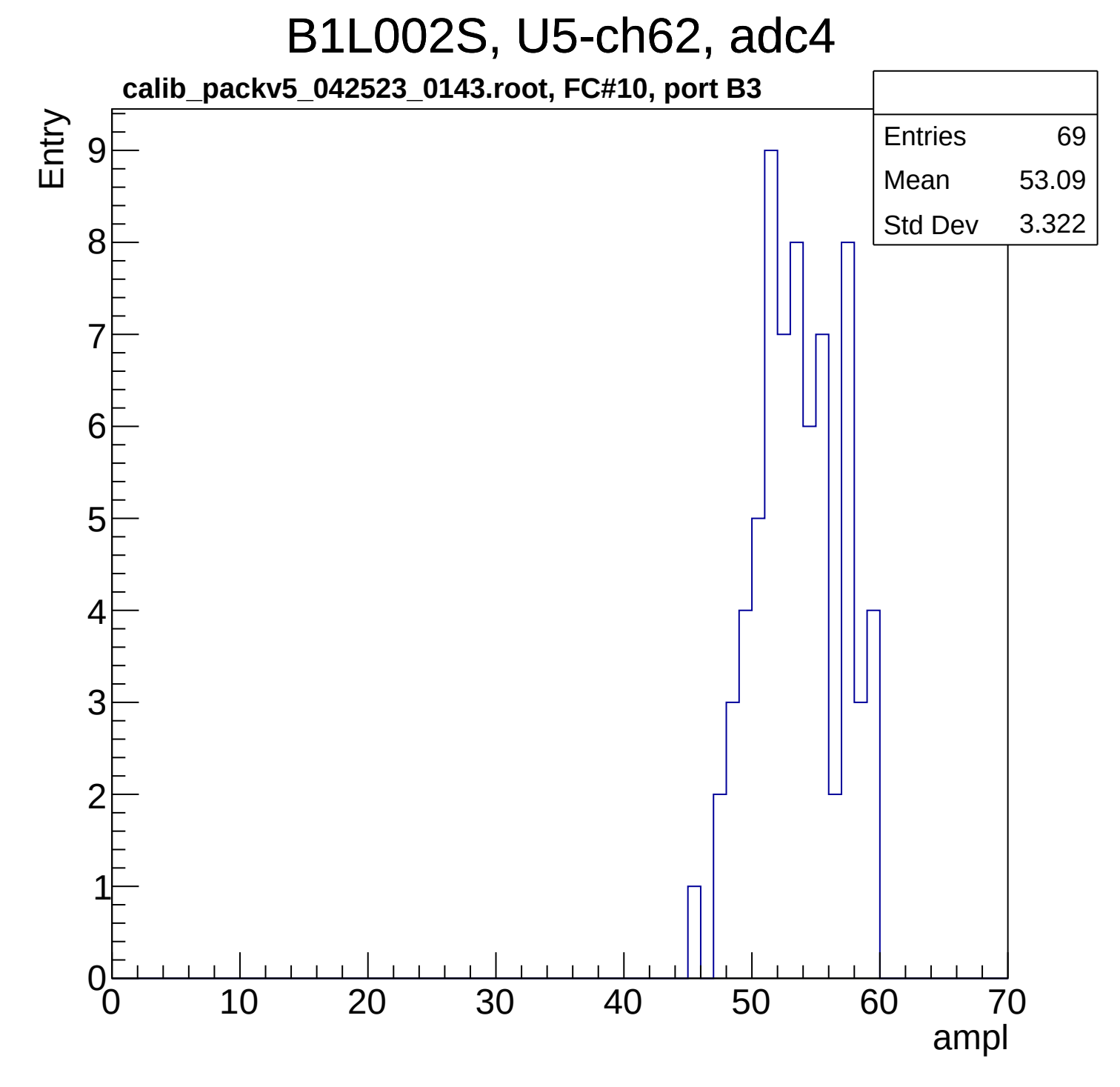
B1L002S, U5-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.09
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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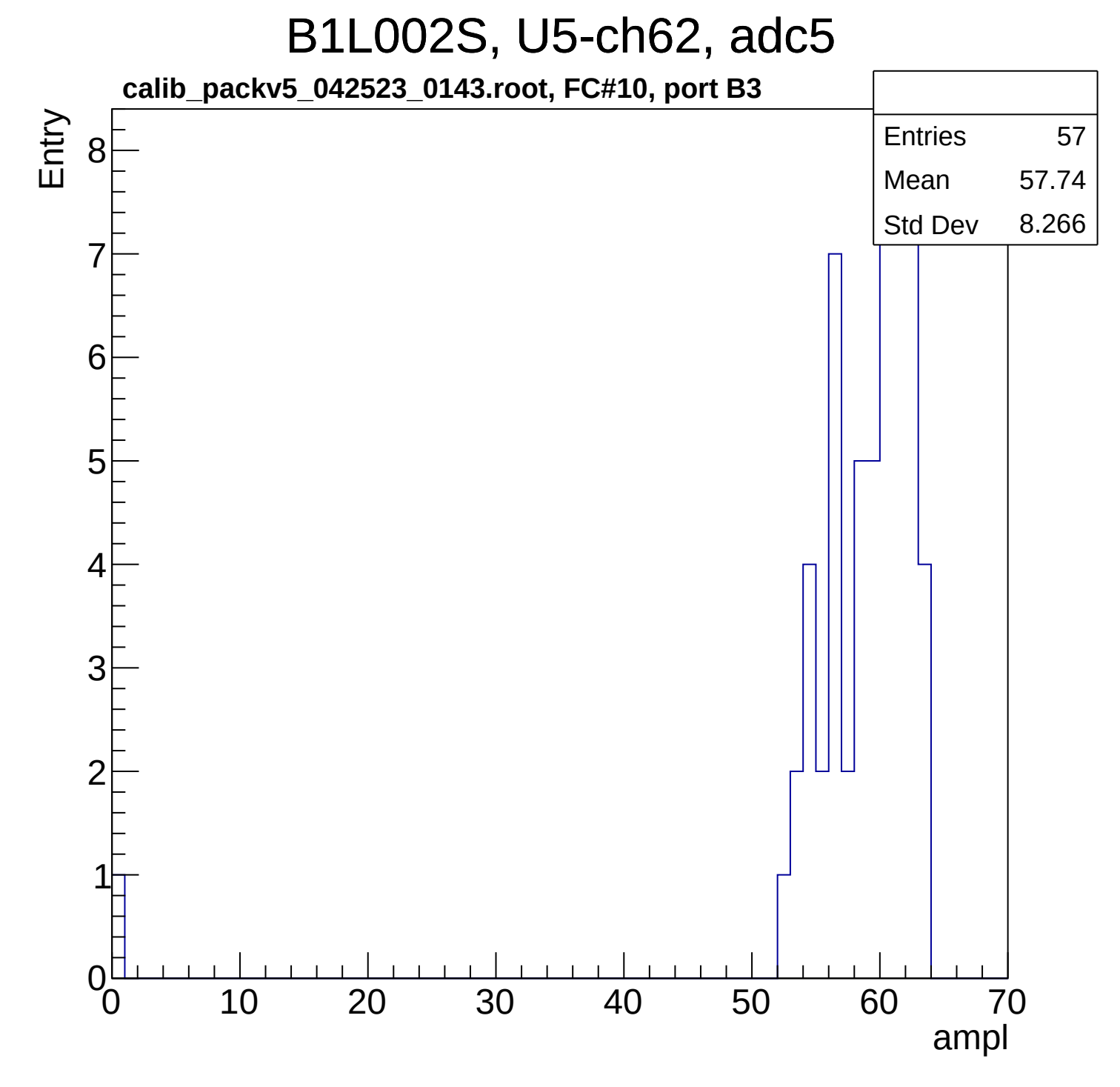
B1L002S, U5-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.74
Std Dev 8.266
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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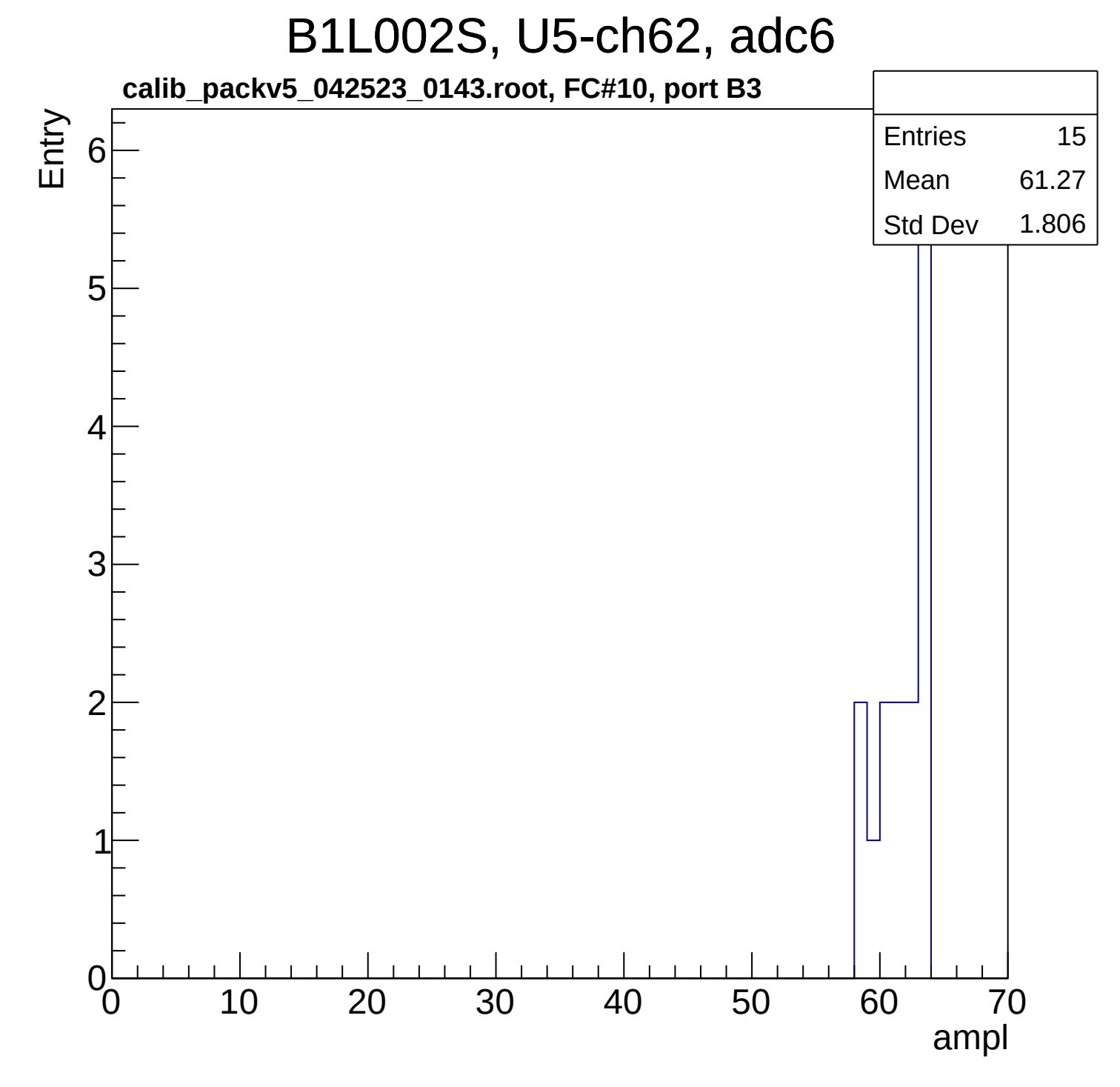
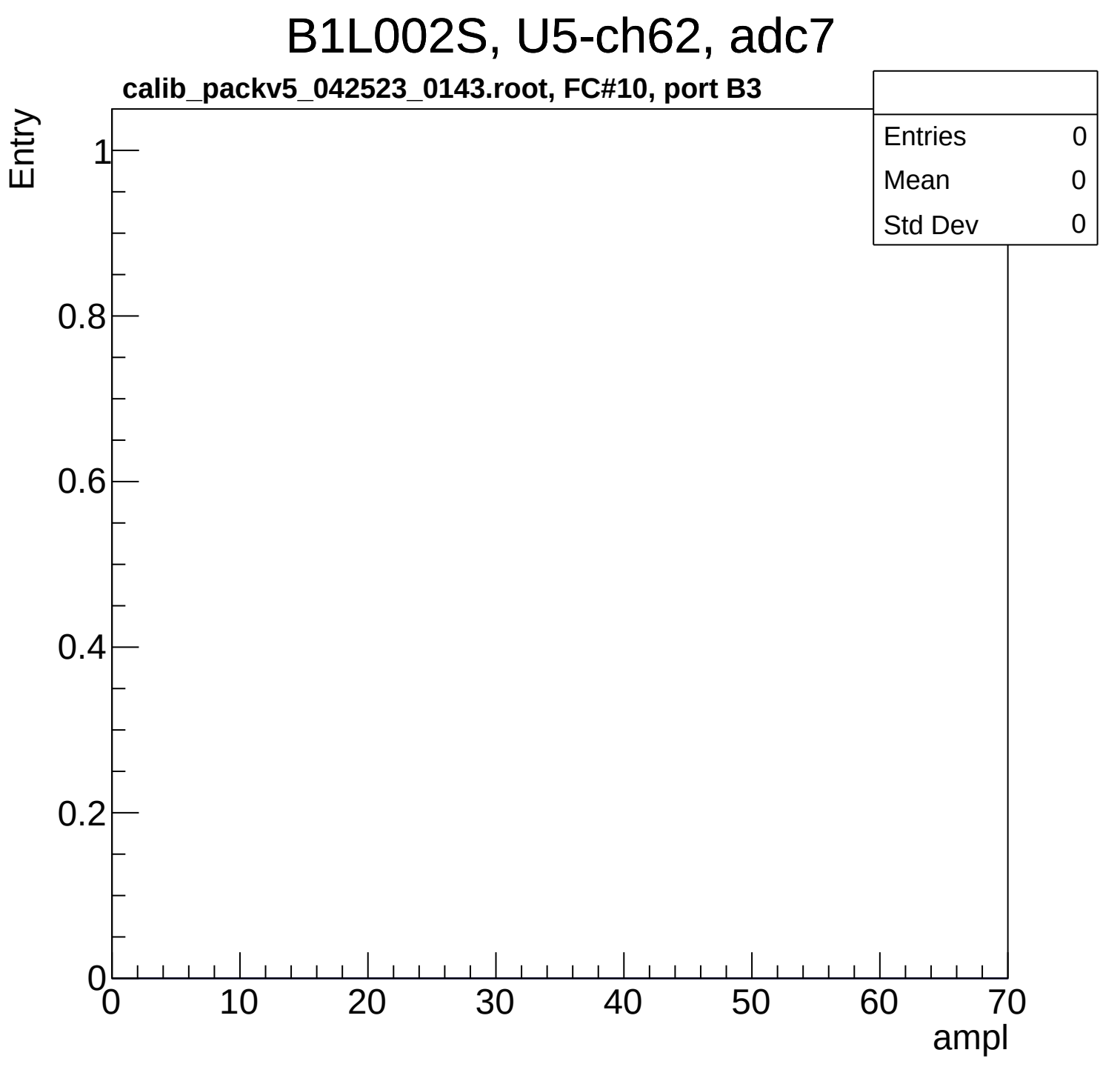
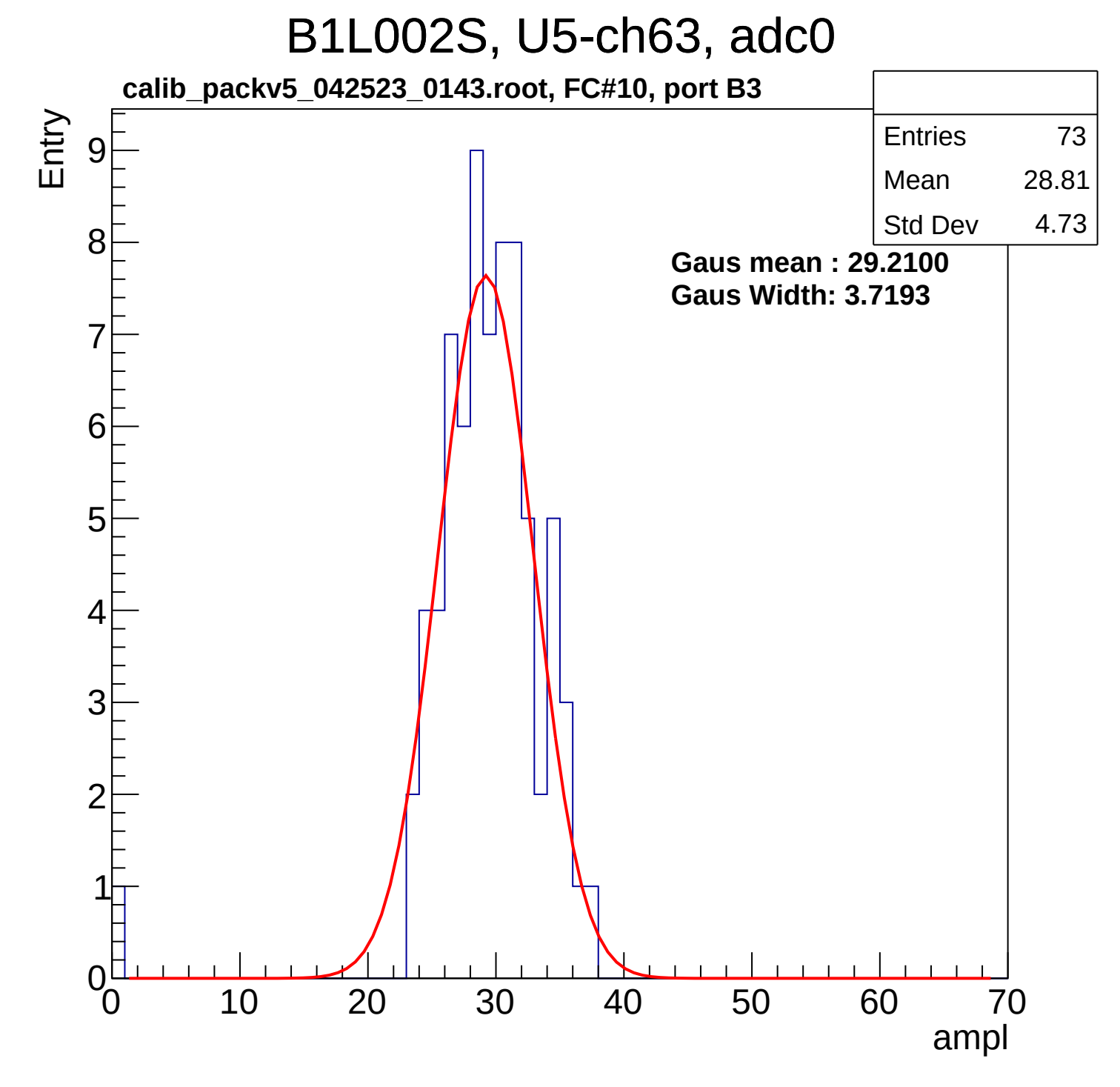
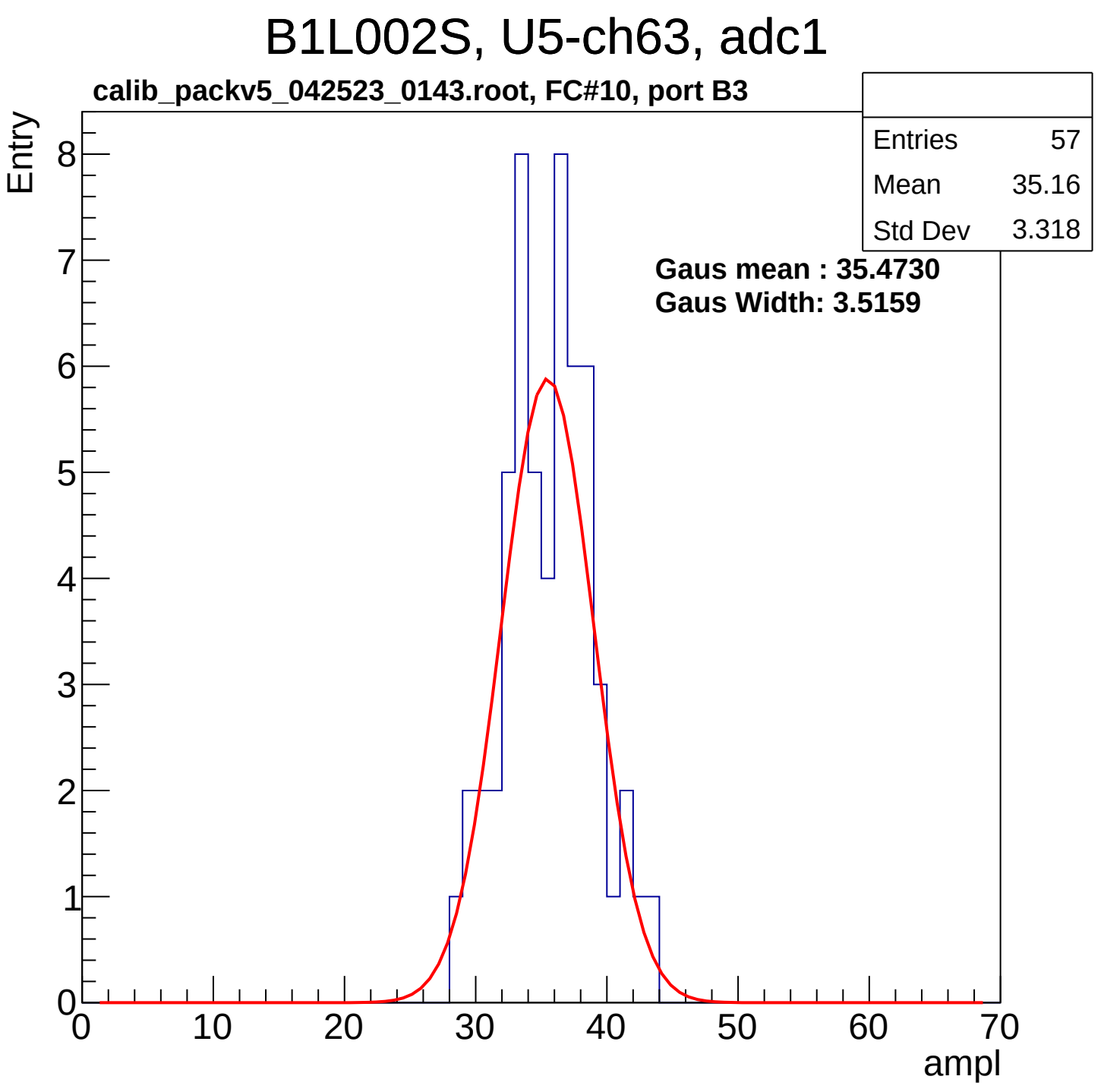
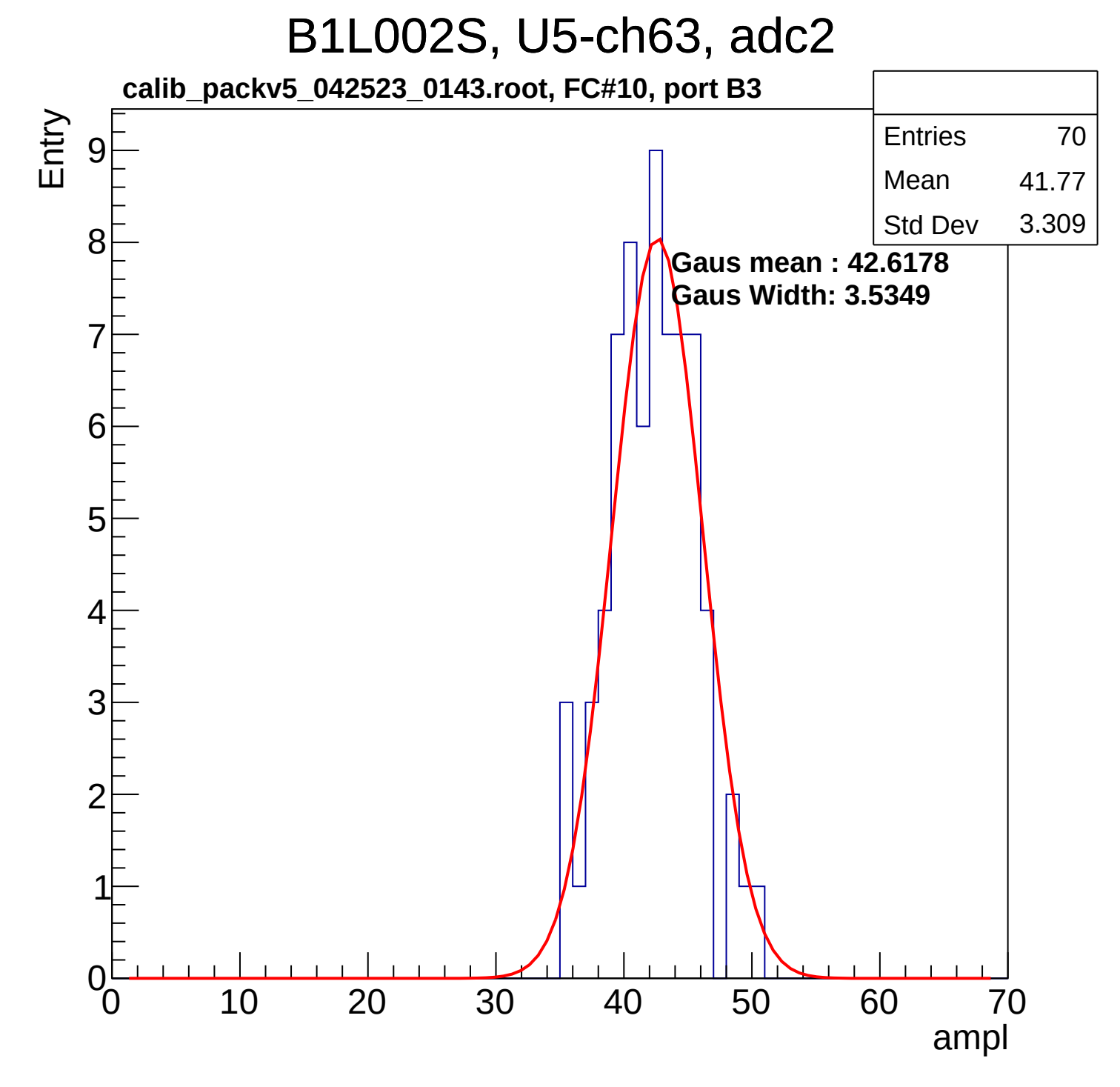

B1L002S, U5-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.27
Std Dev 1.806
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.81
Std Dev 4.73
Gaus mean : 29.2100
Gaus Width: 3.7193
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.16
Std Dev 3.318
Gaus mean : 35.4730
Gaus Width: 3.5159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.77
Std Dev 3.309
Gaus mean : 42.6178
Gaus Width: 3.5349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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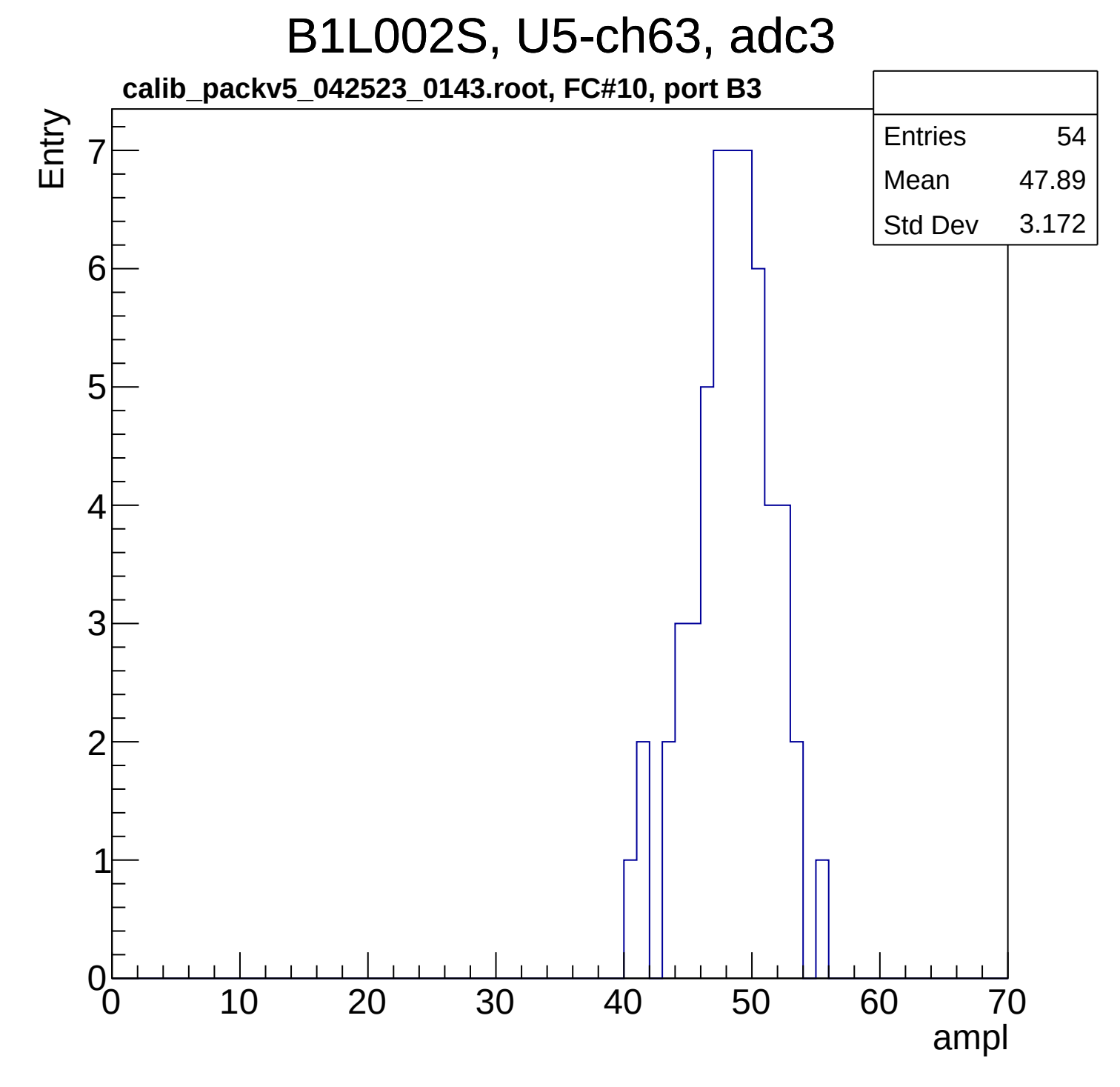
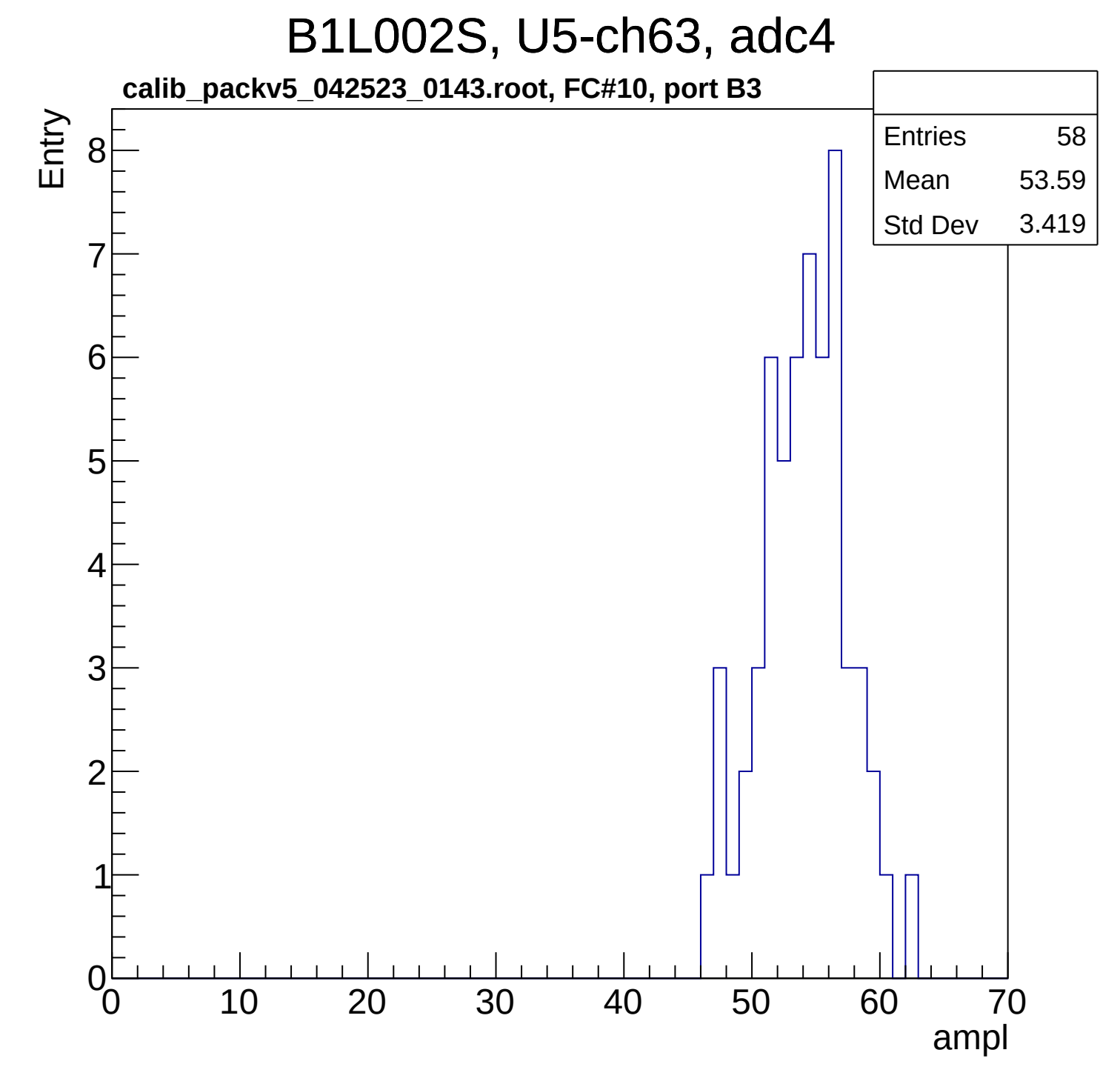
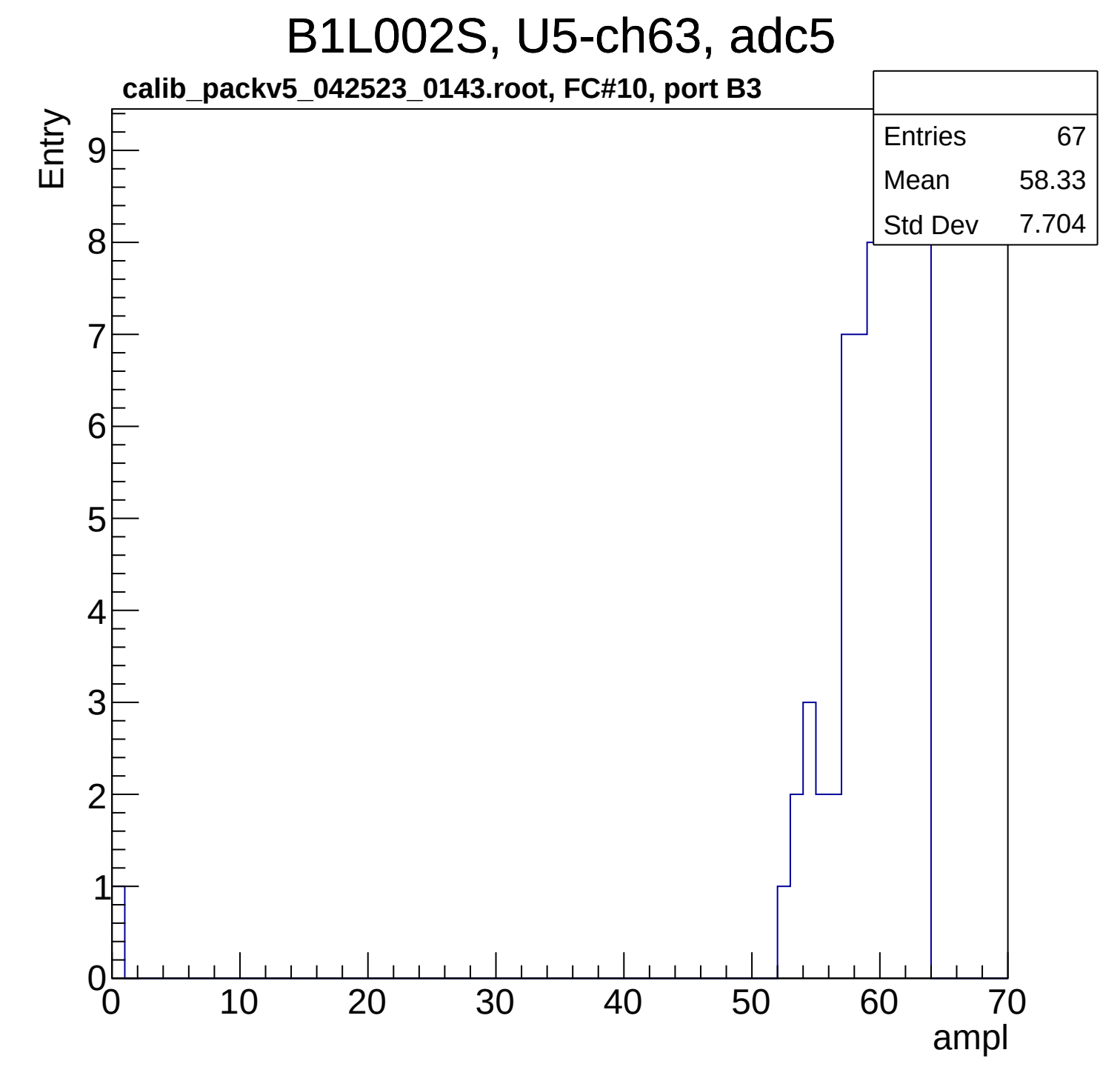
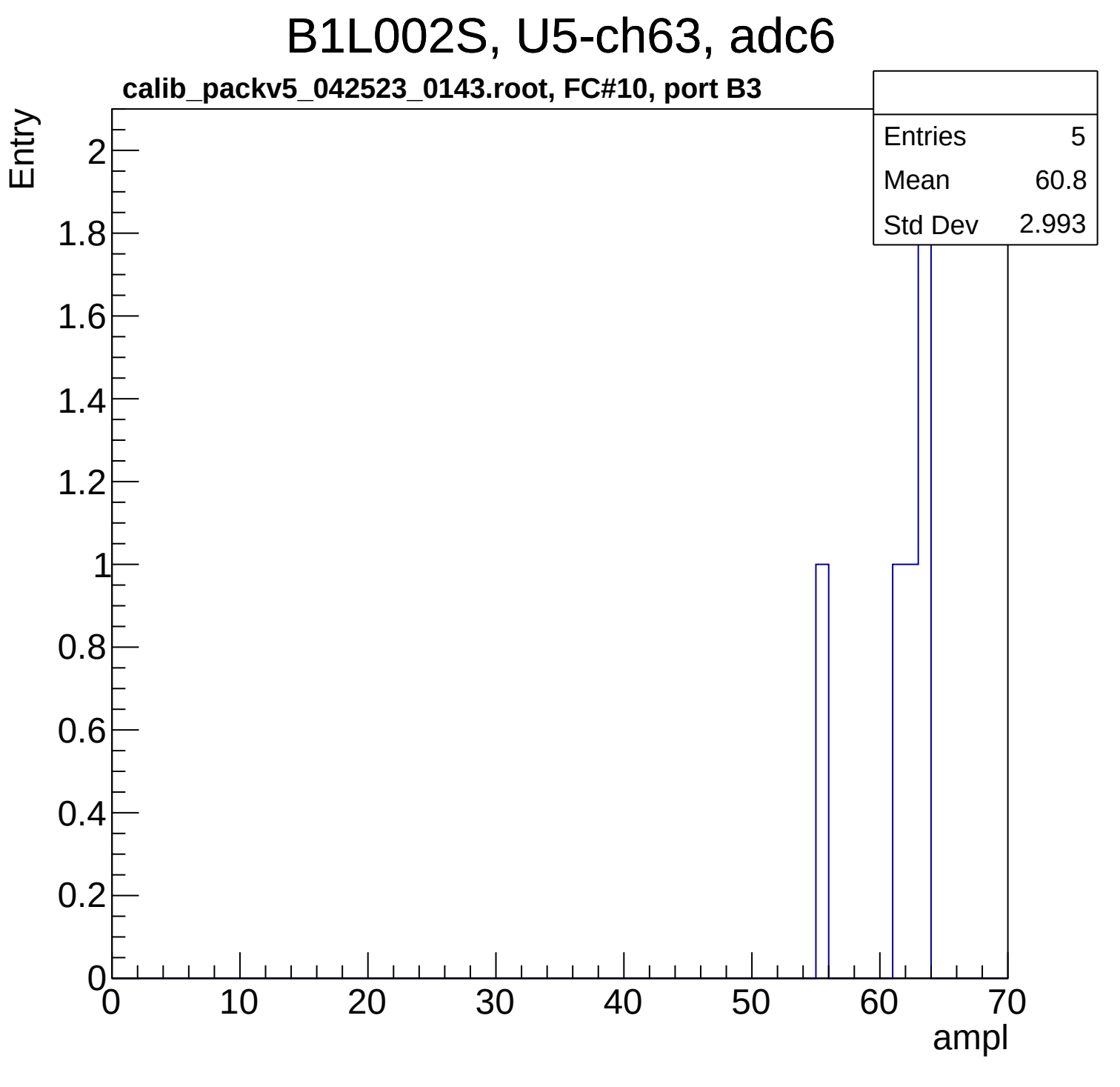
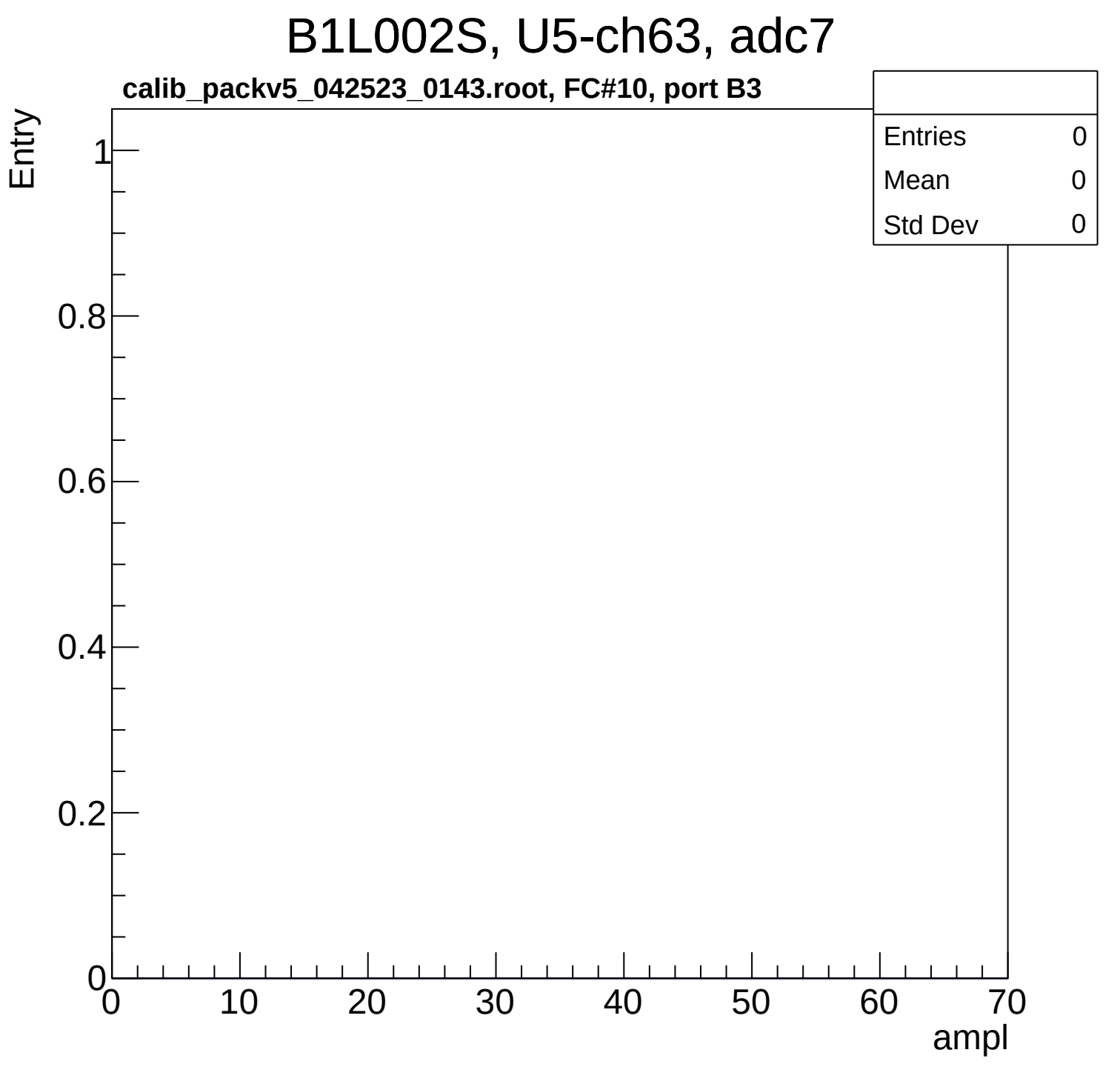
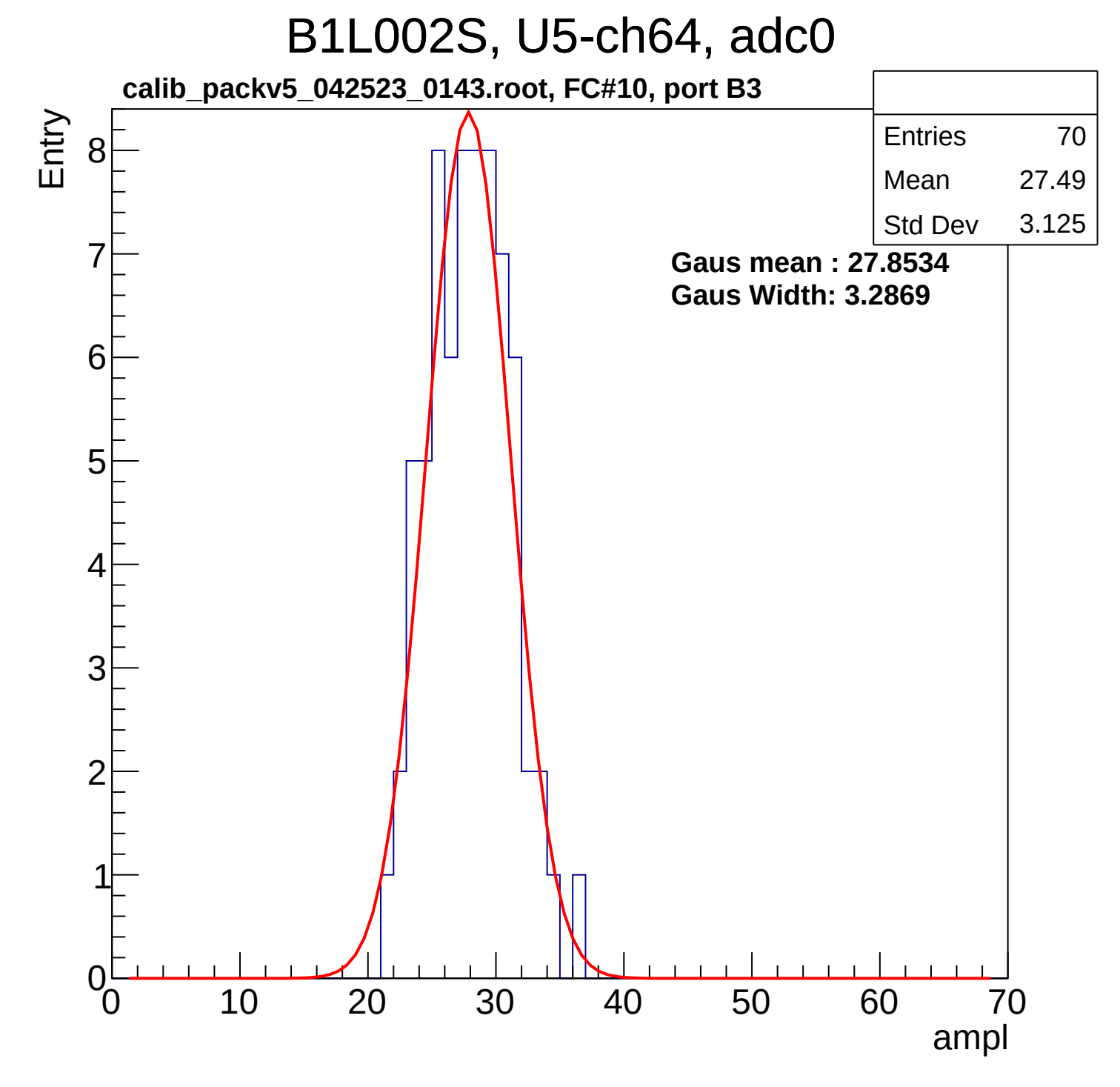

B1L002S, U5-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.89
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

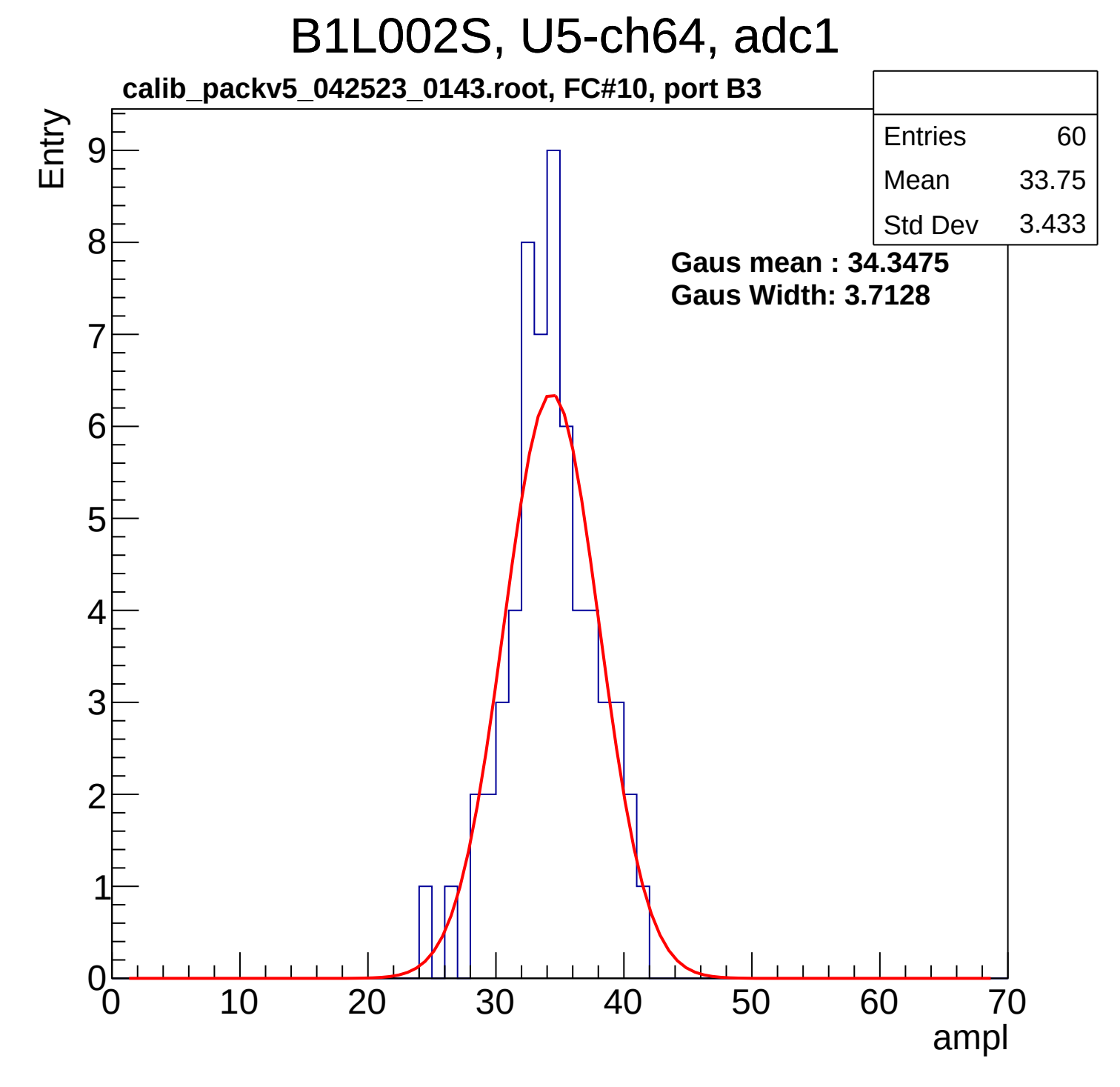

B1L002S, U5-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.59
Std Dev 3.419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

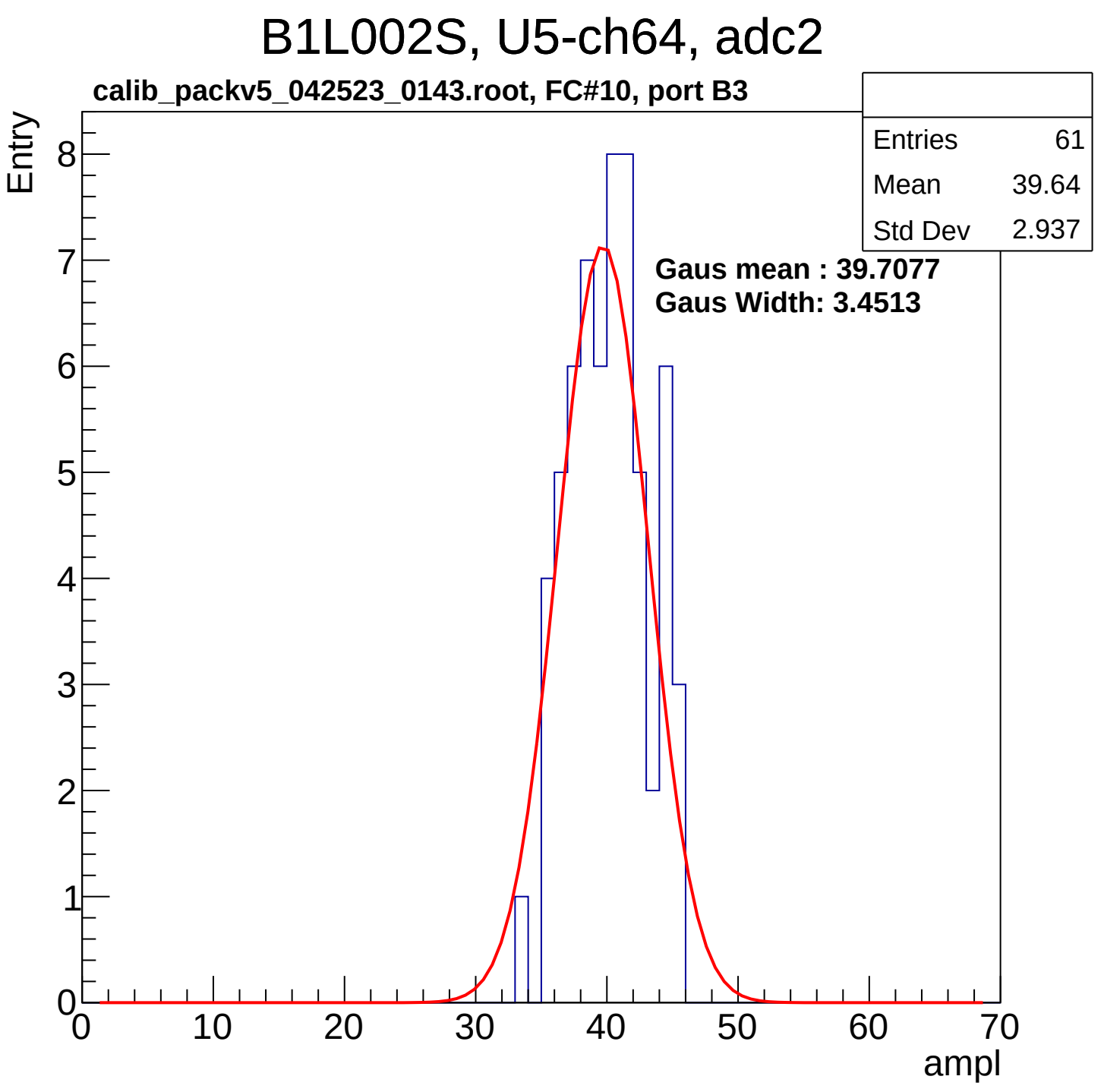

B1L002S, U5-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.33
Std Dev 7.704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 60.8
Std Dev 2.993
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.49
Std Dev 3.125
Gaus mean : 27.8534
Gaus Width: 3.2869
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 33.75
Std Dev 3.433
Gaus mean : 34.3475
Gaus Width: 3.7128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.64
Std Dev 2.937
Gaus mean : 39.7077
Gaus Width: 3.4513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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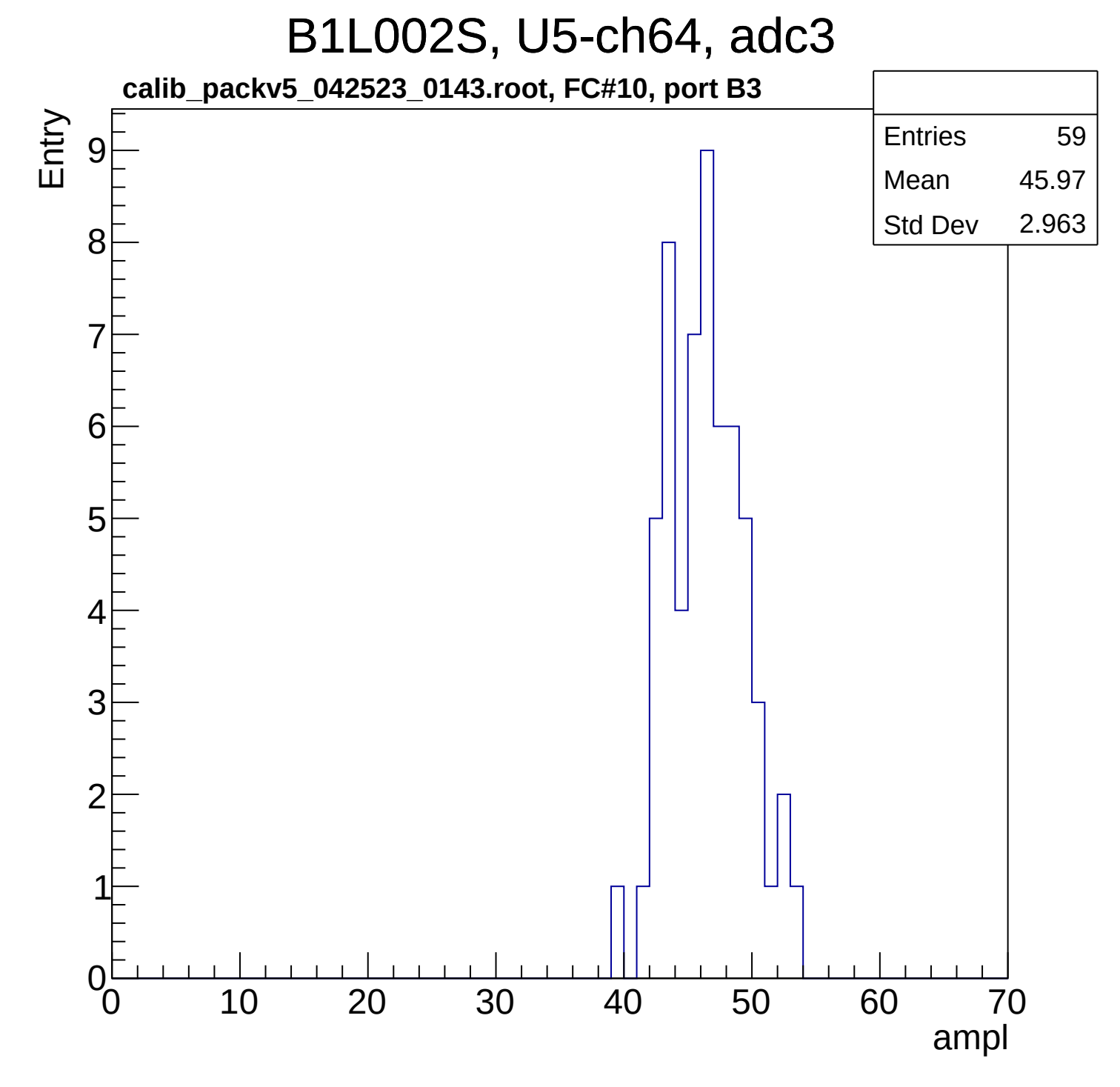
B1L002S, U5-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.97
Std Dev 2.963
Entry
ampl
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4
5
6
7
8
9
0
10
20
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40
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60
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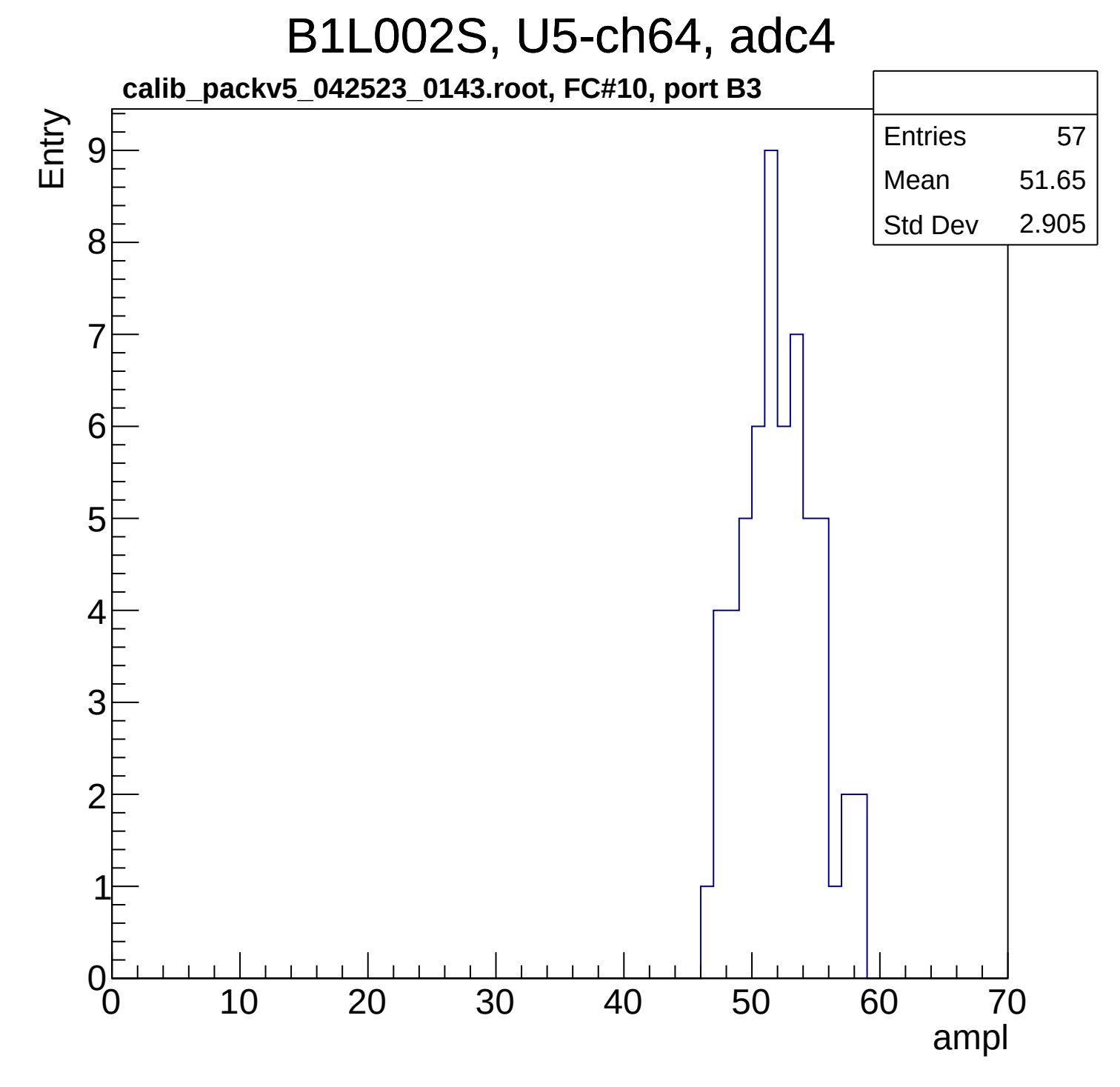
B1L002S, U5-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.65
Std Dev 2.905
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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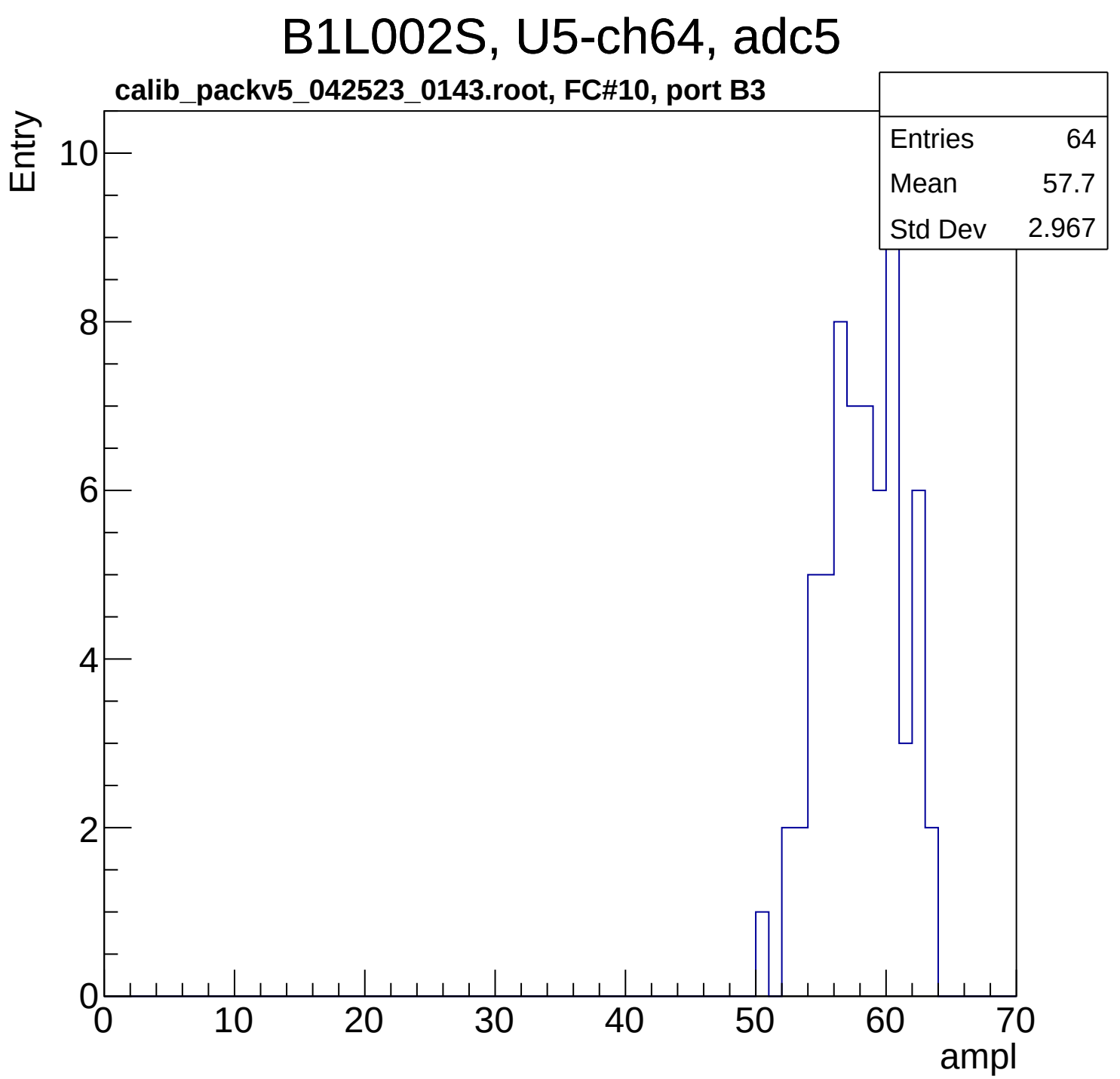
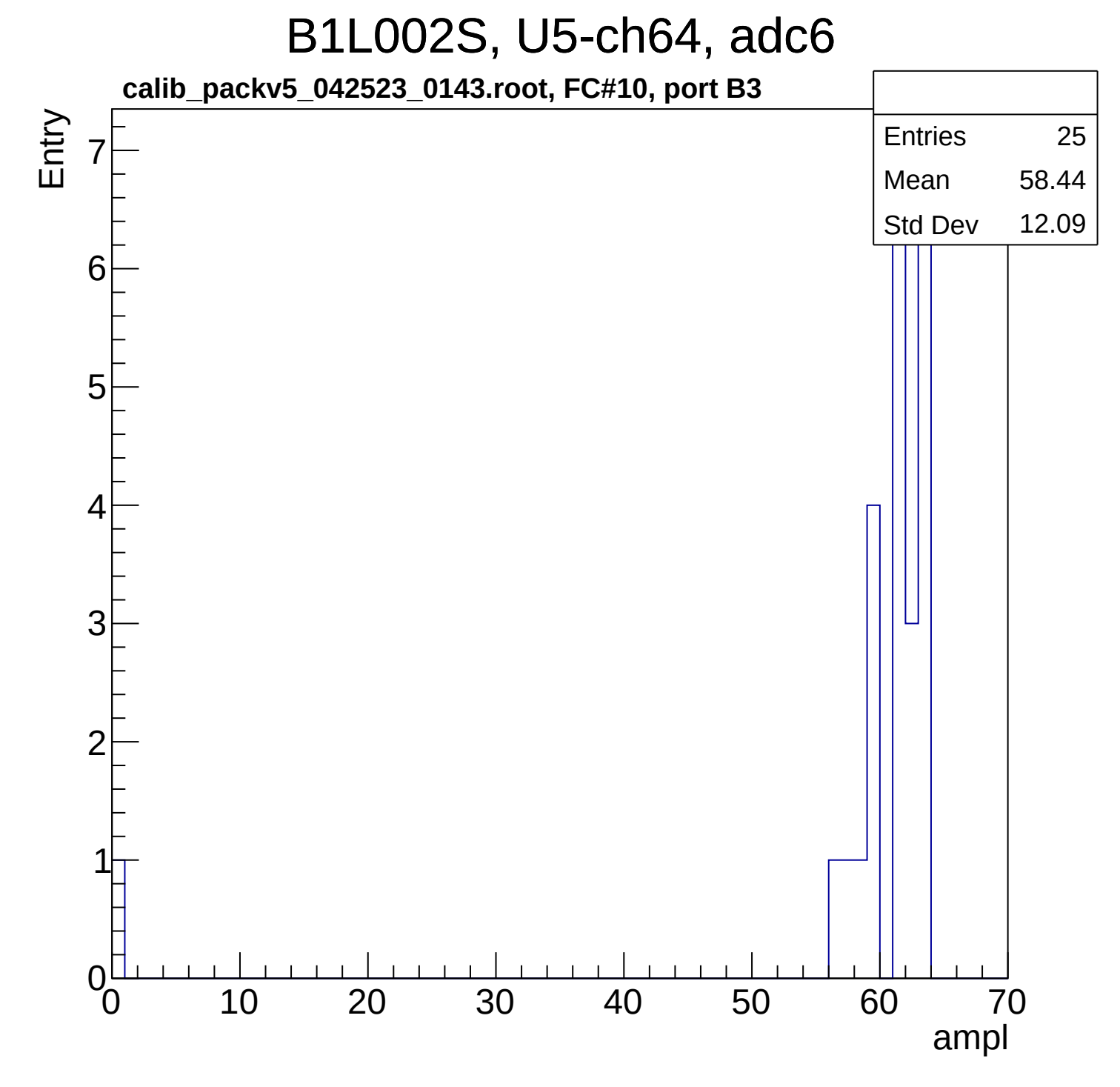

B1L002S, U5-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.7
Std Dev 2.967
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.44
Std Dev 12.09
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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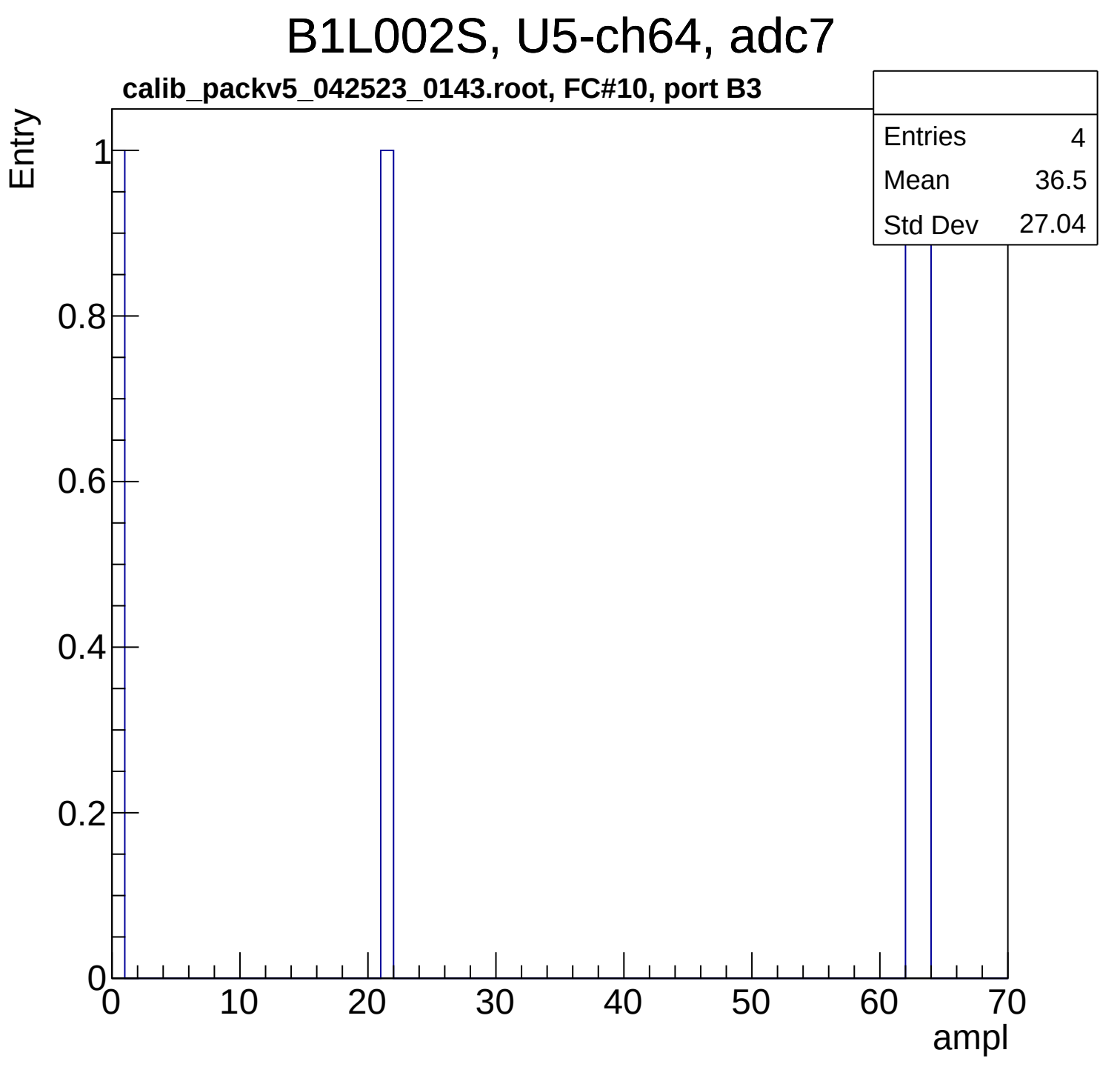
B1L002S, U5-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.5
Std Dev 27.04
Entry
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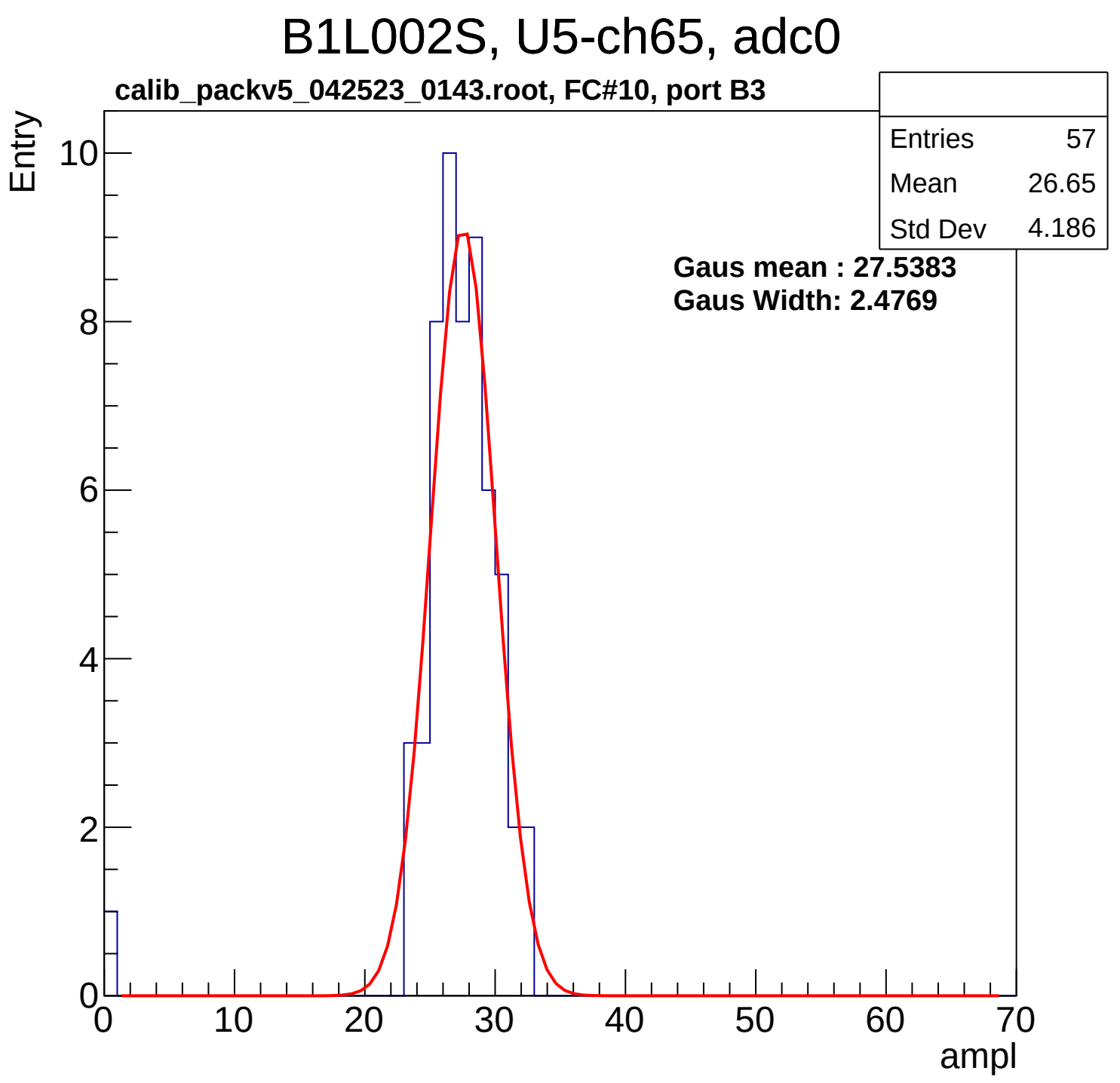
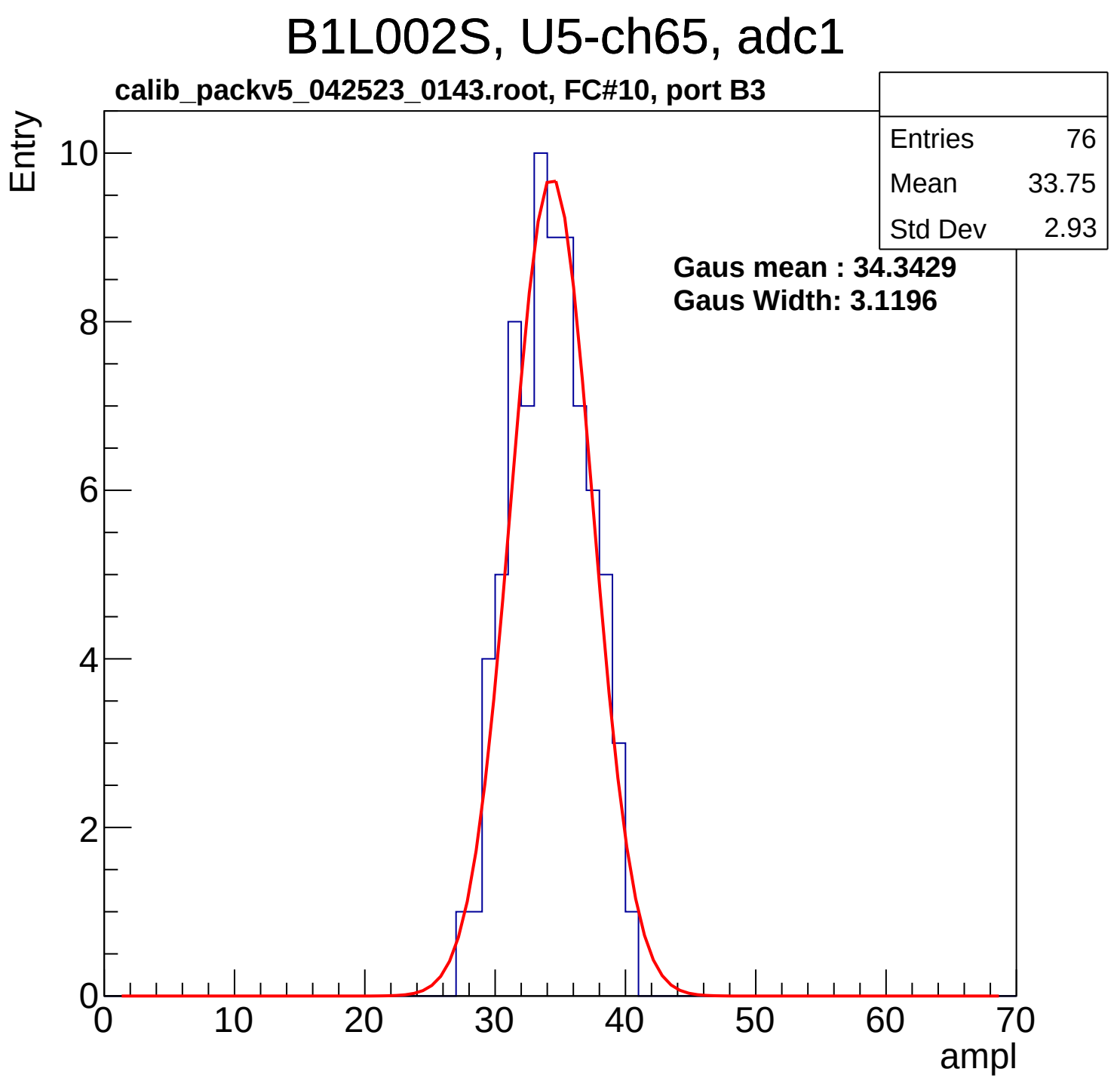
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.65
Std Dev 4.186
Gaus mean : 27.5383
Gaus Width: 2.4769
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.75
Std Dev 2.93
Gaus mean : 34.3429
Gaus Width: 3.1196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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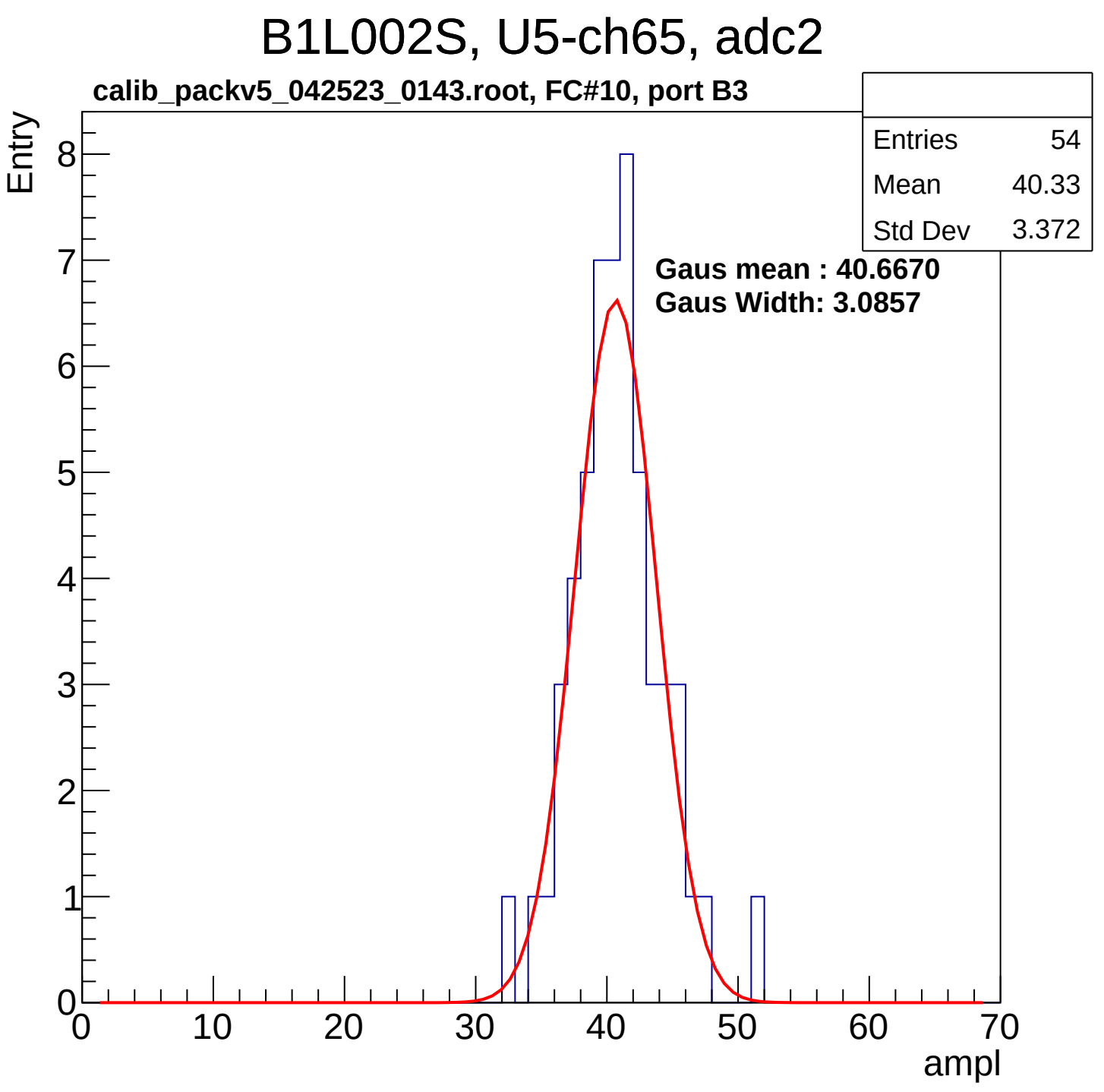
B1L002S, U5-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.33
Std Dev 3.372
Gaus mean : 40.6670
Gaus Width: 3.0857
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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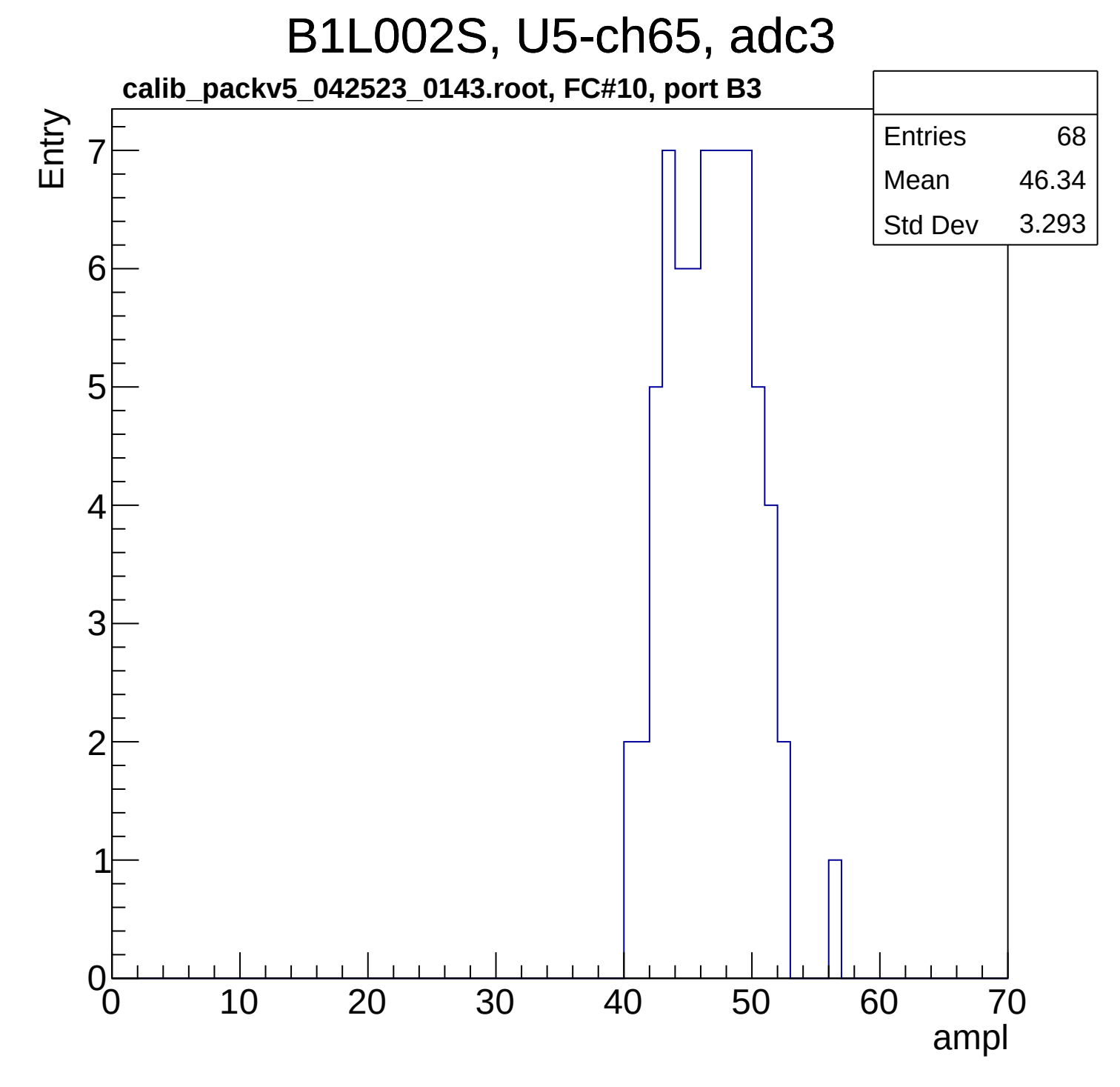
B1L002S, U5-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.34
Std Dev 3.293
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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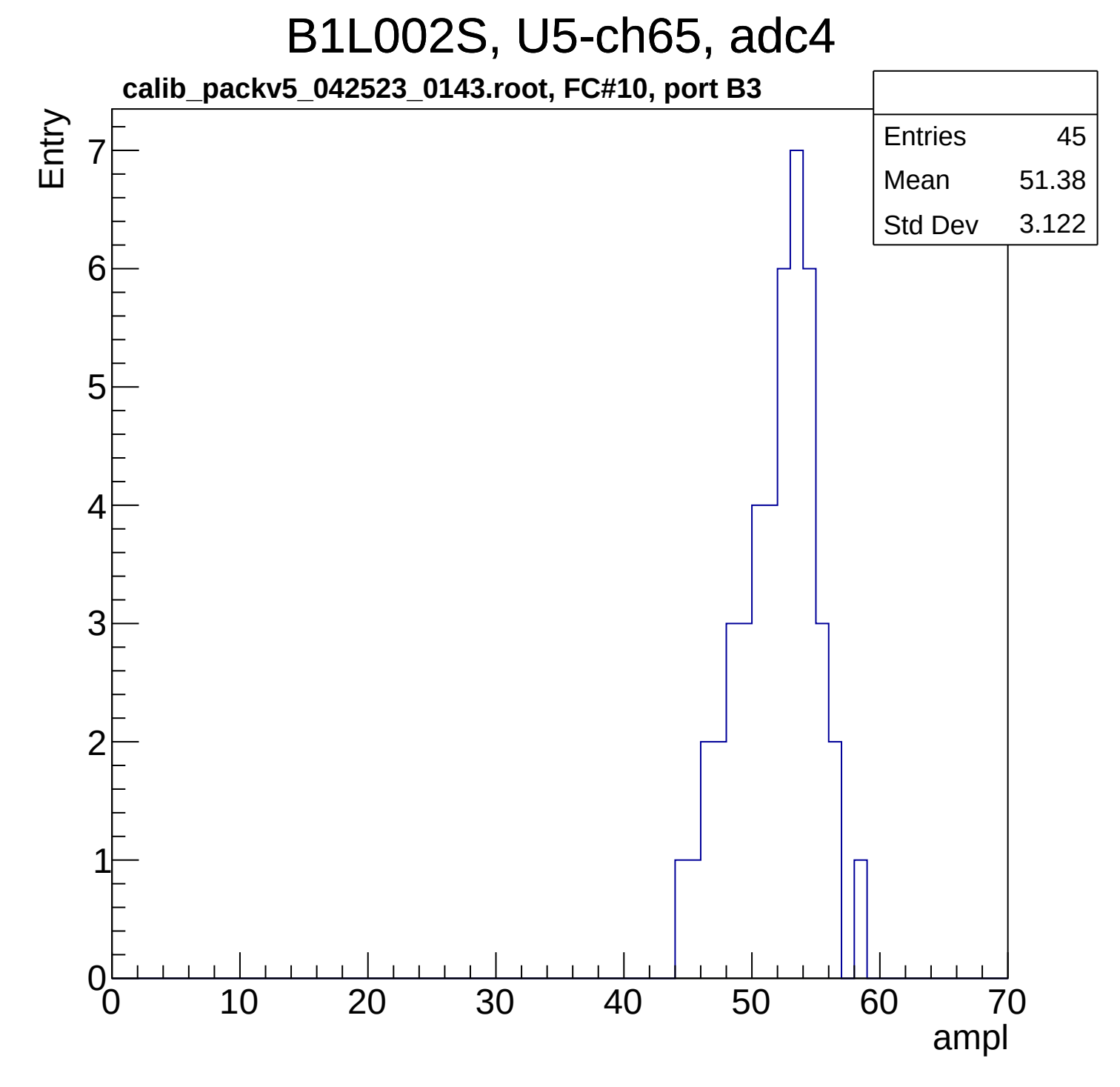
B1L002S, U5-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 51.38
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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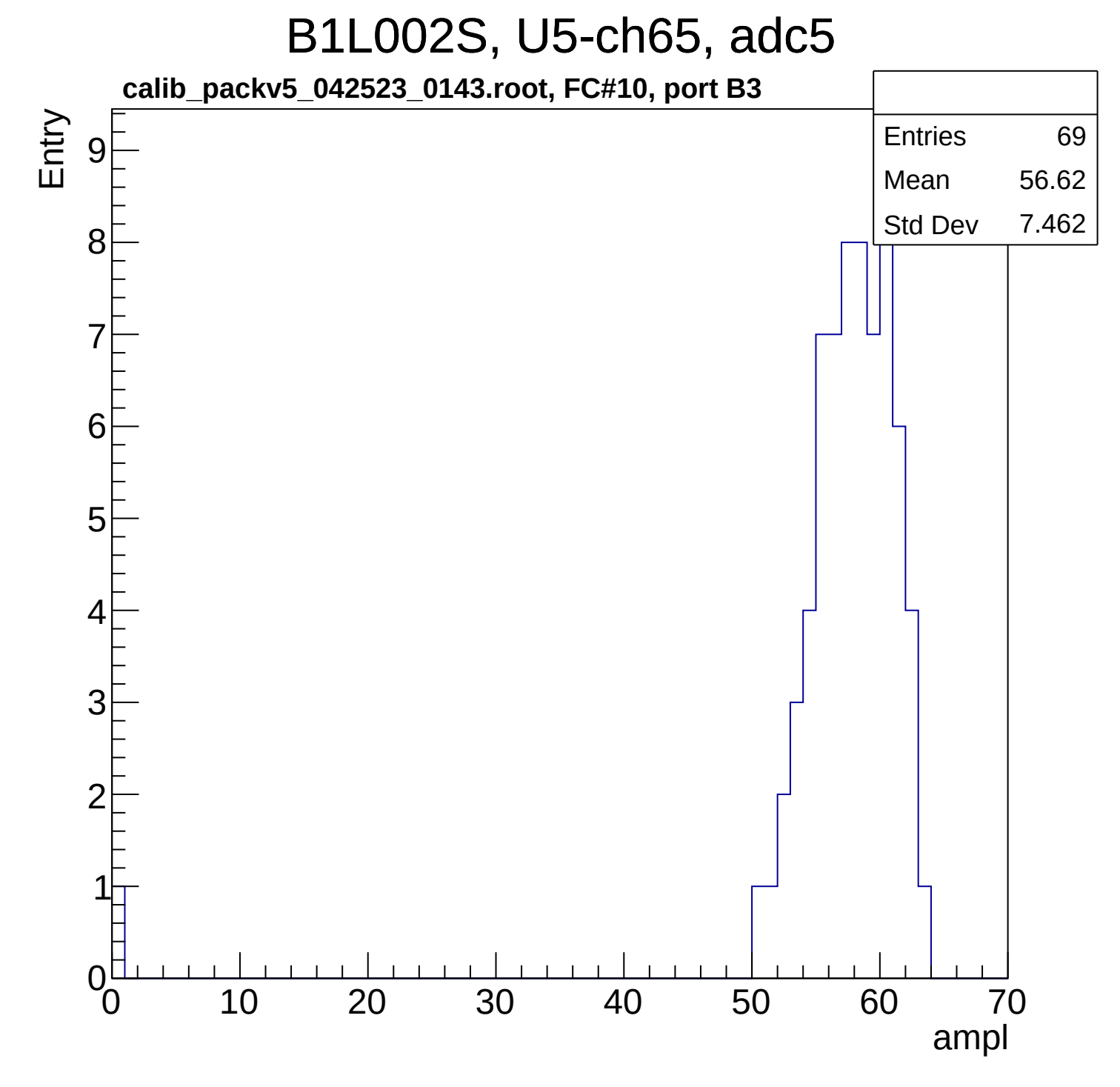
B1L002S, U5-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 56.62
Std Dev 7.462
Entry
ampl
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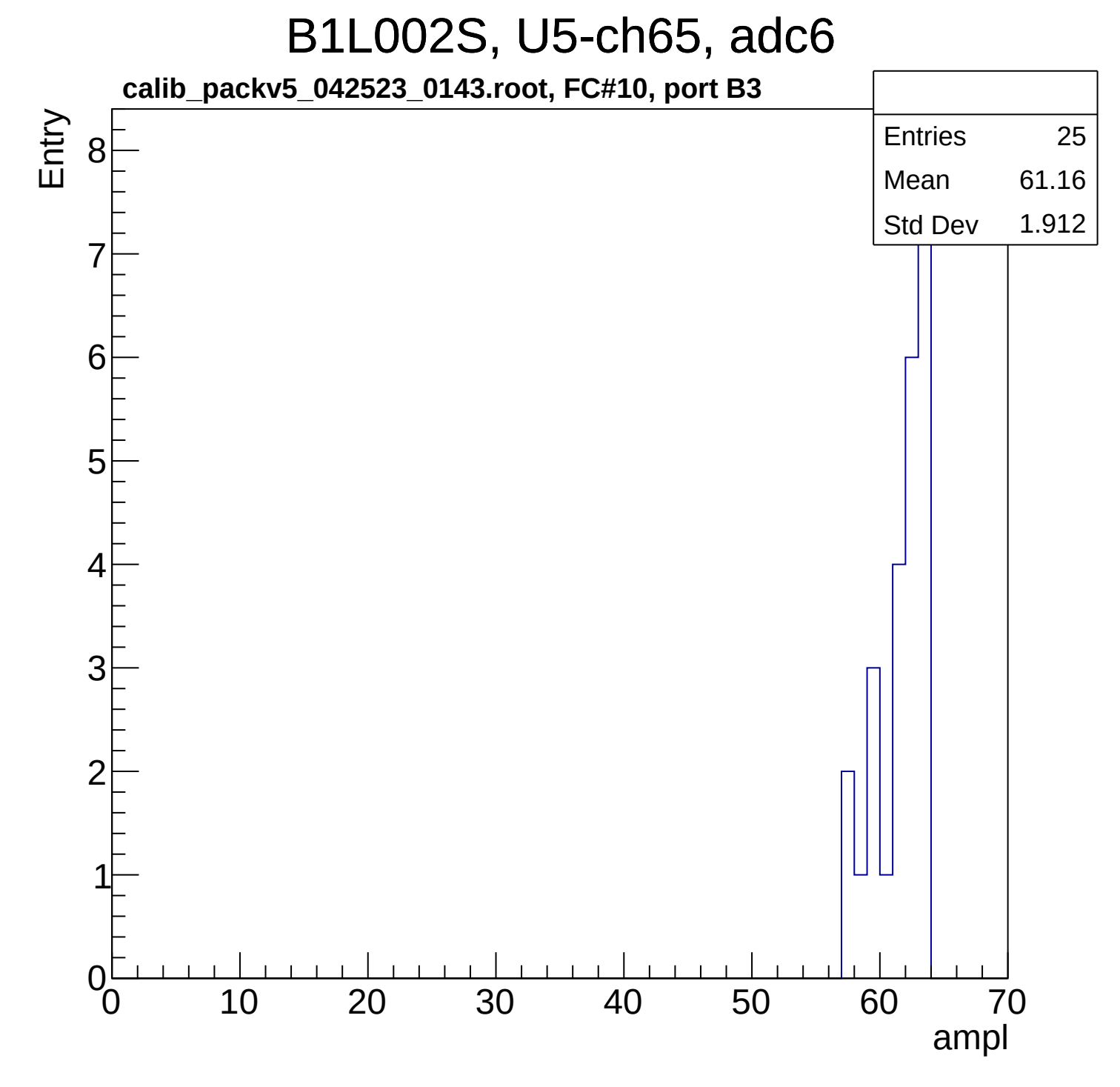
B1L002S, U5-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.16
Std Dev 1.912
Entry
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2
3
4
5
6
7
8
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10
20
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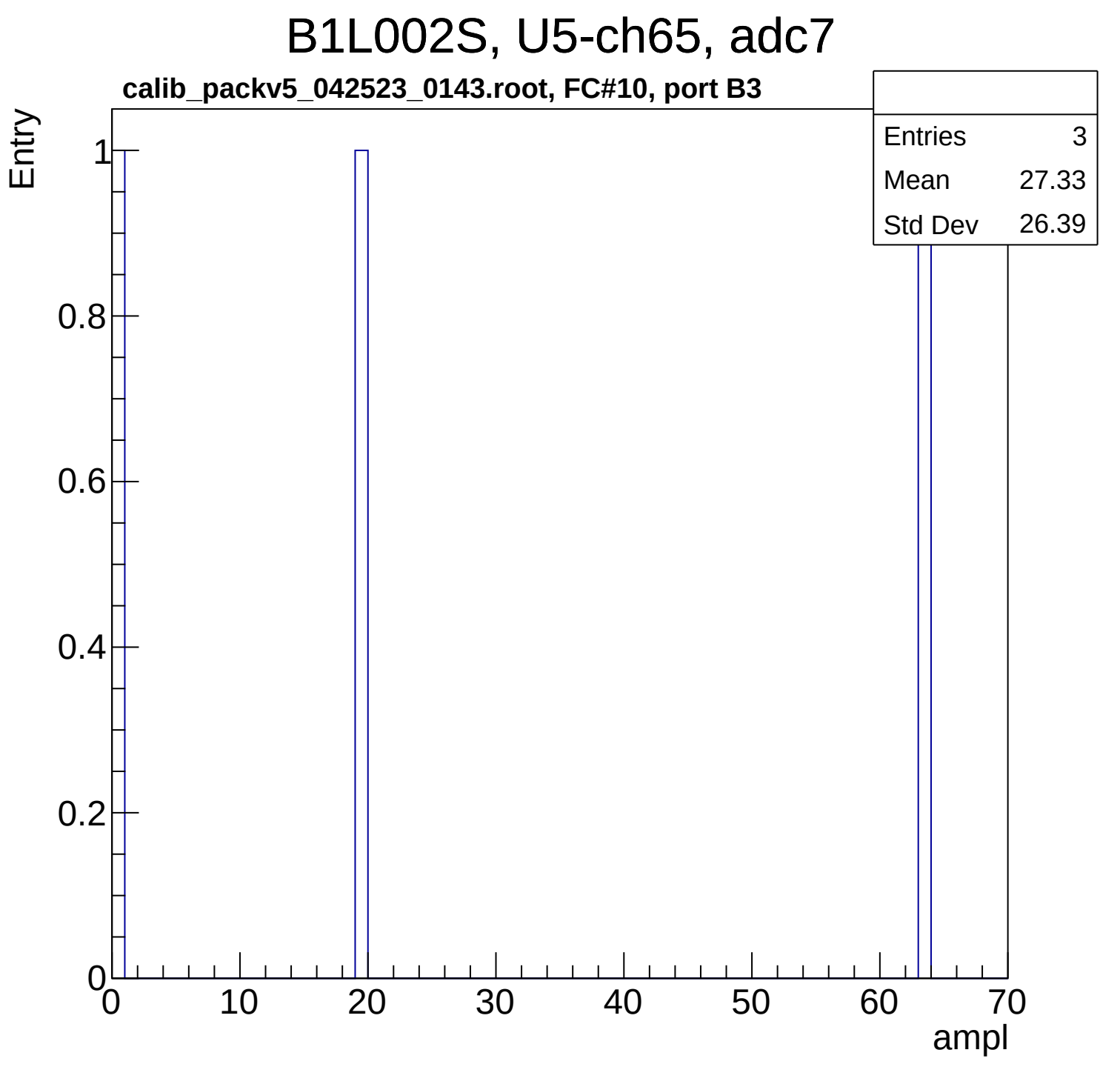
40
50
60
70
ampl


B1L002S, U5-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 26.39
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
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60
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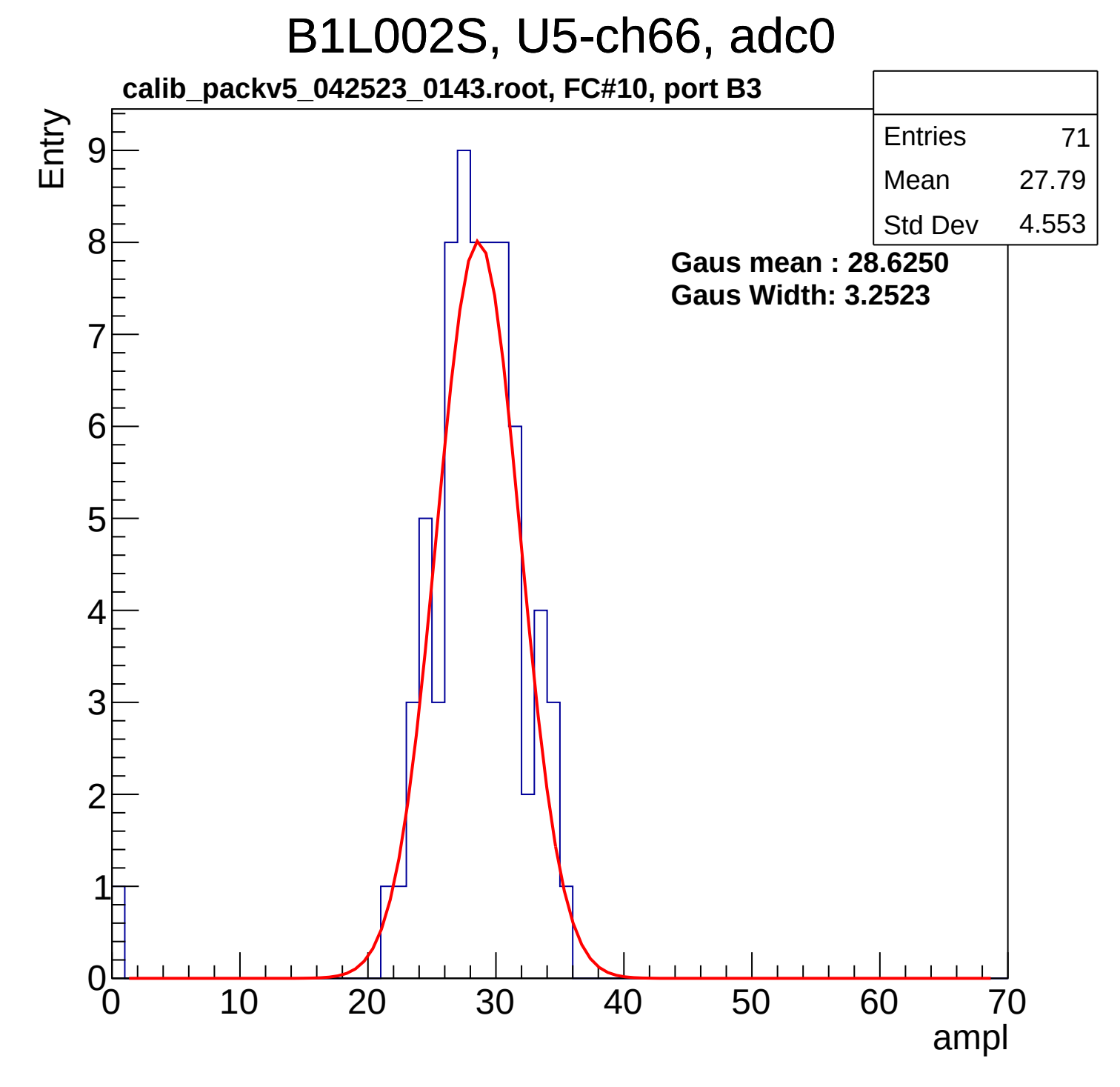
B1L002S, U5-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.79
Std Dev 4.553
Gaus mean : 28.6250
Gaus Width: 3.2523
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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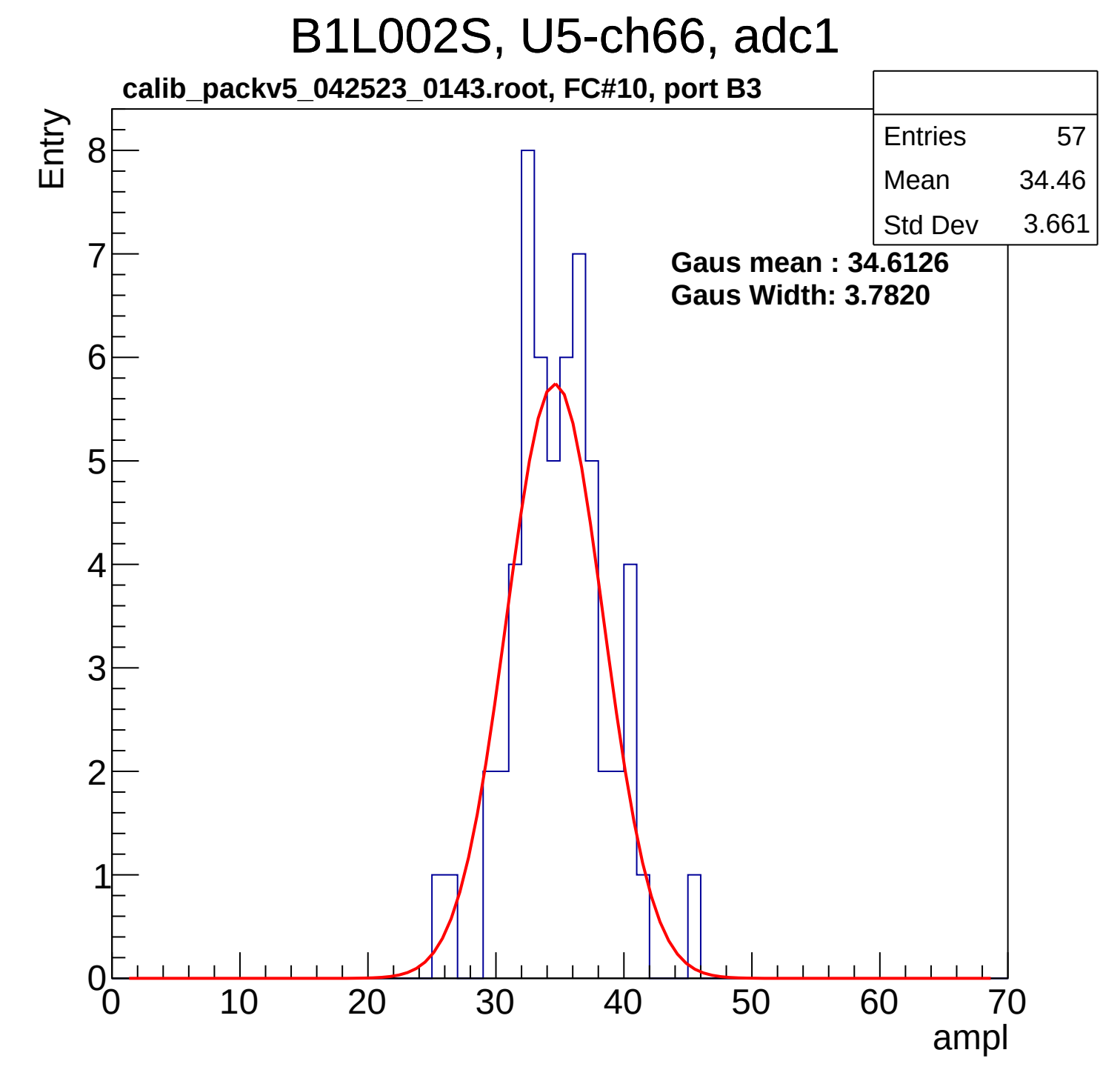
B1L002S, U5-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.46
Std Dev 3.661
Gaus mean : 34.6126
Gaus Width: 3.7820
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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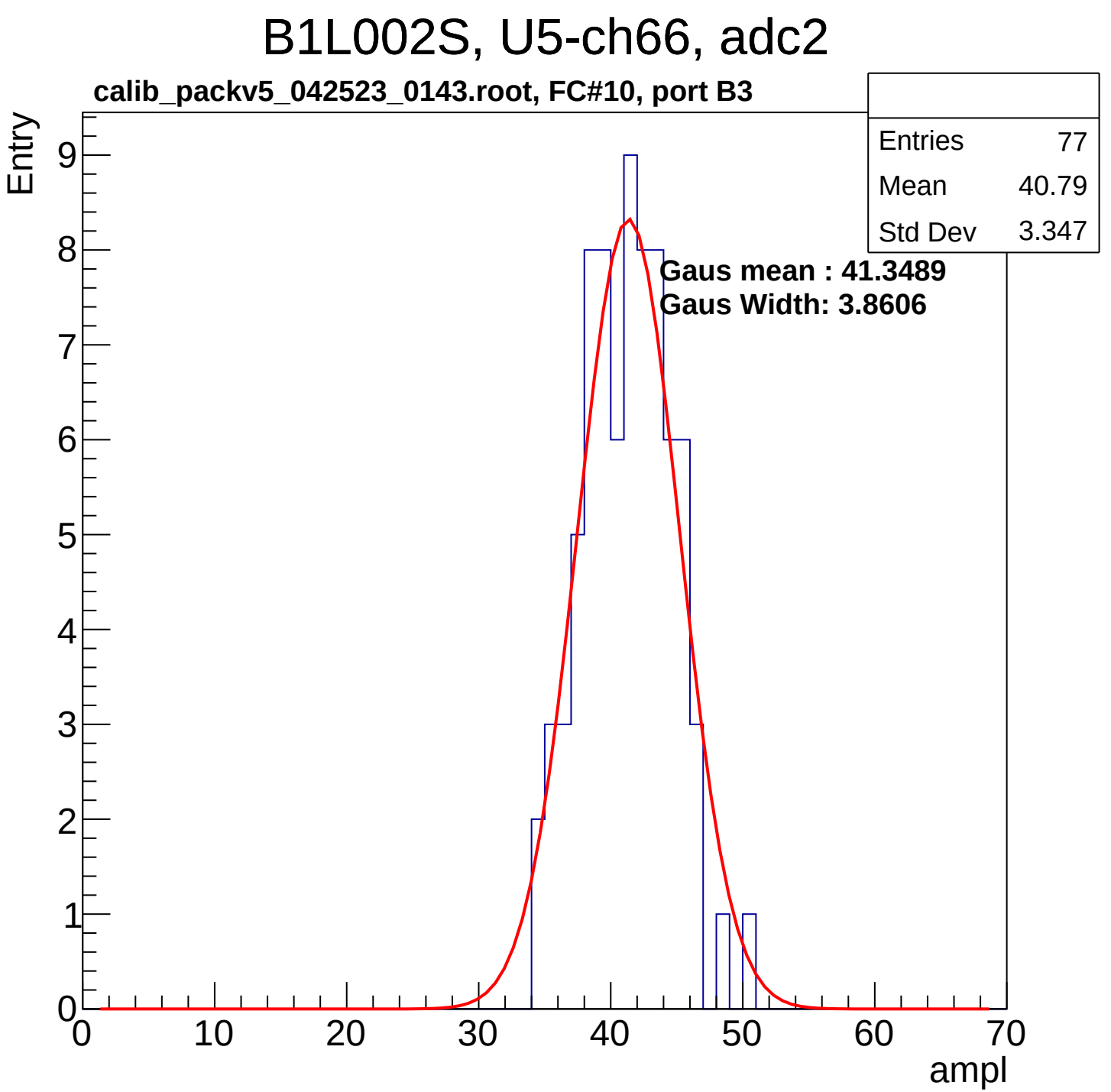
B1L002S, U5-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 40.79
Std Dev 3.347
Gaus mean : 41.3489
Gaus Width: 3.8606
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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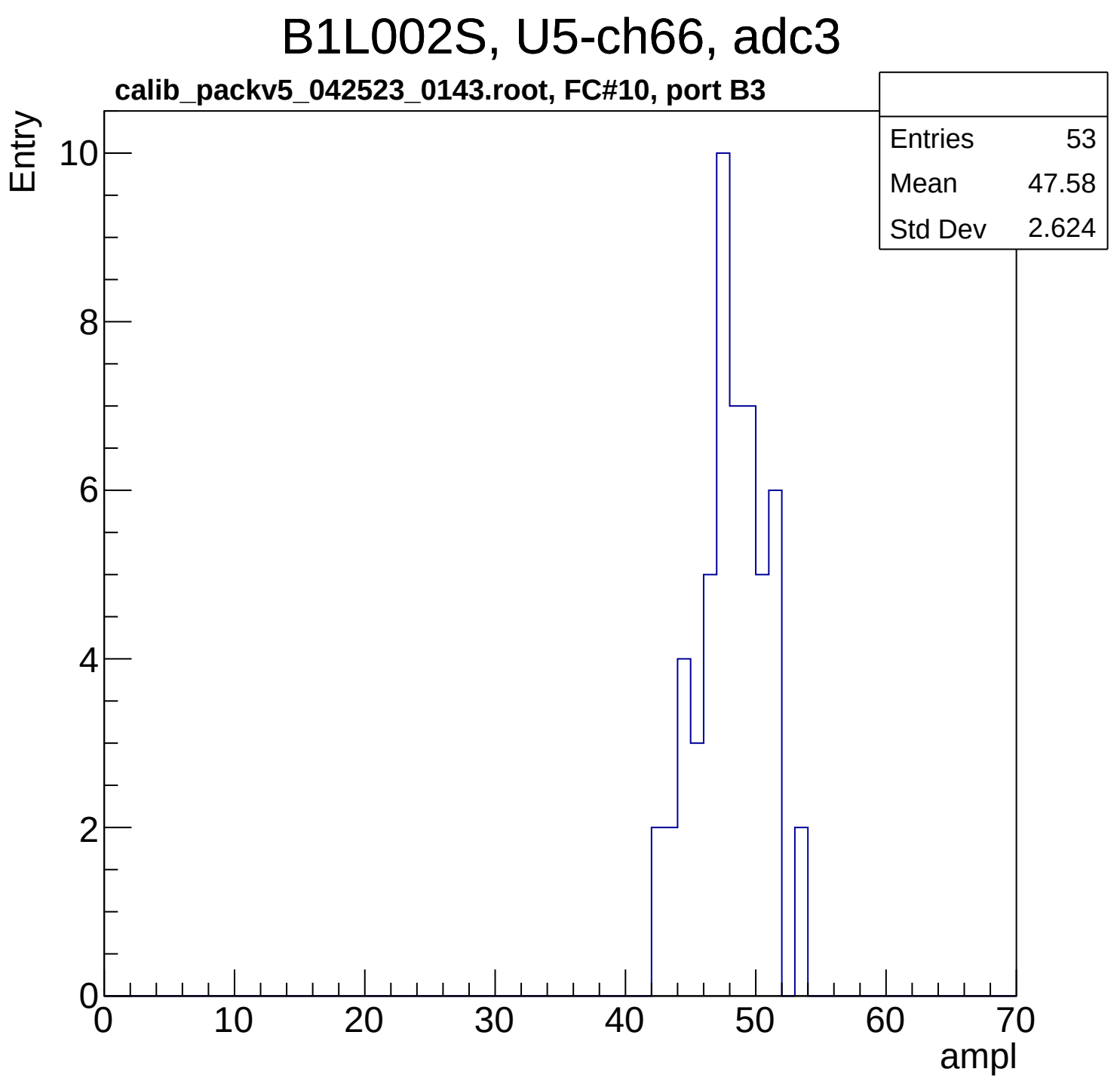
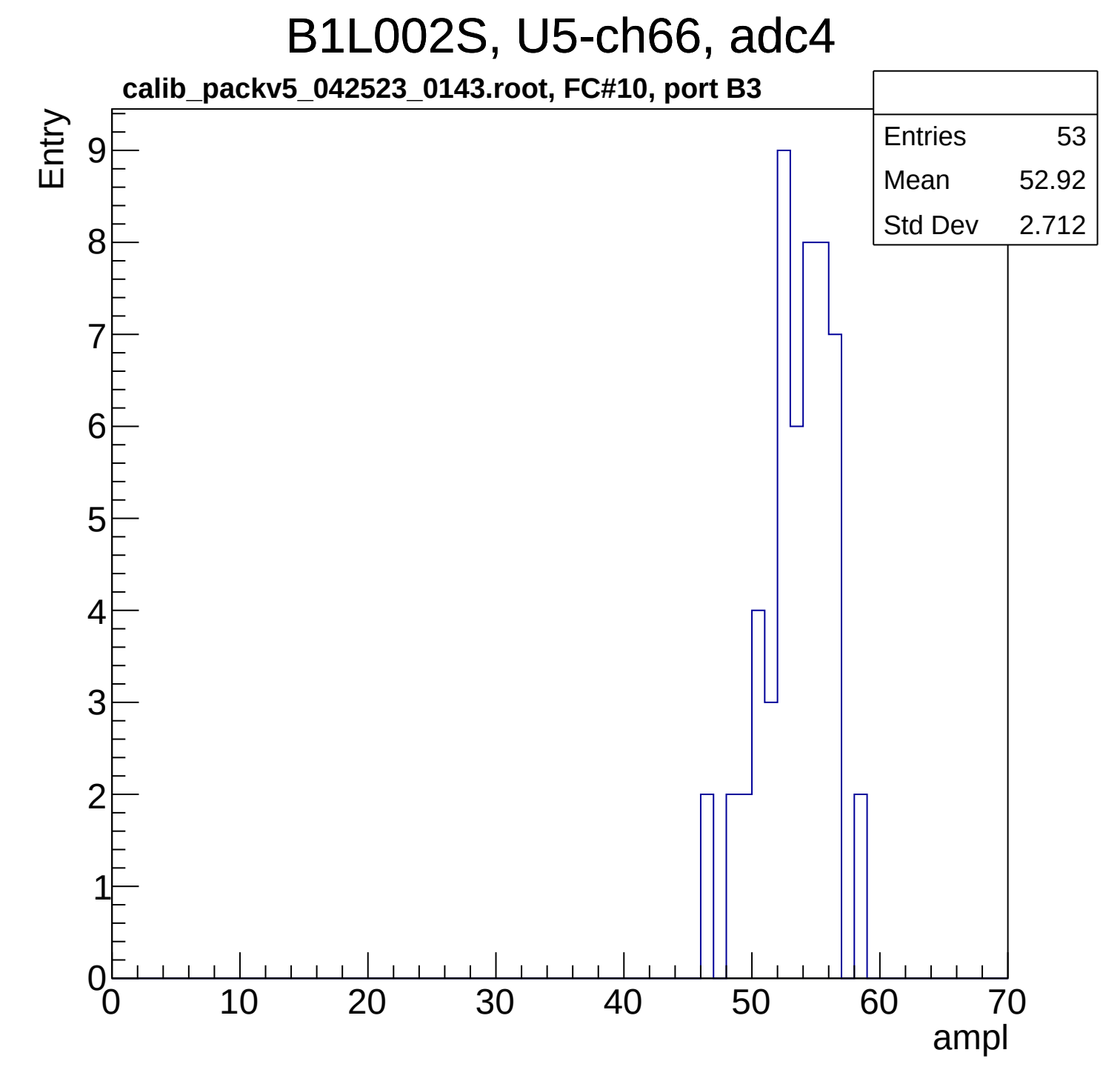

B1L002S, U5-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.58
Std Dev 2.624
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.92
Std Dev 2.712
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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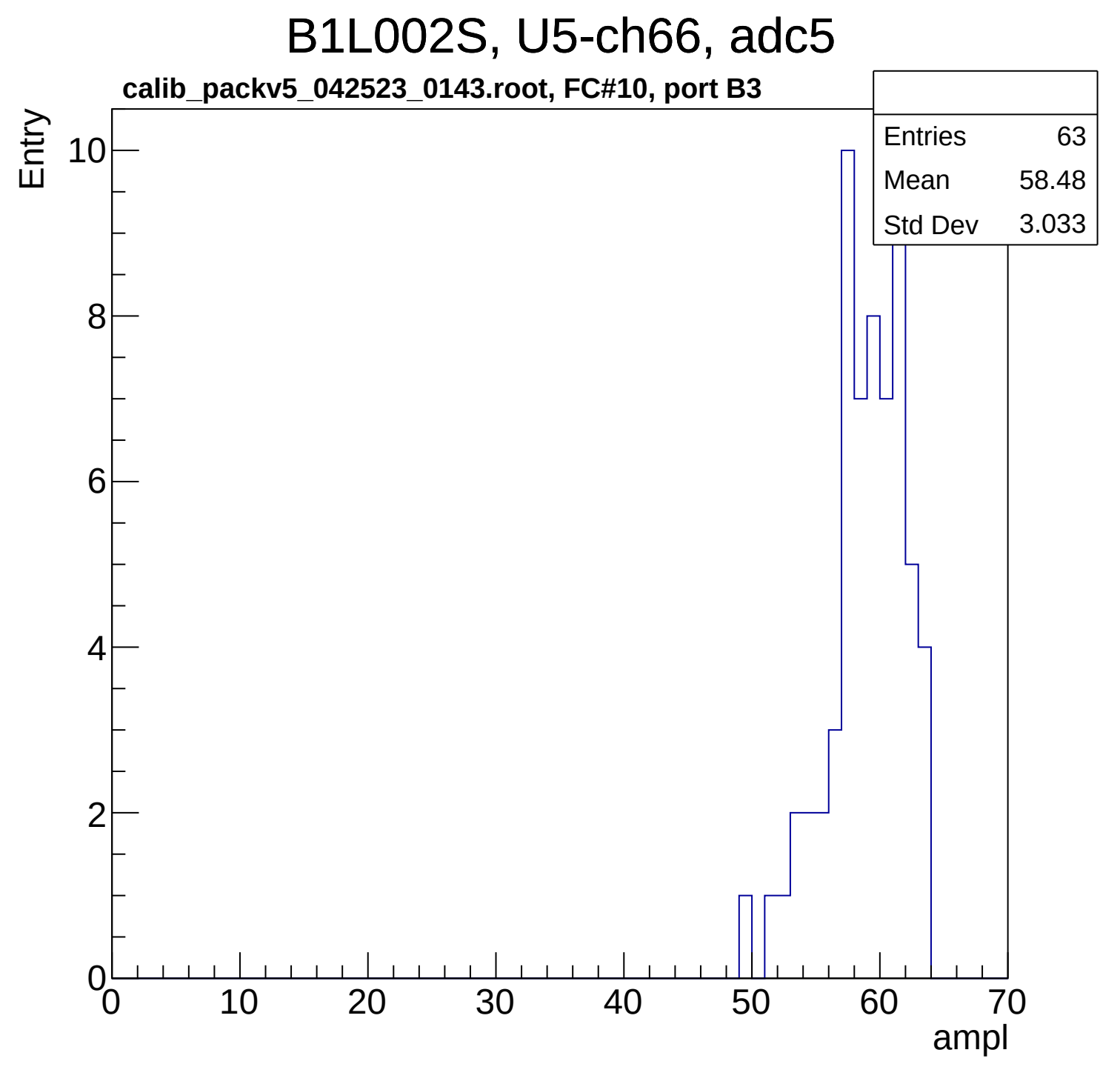
B1L002S, U5-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.48
Std Dev 3.033
Entry
ampl
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4
6
8
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0
10
20
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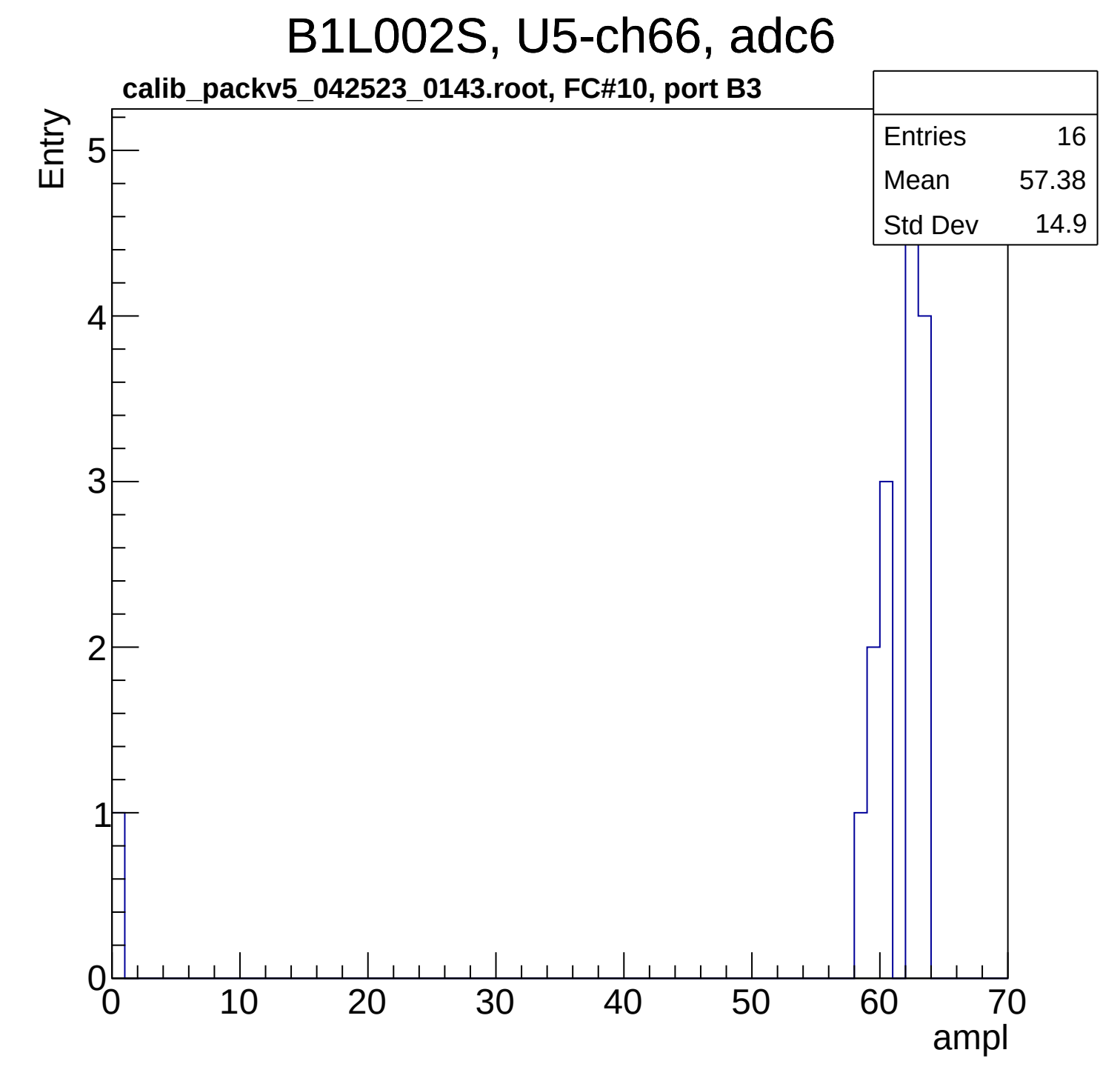
40
50
60
70


B1L002S, U5-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.38
Std Dev 14.9
Entry
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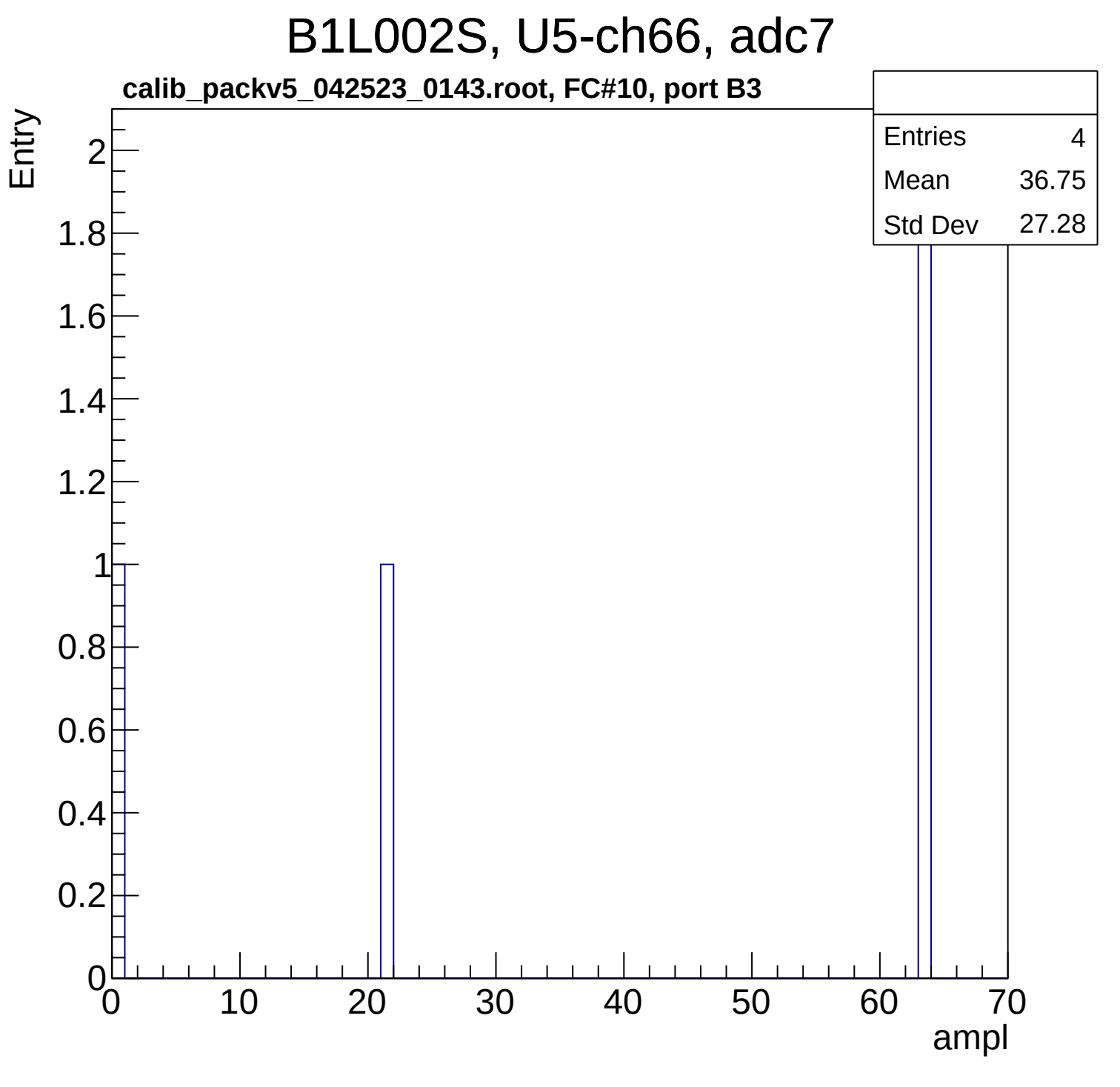
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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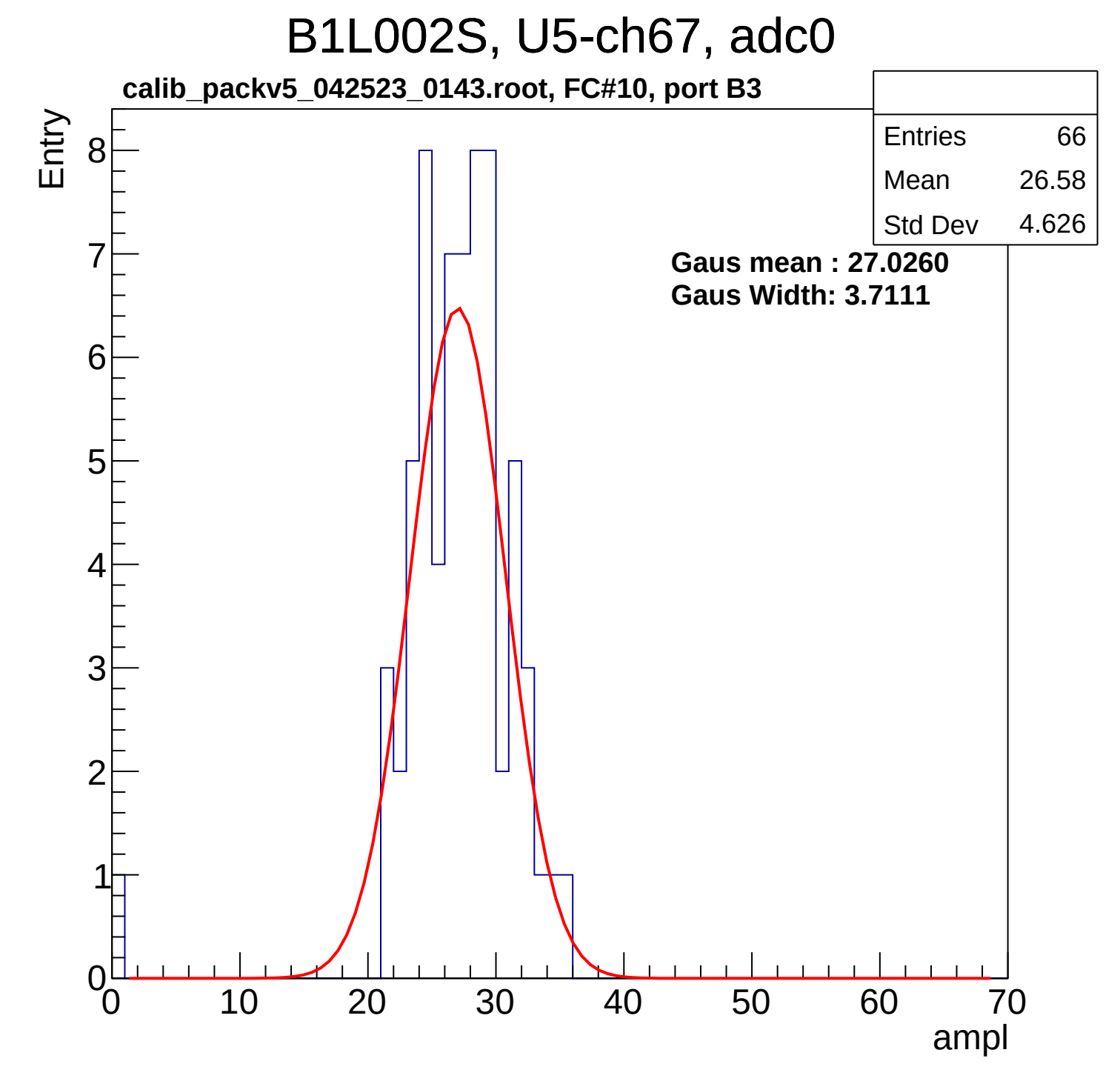
B1L002S, U5-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 26.58
Std Dev 4.626
Gaus mean : 27.0260
Gaus Width: 3.7111
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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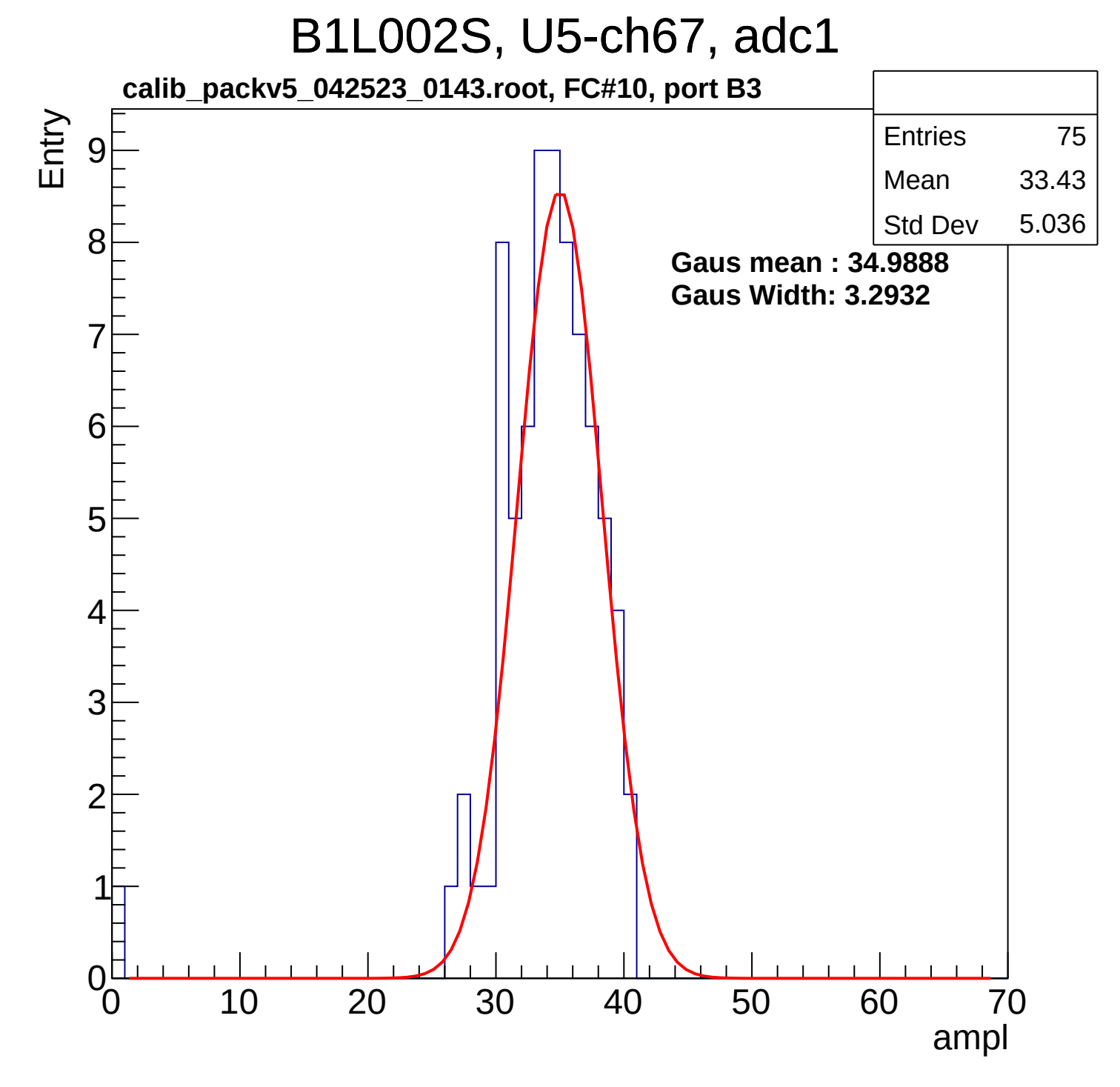
B1L002S, U5-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.43
Std Dev 5.036
Gaus mean : 34.9888
Gaus Width: 3.2932
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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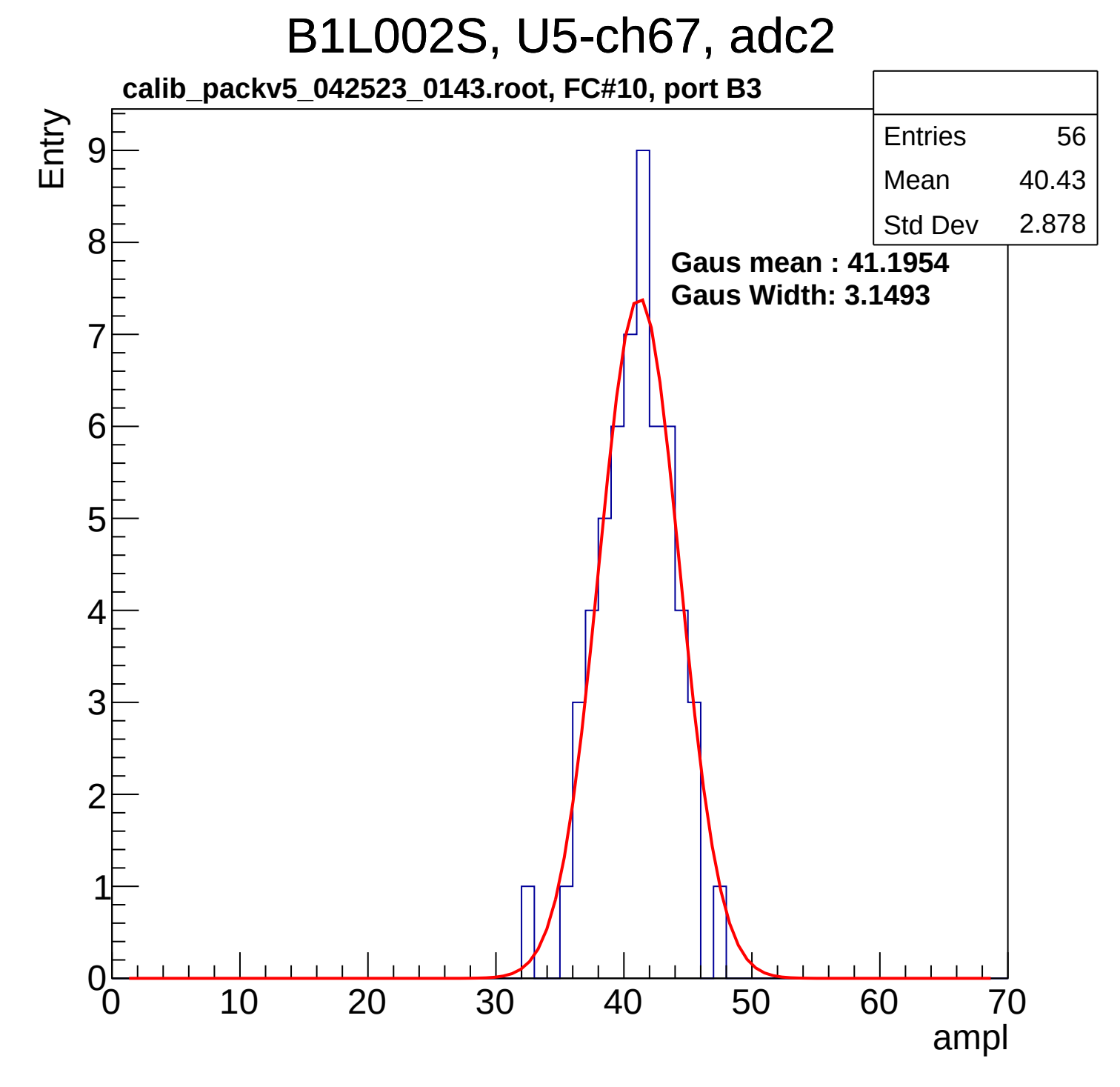
B1L002S, U5-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.43
Std Dev 2.878
Gaus mean : 41.1954
Gaus Width: 3.1493
Entry
ampl
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2
3
4
5
6
7
8
9
0
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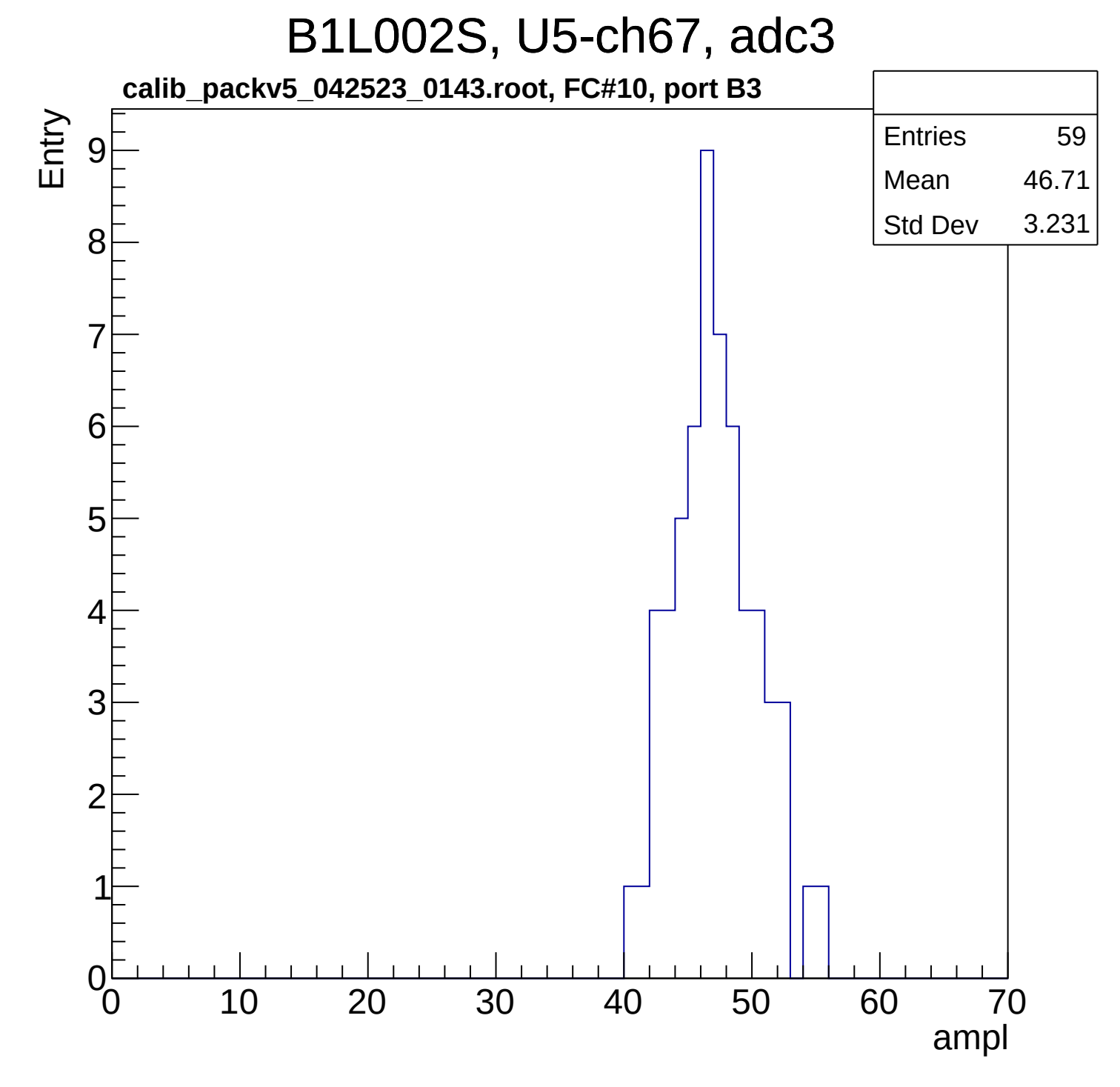
B1L002S, U5-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.71
Std Dev 3.231
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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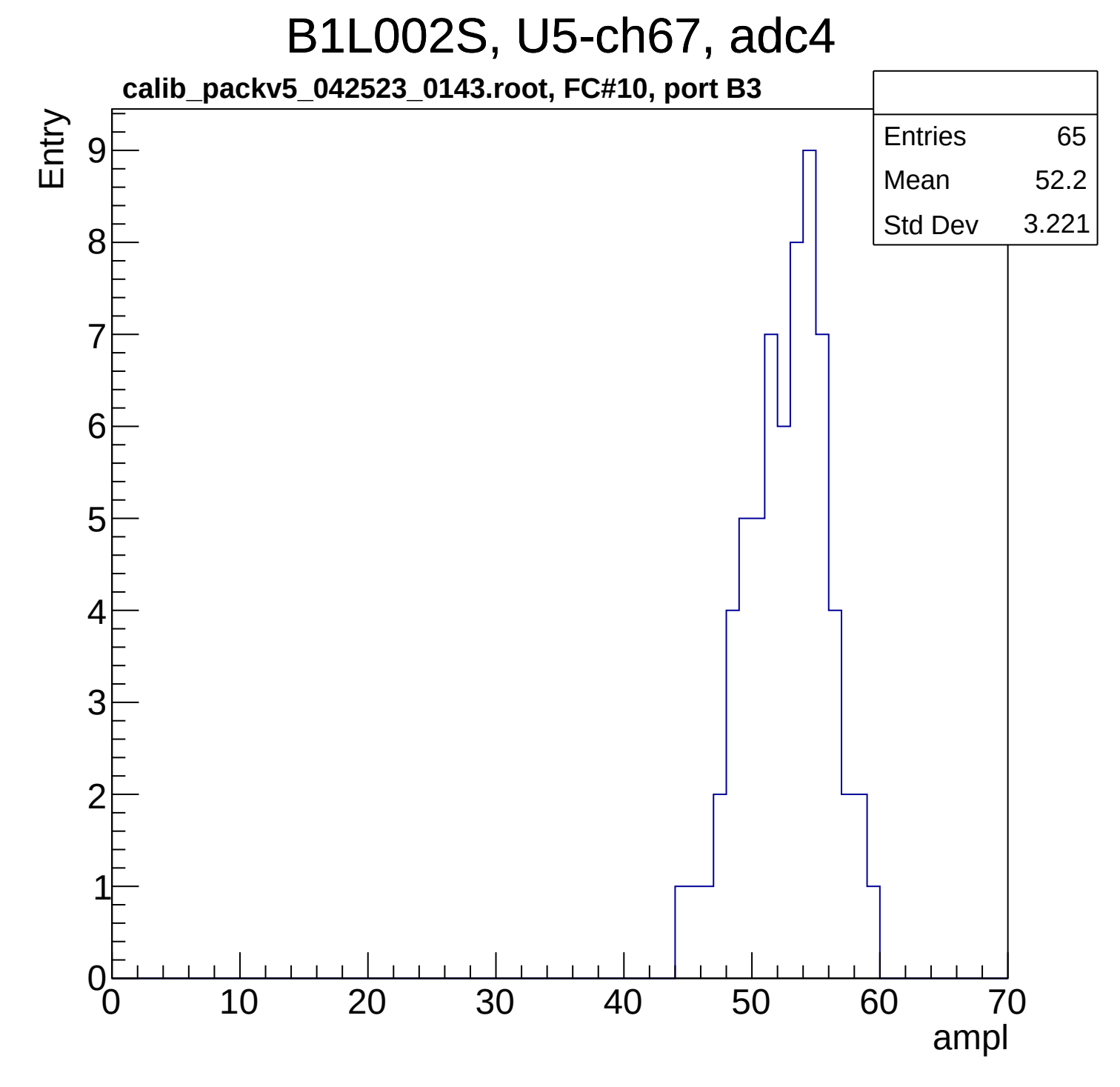
B1L002S, U5-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.2
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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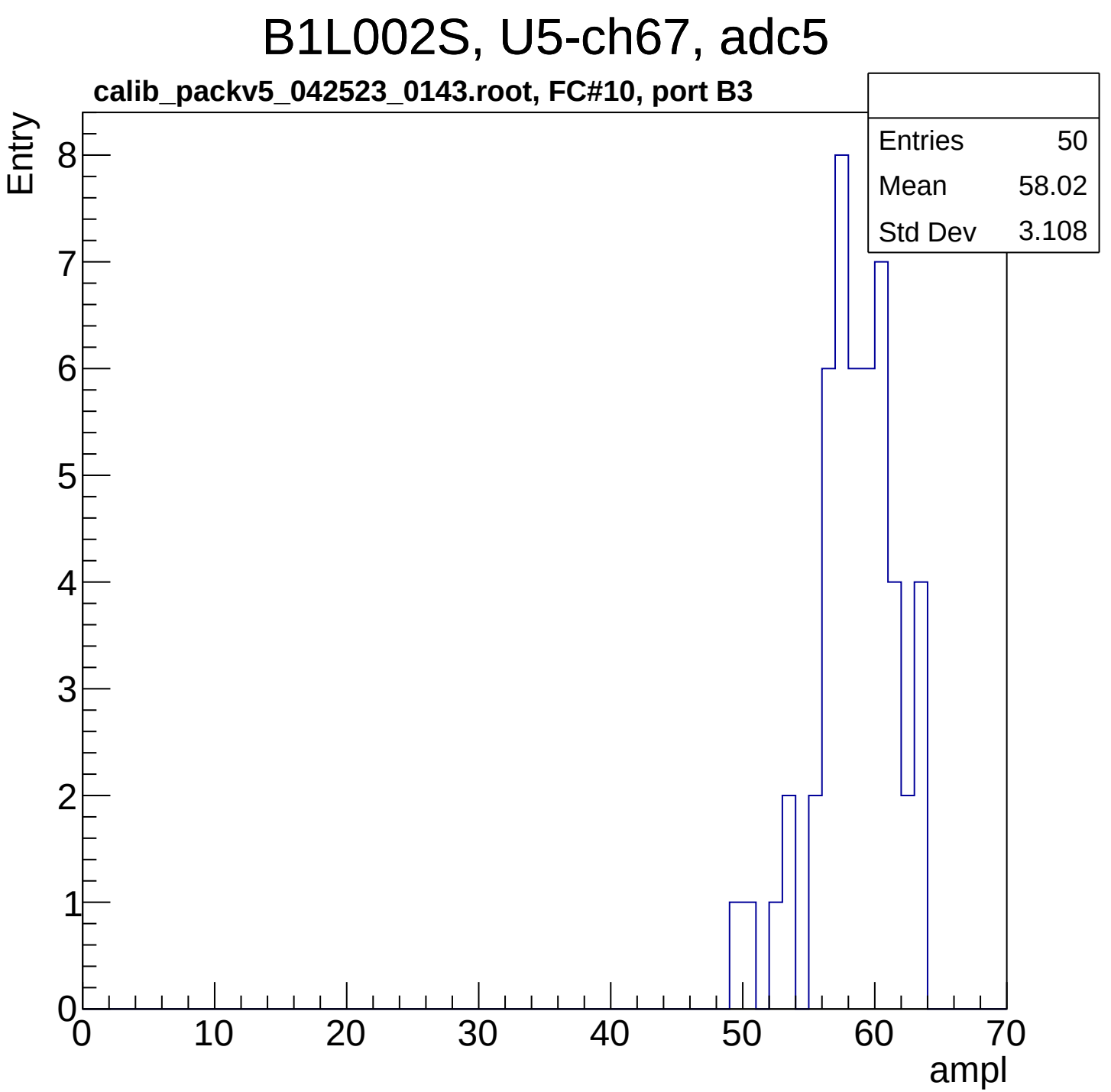
B1L002S, U5-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.02
Std Dev 3.108
Entry
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2
3
4
5
6
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8
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10
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30
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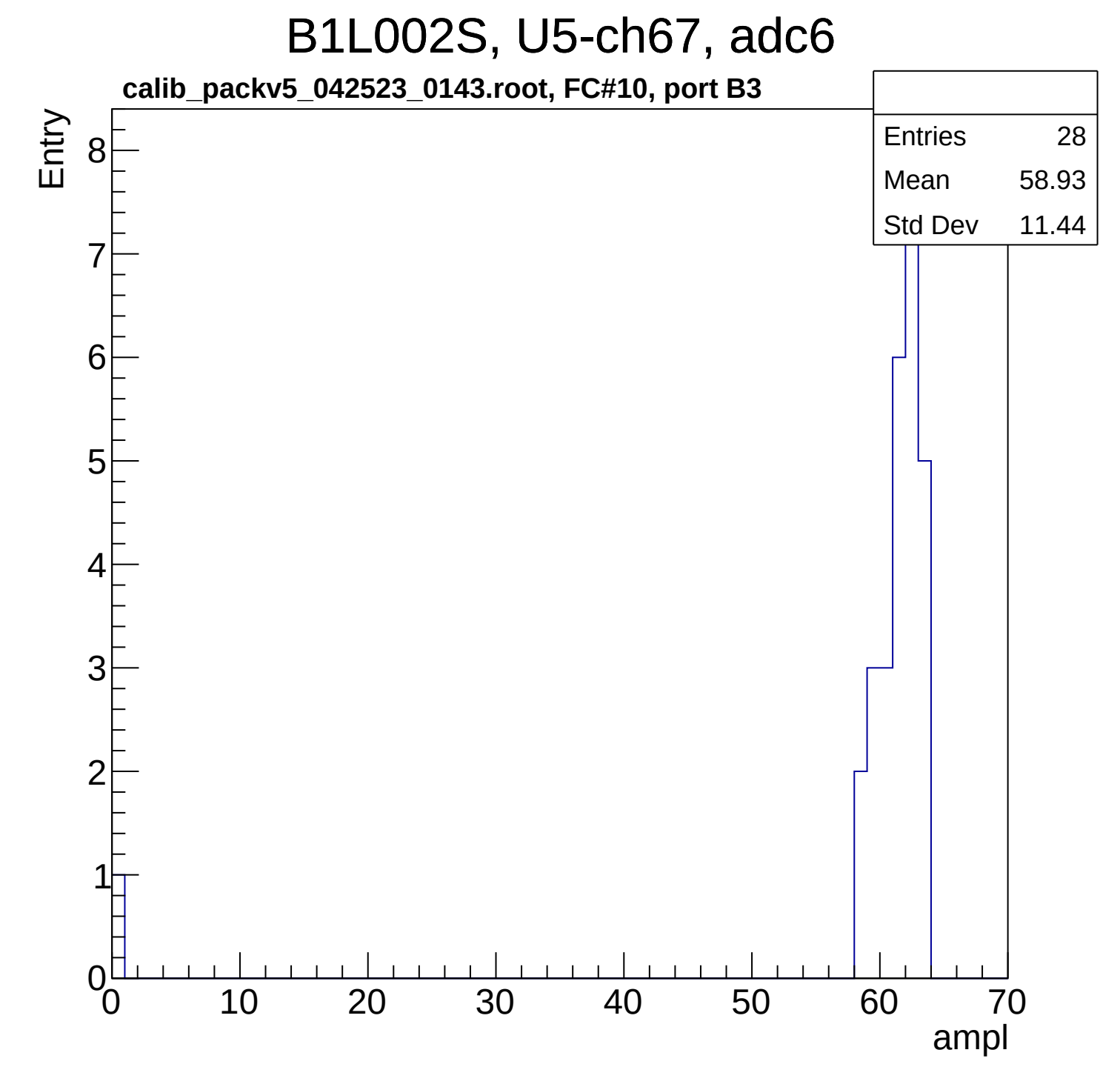
50
60
70
ampl


B1L002S, U5-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.93
Std Dev 11.44
Entry
ampl
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2
3
4
5
6
7
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0
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20
30
40
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60
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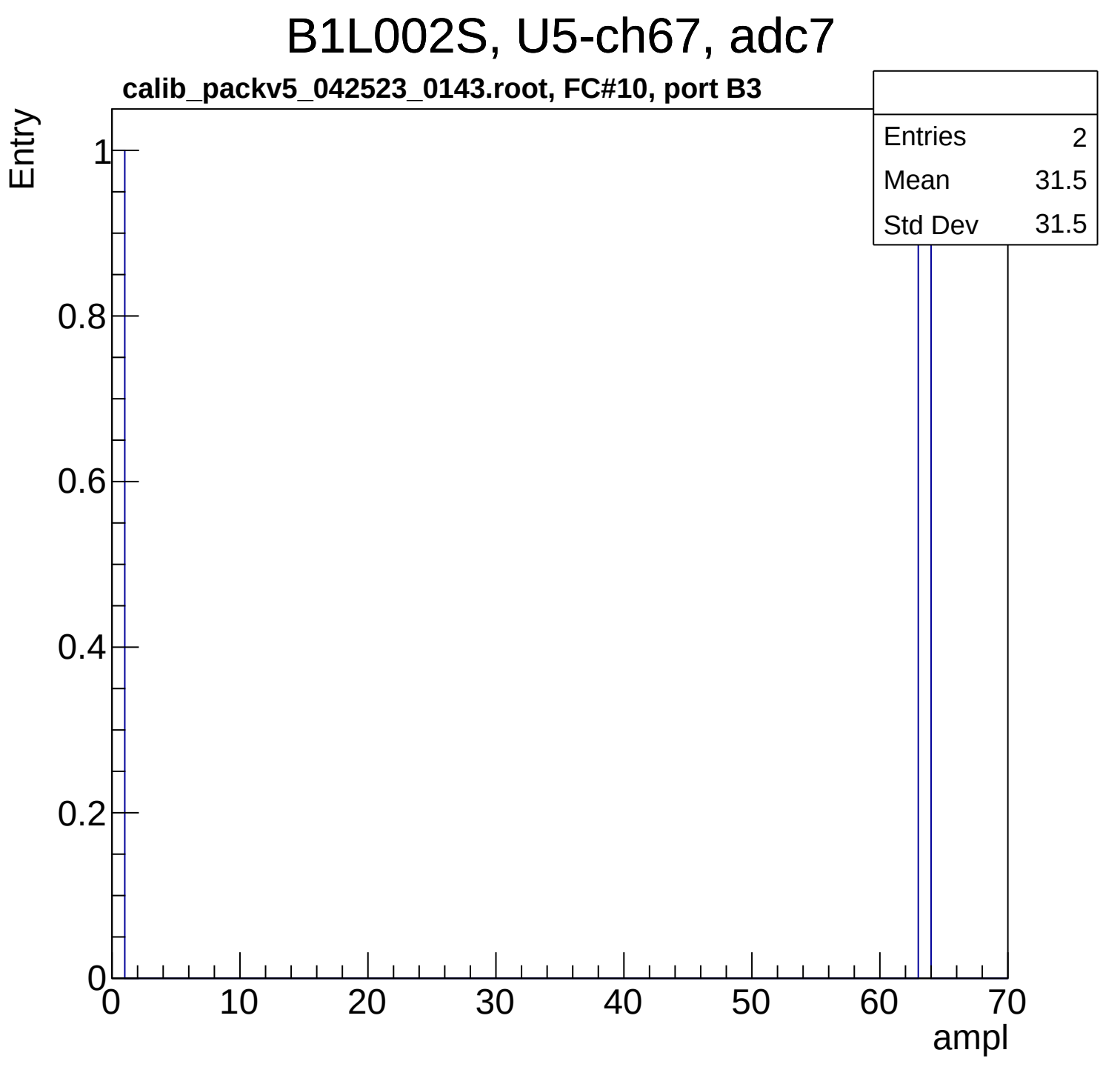
B1L002S, U5-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
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10
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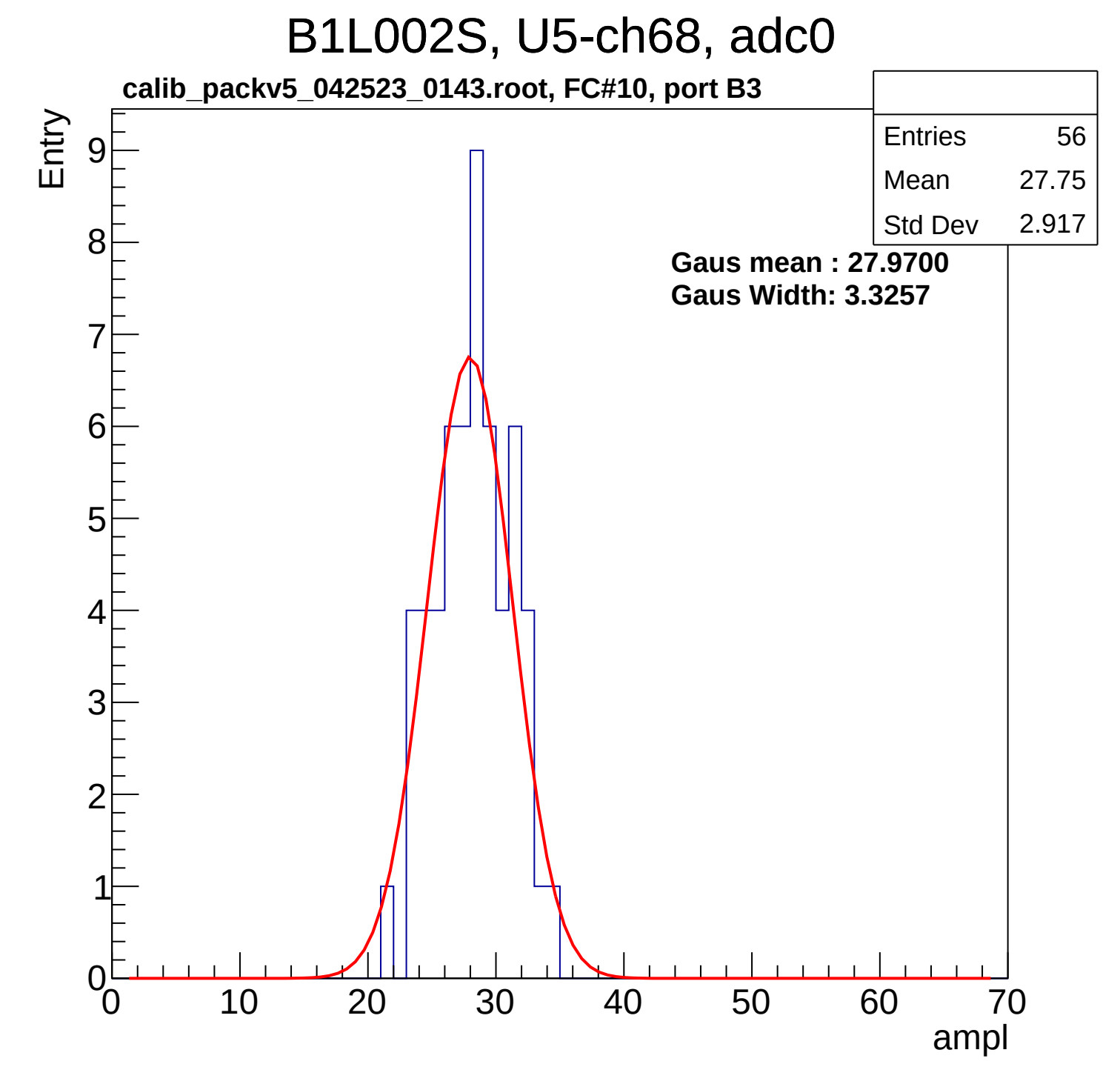
50
60
70
ampl


B1L002S, U5-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.75
Std Dev 2.917
Gaus mean : 27.9700
Gaus Width: 3.3257
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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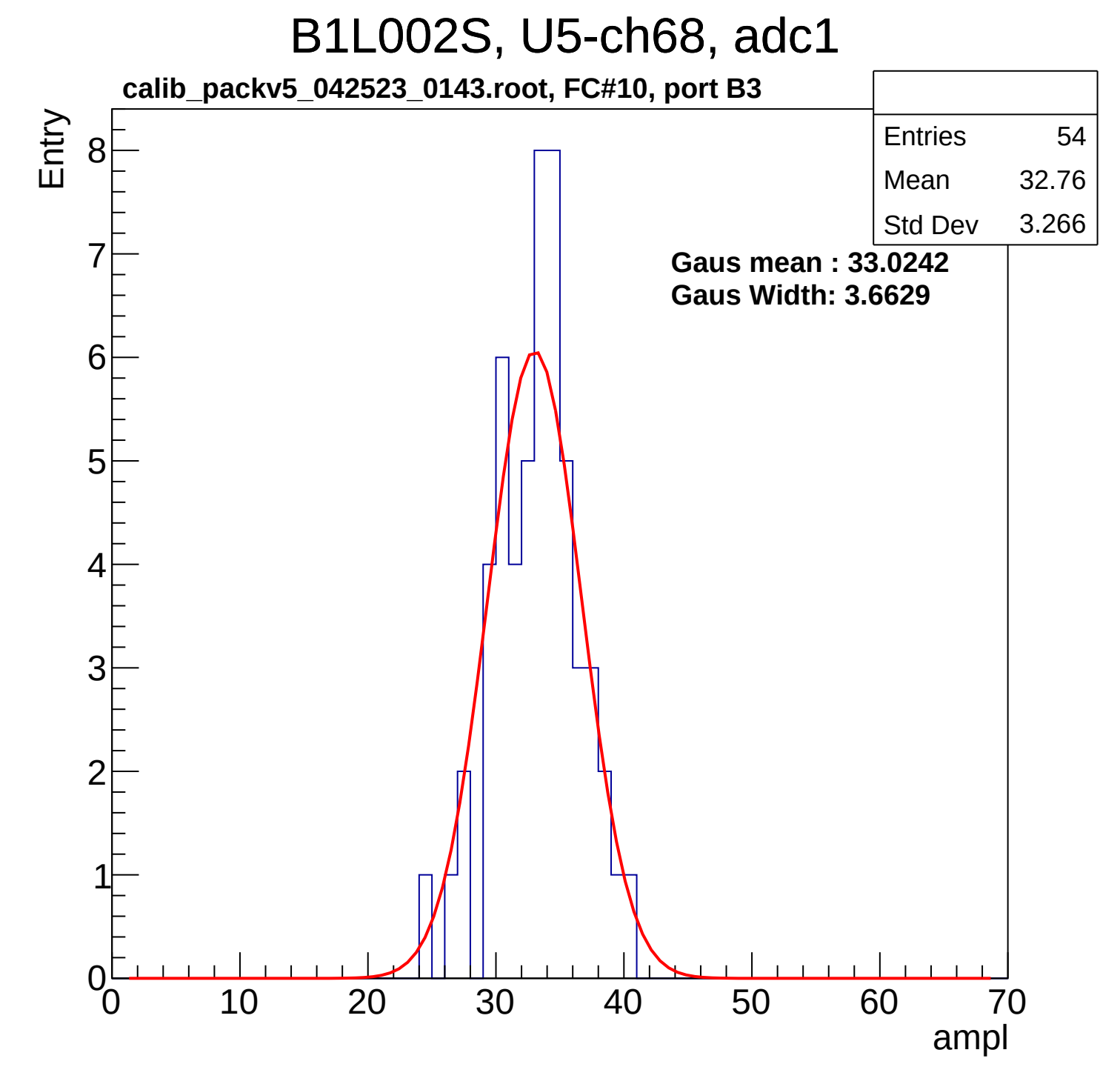
B1L002S, U5-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 32.76
Std Dev 3.266
Gaus mean : 33.0242
Gaus Width: 3.6629
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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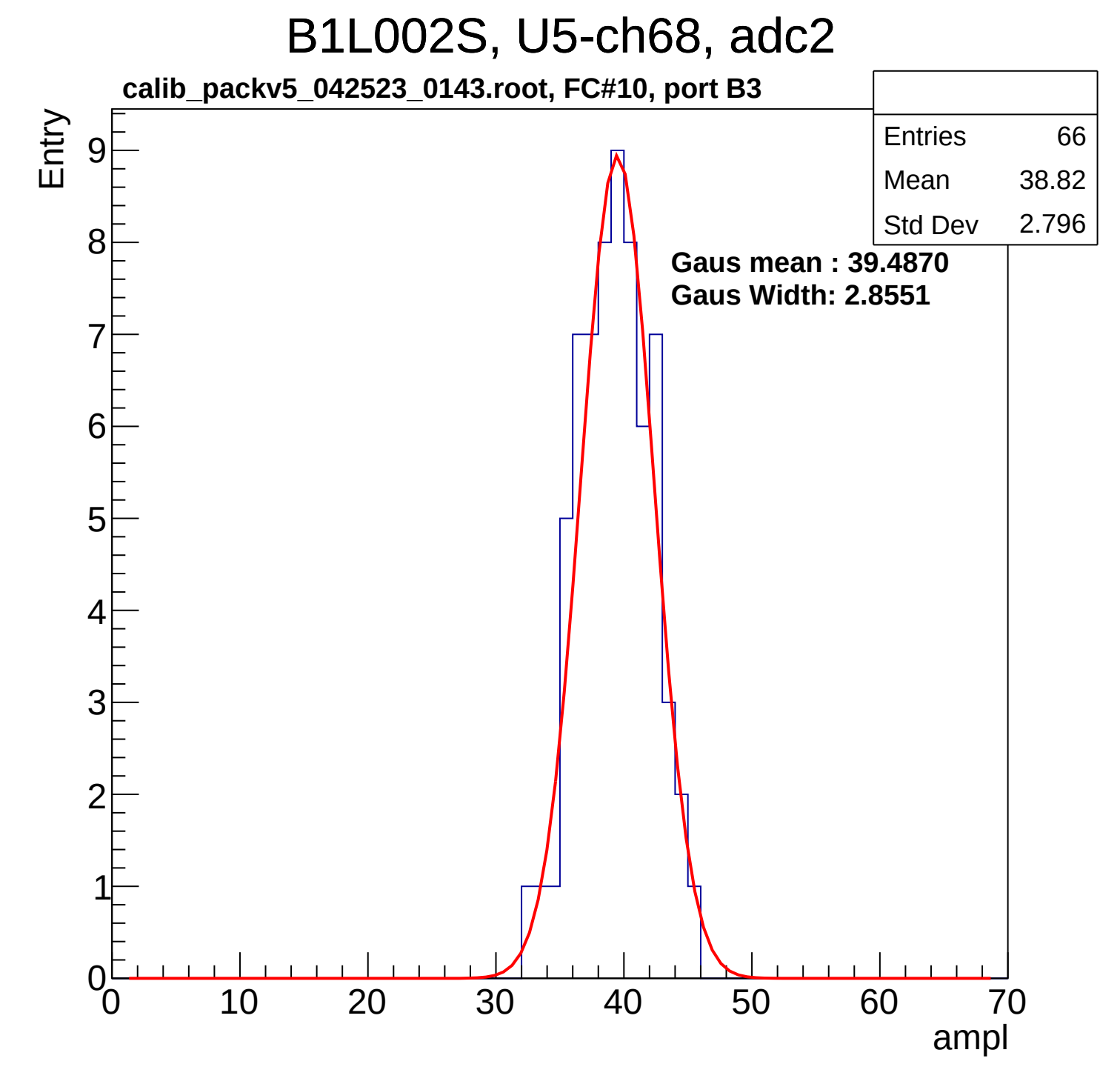
40
50
60
70


B1L002S, U5-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 38.82
Std Dev 2.796
Gaus mean : 39.4870
Gaus Width: 2.8551
Entry
ampl
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2
3
4
5
6
7
8
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0
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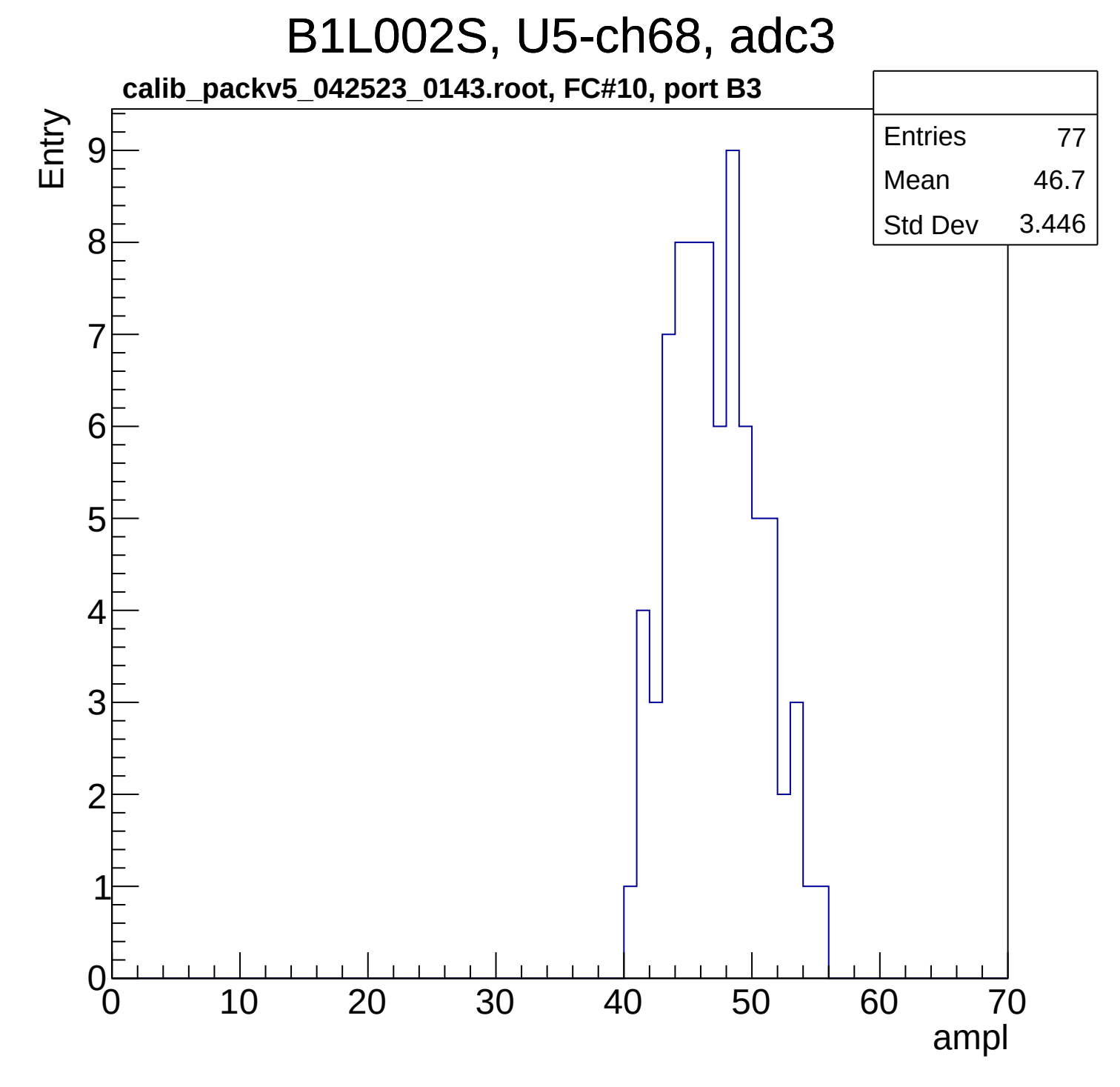
B1L002S, U5-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 46.7
Std Dev 3.446
Entry
ampl
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6
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8
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0
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40
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60
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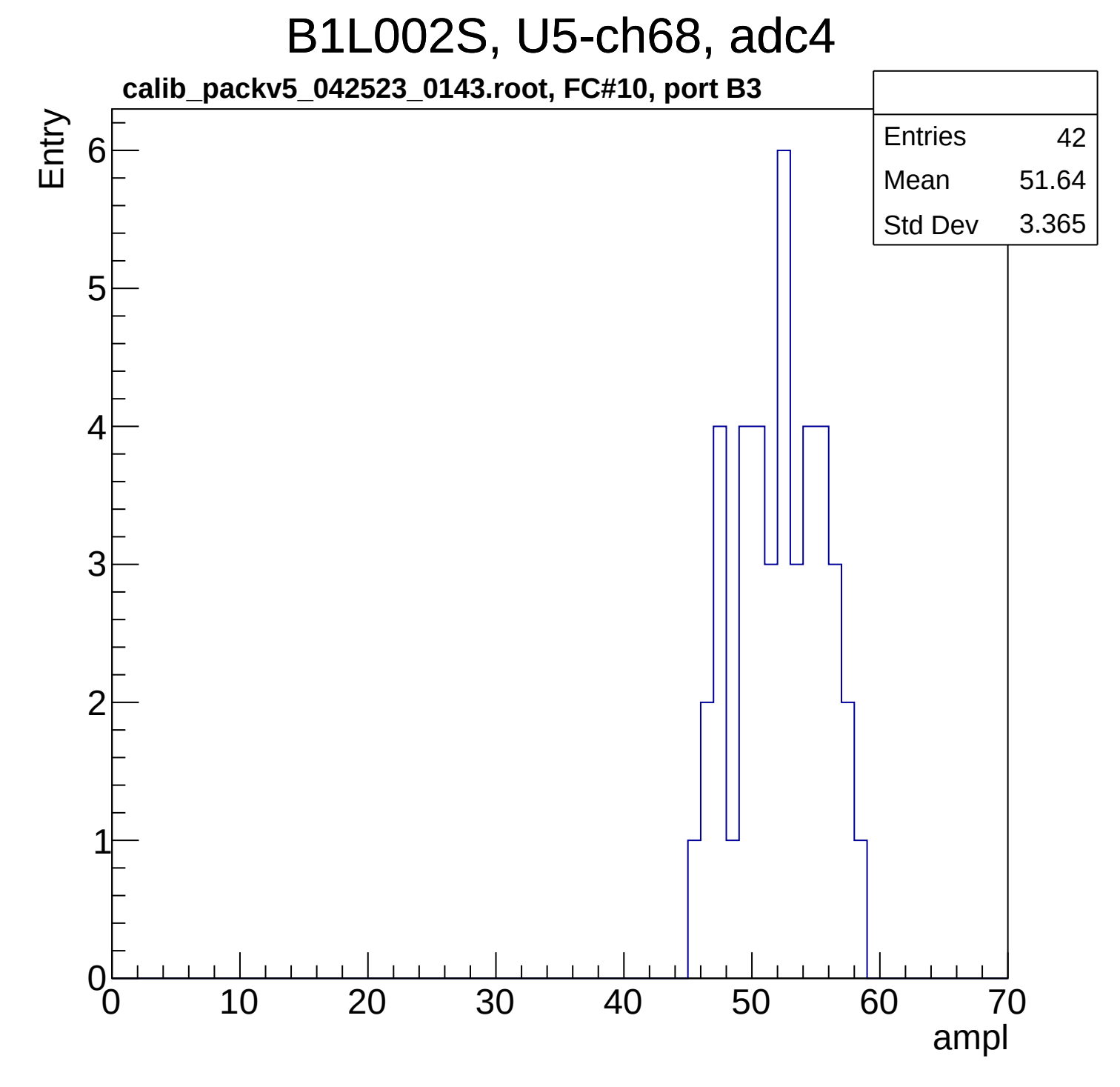
B1L002S, U5-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 51.64
Std Dev 3.365
Entry
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4
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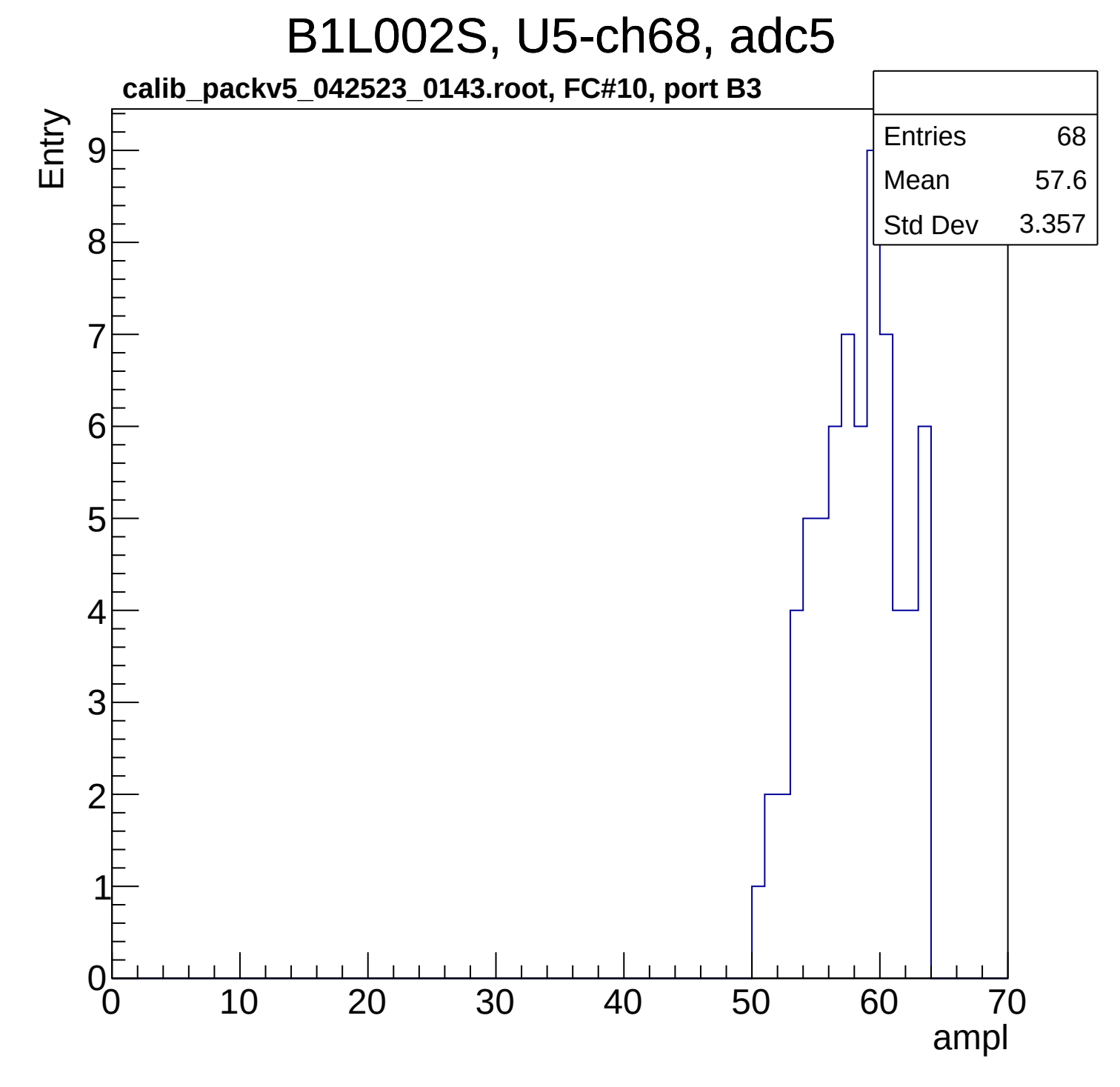
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.6
Std Dev 3.357
Entry
ampl
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3
4
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60
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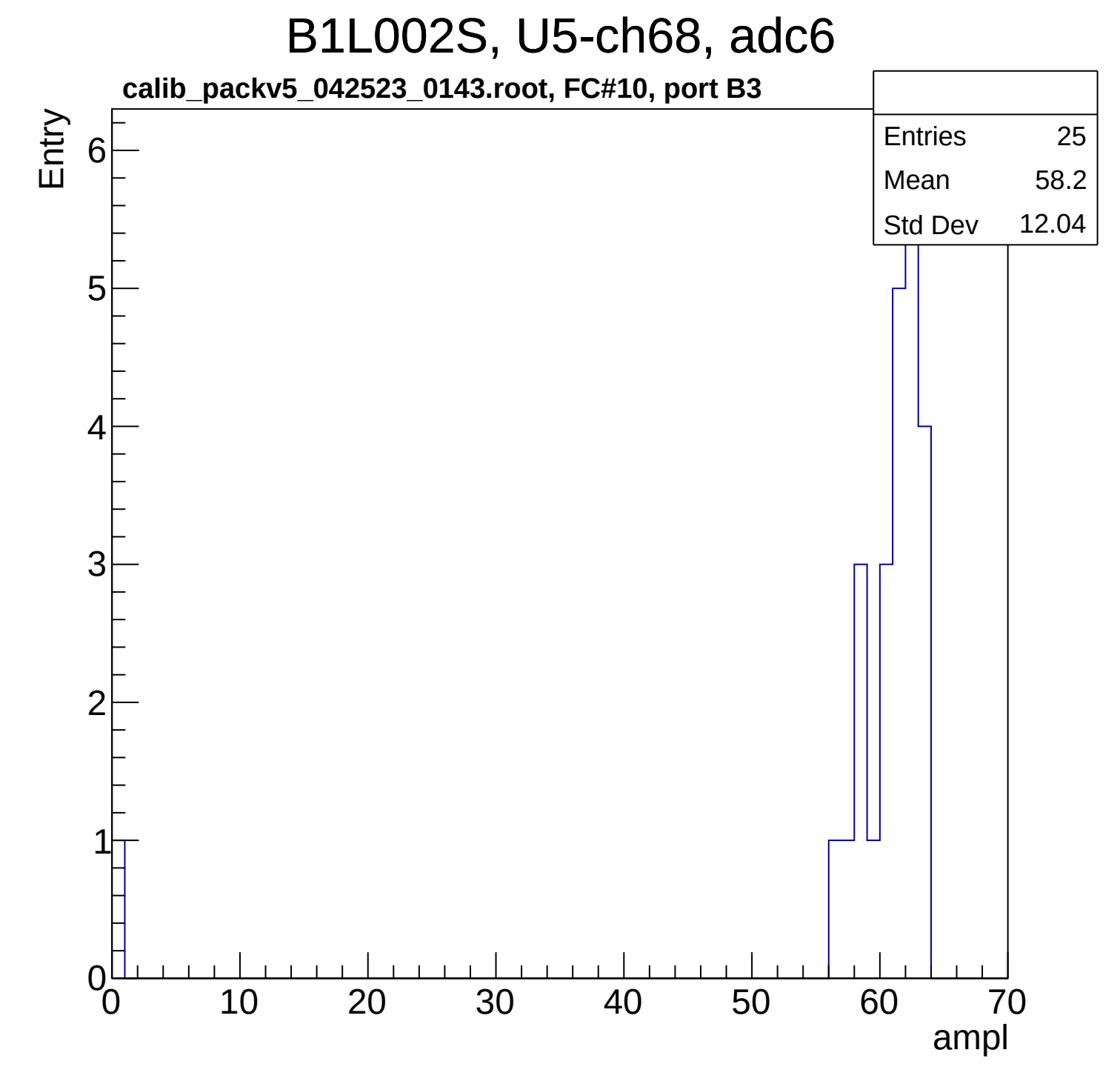
B1L002S, U5-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.2
Std Dev 12.04
Entry
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10
20
30
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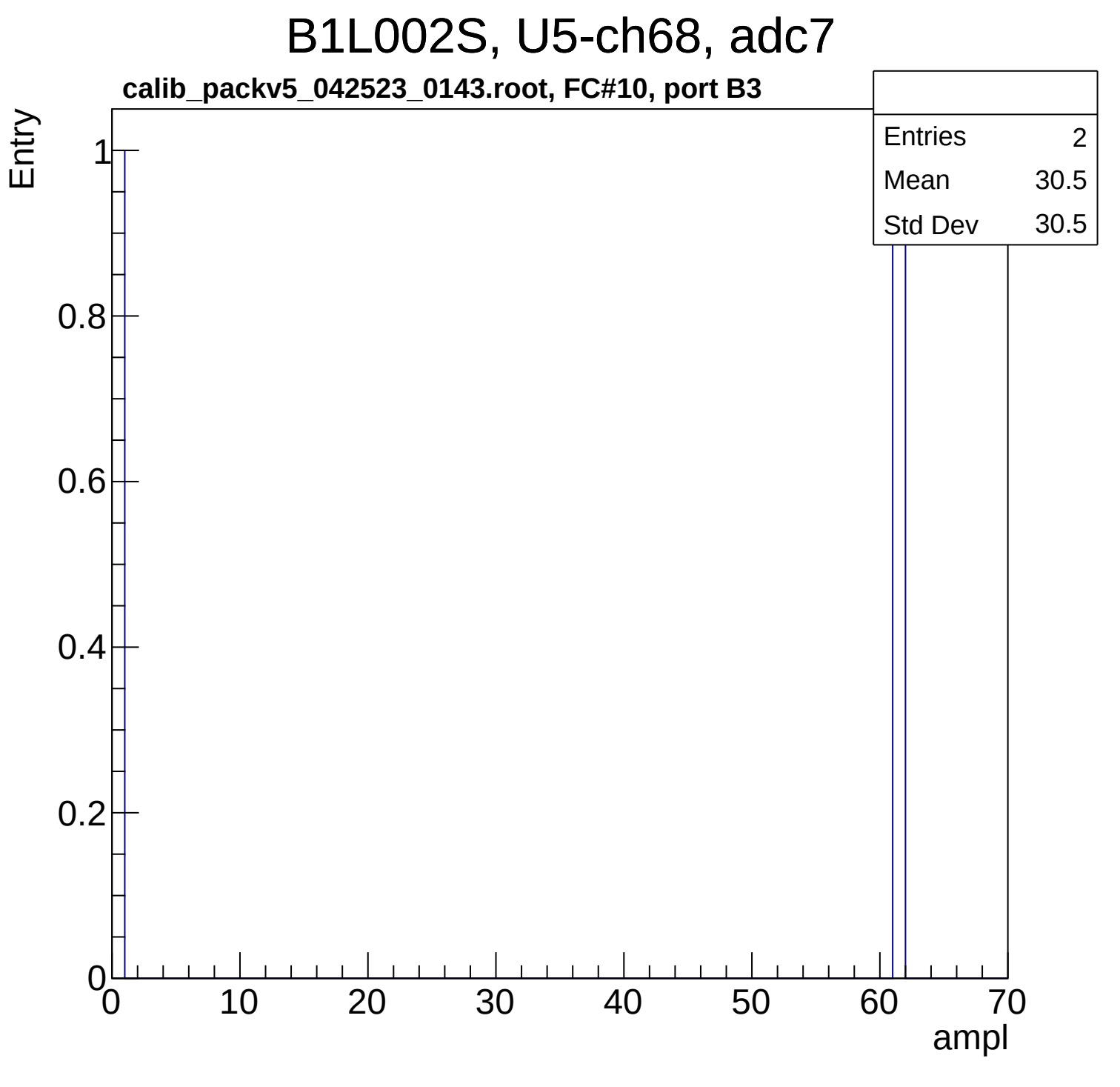
50
60
70
ampl


B1L002S, U5-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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0.6
0.4
0.2
0
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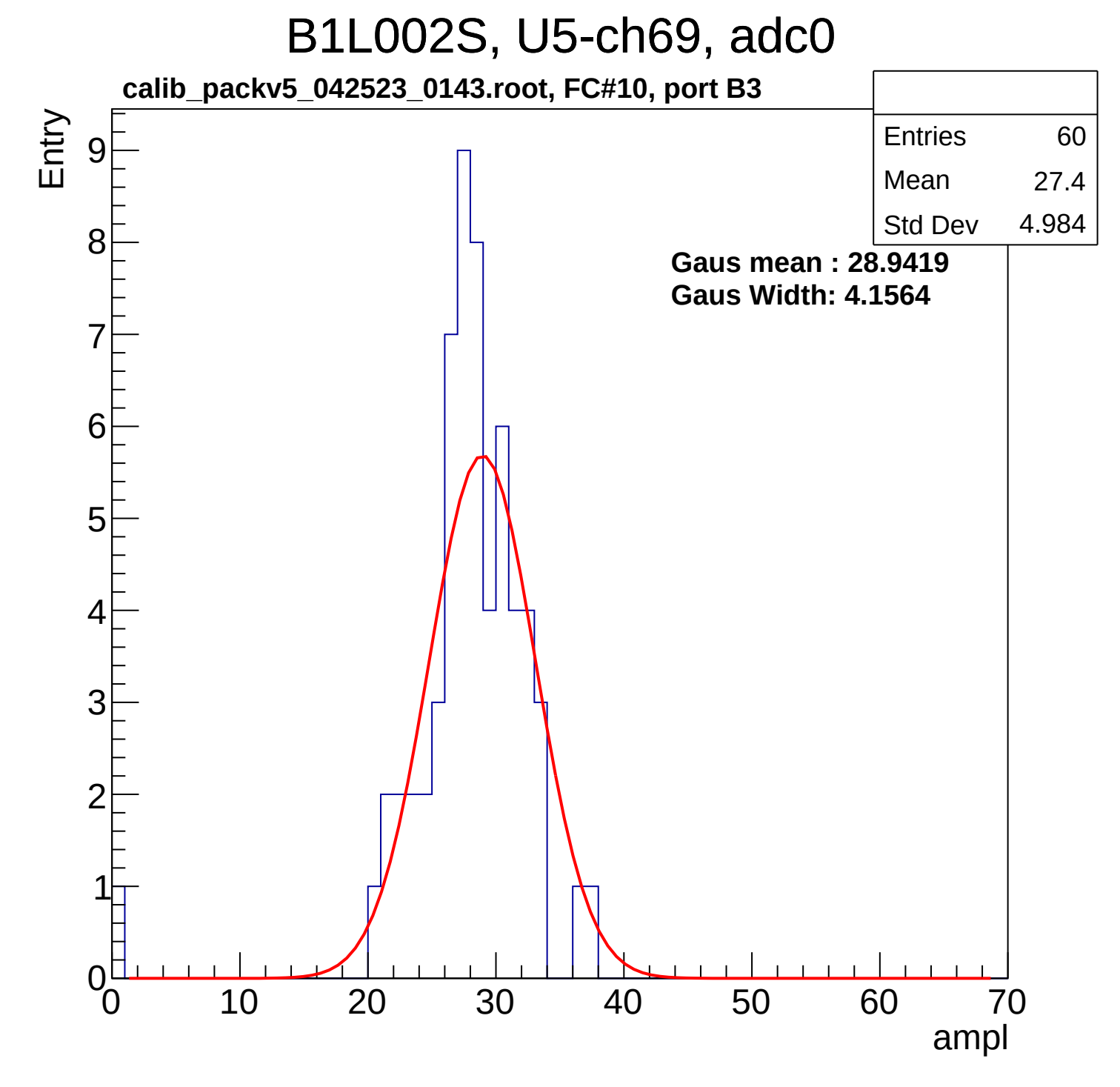
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.4
Std Dev 4.984
Gaus mean : 28.9419
Gaus Width: 4.1564
Entry
ampl
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3
4
5
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60
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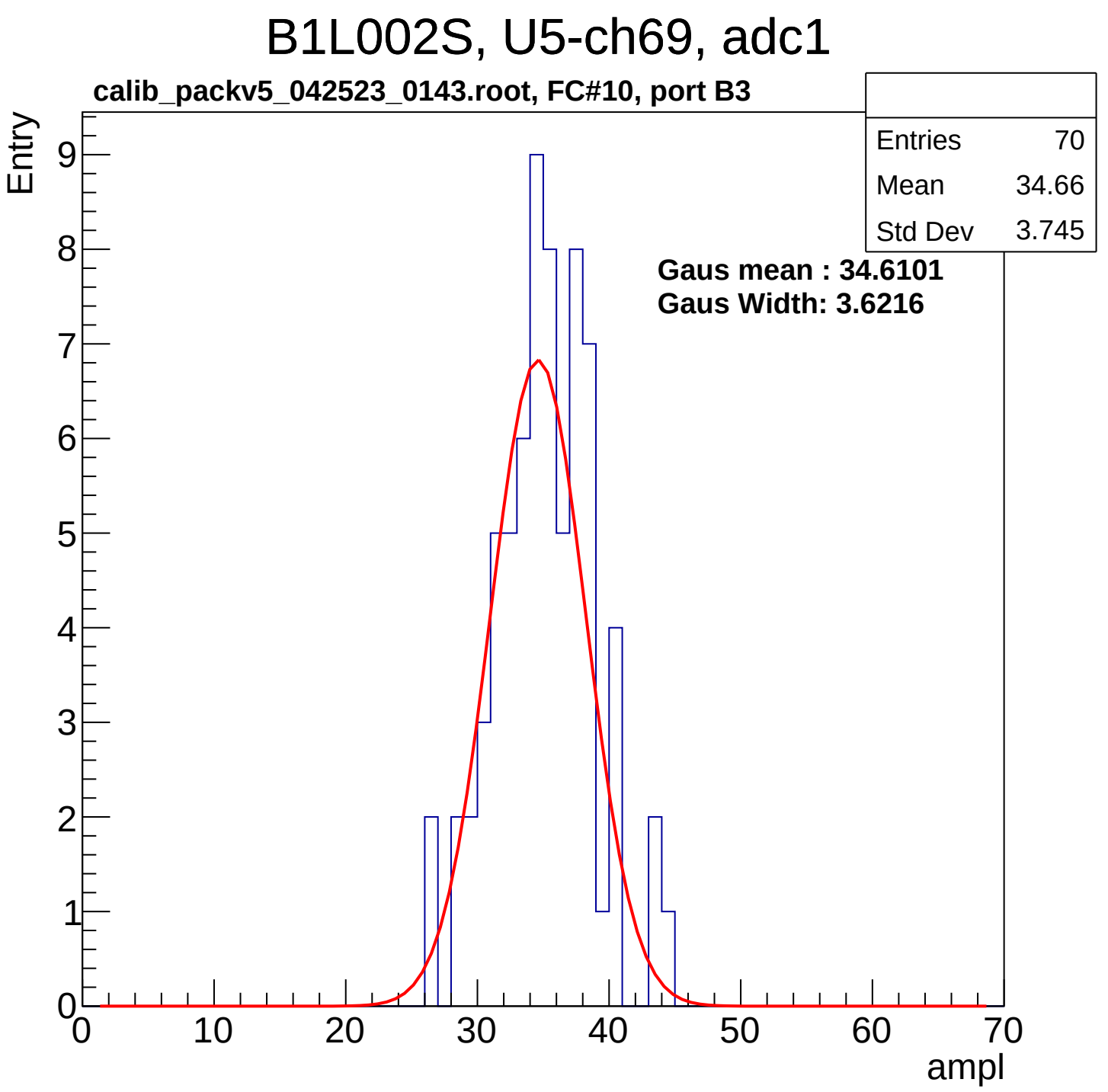
B1L002S, U5-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.66
Std Dev 3.745
Gaus mean : 34.6101
Gaus Width: 3.6216
Entry
ampl
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3
4
5
6
7
8
9
0
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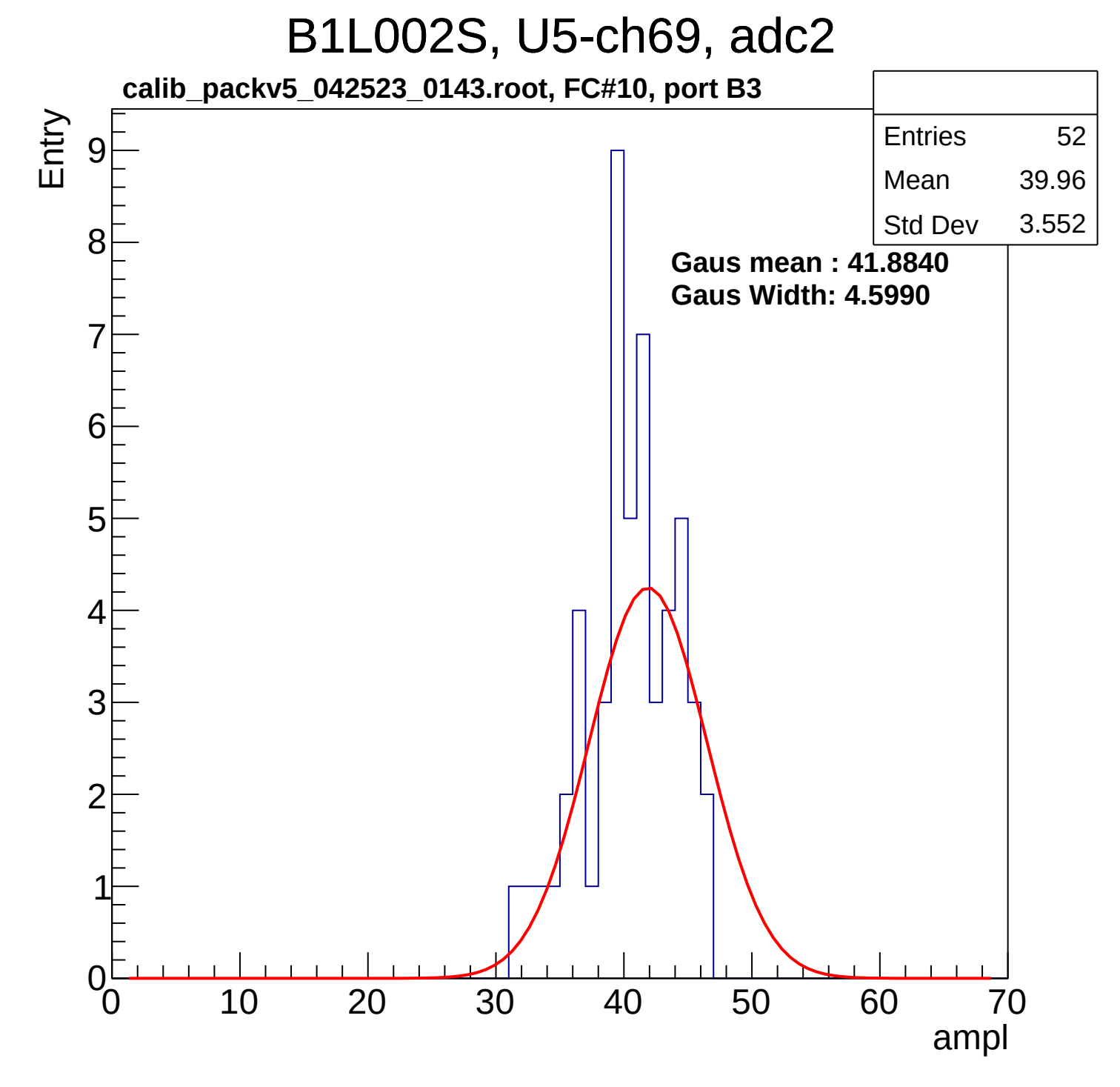
B1L002S, U5-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 39.96
Std Dev 3.552
Gaus mean : 41.8840
Gaus Width: 4.5990
Entry
ampl
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2
3
4
5
6
7
8
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0
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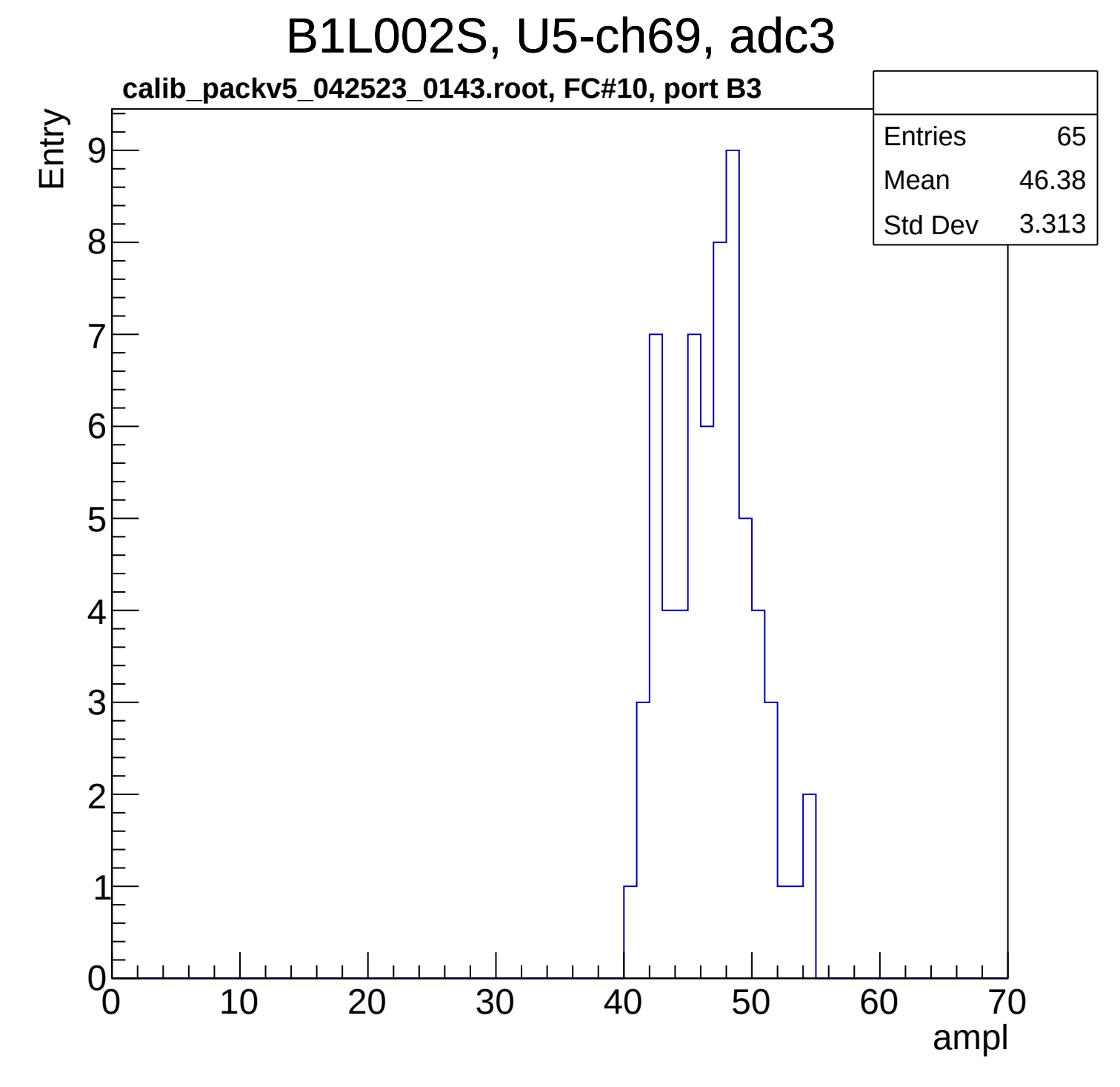
B1L002S, U5-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.38
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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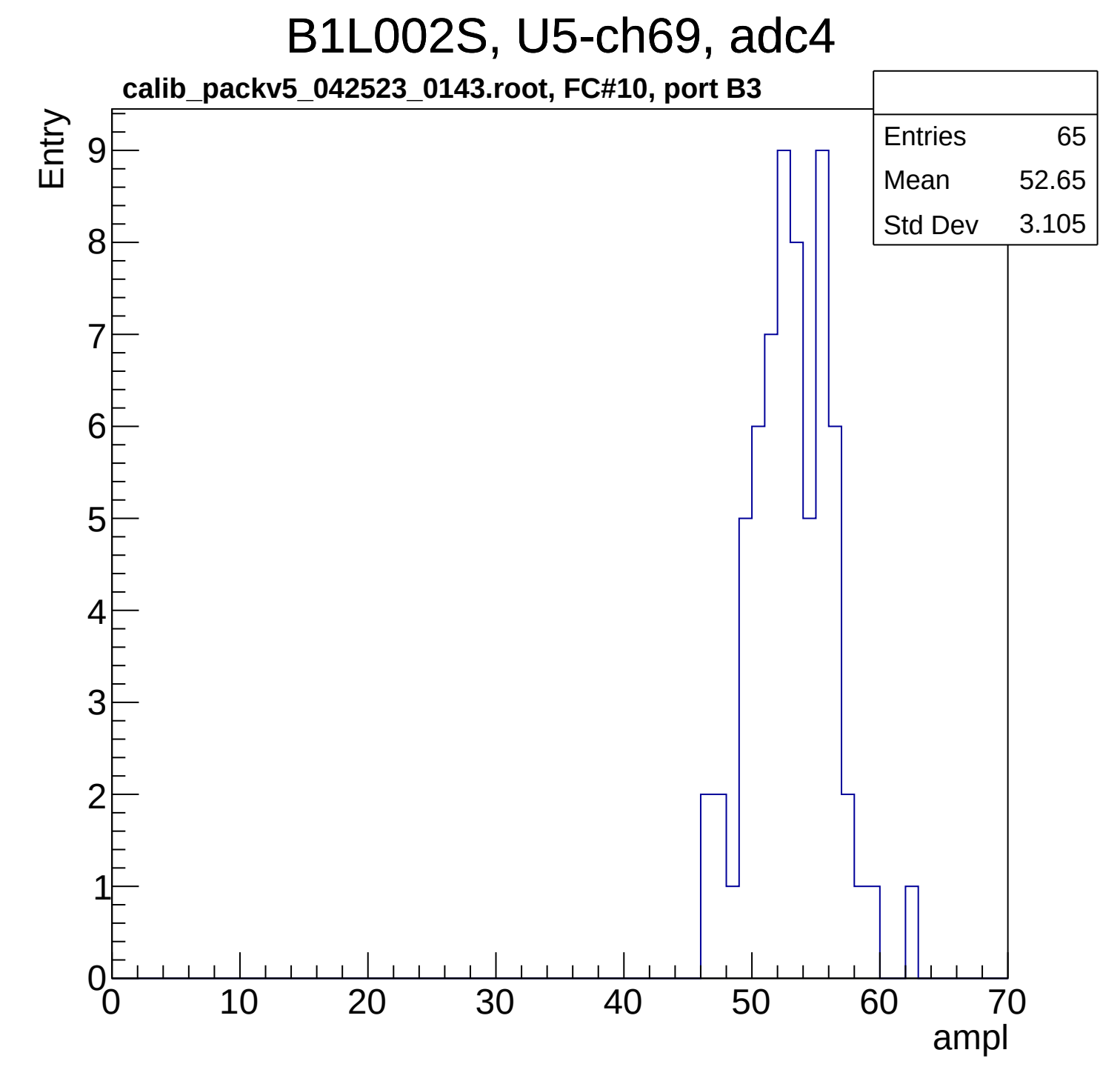
B1L002S, U5-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.65
Std Dev 3.105
Entry
ampl
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1
2
3
4
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0
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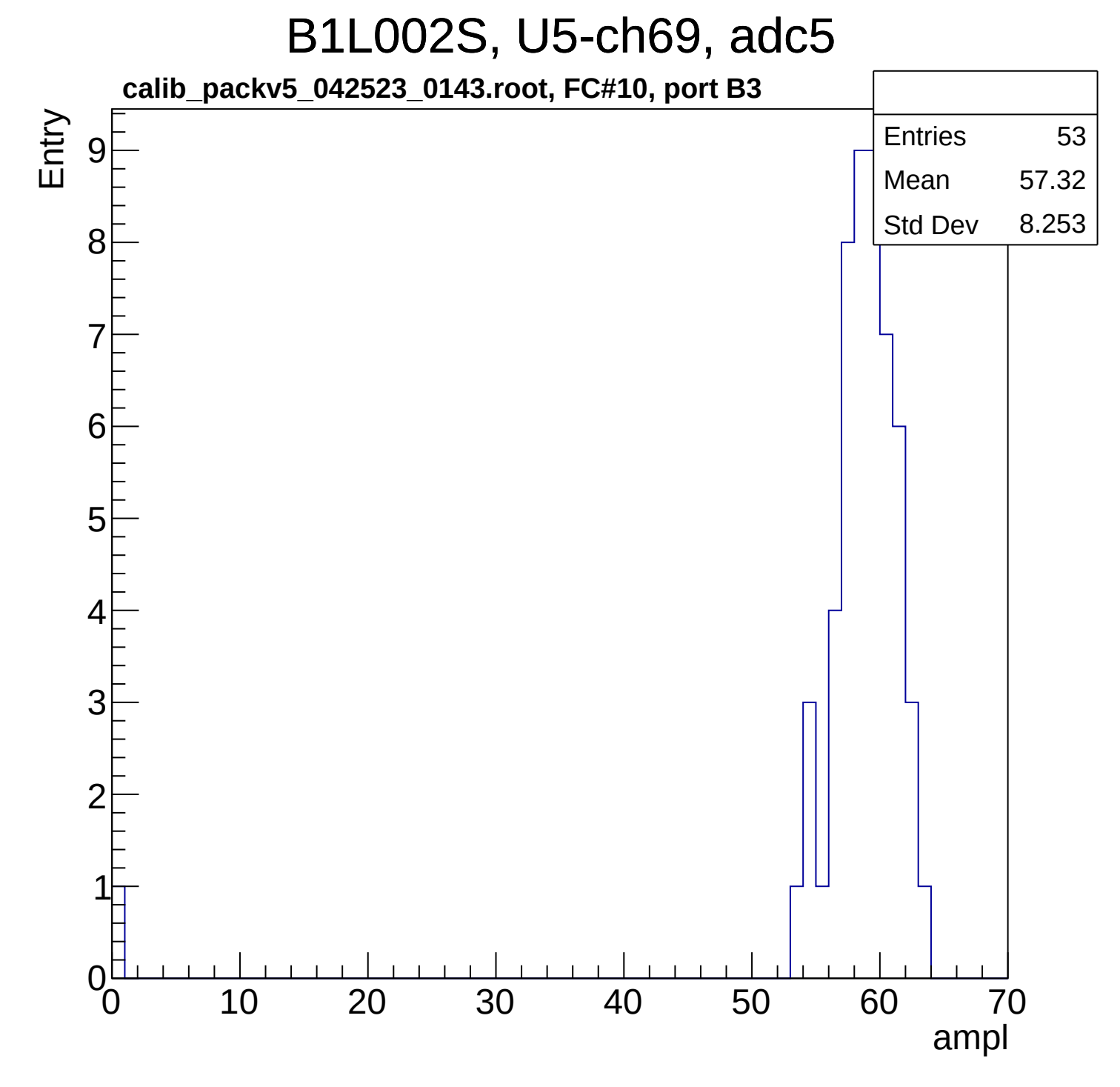
B1L002S, U5-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.32
Std Dev 8.253
Entry
ampl
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4
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6
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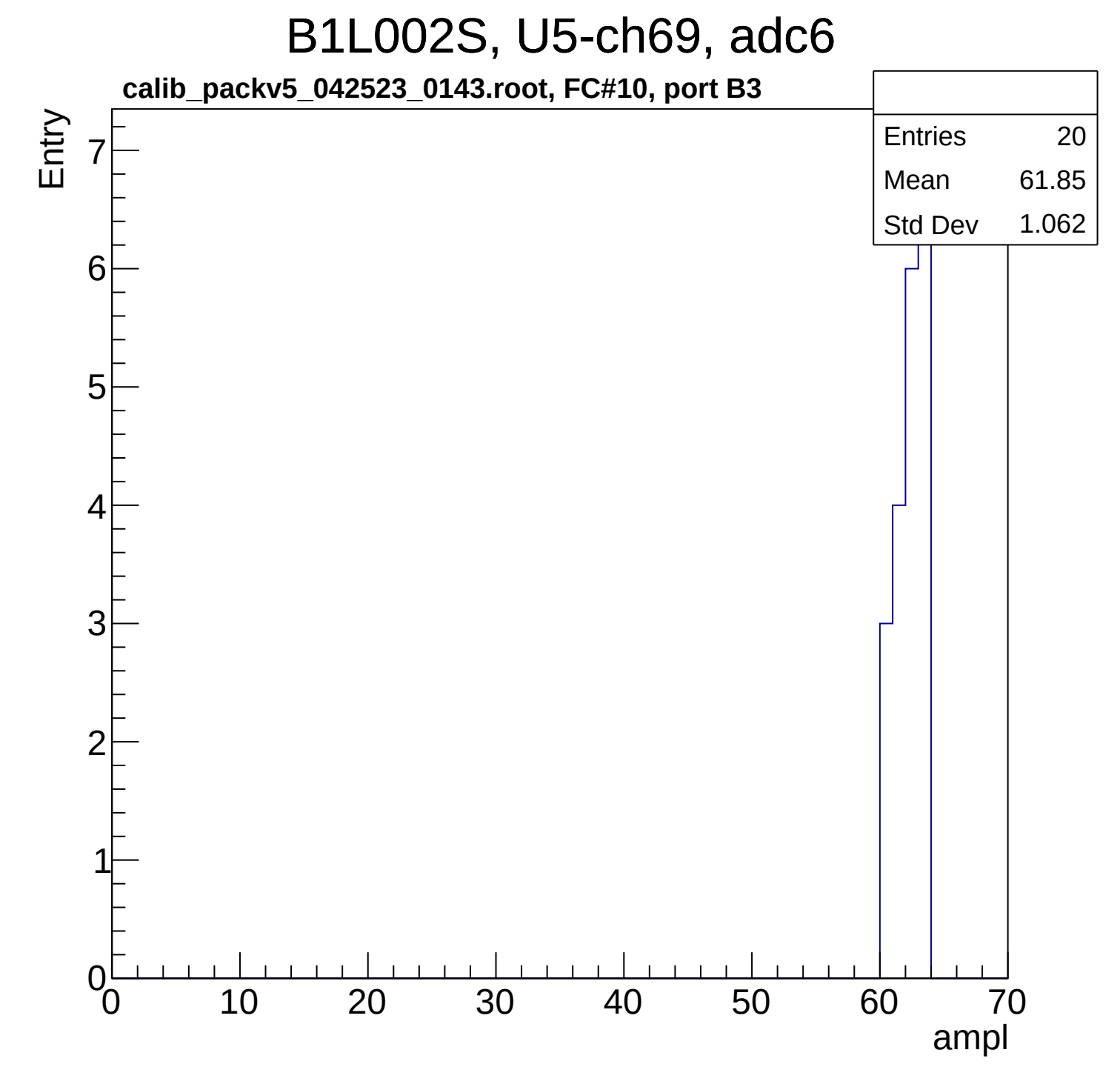
B1L002S, U5-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.85
Std Dev 1.062
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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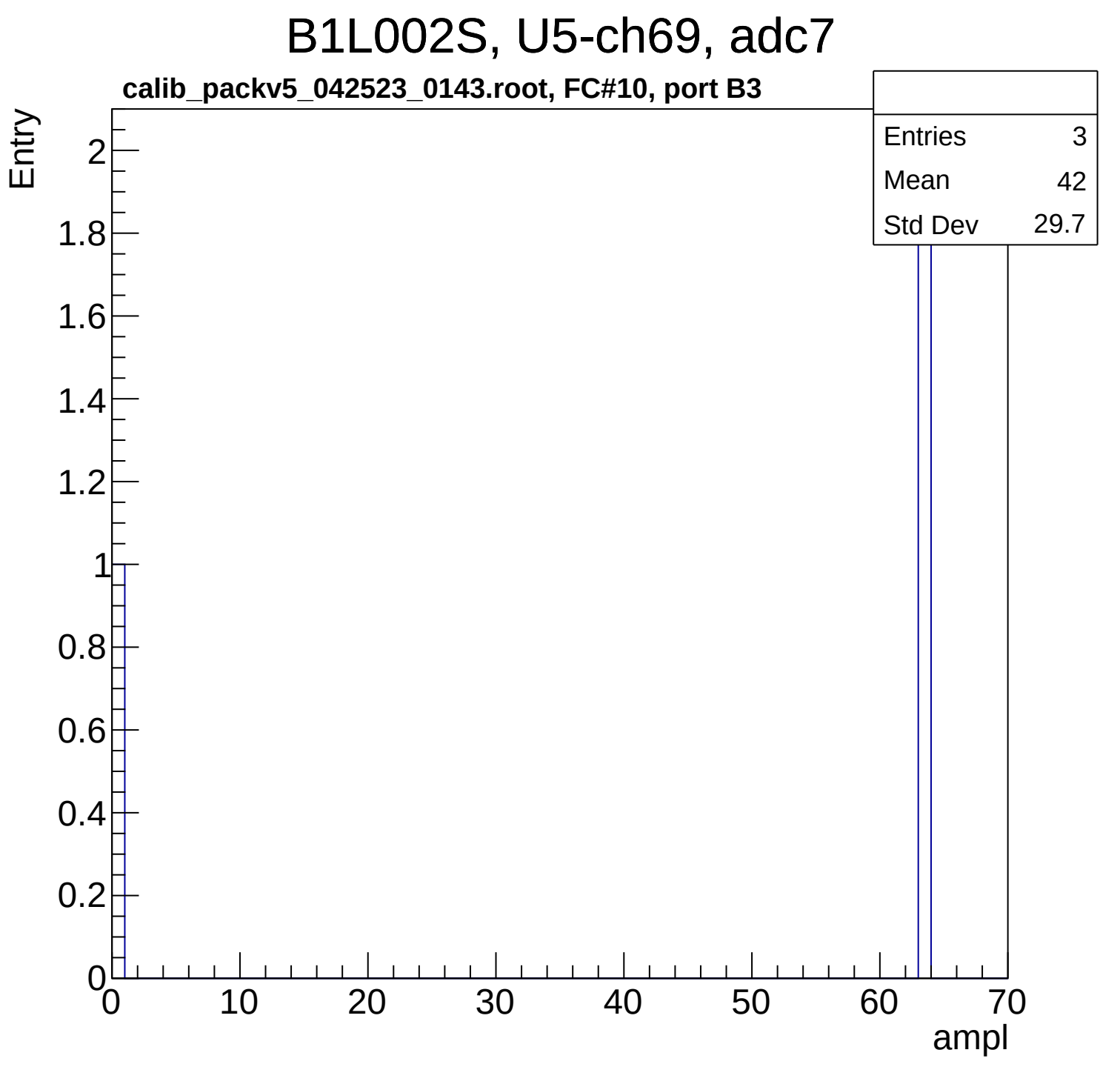
B1L002S, U5-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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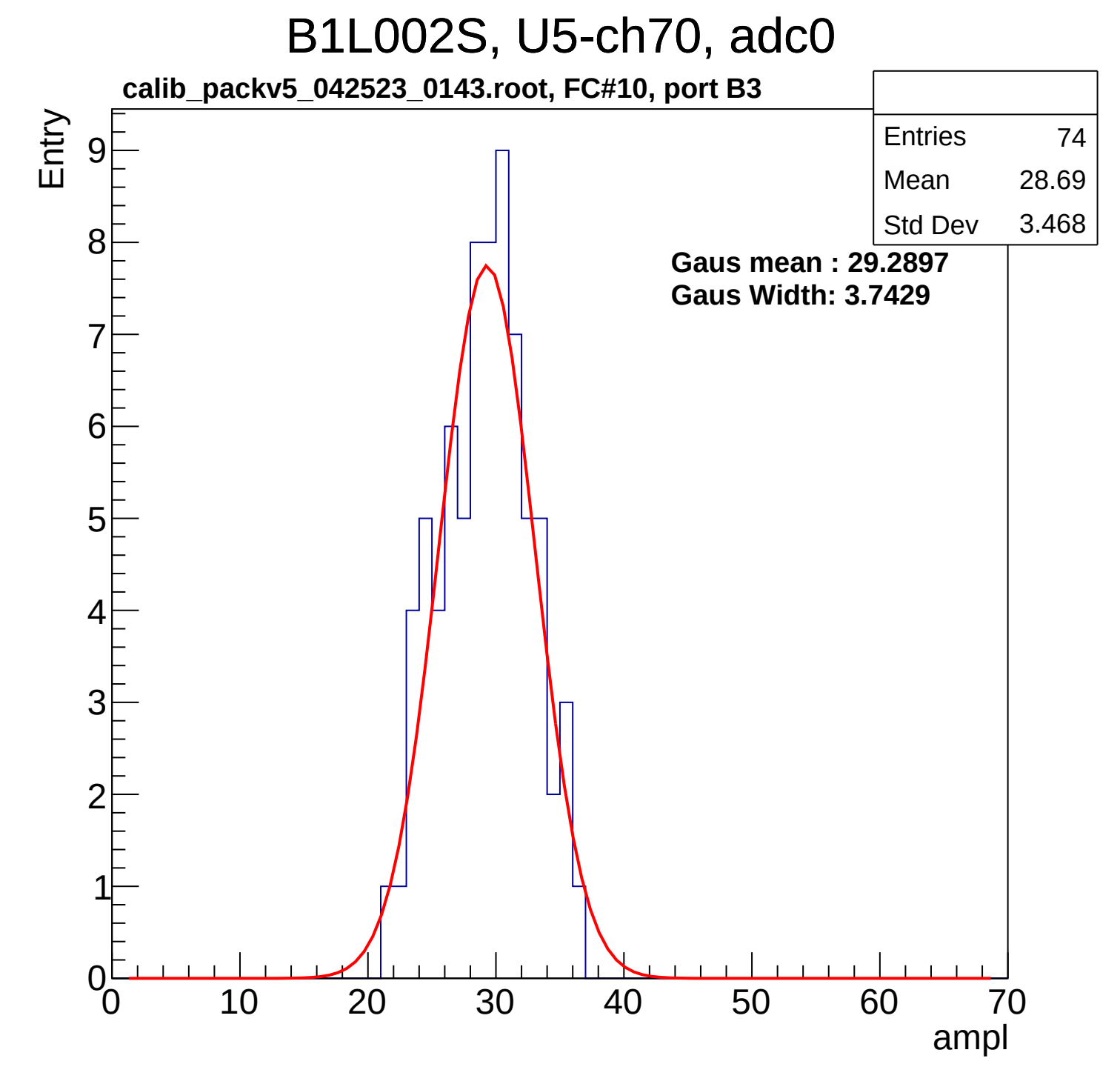
B1L002S, U5-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.69
Std Dev 3.468
Gaus mean : 29.2897
Gaus Width: 3.7429
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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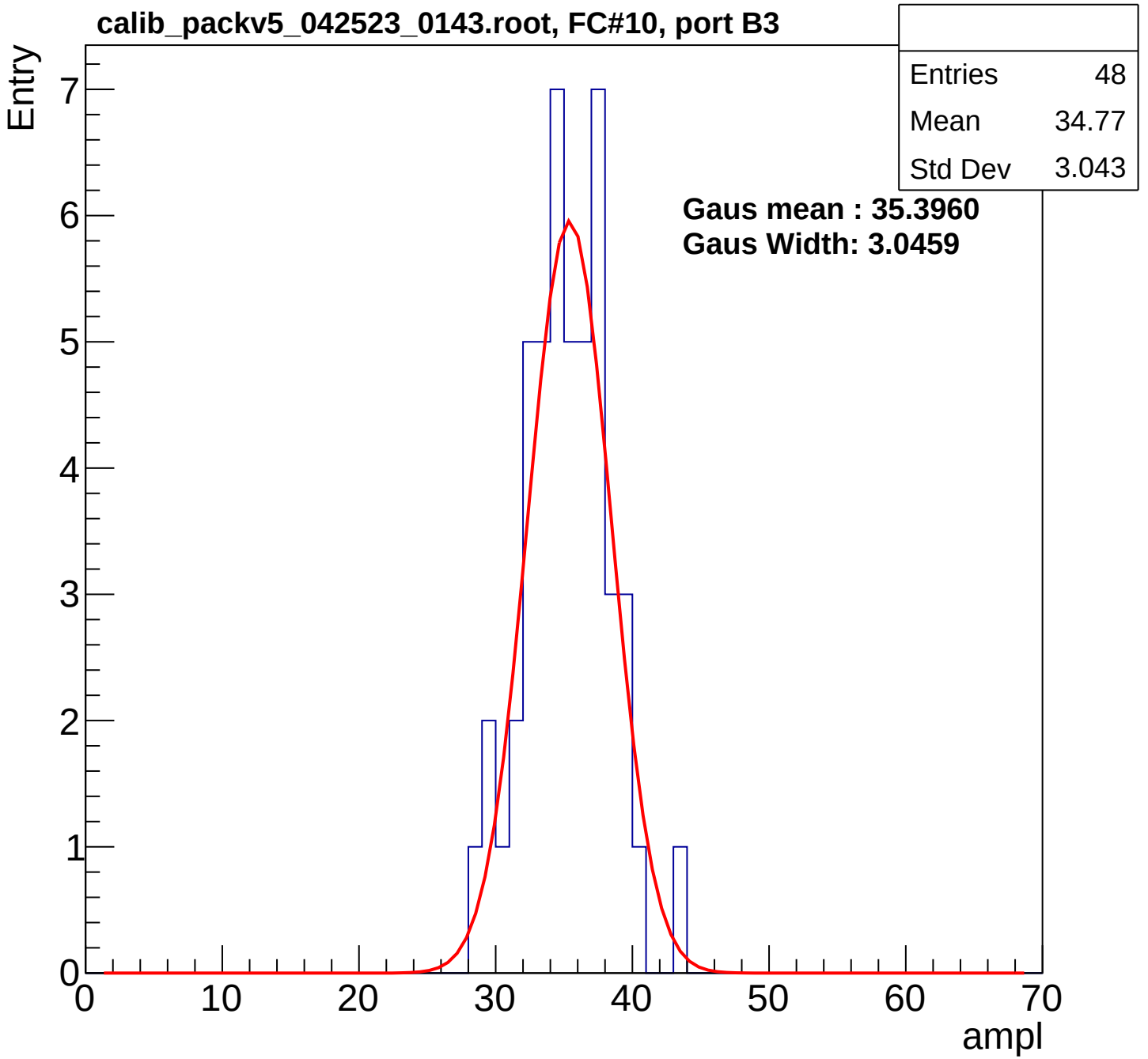
6
7
8
9


B1L002S, U5-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 34.77
Std Dev 3.043
Gaus mean : 35.3960
Gaus Width: 3.0459
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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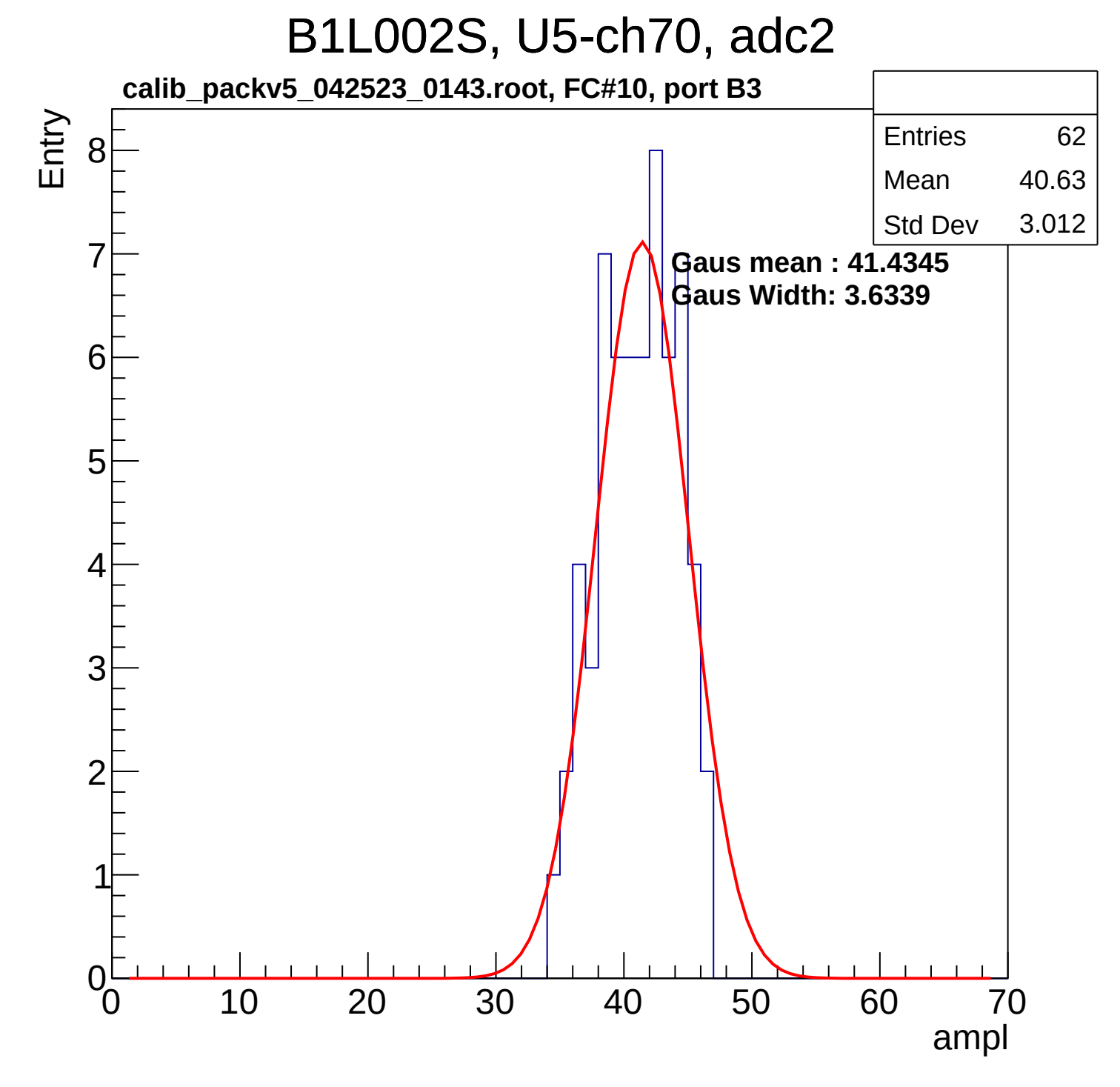
B1L002S, U5-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.63
Std Dev 3.012
Gaus mean : 41.4345
Gaus Width: 3.6339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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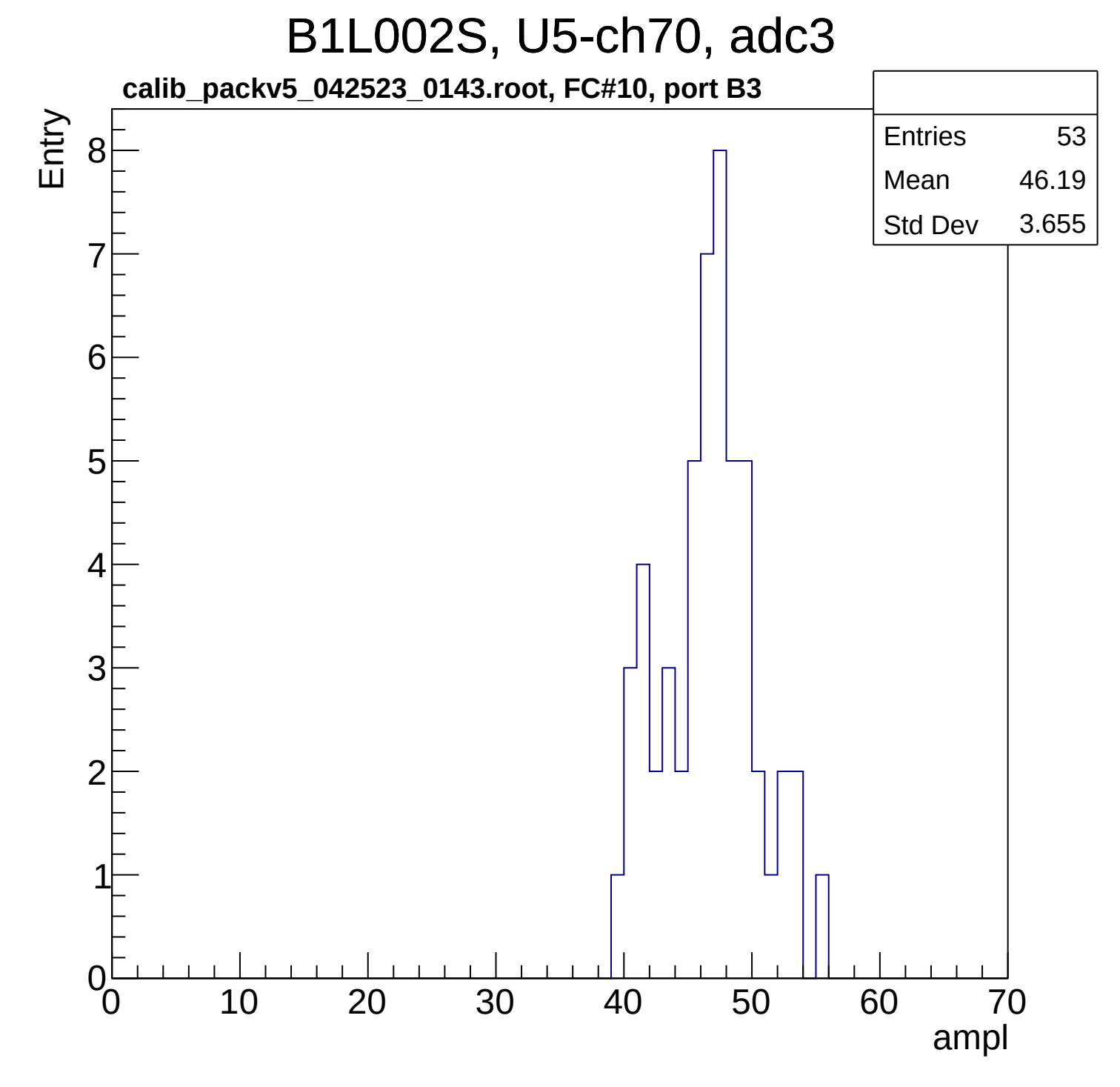
B1L002S, U5-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 46.19
Std Dev 3.655
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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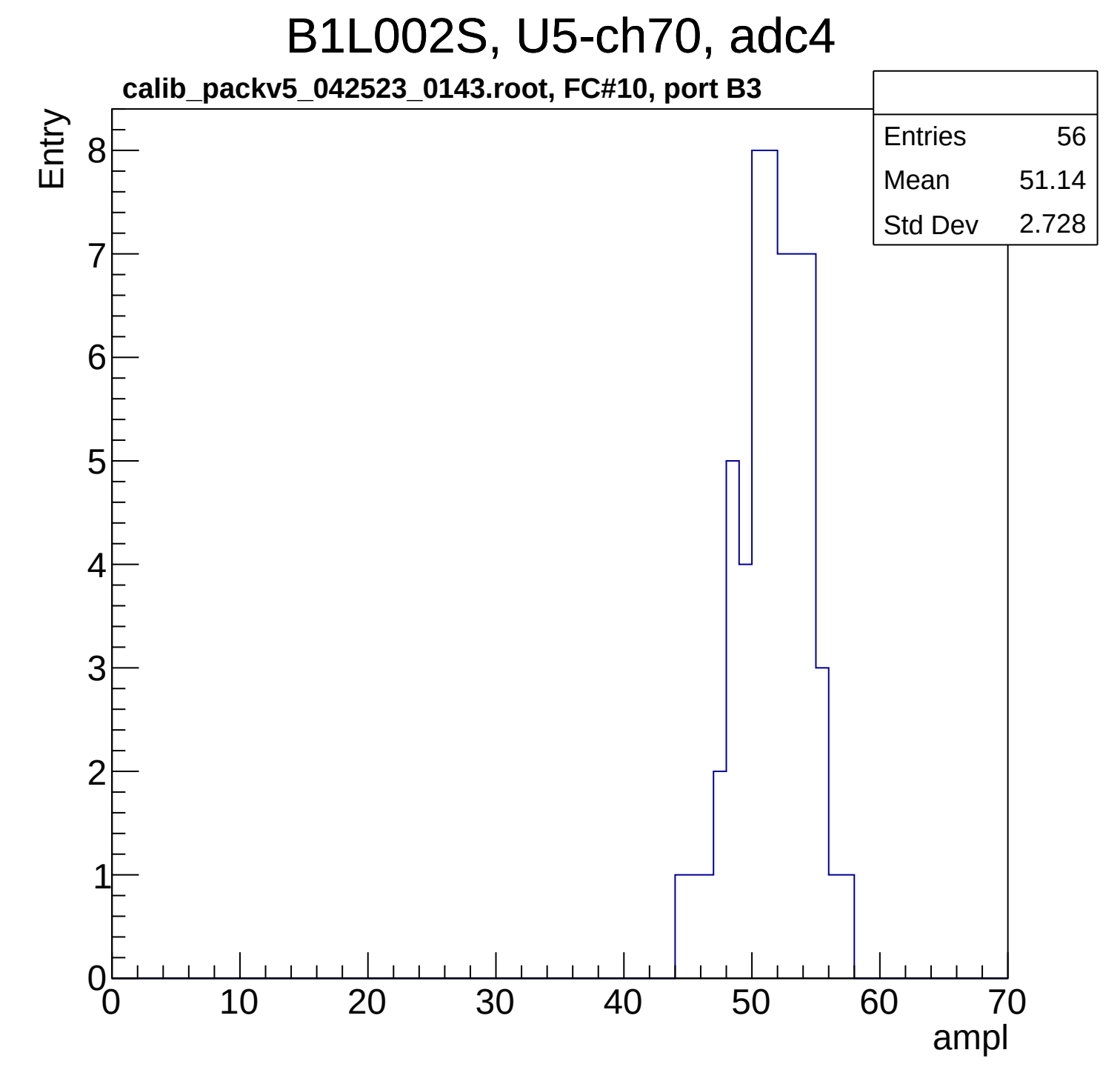
B1L002S, U5-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.14
Std Dev 2.728
Entry
ampl
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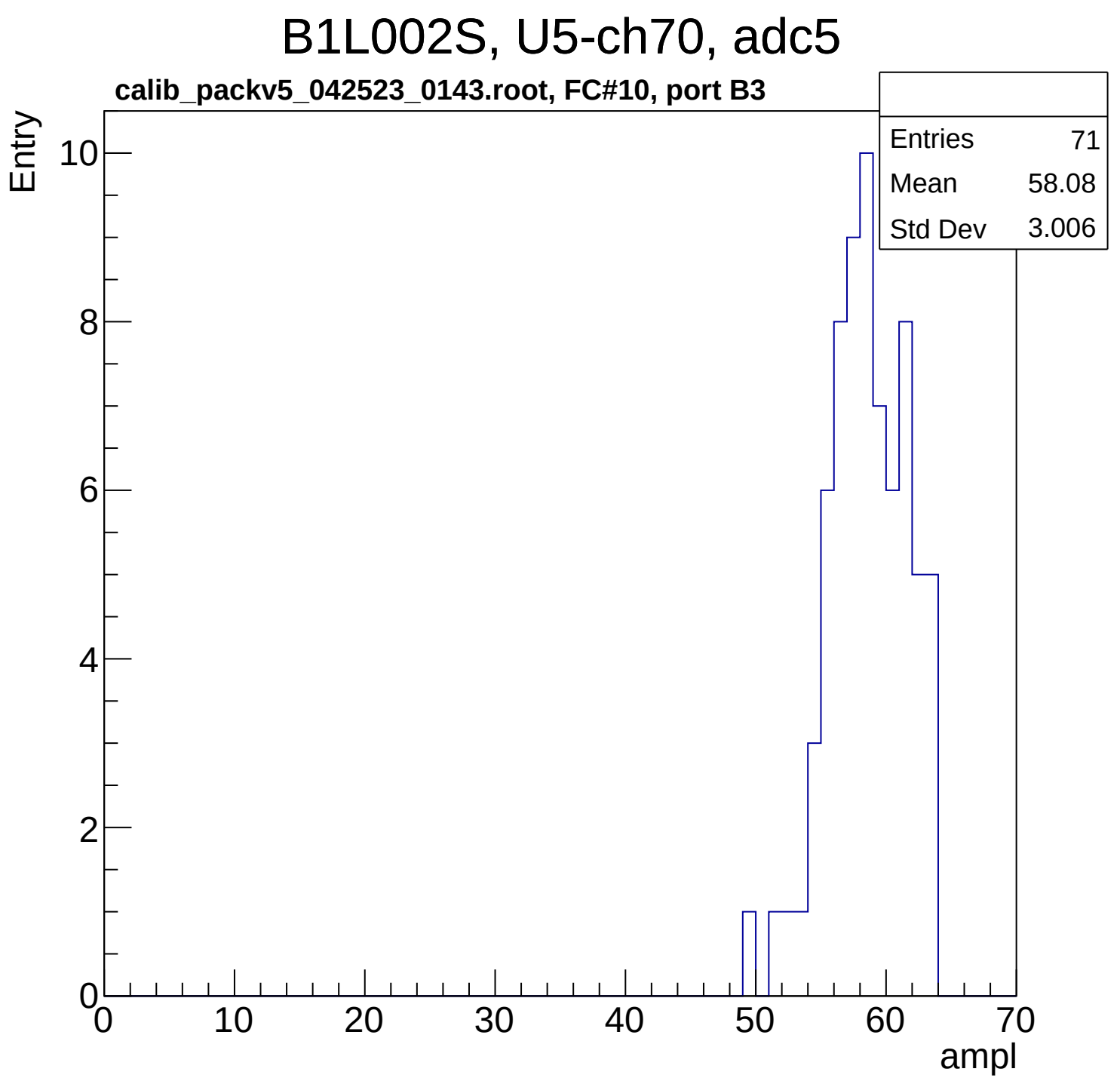
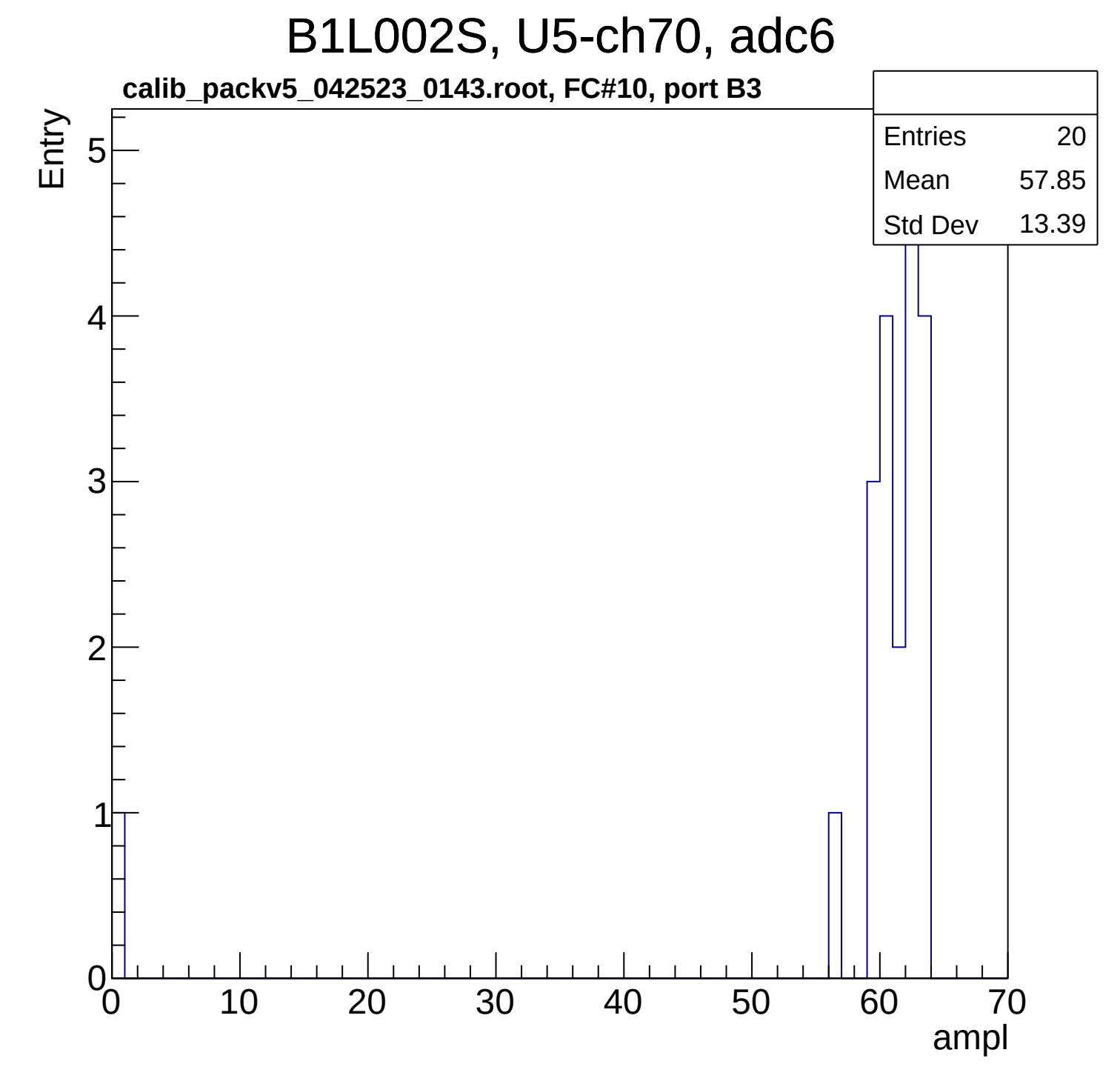
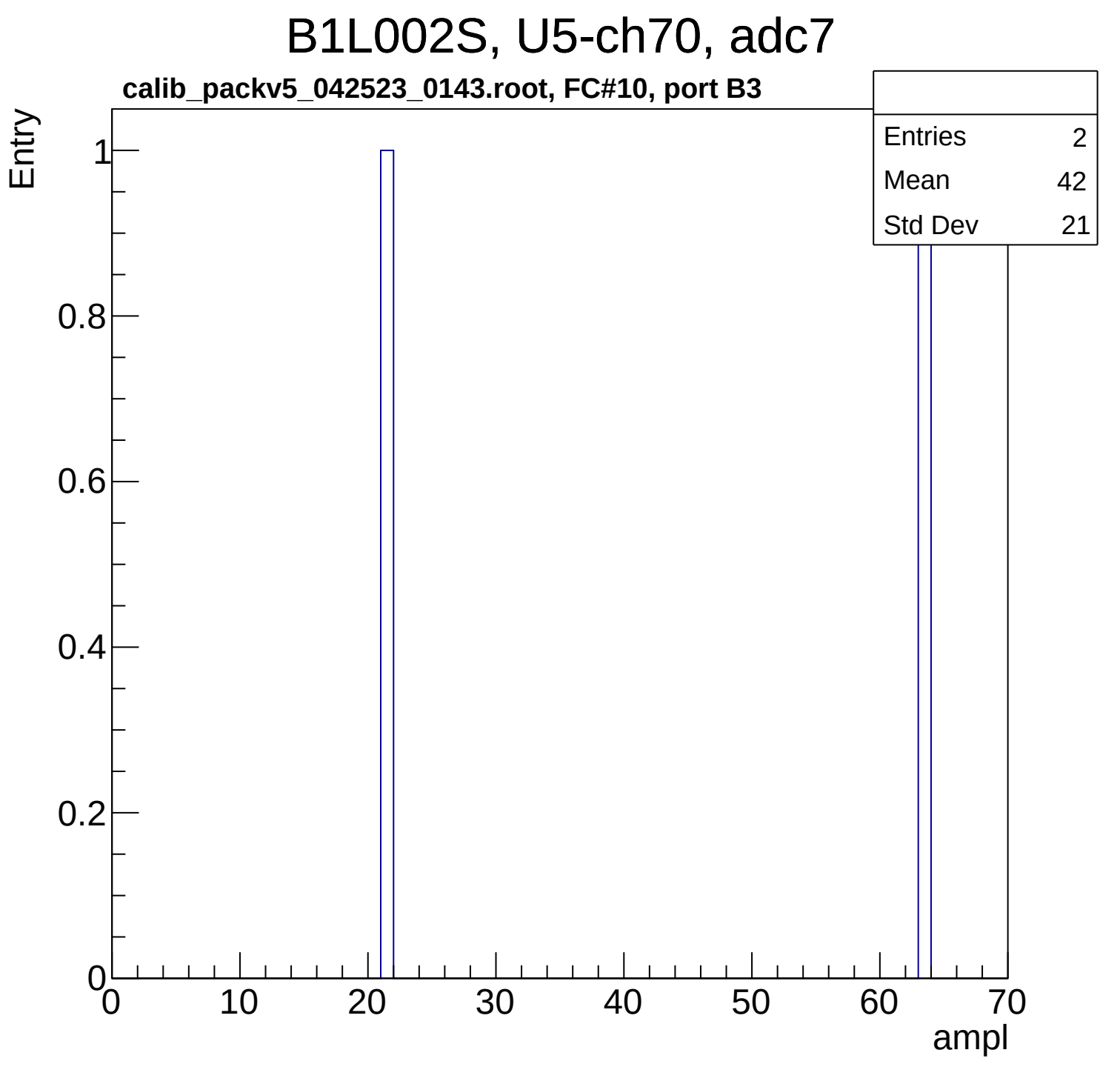
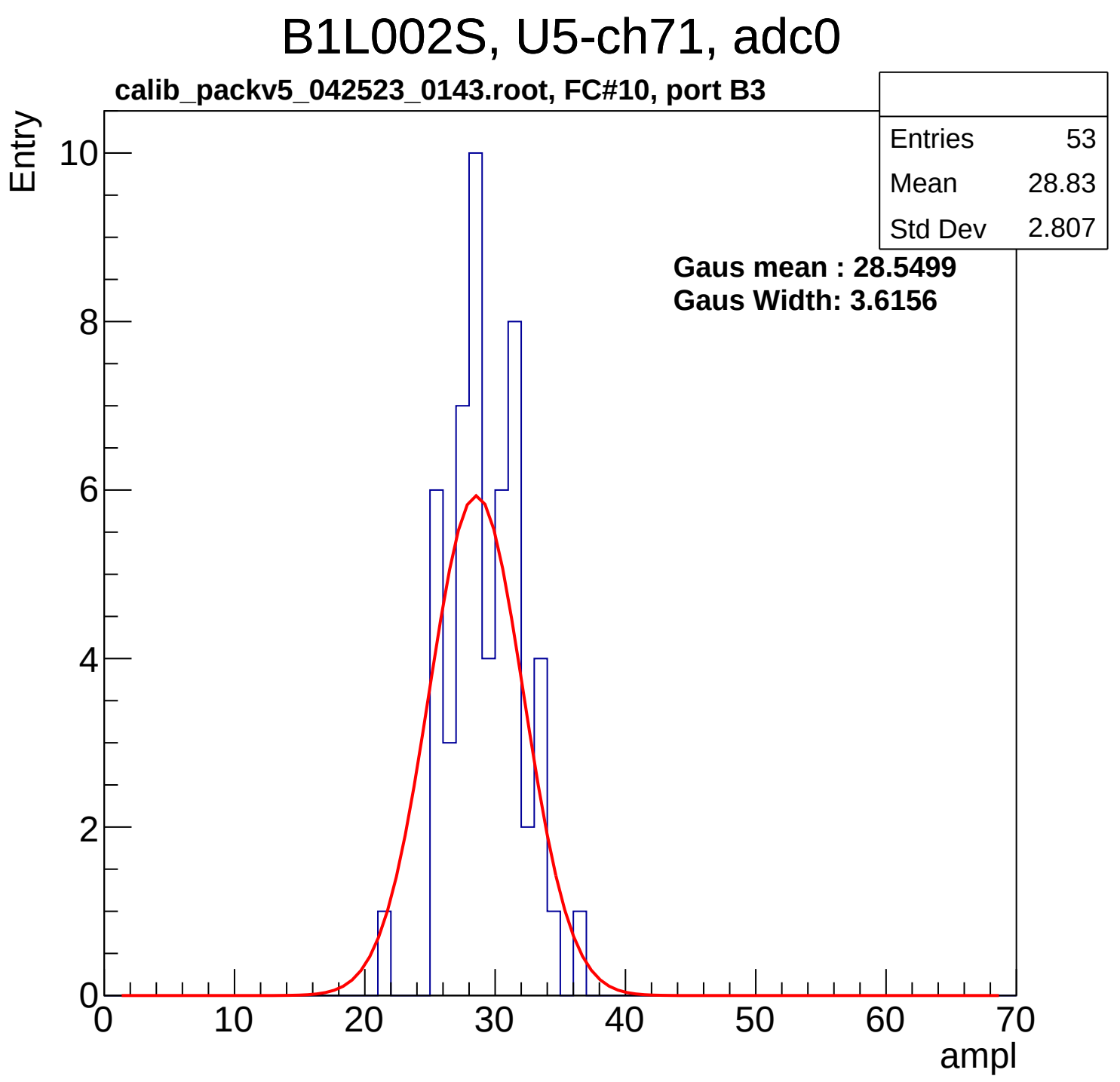
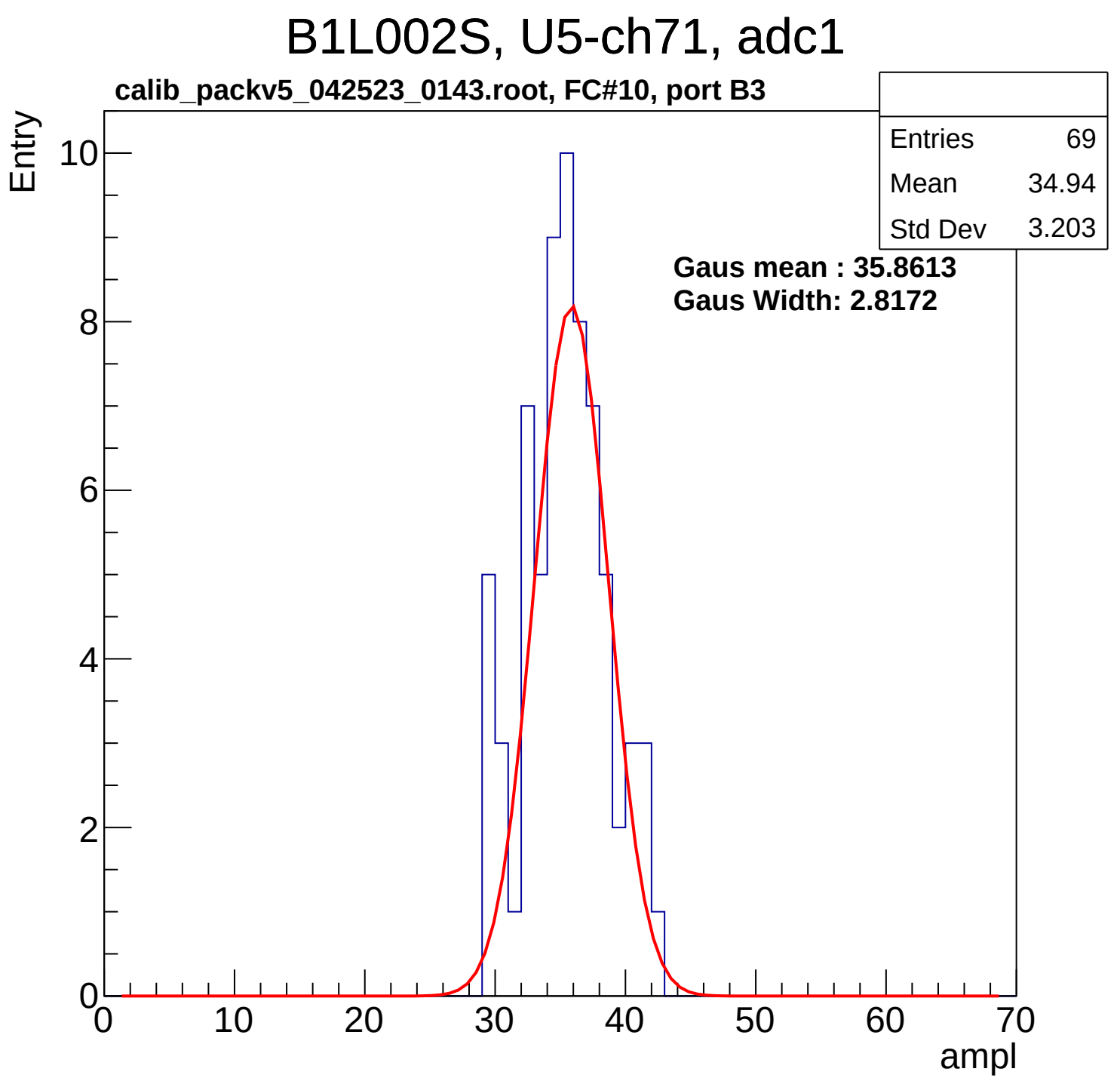
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 58.08
Std Dev 3.006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 57.85
Std Dev 13.39
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 28.83
Std Dev 2.807
Gaus mean : 28.5499
Gaus Width: 3.6156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.94
Std Dev 3.203
Gaus mean : 35.8613
Gaus Width: 2.8172
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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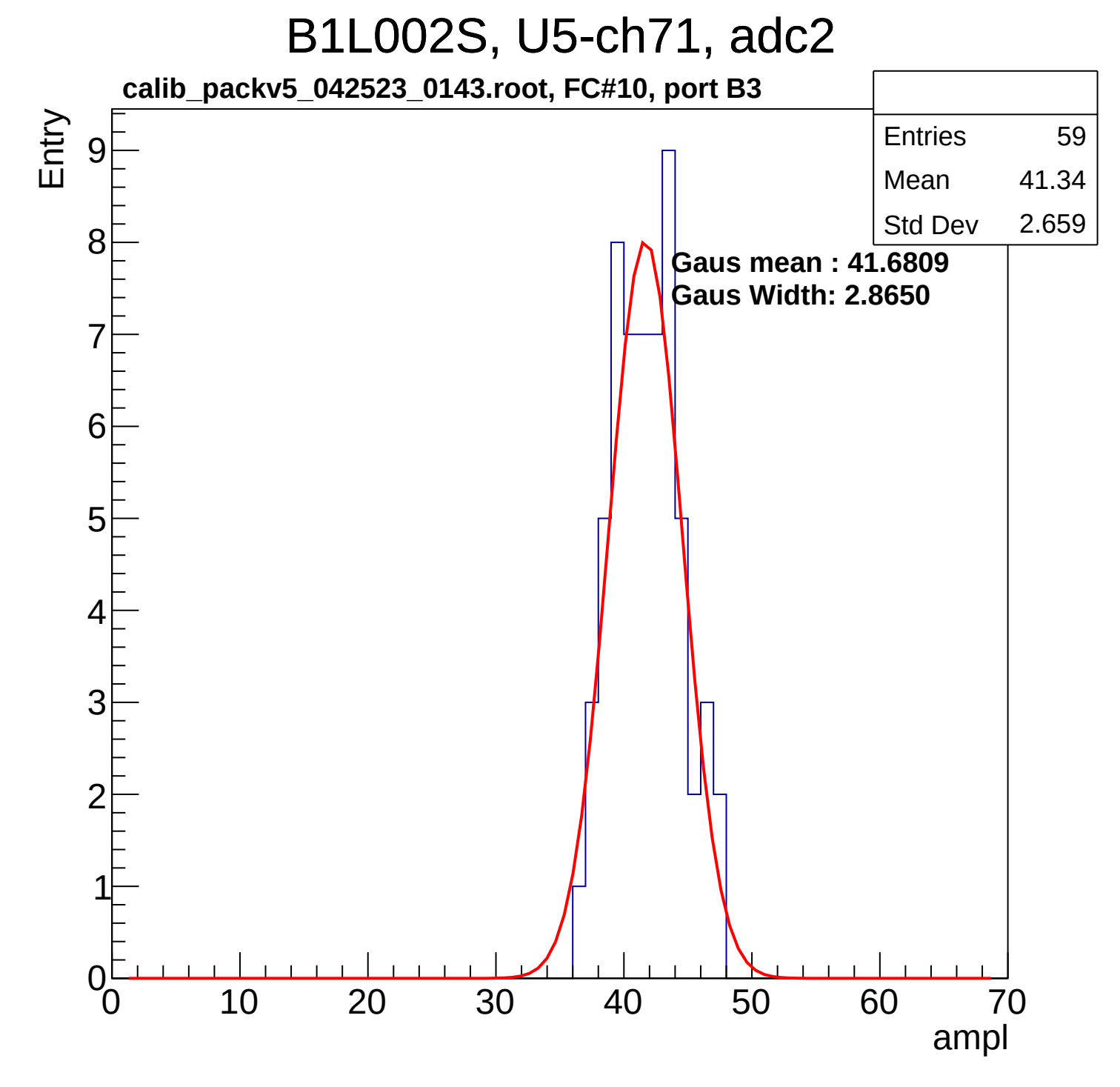
B1L002S, U5-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.34
Std Dev 2.659
Gaus mean : 41.6809
Gaus Width: 2.8650
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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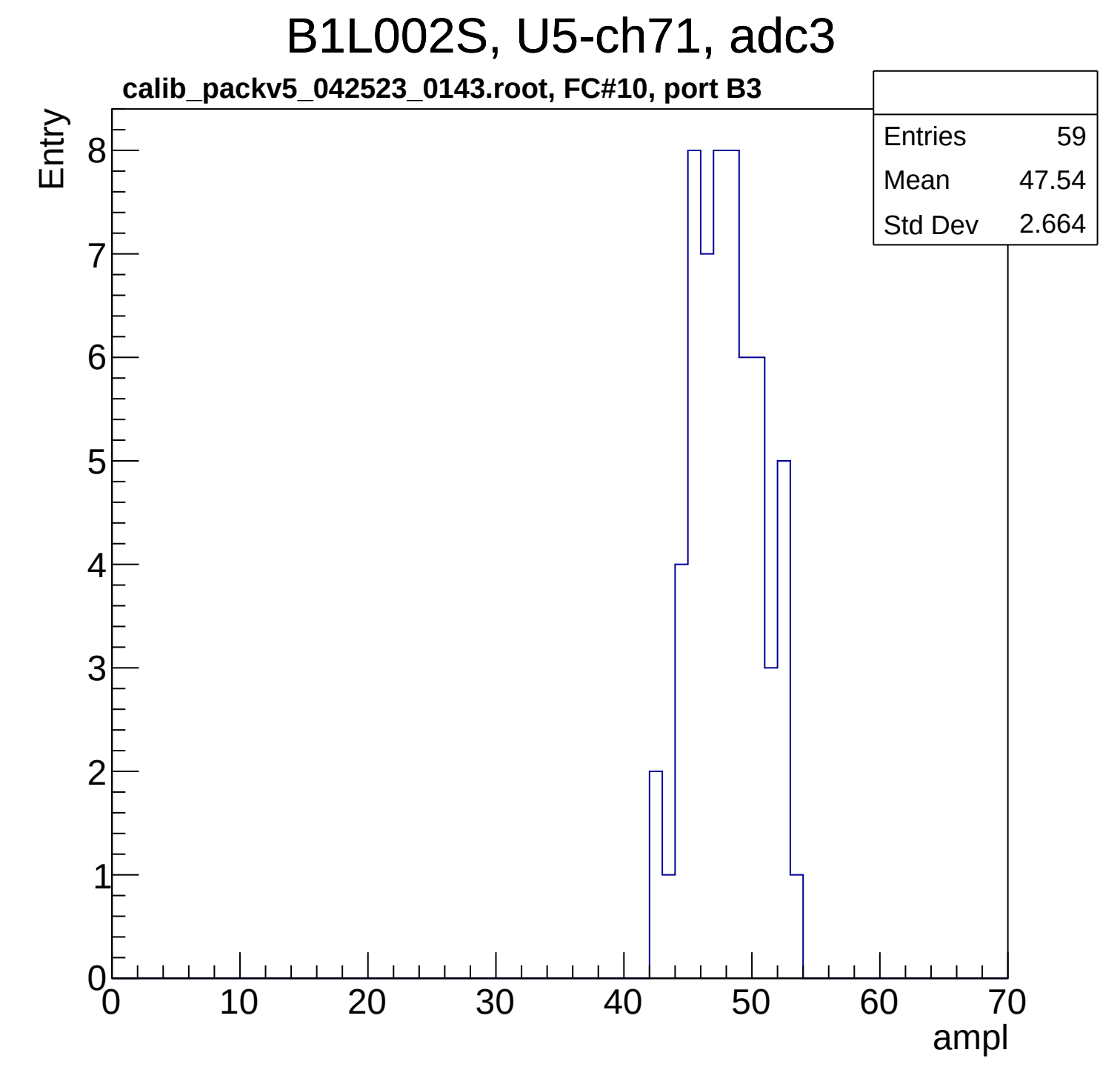
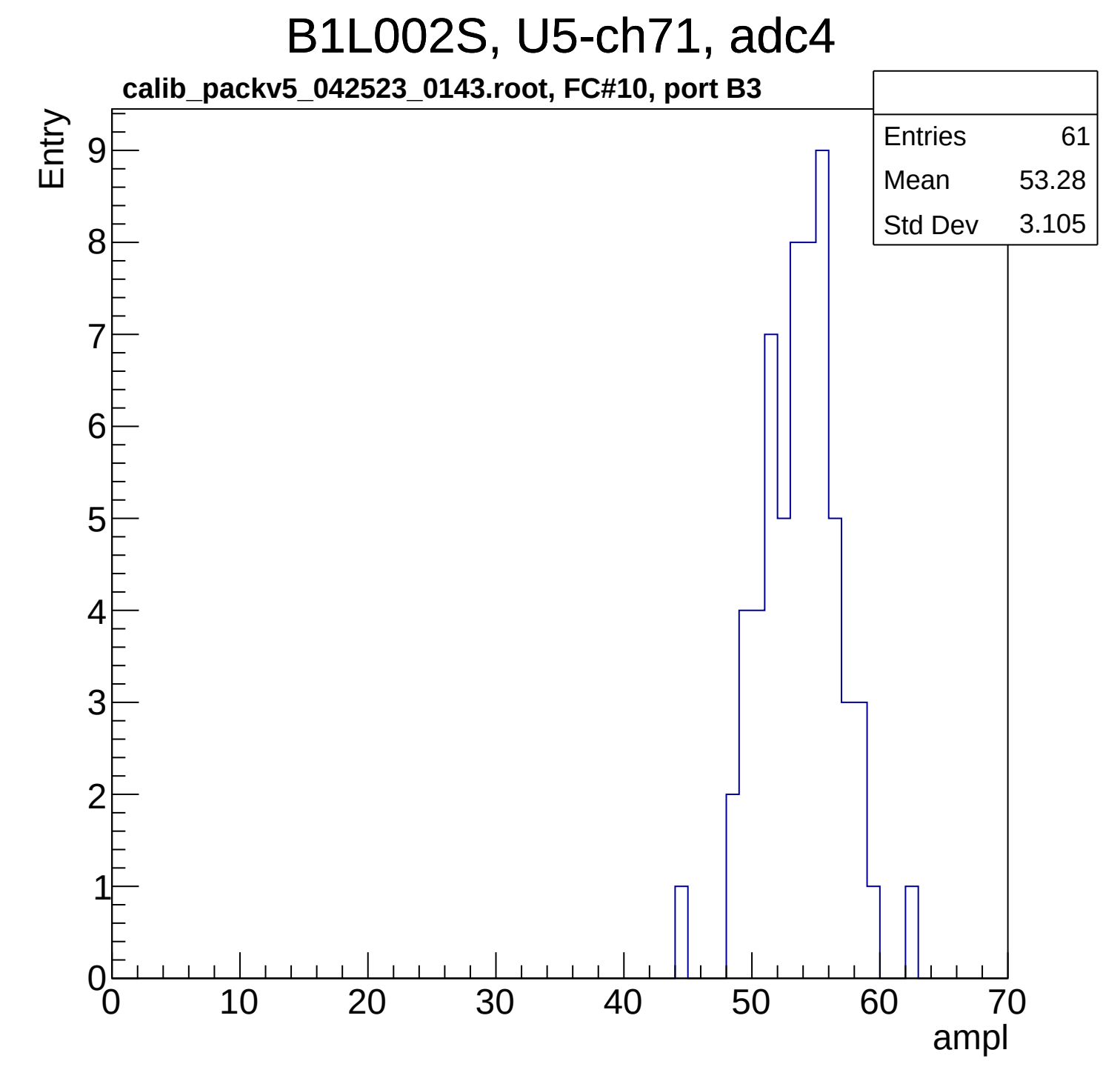

B1L002S, U5-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.54
Std Dev 2.664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.28
Std Dev 3.105
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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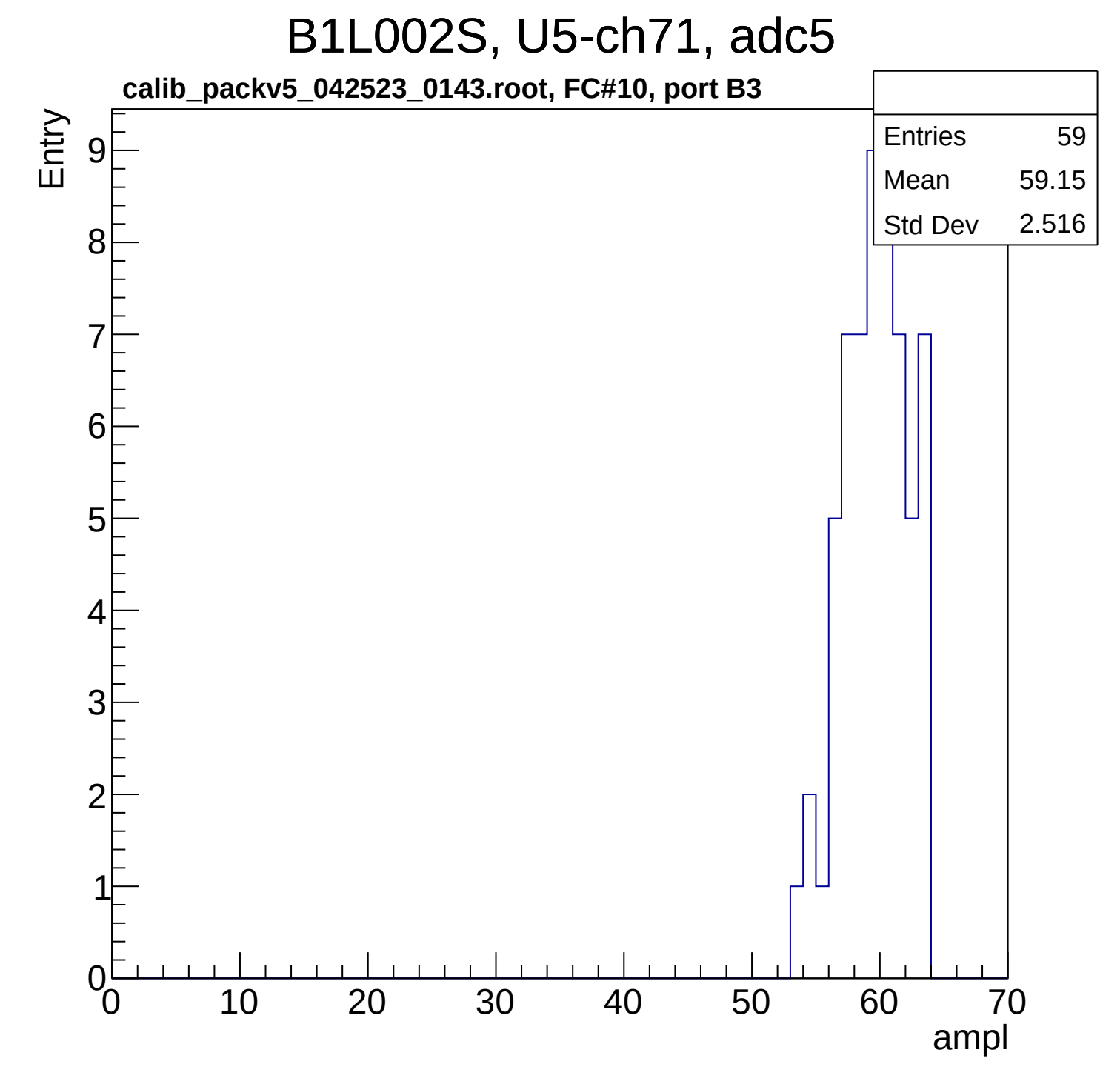
B1L002S, U5-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.15
Std Dev 2.516
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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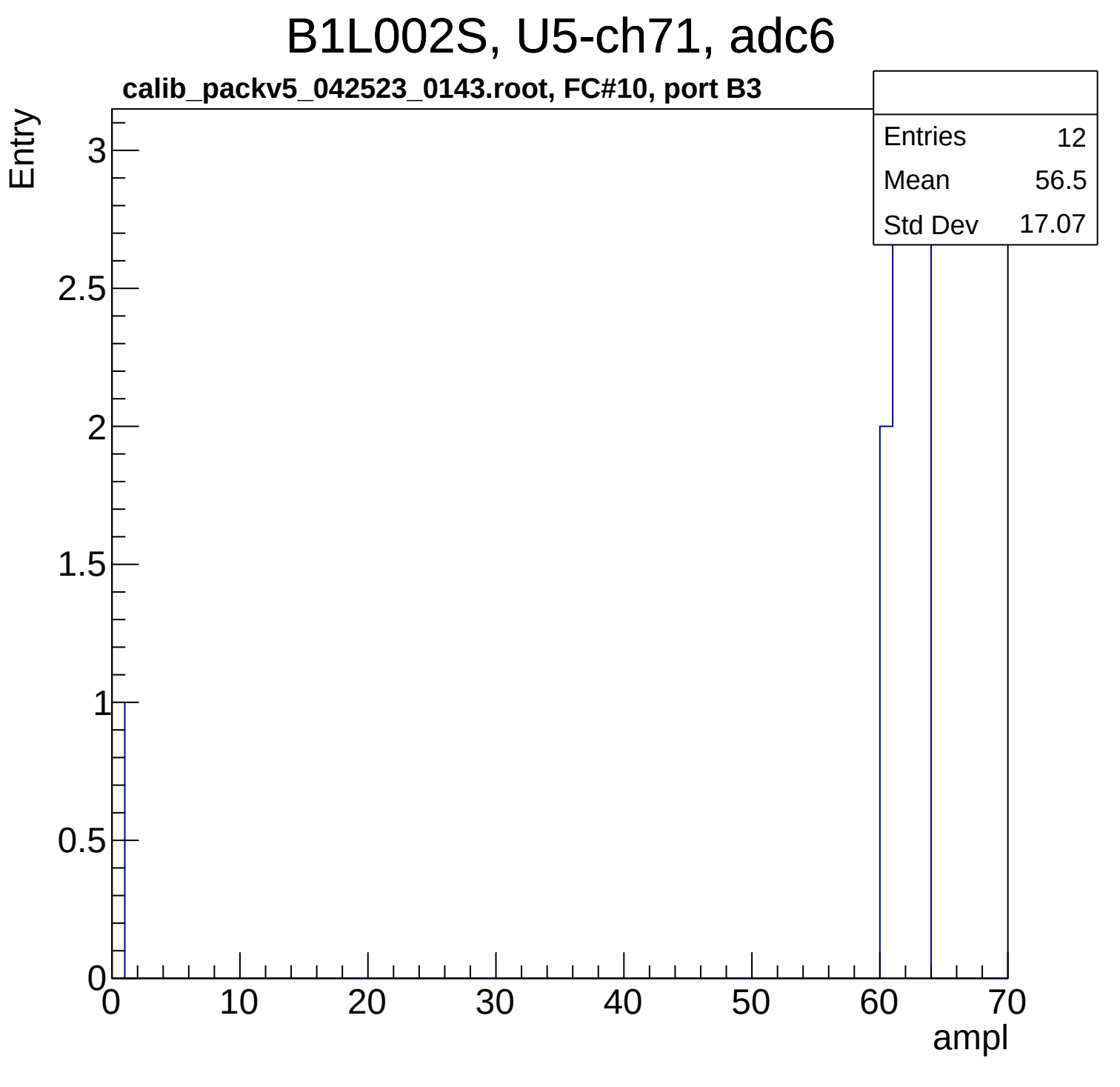
B1L002S, U5-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.5
Std Dev 17.07
Entry
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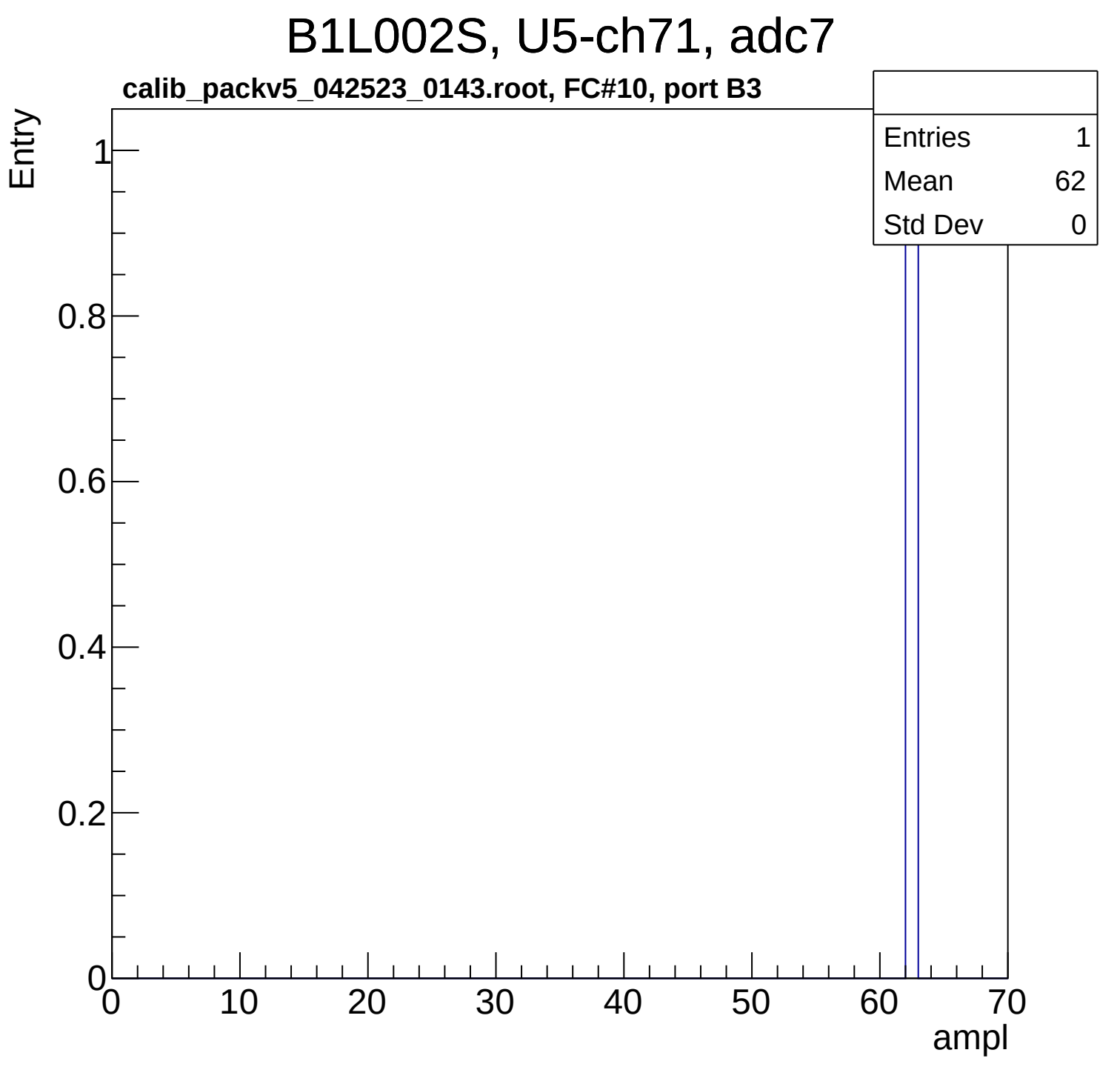
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
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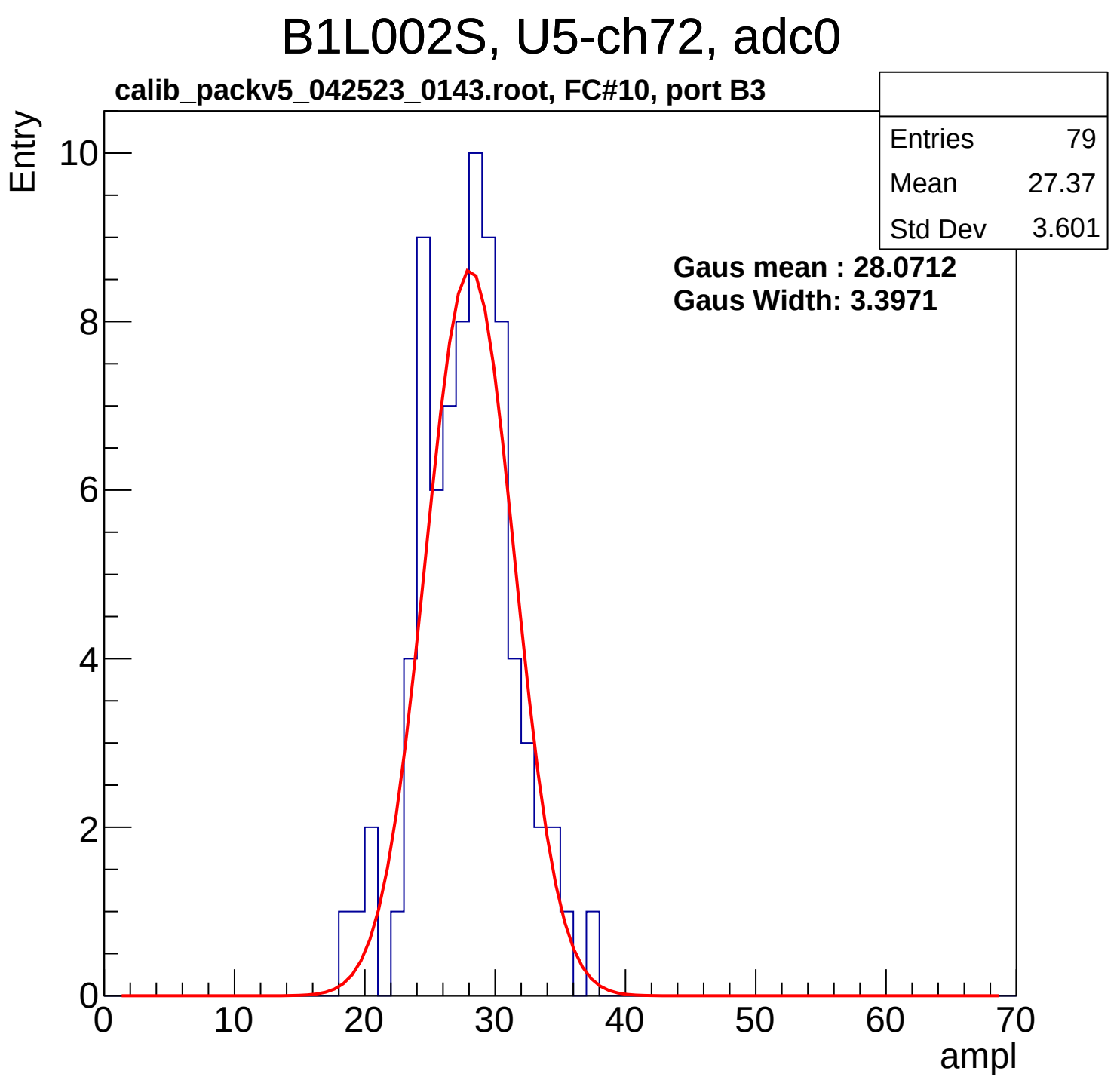
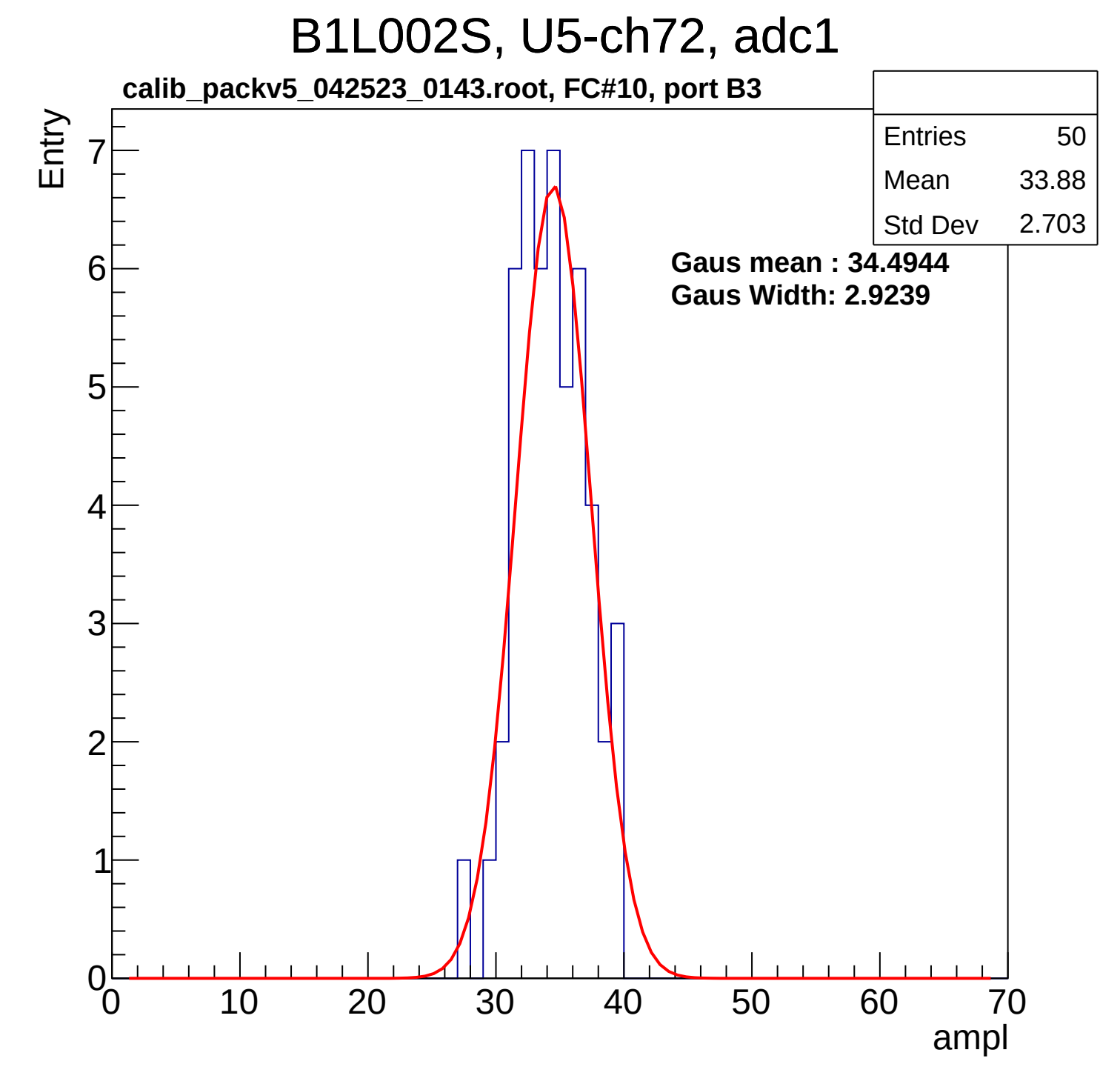
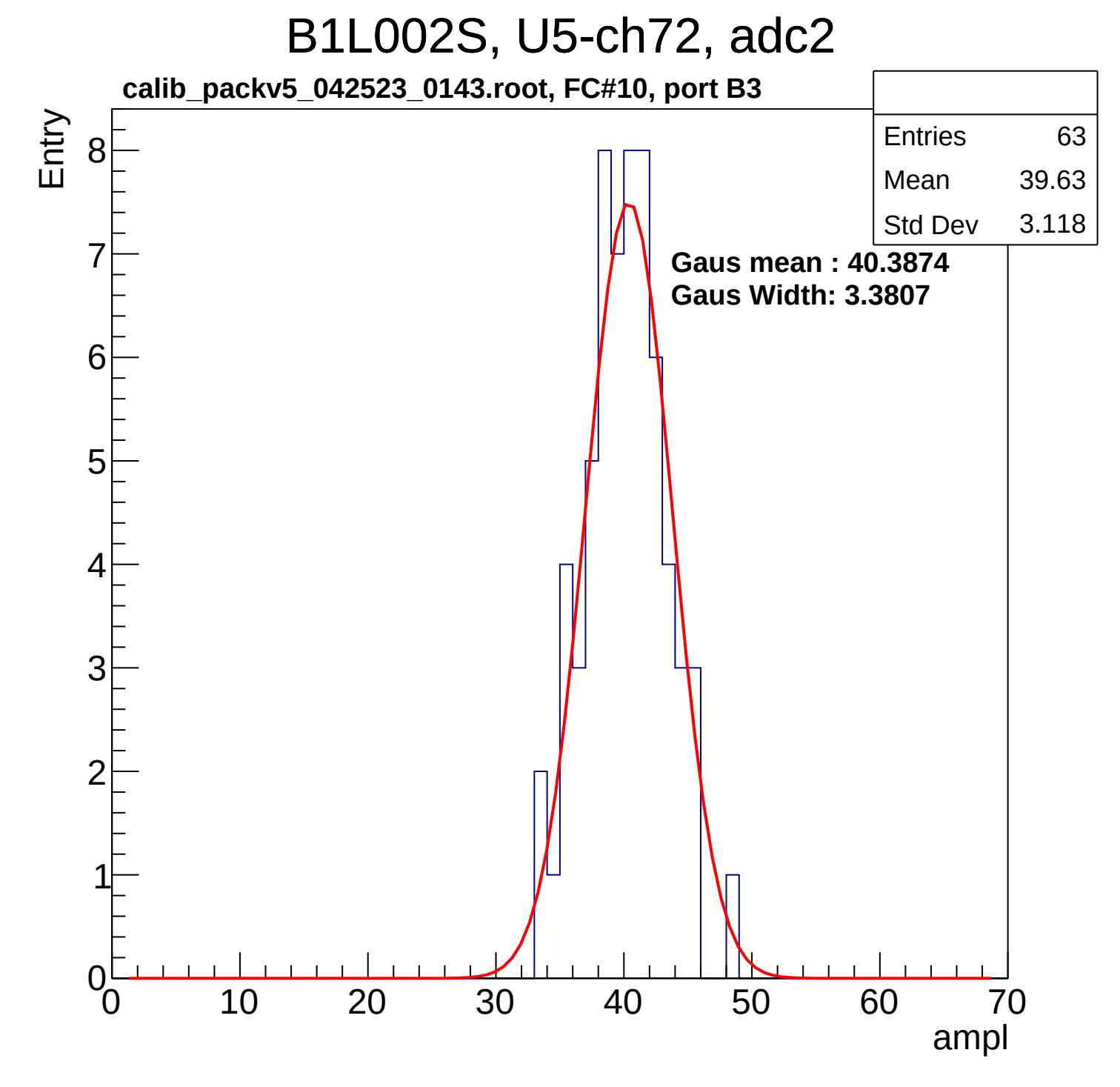
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.37
Std Dev 3.601
Gaus mean : 28.0712
Gaus Width: 3.3971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 33.88
Std Dev 2.703
Gaus mean : 34.4944
Gaus Width: 2.9239
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.63
Std Dev 3.118
Gaus mean : 40.3874
Gaus Width: 3.3807
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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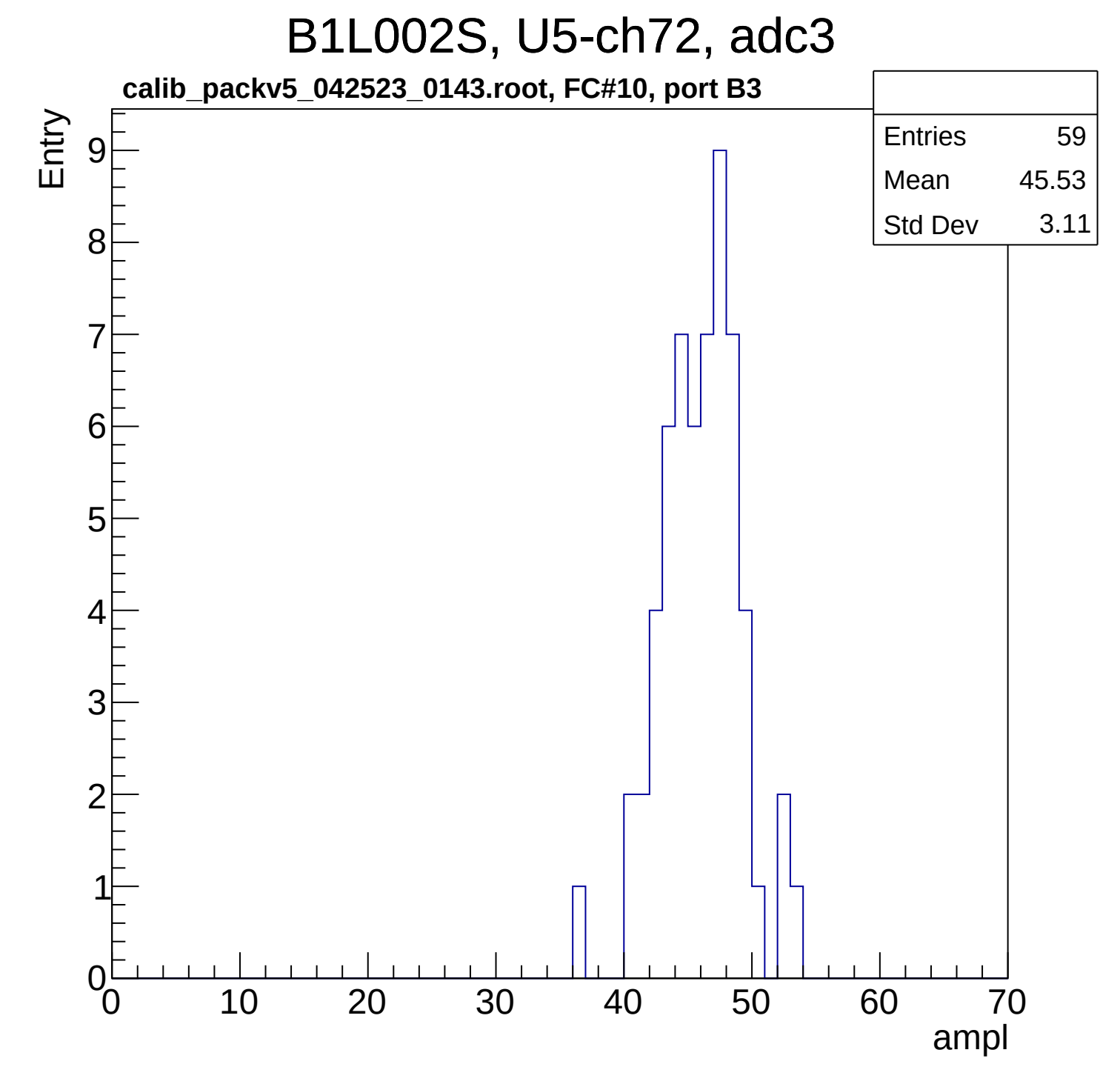
B1L002S, U5-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.53
Std Dev 3.11
Entry
ampl
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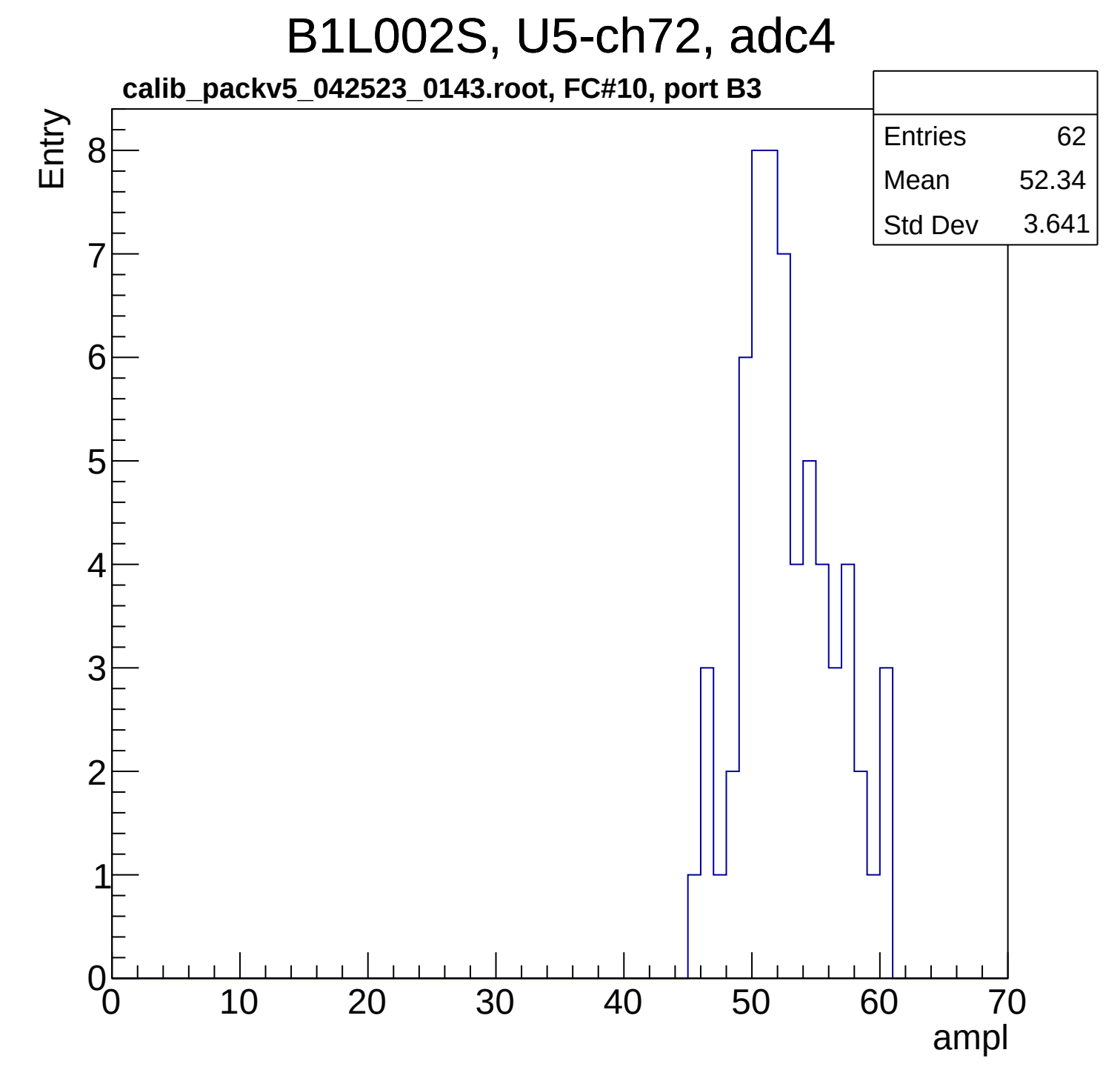
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.34
Std Dev 3.641
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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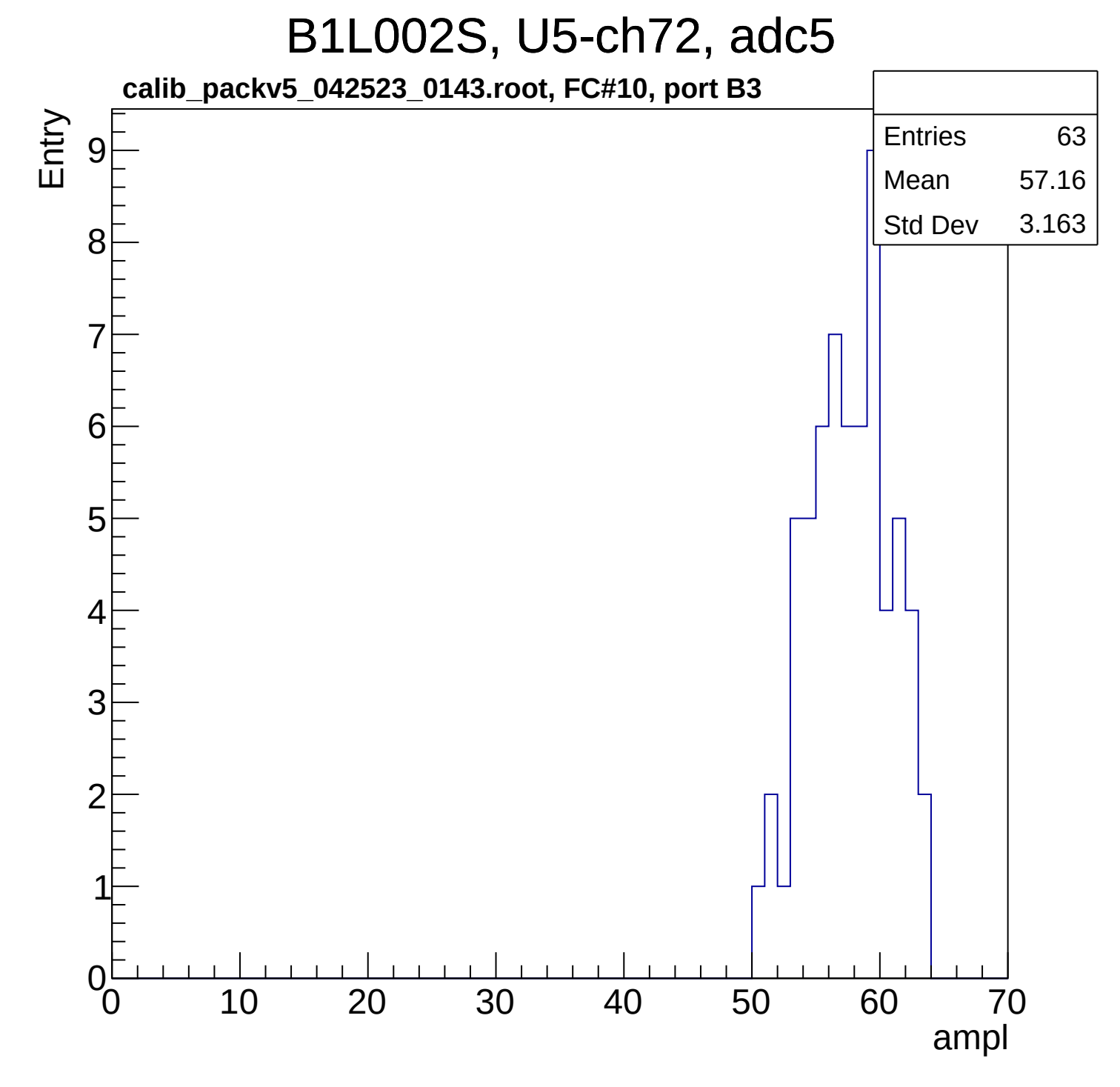
B1L002S, U5-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.16
Std Dev 3.163
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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50
60
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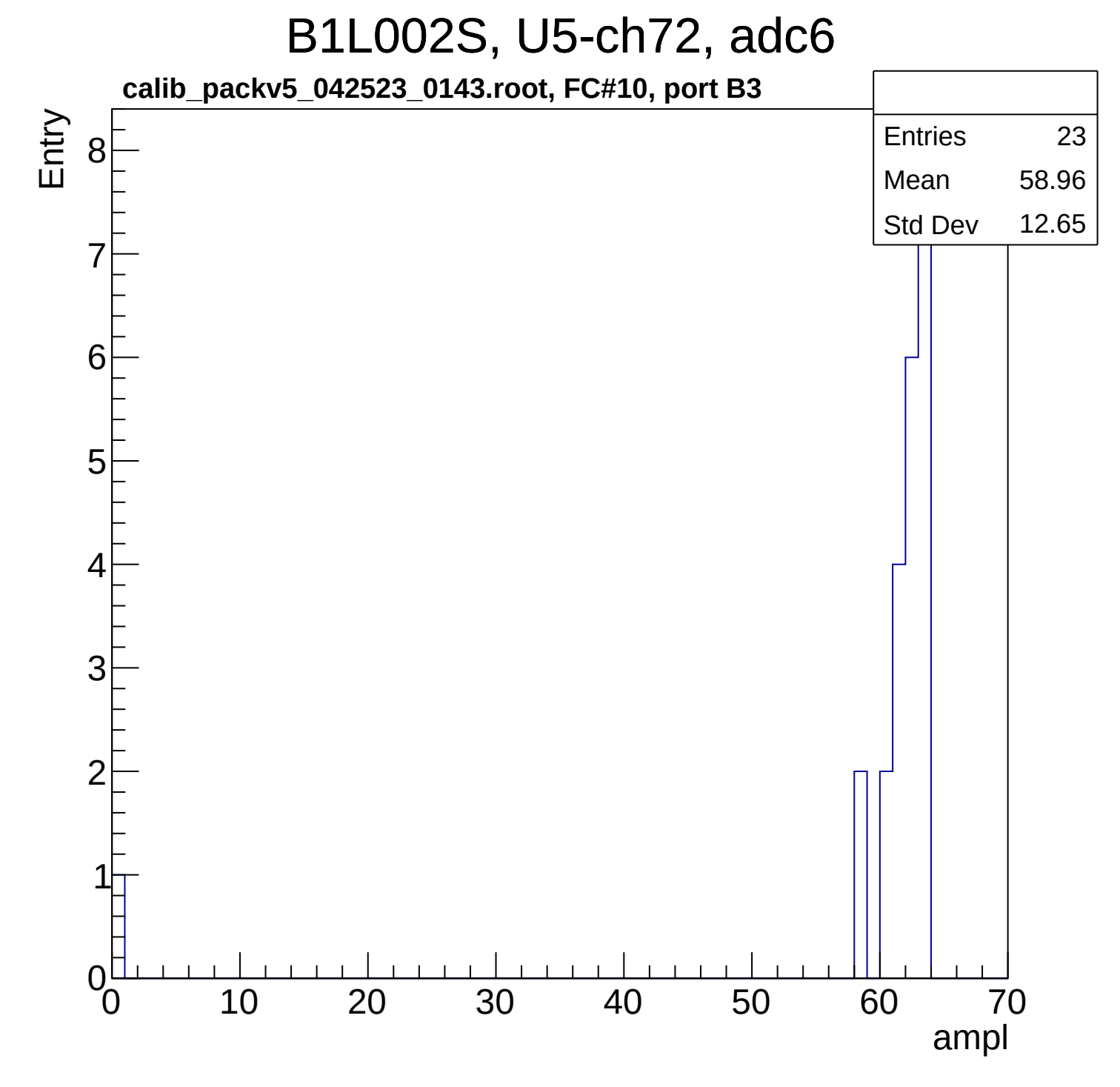
B1L002S, U5-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.96
Std Dev 12.65
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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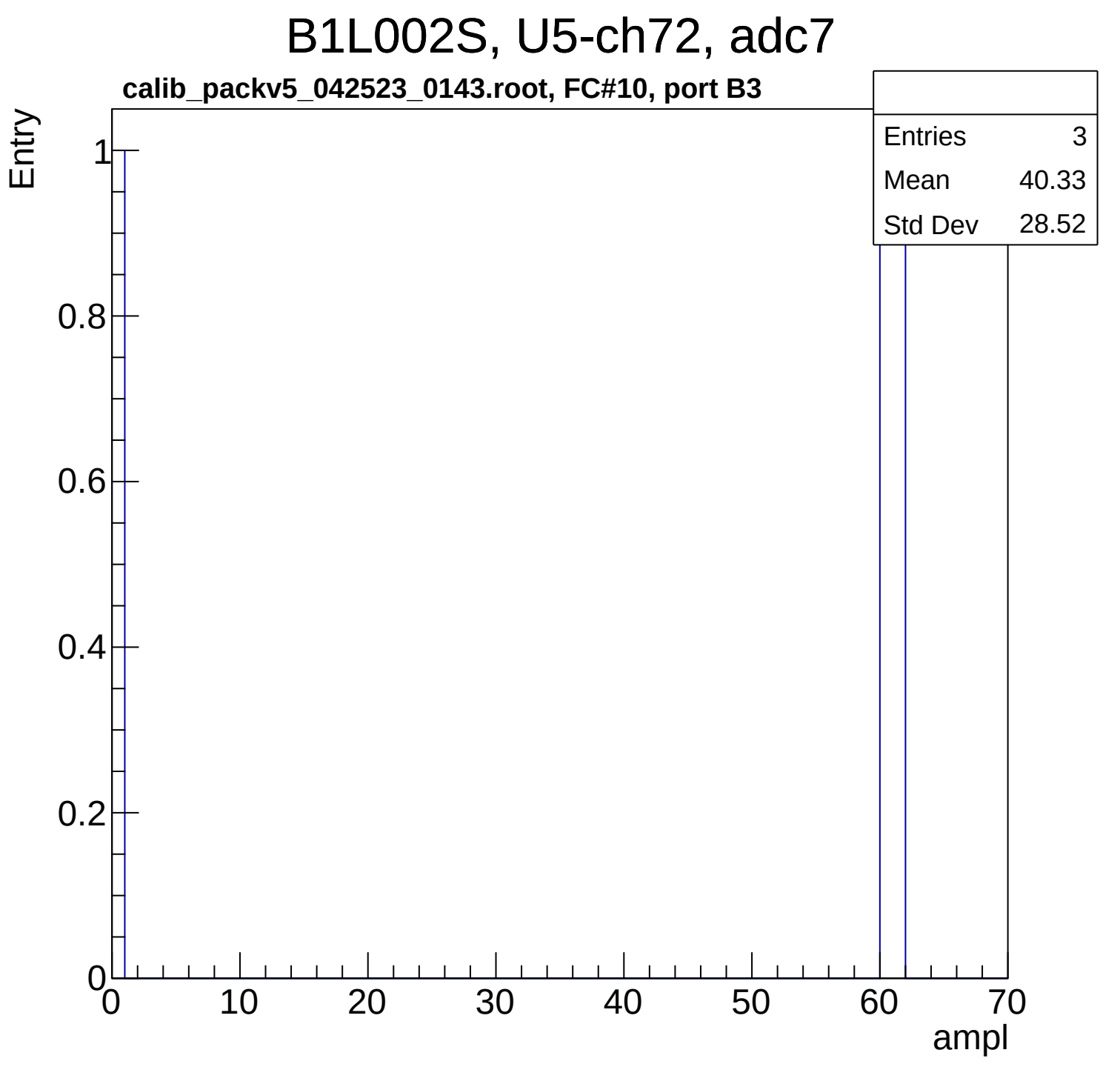
60
70


B1L002S, U5-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 40.33
Std Dev 28.52
Entry
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0.6
0.4
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0
0
10
20
30
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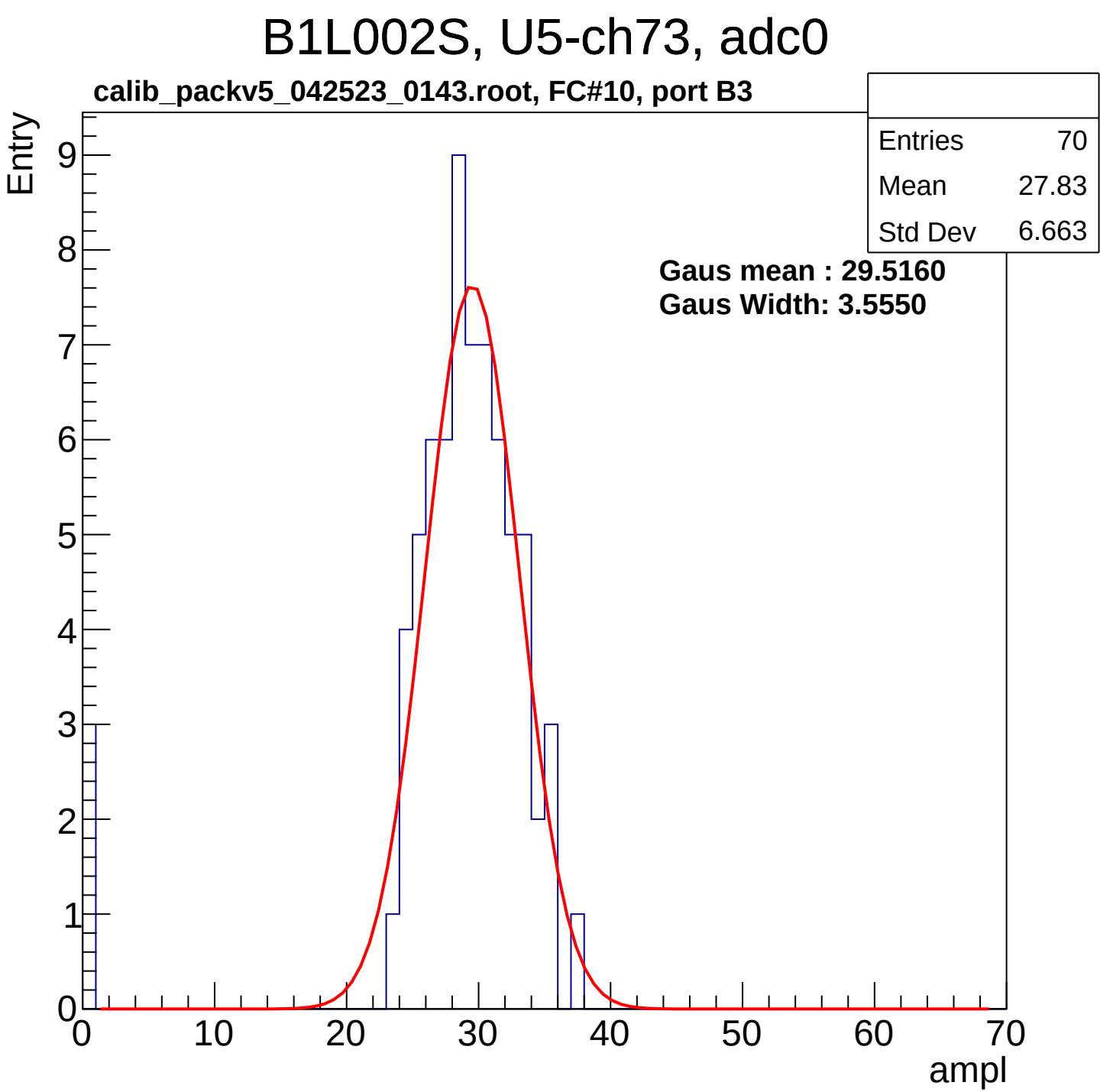
50
60
70
ampl


B1L002S, U5-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.83
Std Dev 6.663
Gaus mean : 29.5160
Gaus Width: 3.5550
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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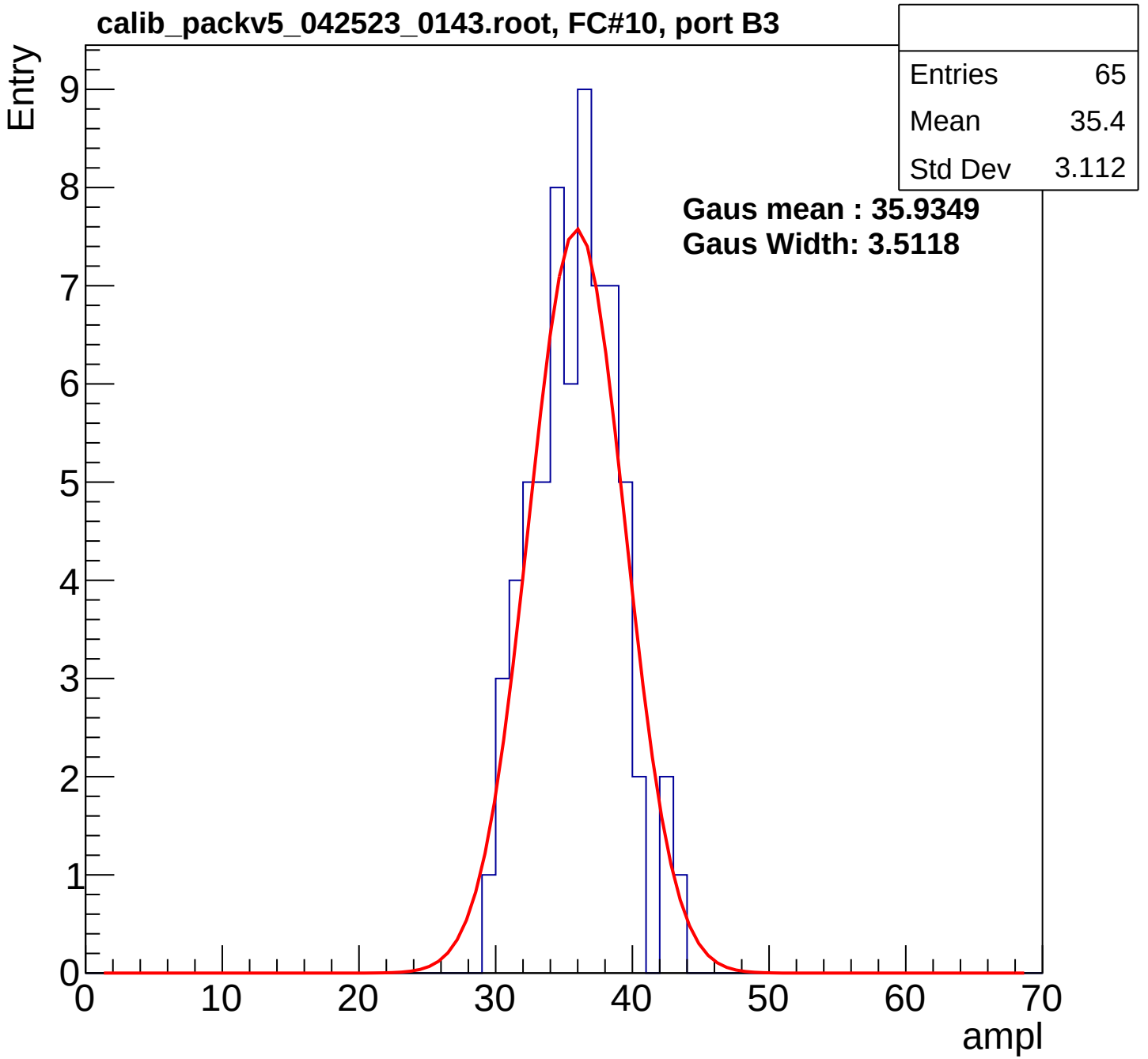
60
70


B1L002S, U5-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.4
Std Dev 3.112
Gaus mean : 35.9349
Gaus Width: 3.5118
Entry
ampl
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2
3
4
5
6
7
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0
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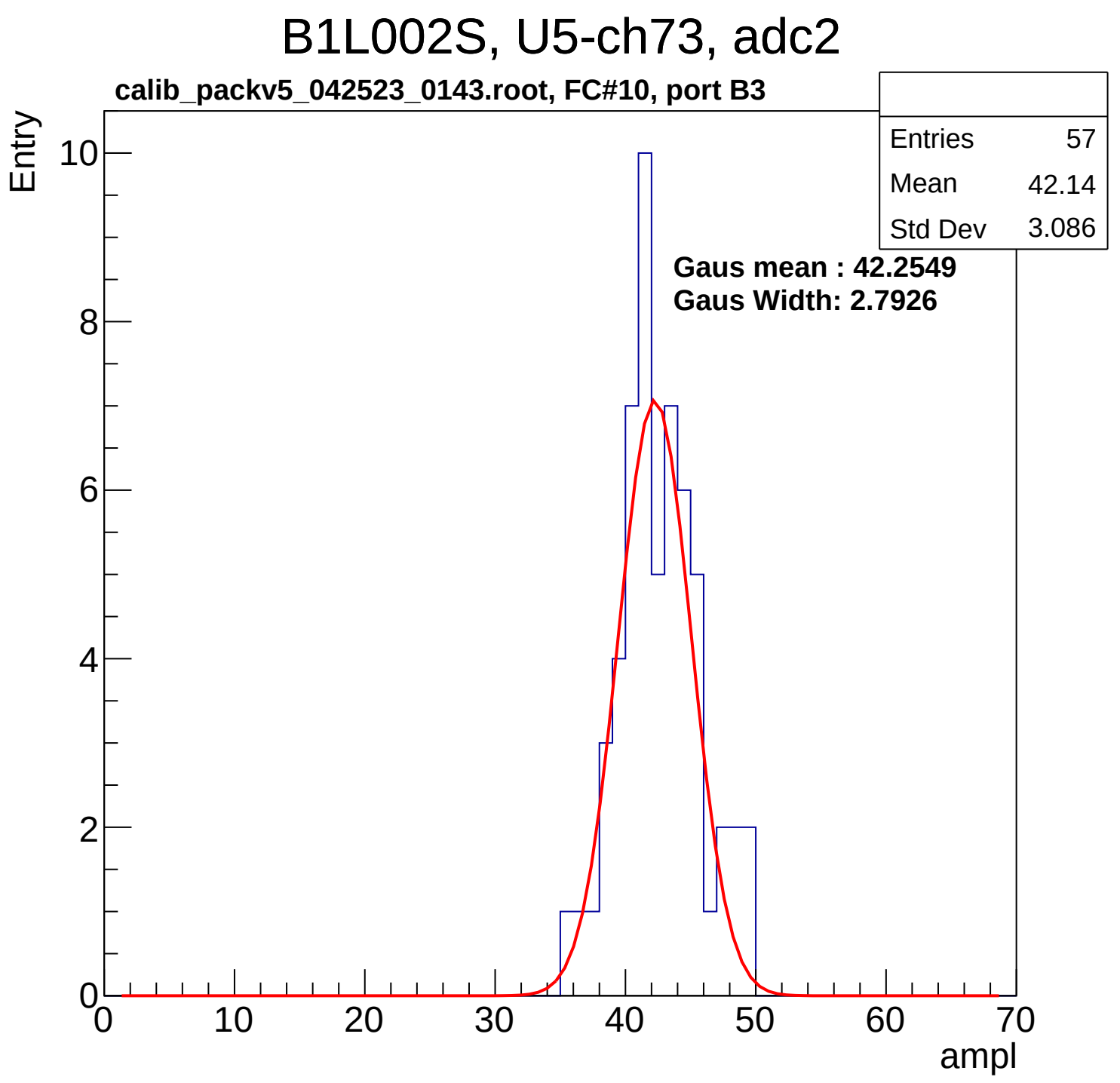
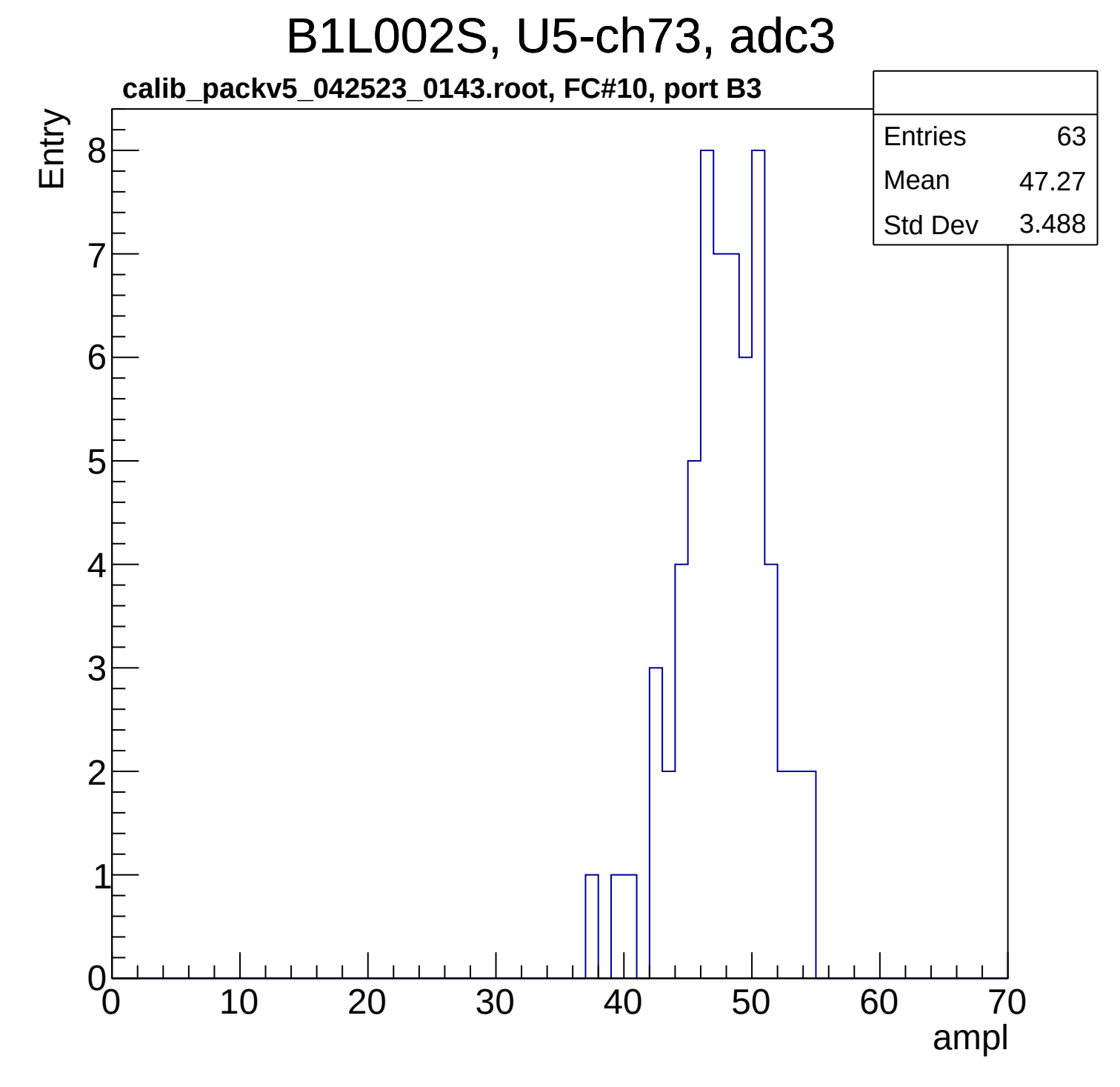

B1L002S, U5-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.14
Std Dev 3.086
Gaus mean : 42.2549
Gaus Width: 2.7926
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.27
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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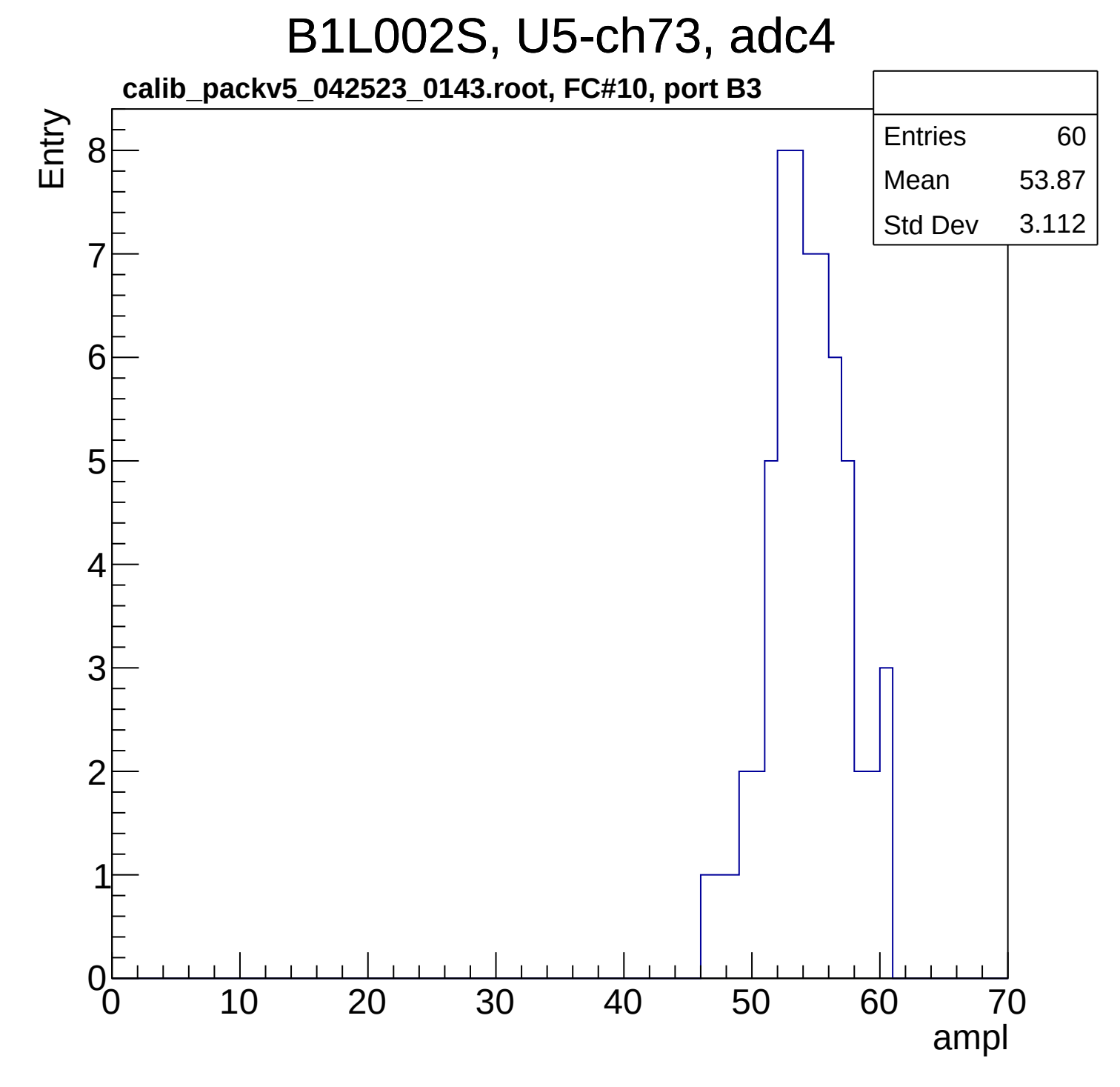
B1L002S, U5-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.87
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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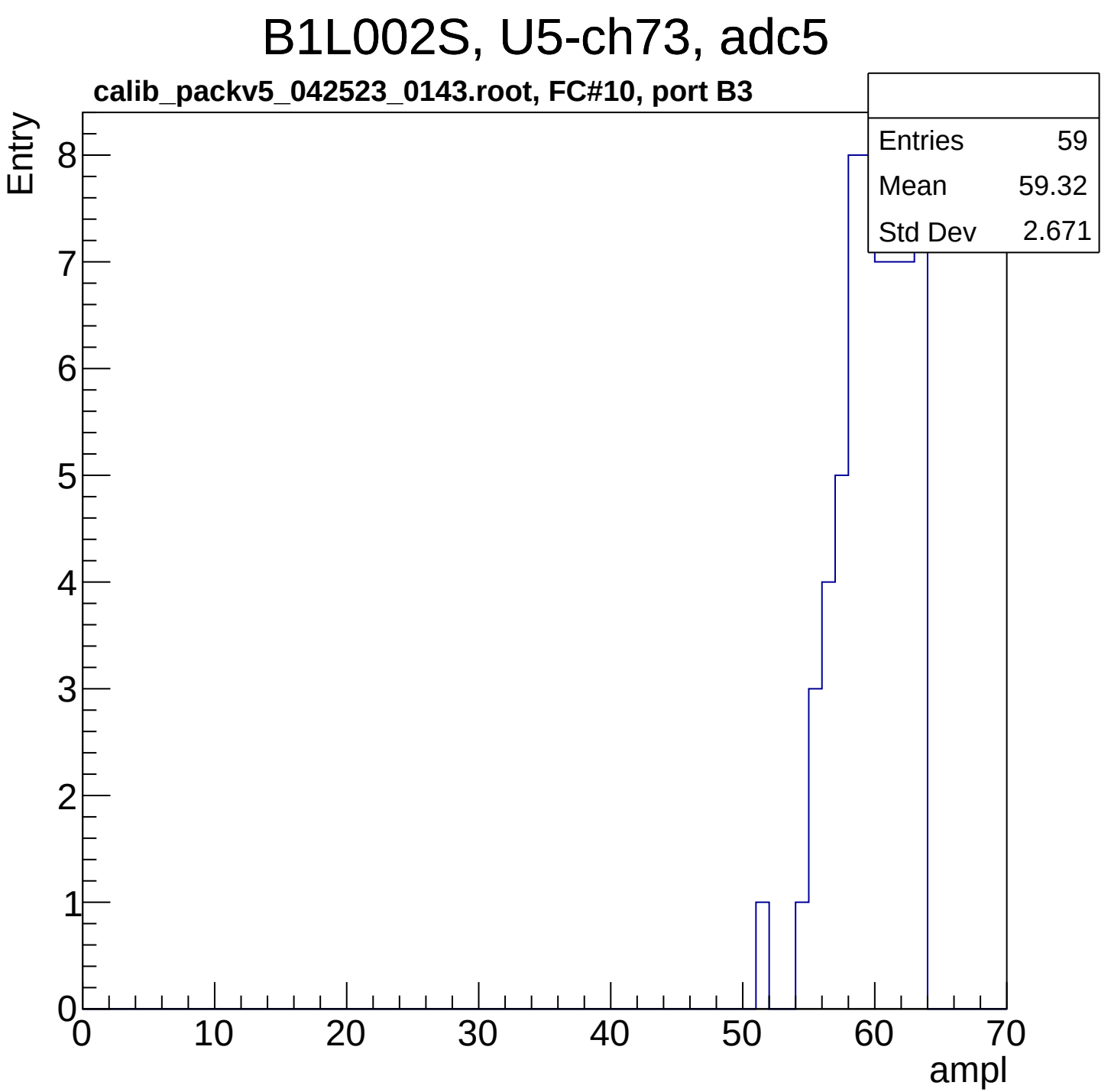
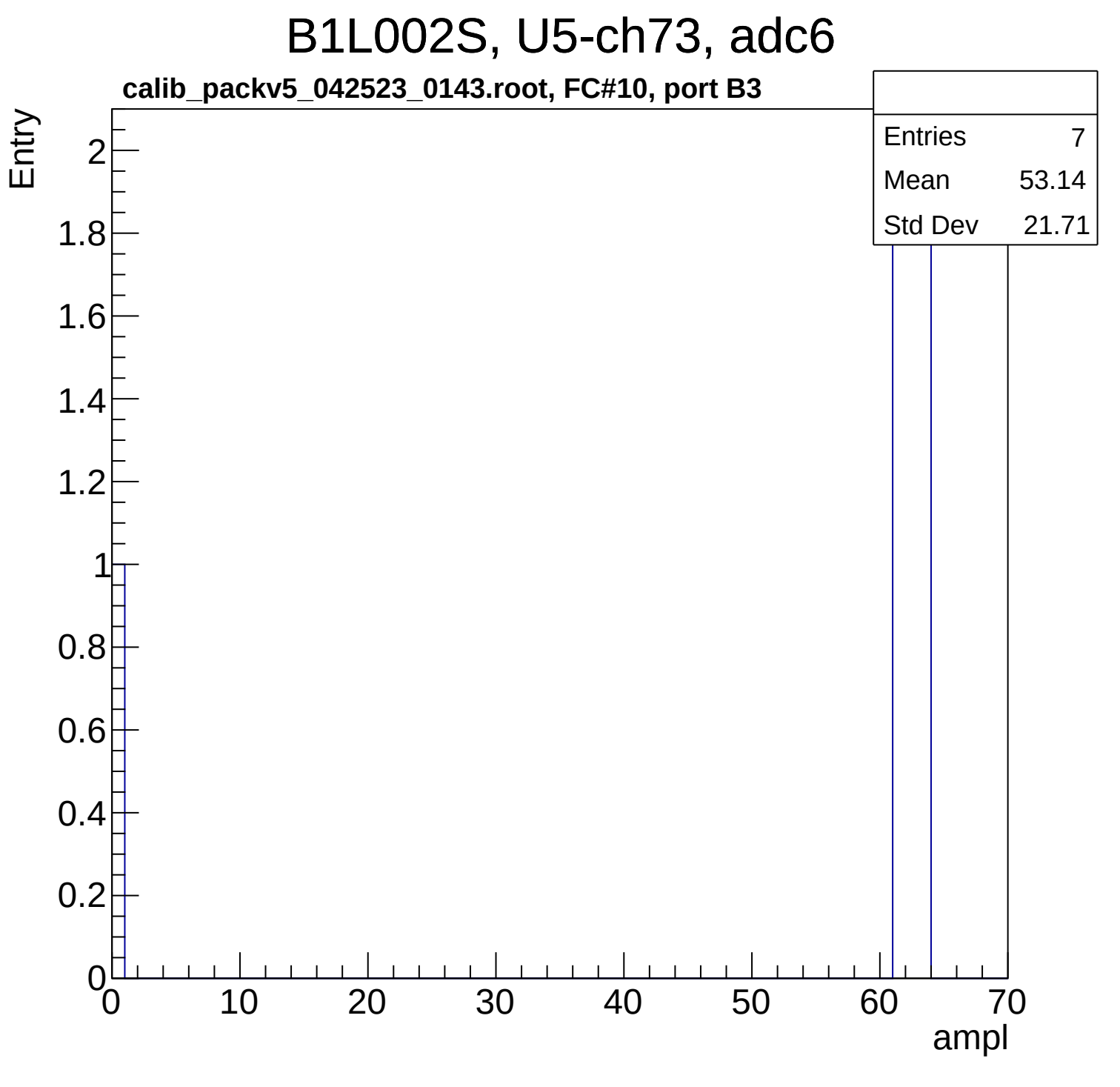
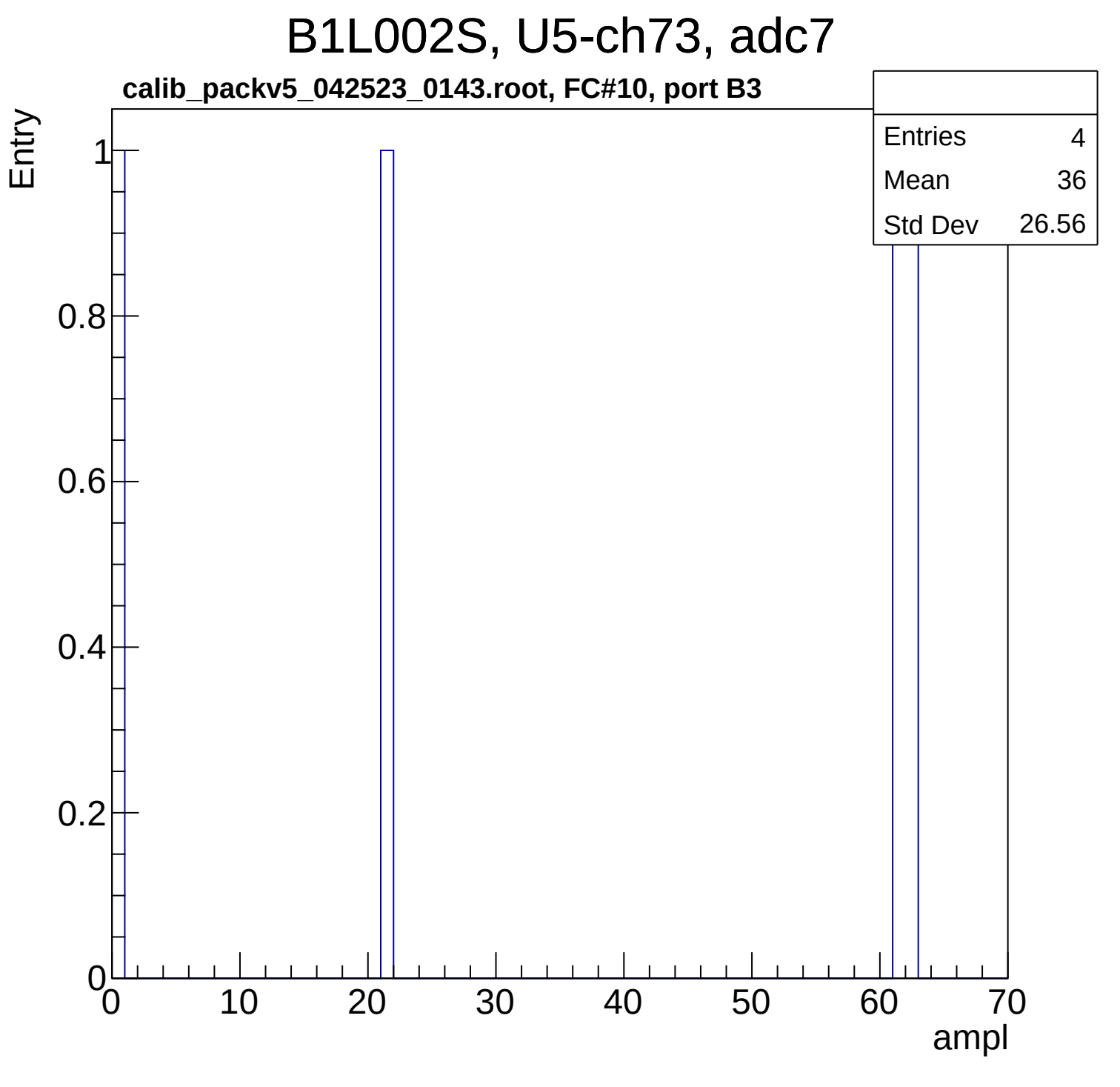

B1L002S, U5-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.32
Std Dev 2.671
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.14
Std Dev 21.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36
Std Dev 26.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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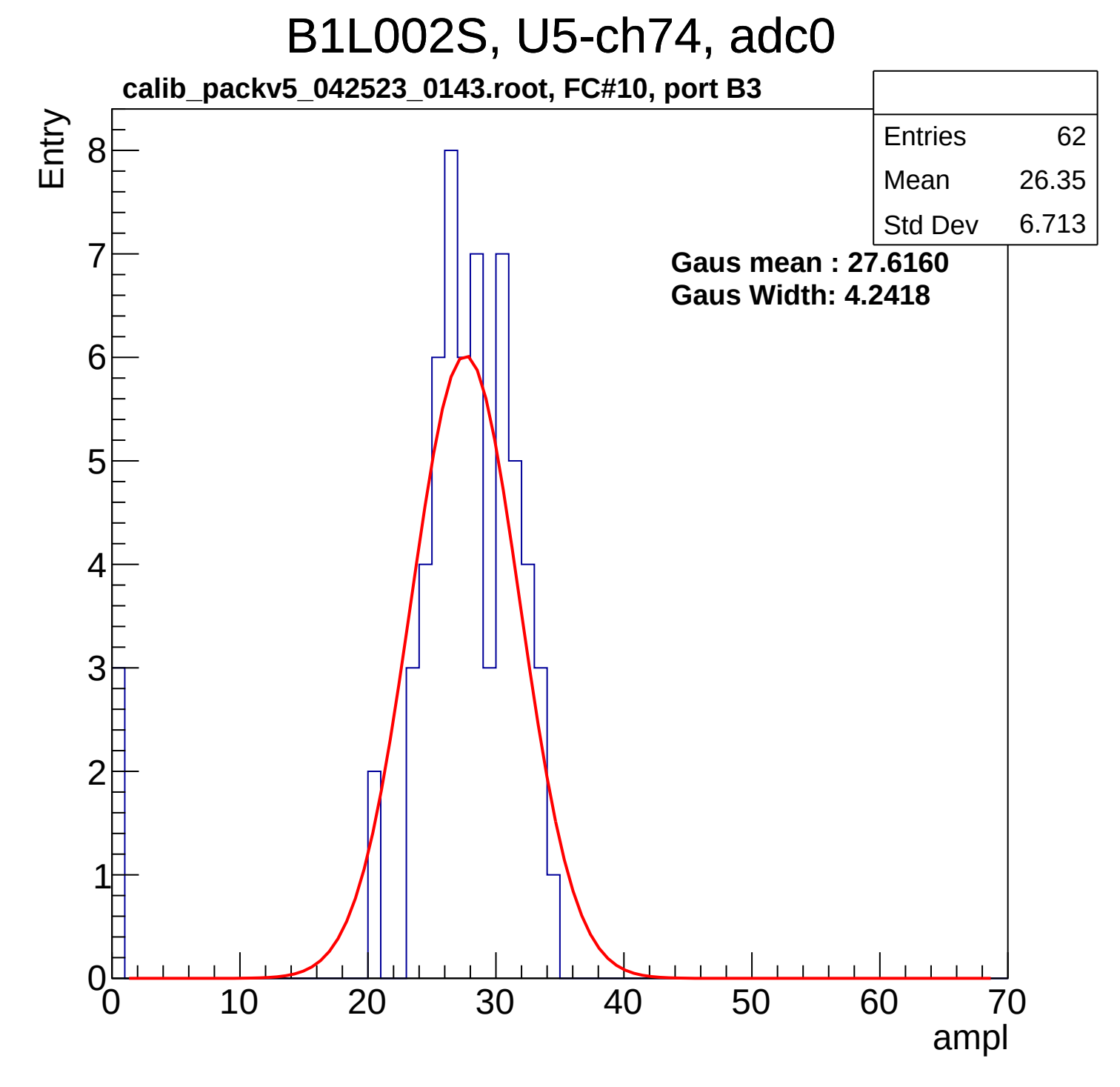
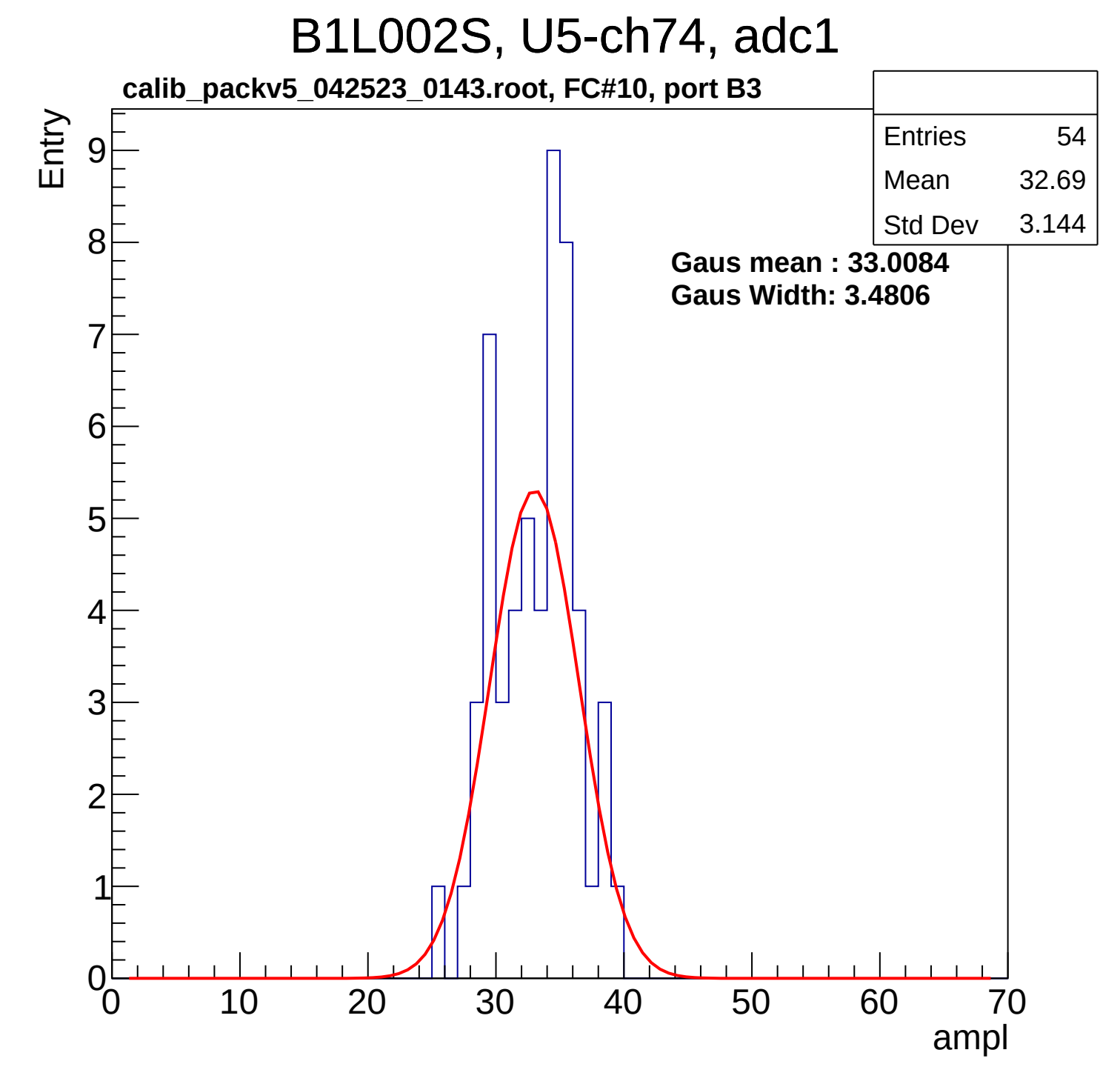
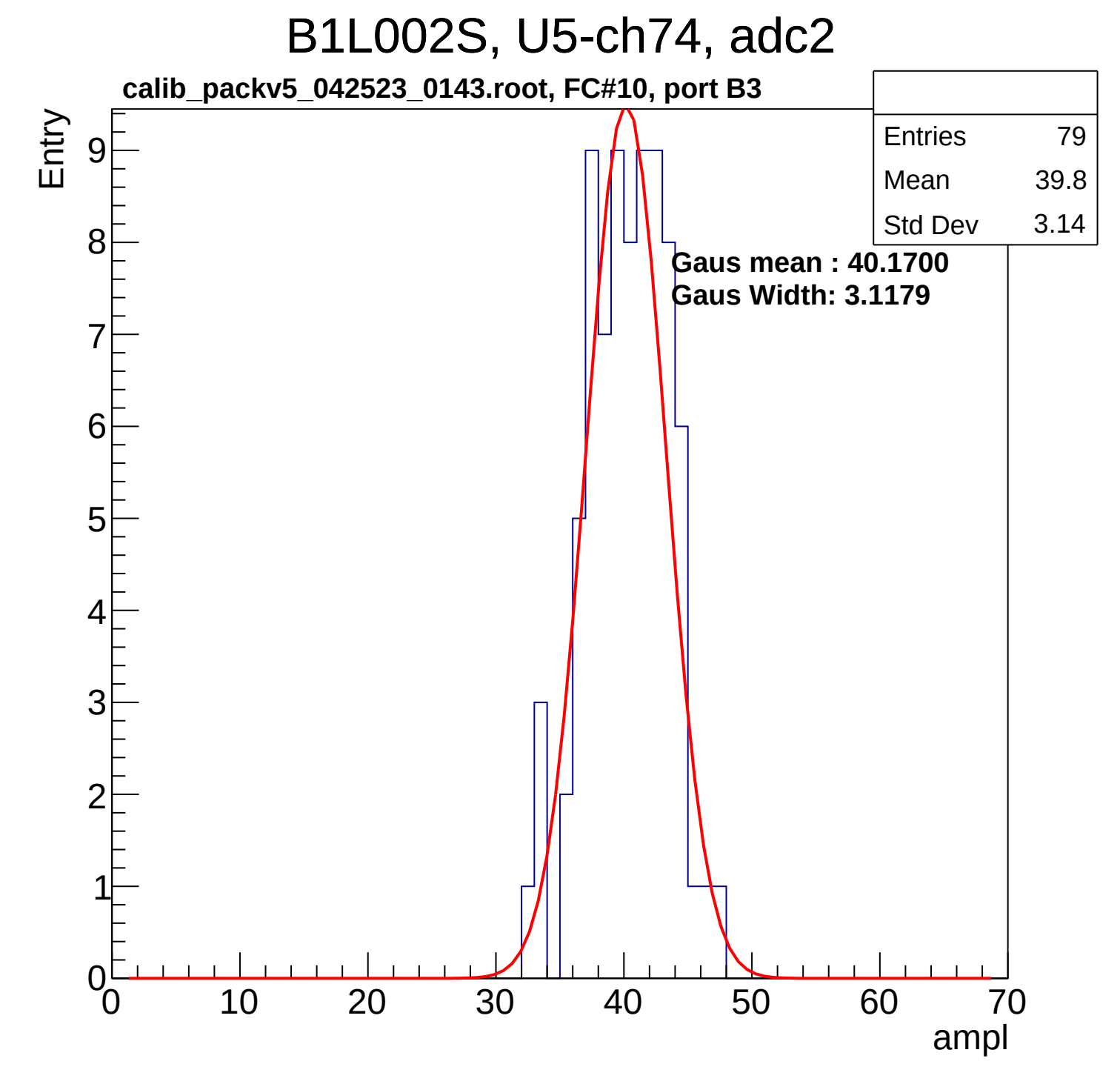
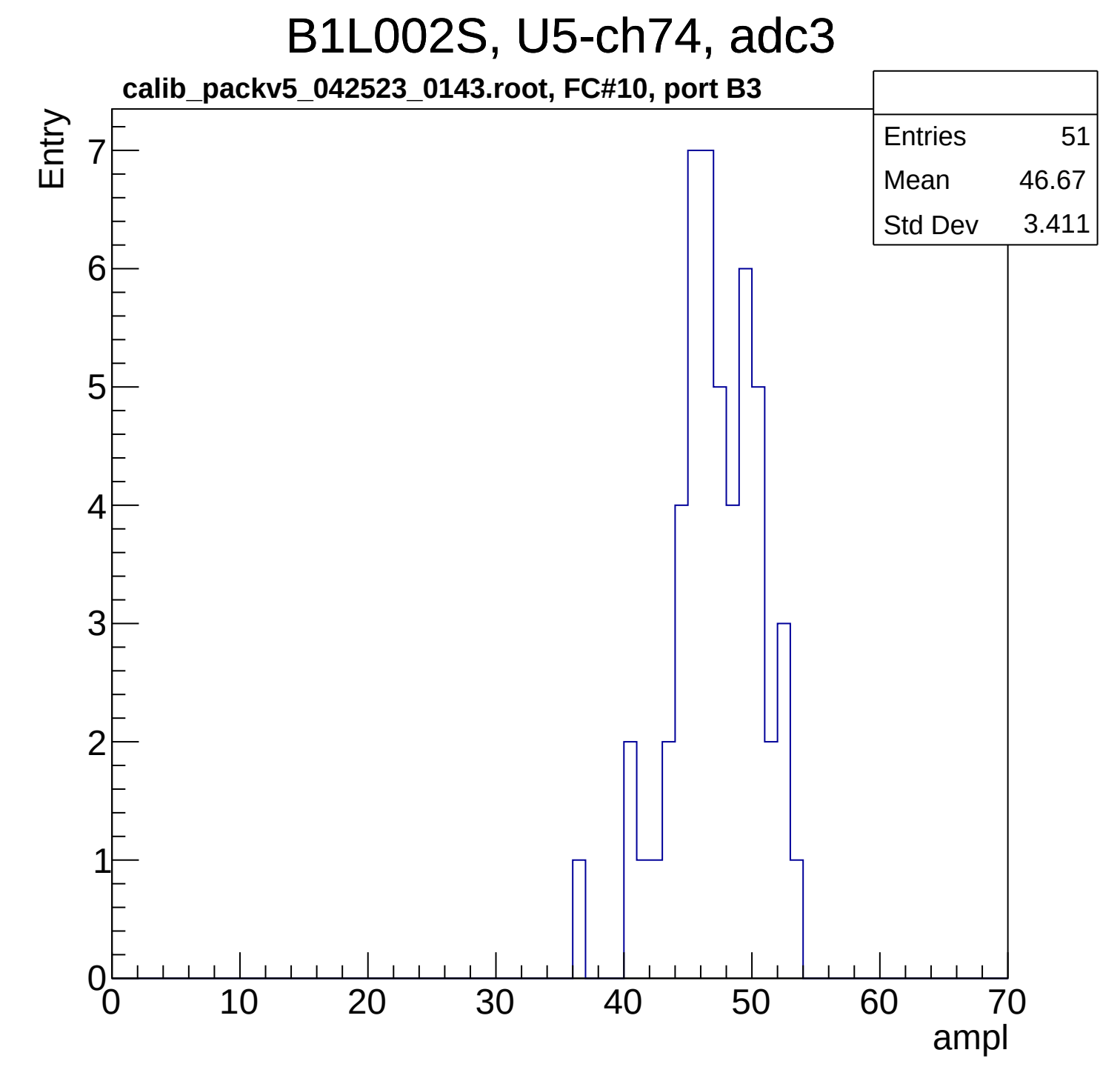
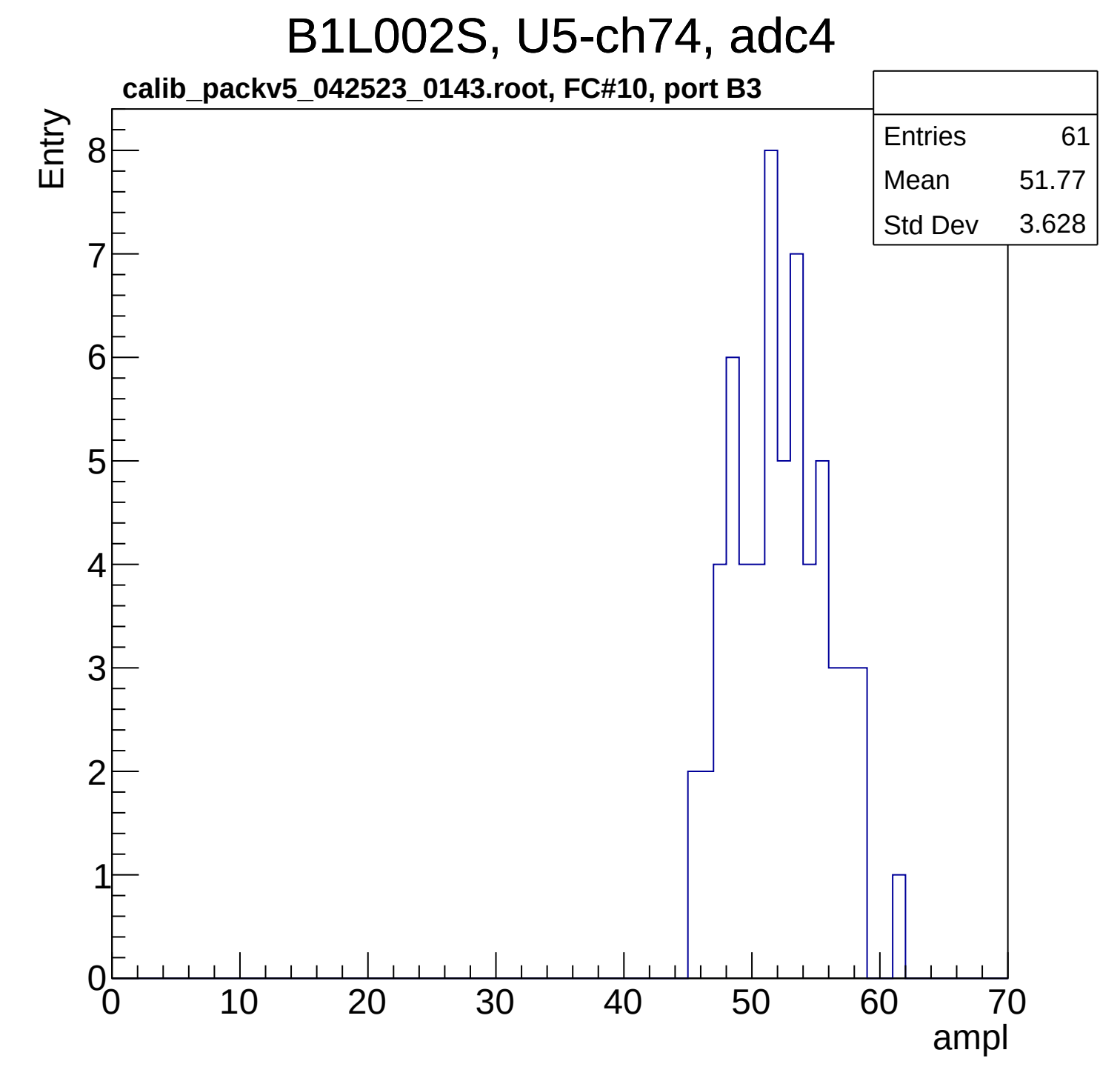
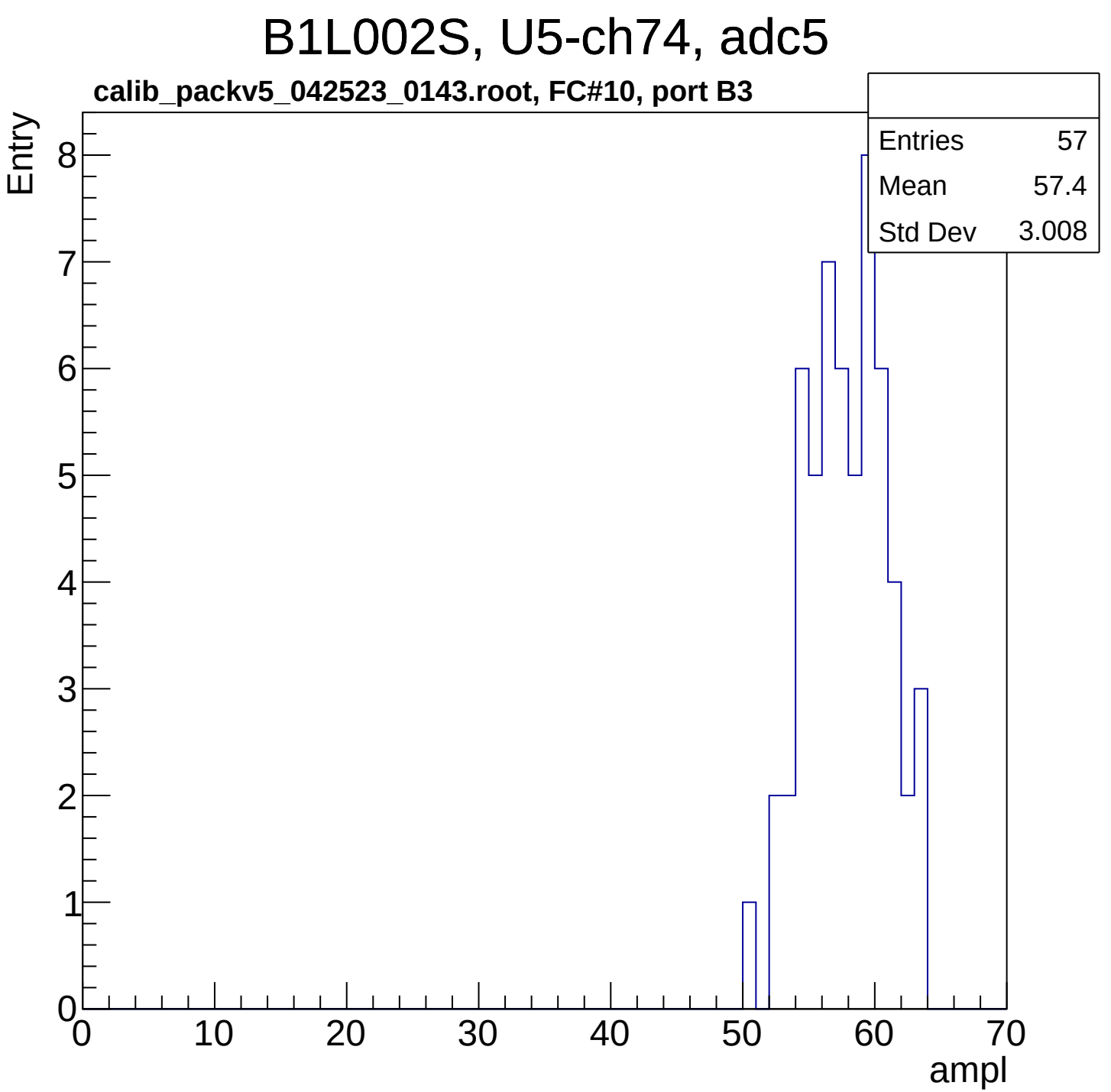

B1L002S, U5-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.35
Std Dev 6.713
Gaus mean : 27.6160
Gaus Width: 4.2418
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

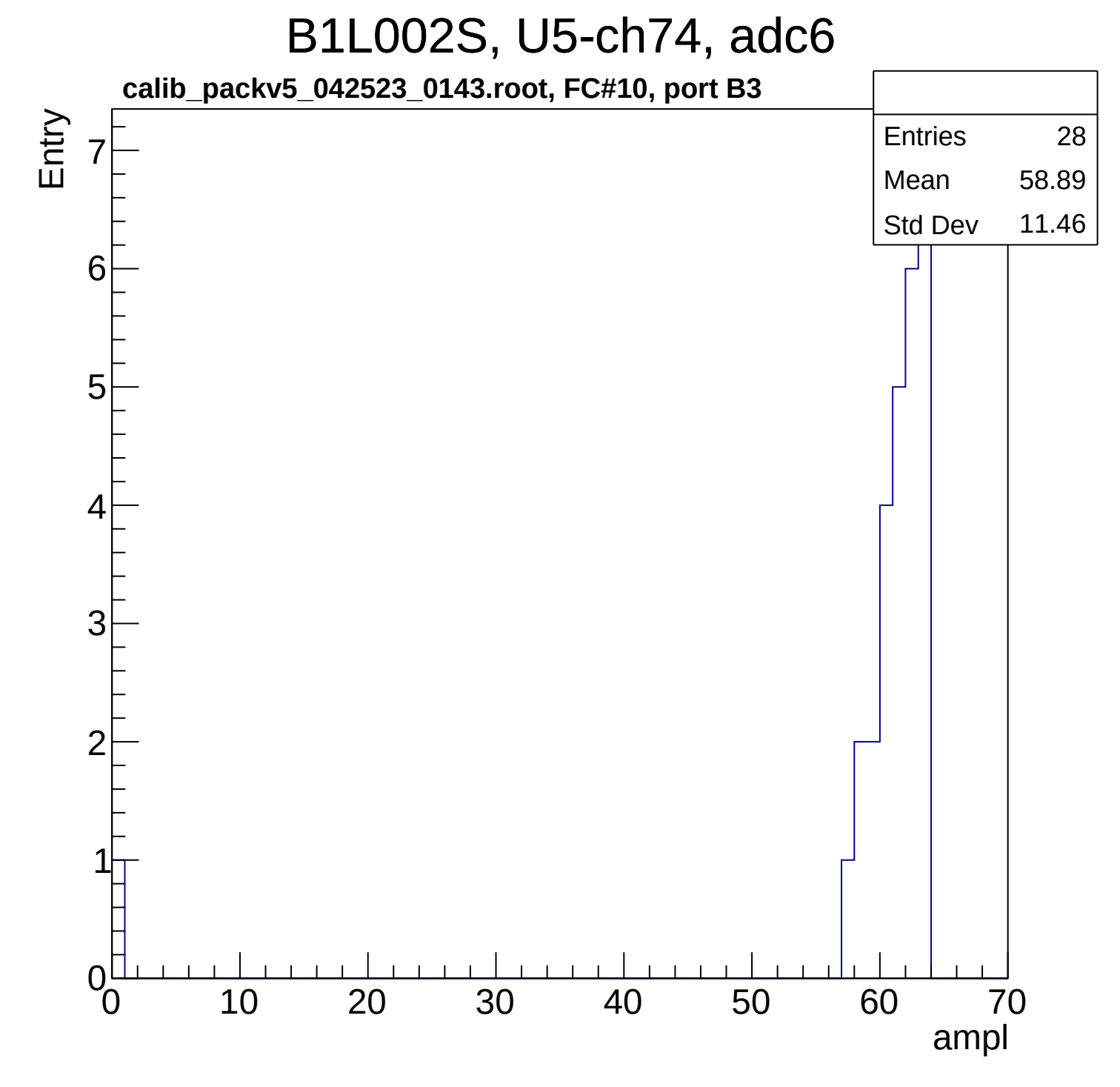

B1L002S, U5-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 32.69
Std Dev 3.144
Gaus mean : 33.0084
Gaus Width: 3.4806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

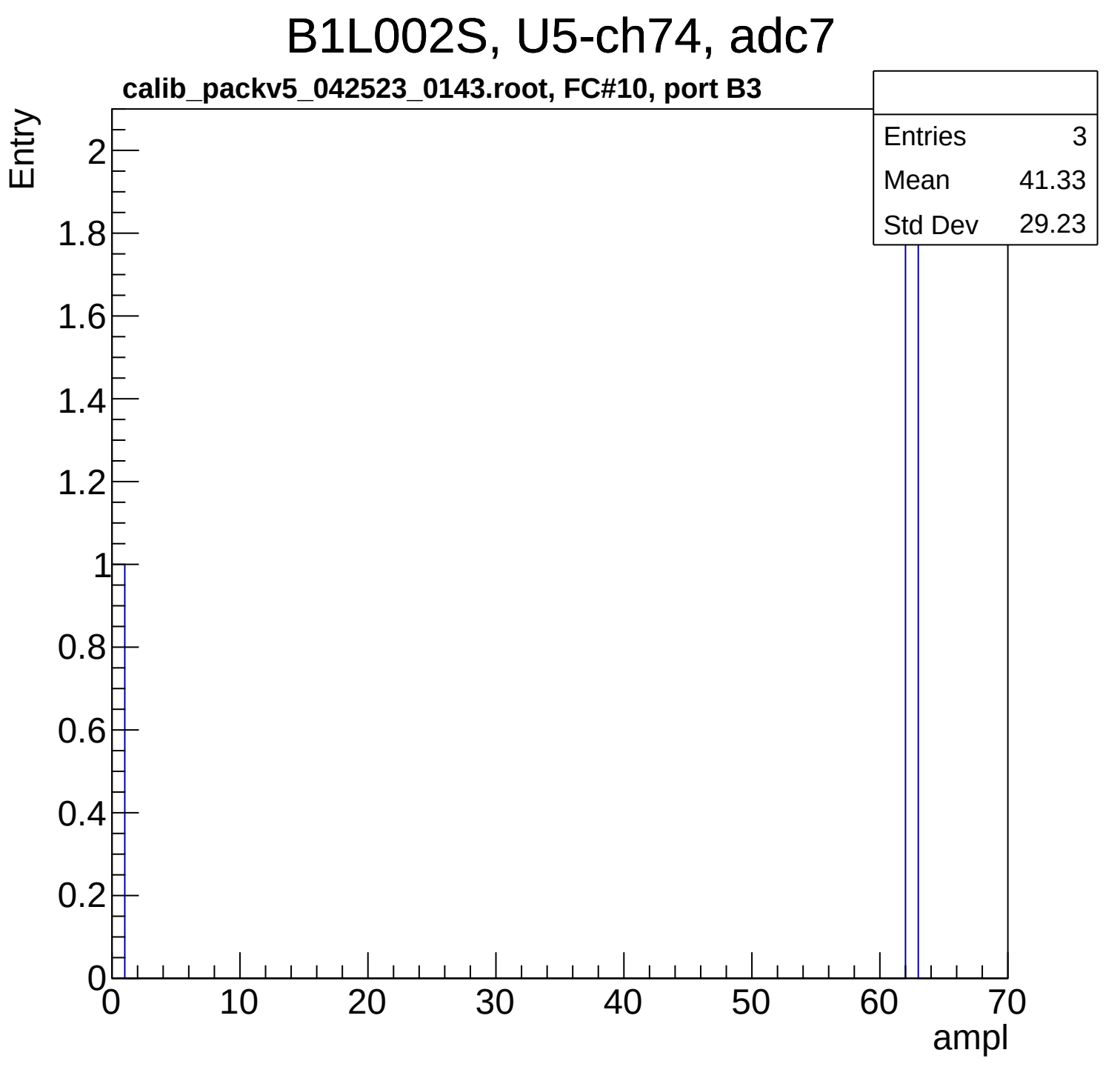

B1L002S, U5-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.8
Std Dev 3.14
Gaus mean : 40.1700
Gaus Width: 3.1179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

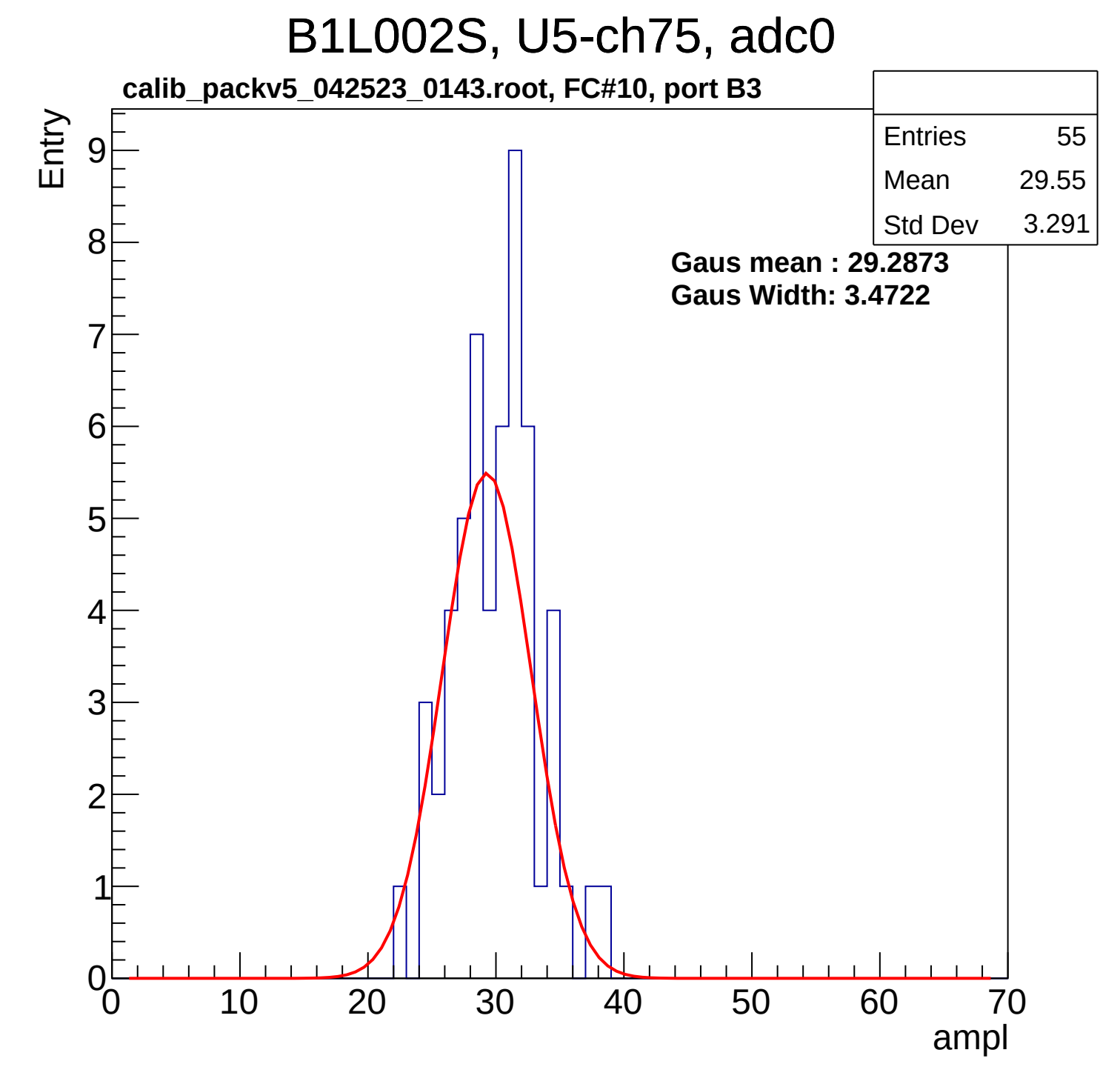

B1L002S, U5-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.67
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 51.77
Std Dev 3.628
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.4
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.89
Std Dev 11.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 29.55
Std Dev 3.291
Gaus mean : 29.2873
Gaus Width: 3.4722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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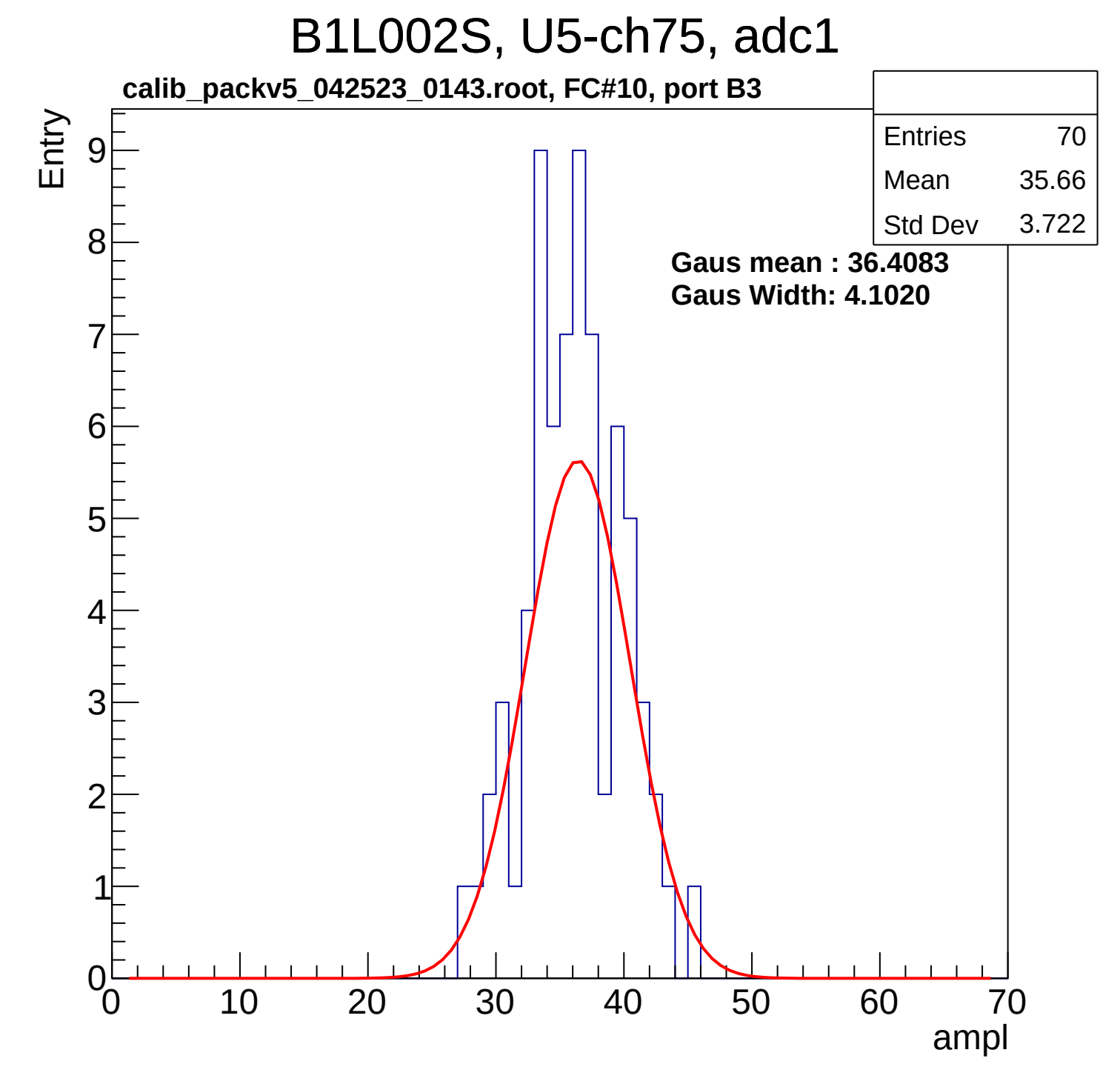
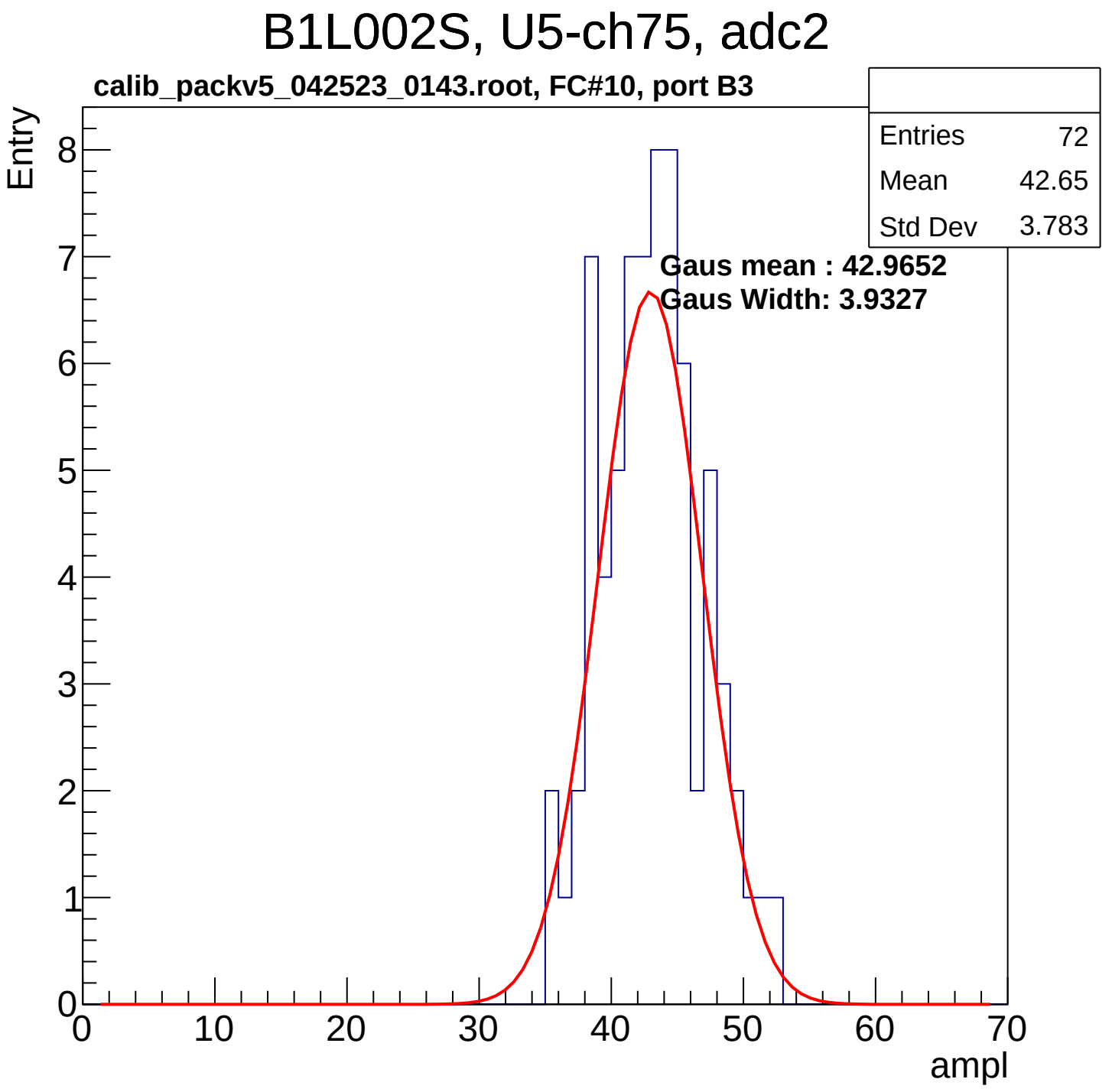
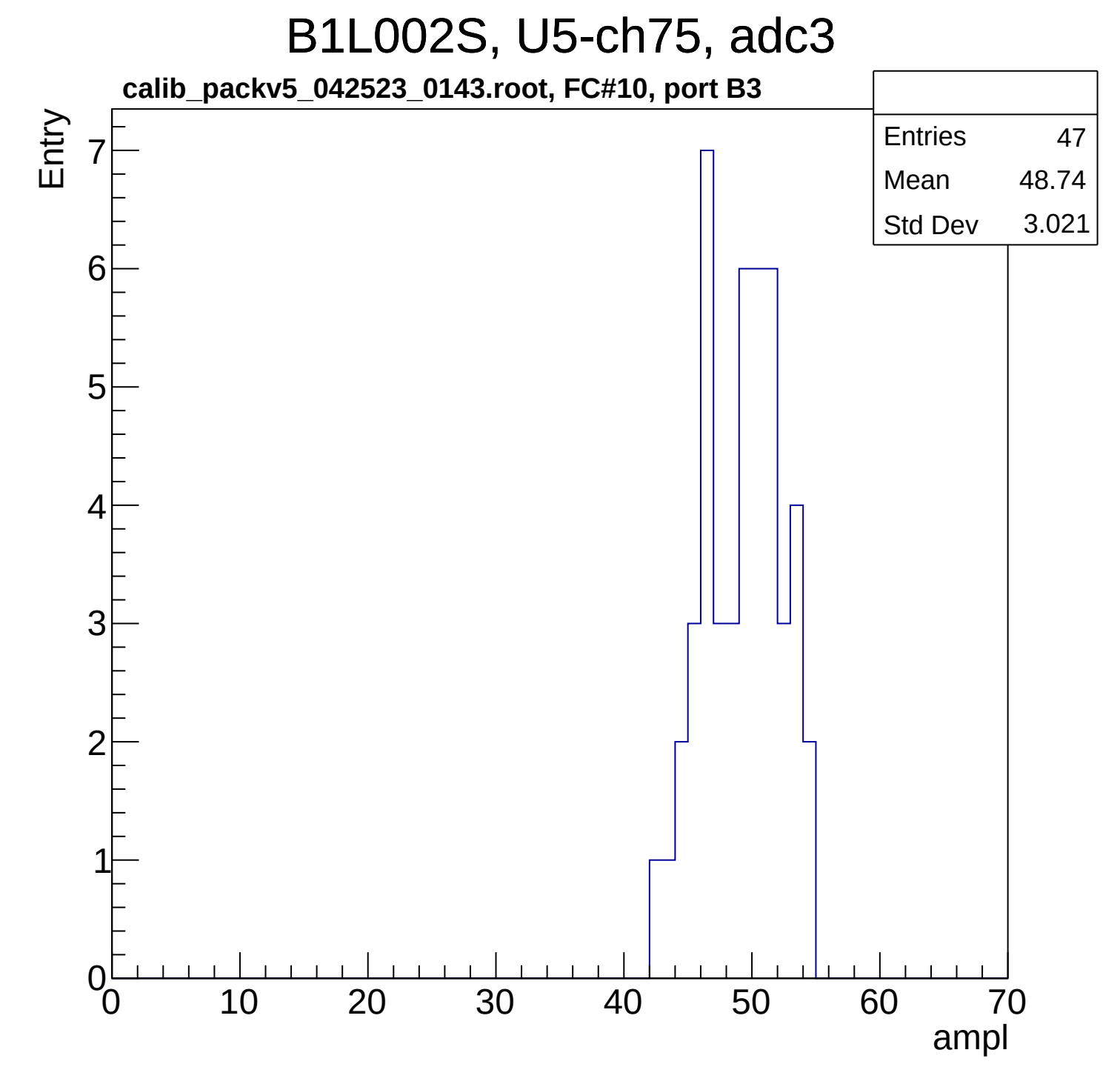
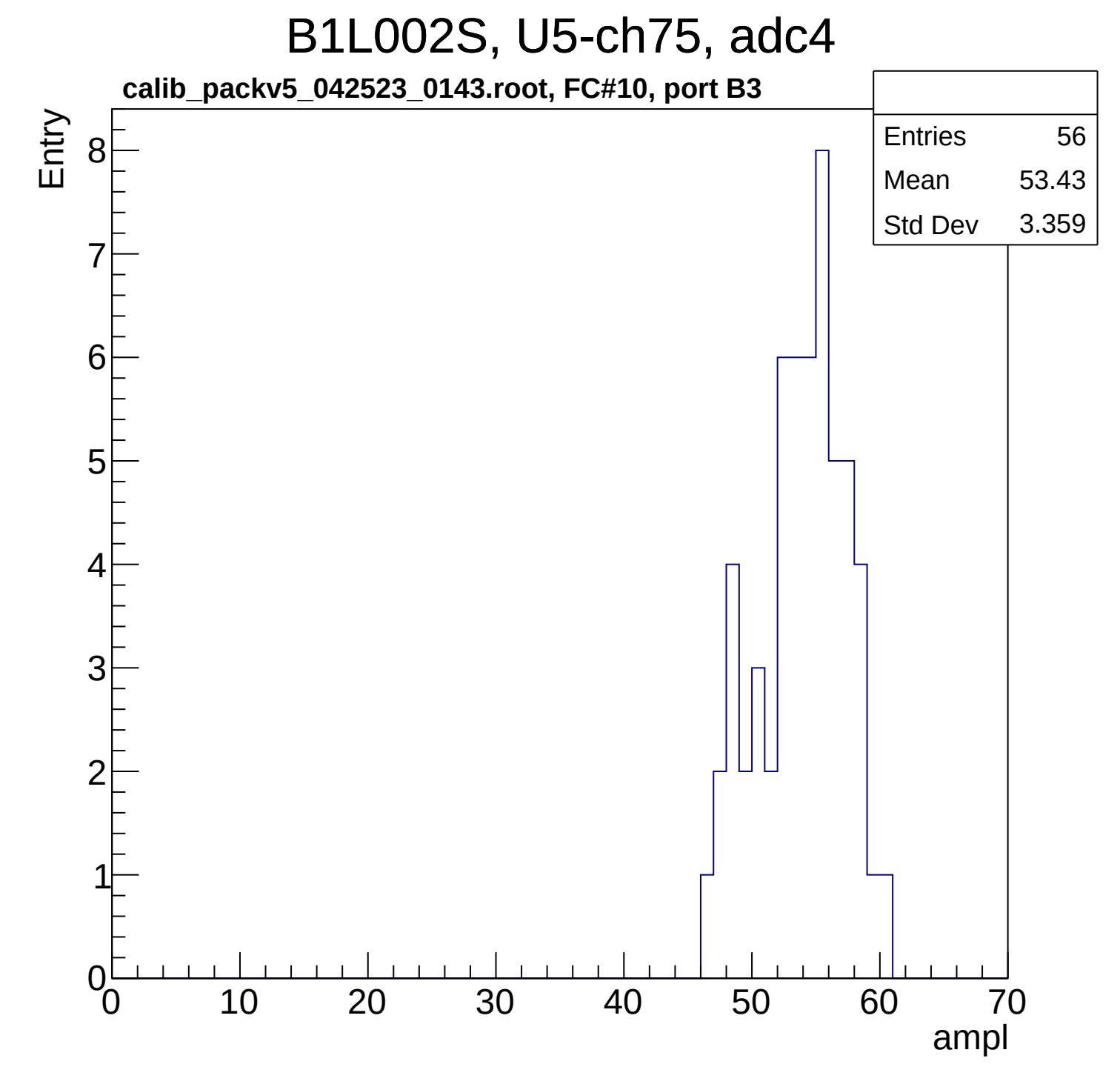

B1L002S, U5-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.66
Std Dev 3.722
Gaus mean : 36.4083
Gaus Width: 4.1020
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 42.65
Std Dev 3.783
Gaus mean : 42.9652
Gaus Width: 3.9327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 48.74
Std Dev 3.021
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.43
Std Dev 3.359
Entry
ampl
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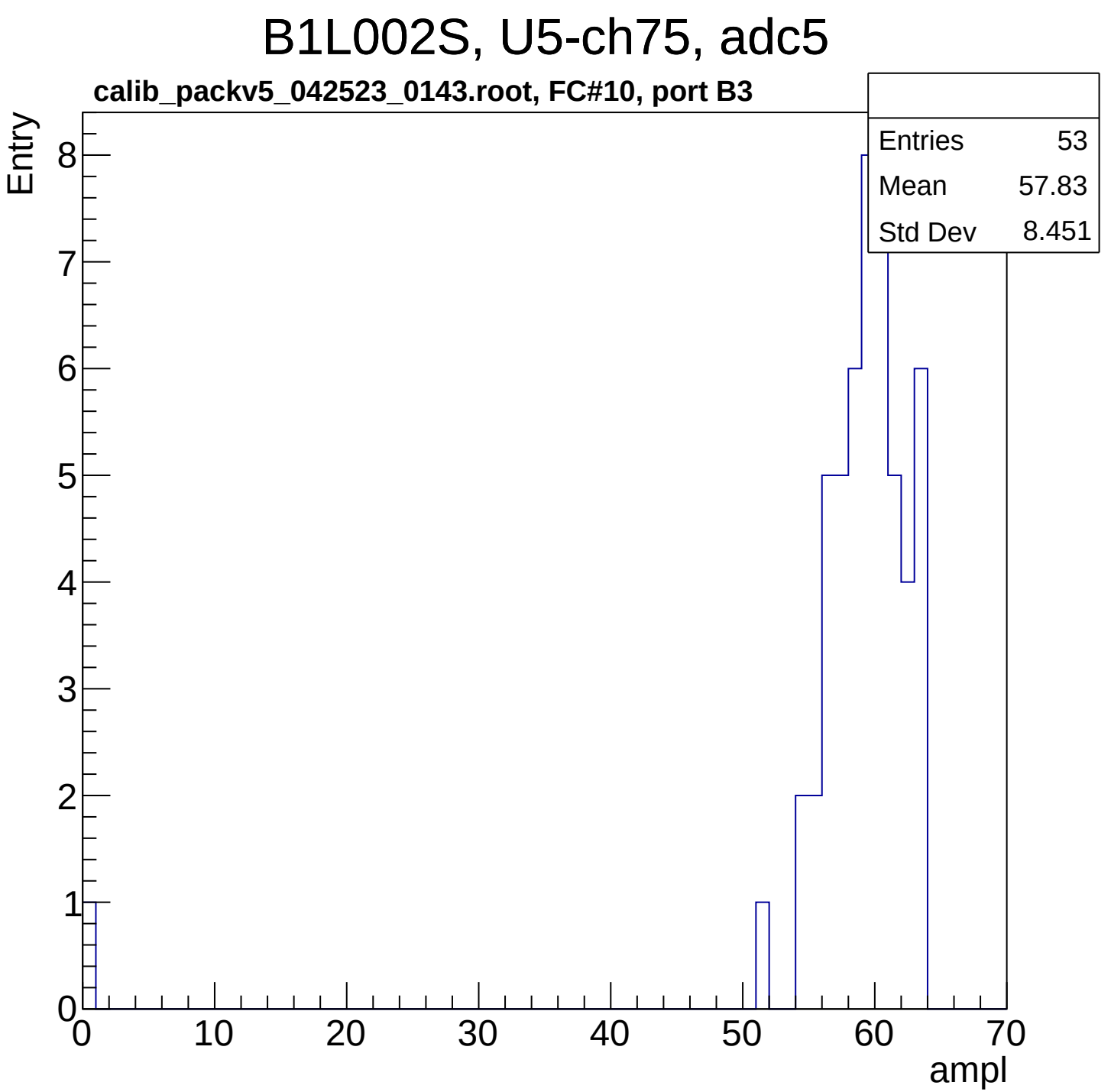
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.83
Std Dev 8.451
Entry
0
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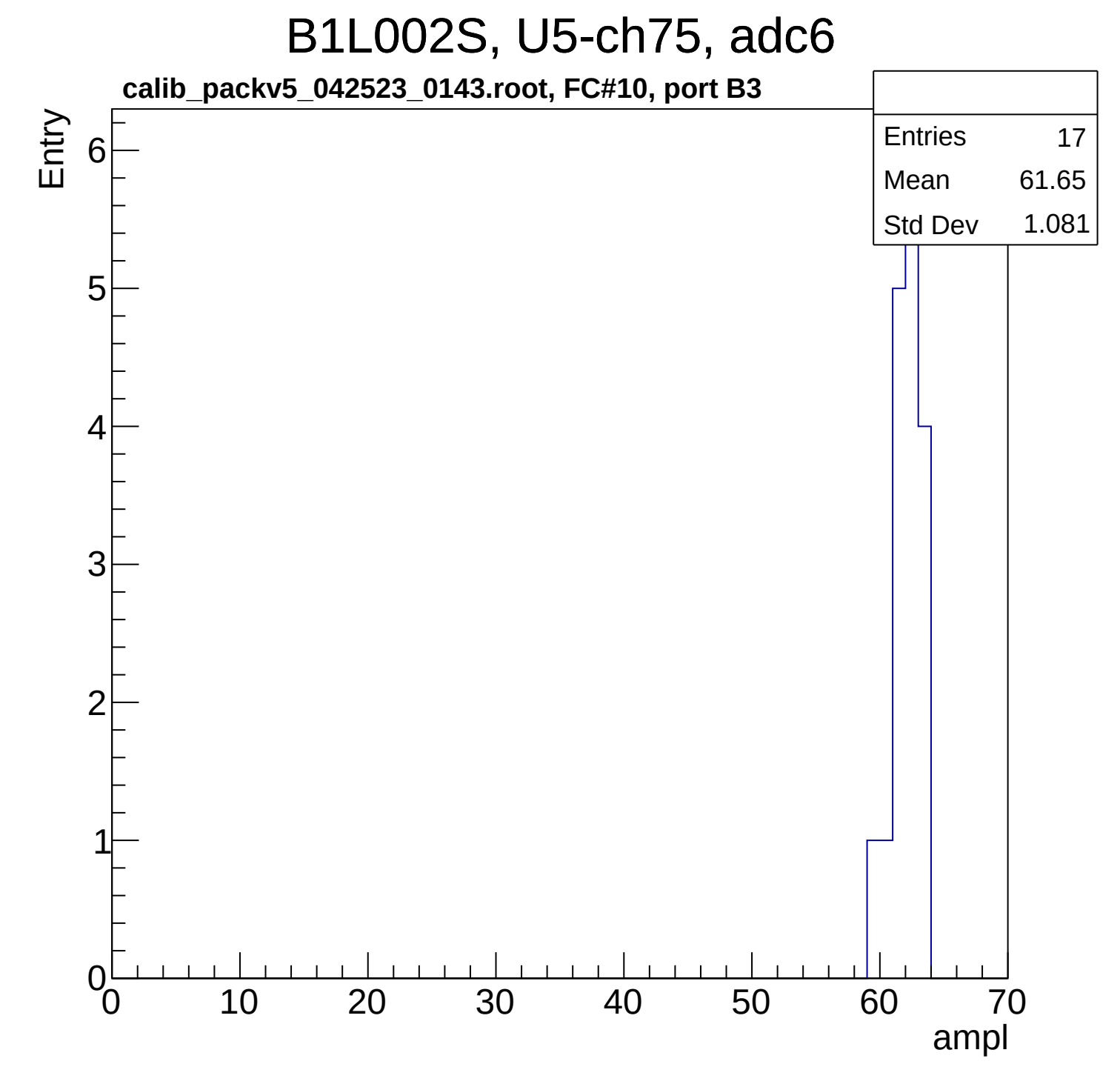
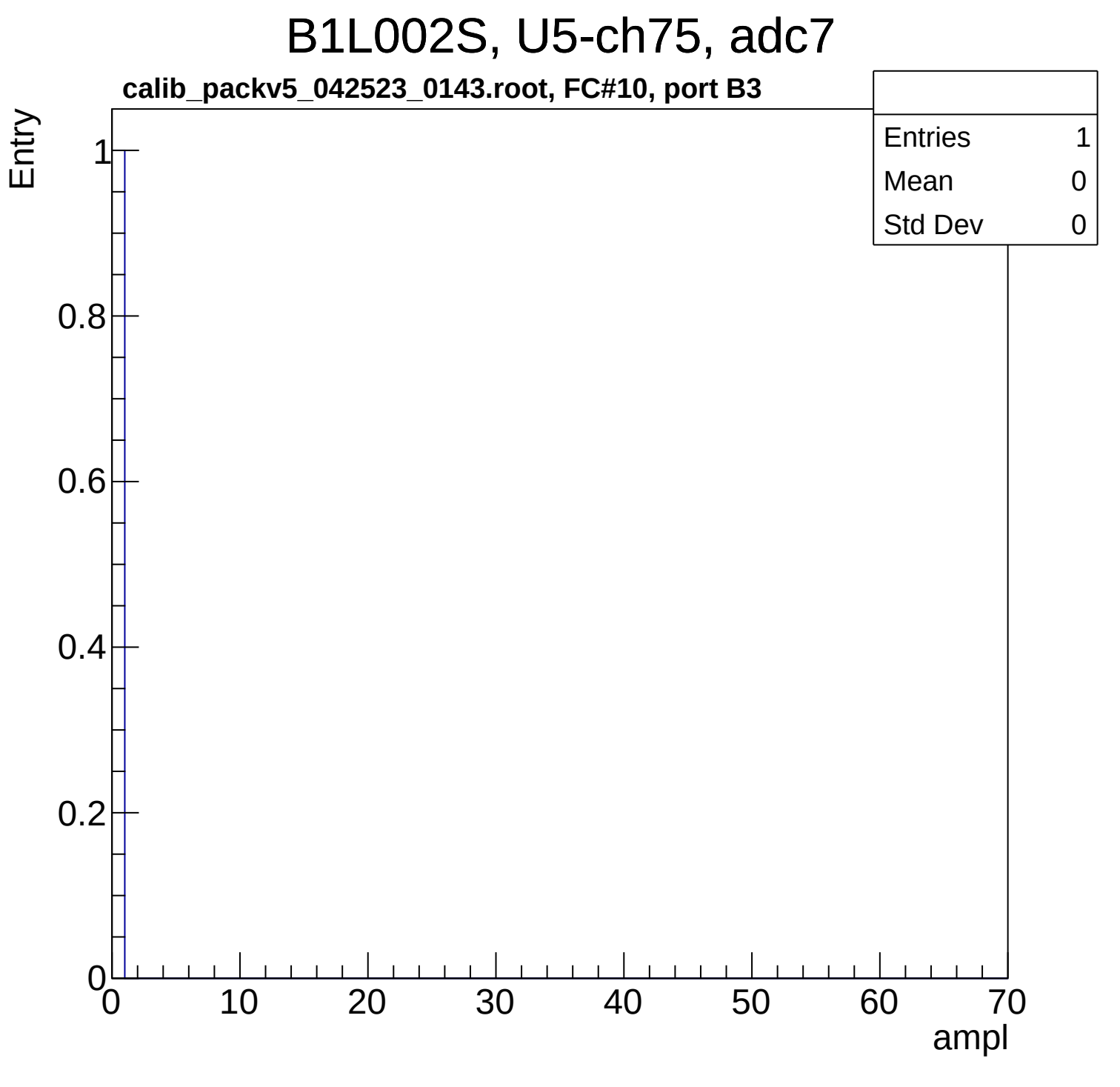
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.65
Std Dev 1.081
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
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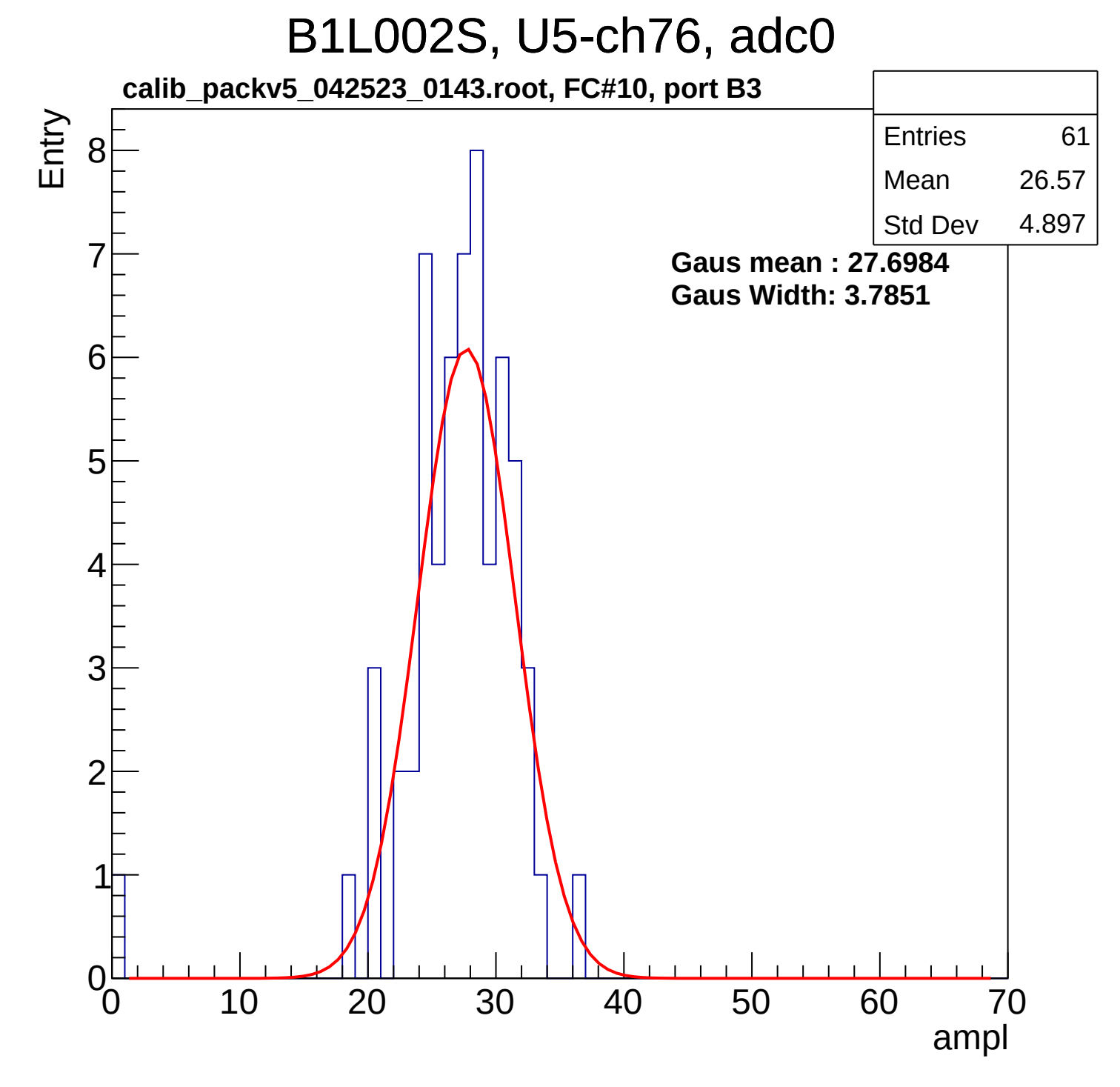
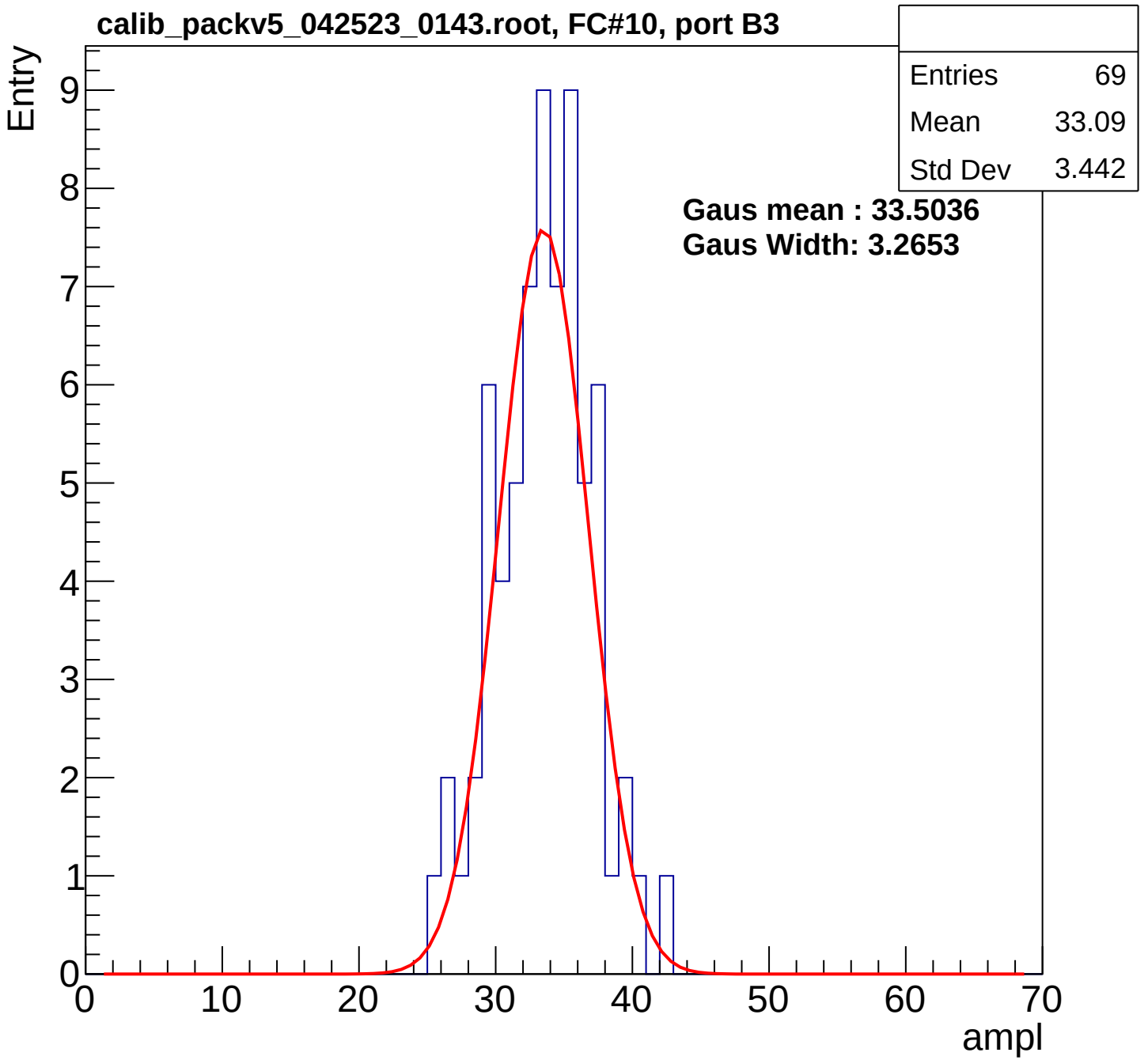
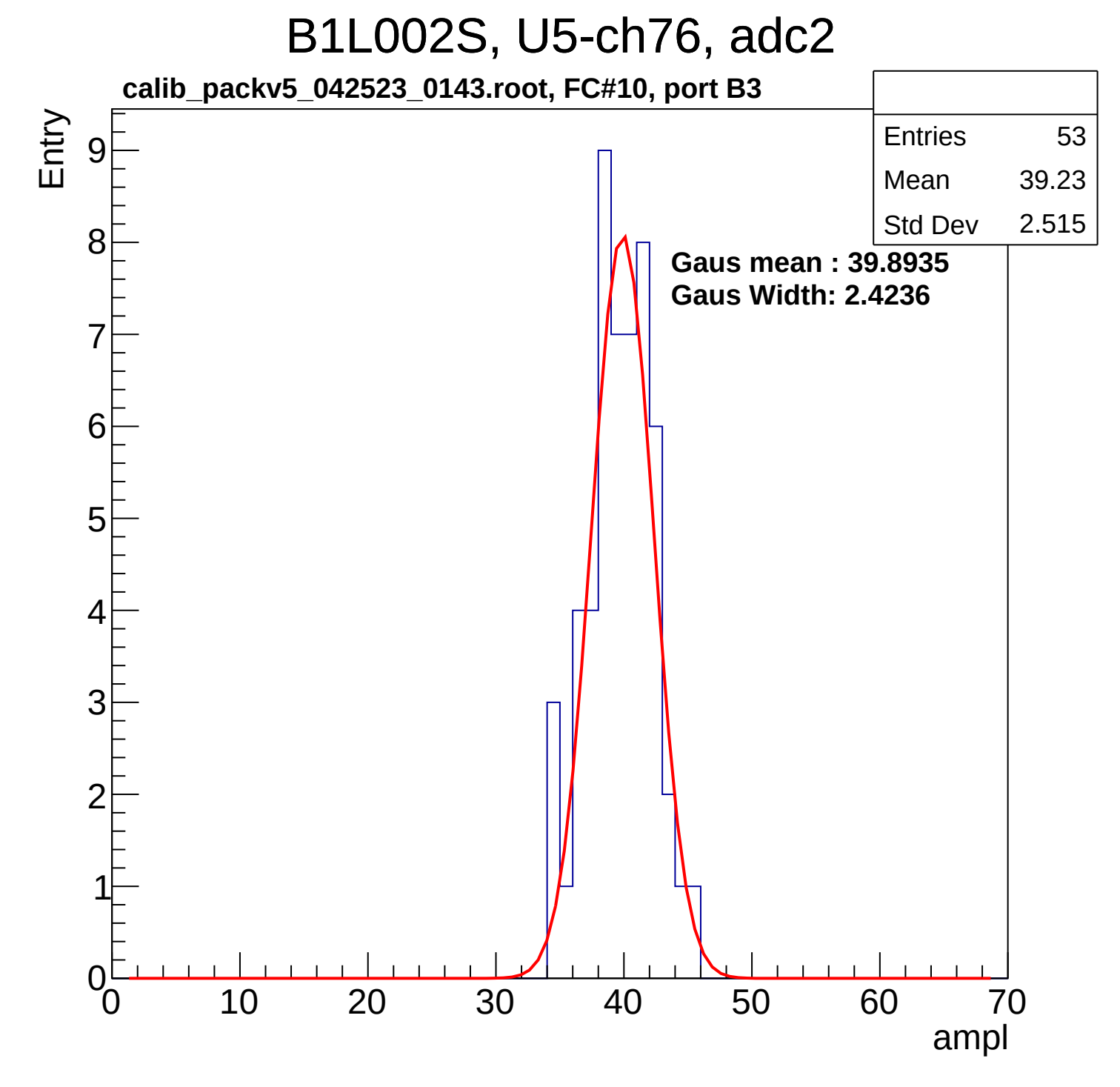
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.57
Std Dev 4.897
Gaus mean : 27.6984
Gaus Width: 3.7851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.09
Std Dev 3.442
Gaus mean : 33.5036
Gaus Width: 3.2653
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 39.23
Std Dev 2.515
Gaus mean : 39.8935
Gaus Width: 2.4236
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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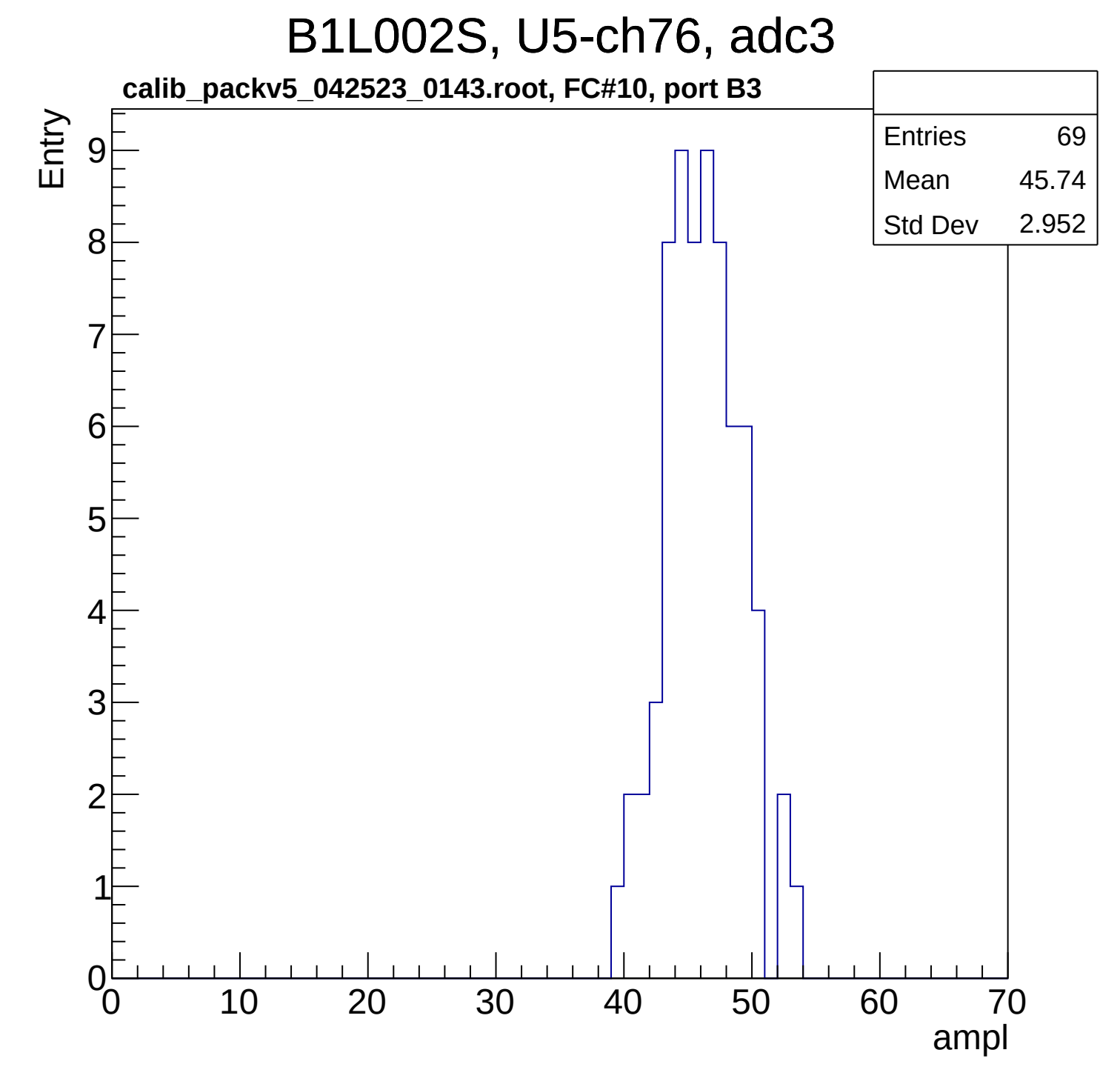
B1L002S, U5-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.74
Std Dev 2.952
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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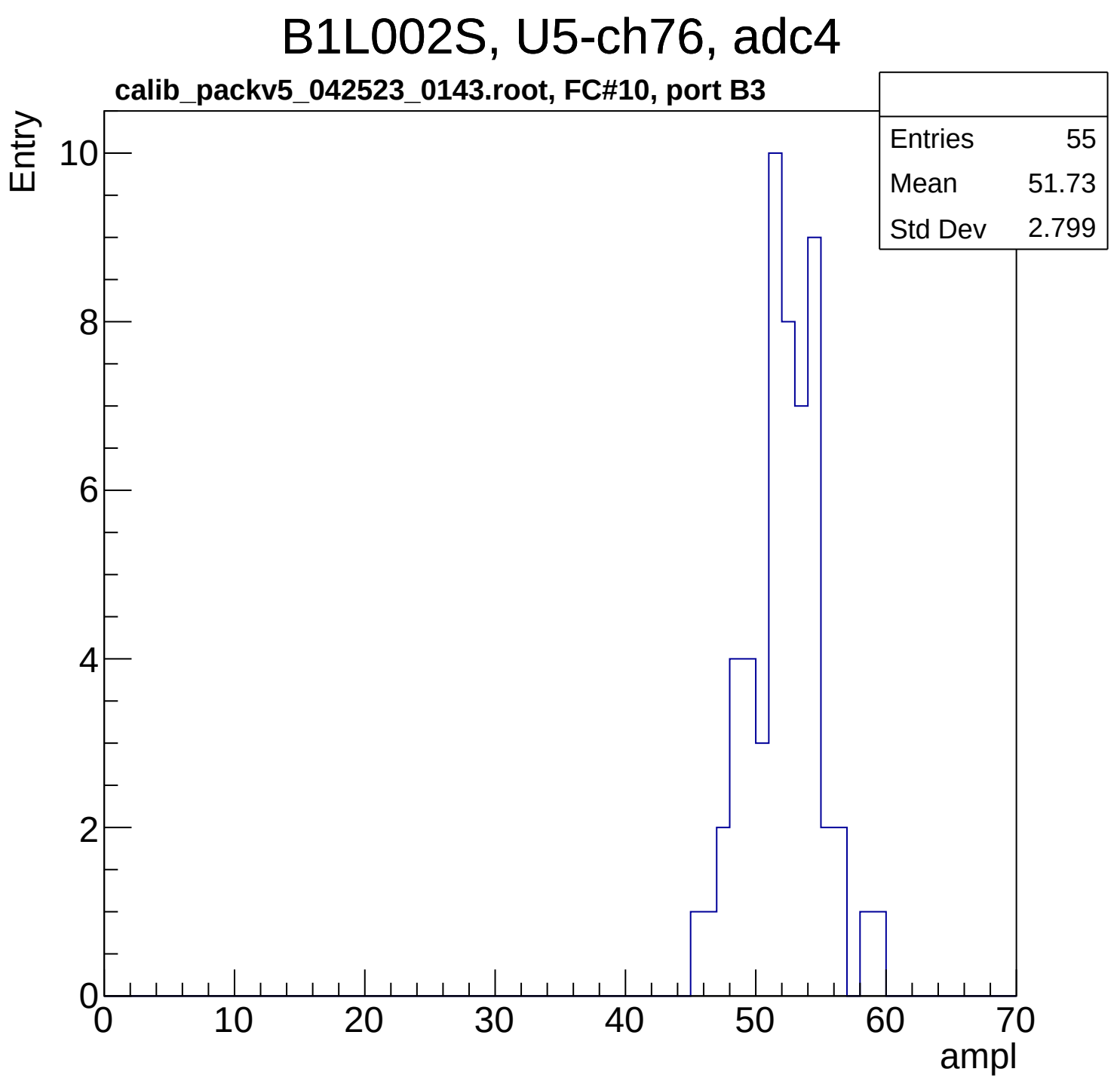
B1L002S, U5-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.73
Std Dev 2.799
Entry
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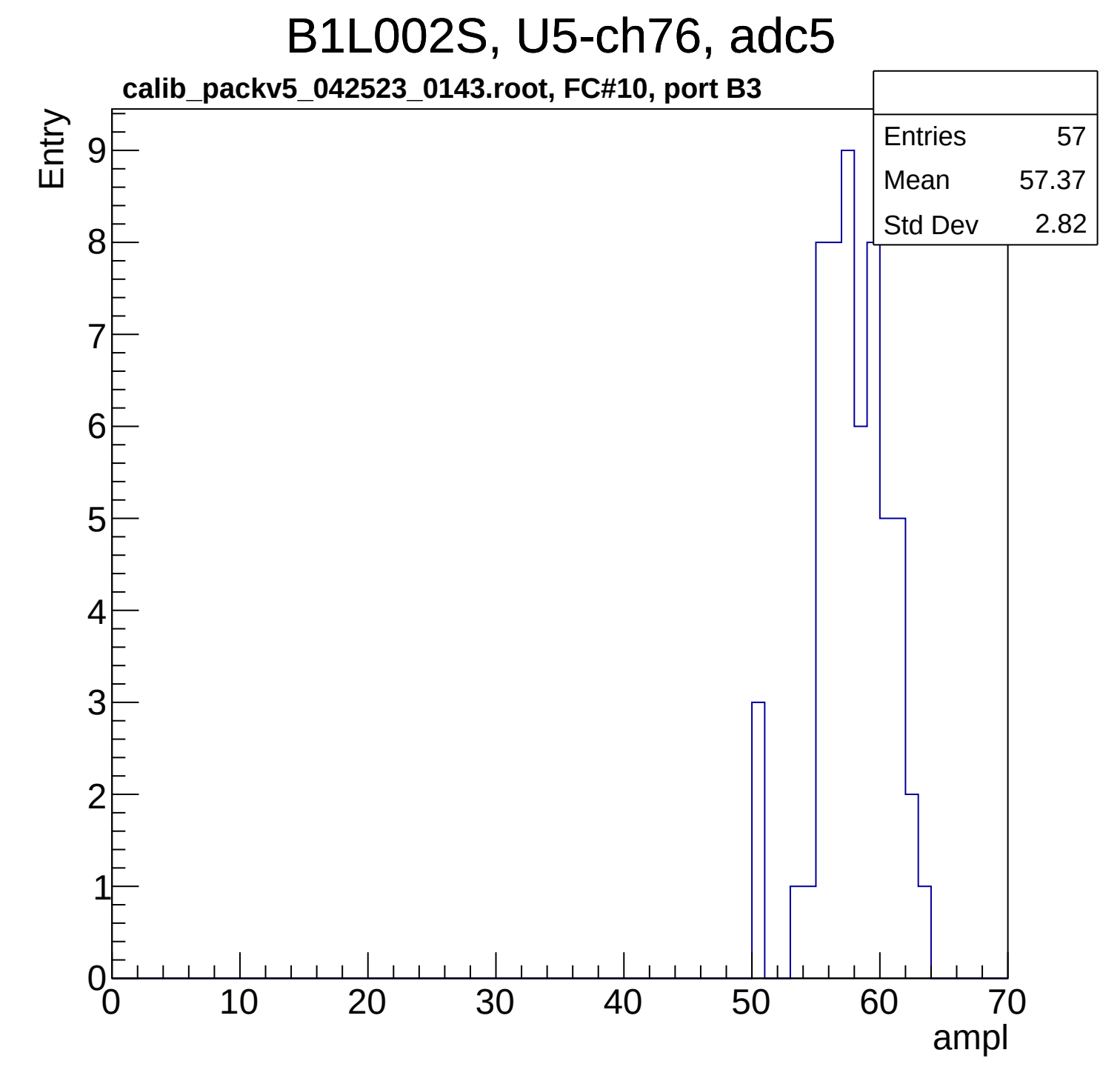
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.37
Std Dev 2.82
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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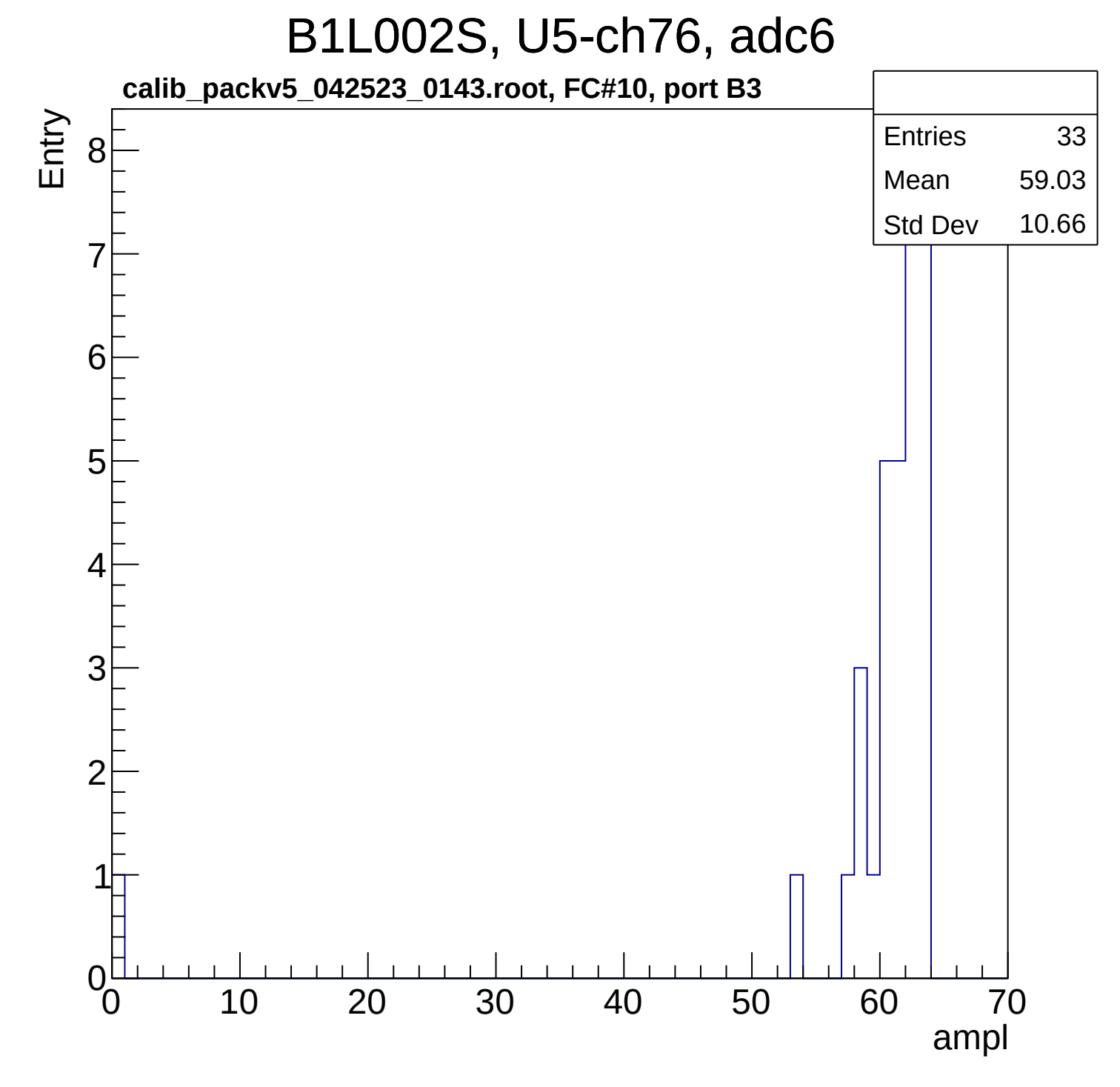
B1L002S, U5-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 59.03
Std Dev 10.66
Entry
ampl
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1
2
3
4
5
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7
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10
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40
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60
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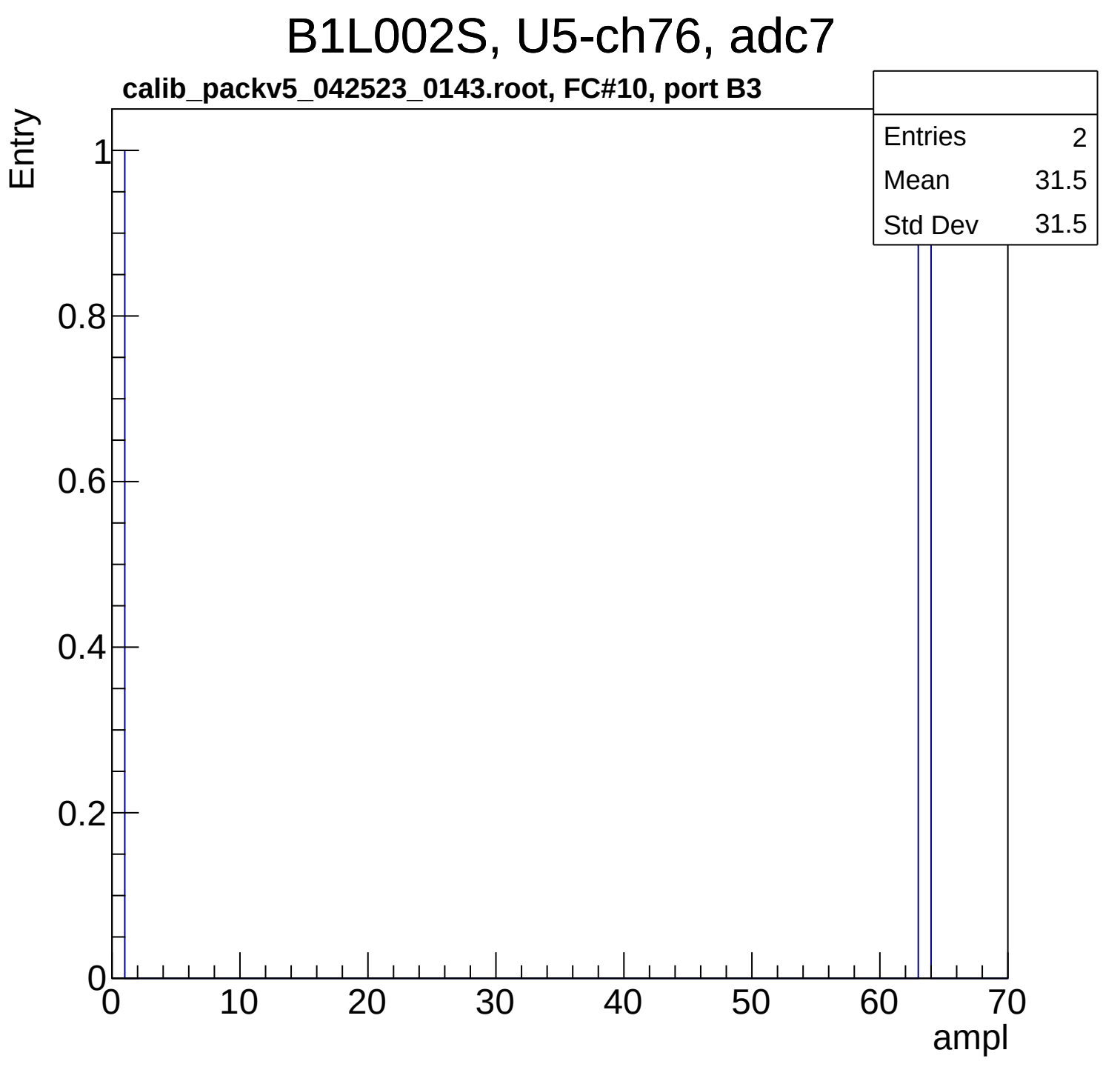
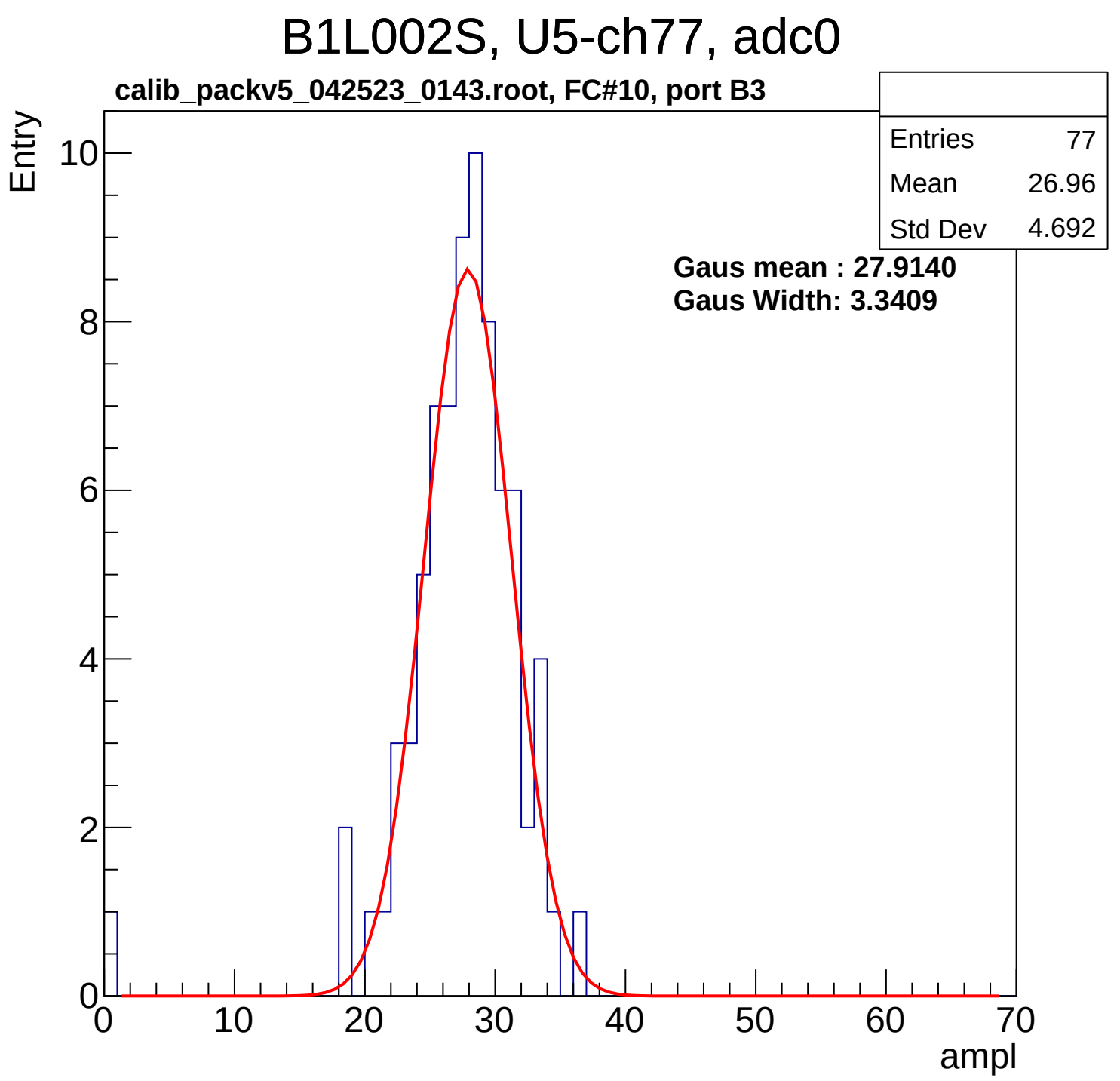

B1L002S, U5-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 26.96
Std Dev 4.692
Gaus mean : 27.9140
Gaus Width: 3.3409
Entry
ampl
0
2
4
6
8
10
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10
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60
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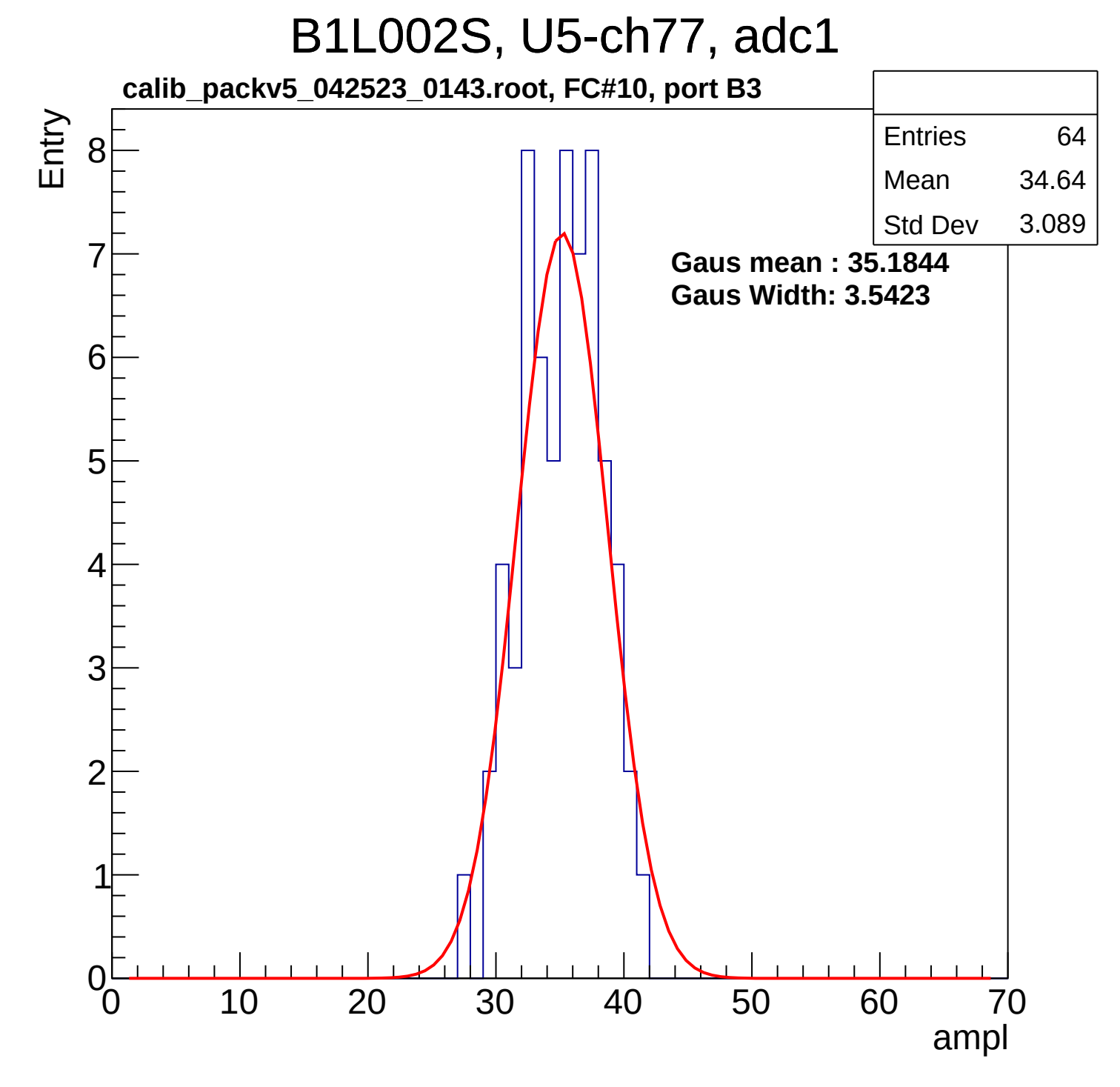
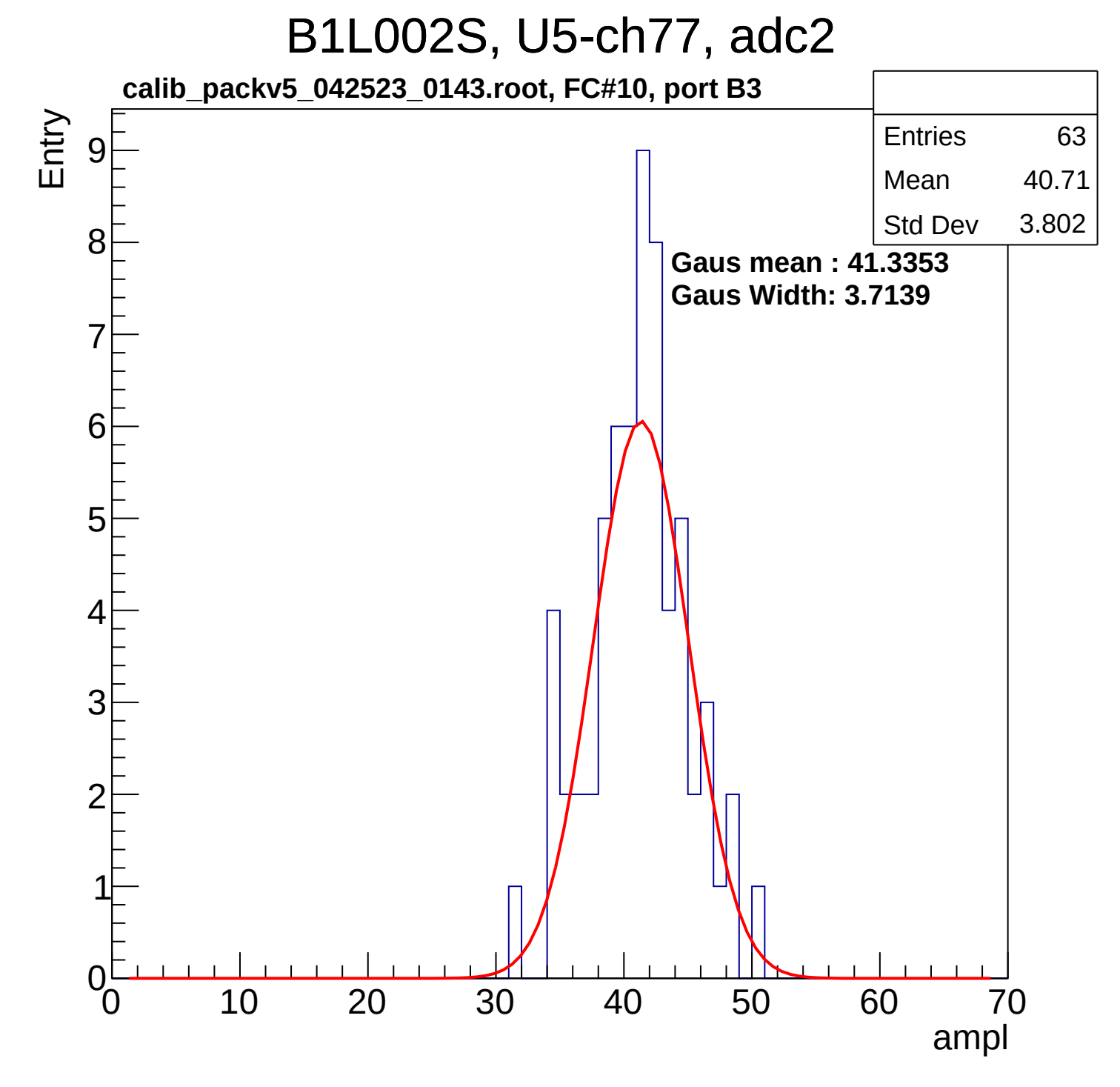

B1L002S, U5-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.64
Std Dev 3.089
Gaus mean : 35.1844
Gaus Width: 3.5423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.71
Std Dev 3.802
Gaus mean : 41.3353
Gaus Width: 3.7139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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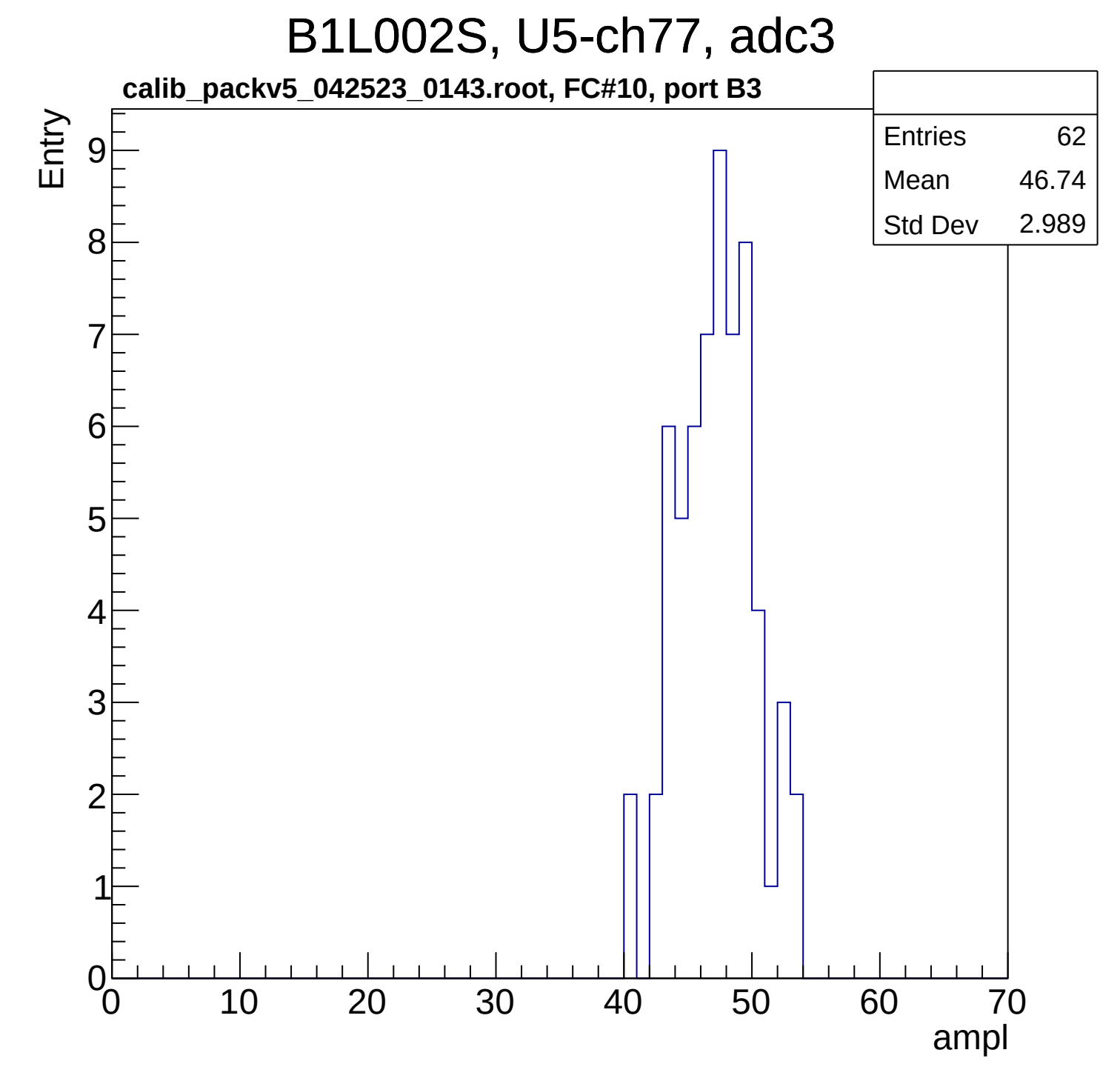
B1L002S, U5-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.74
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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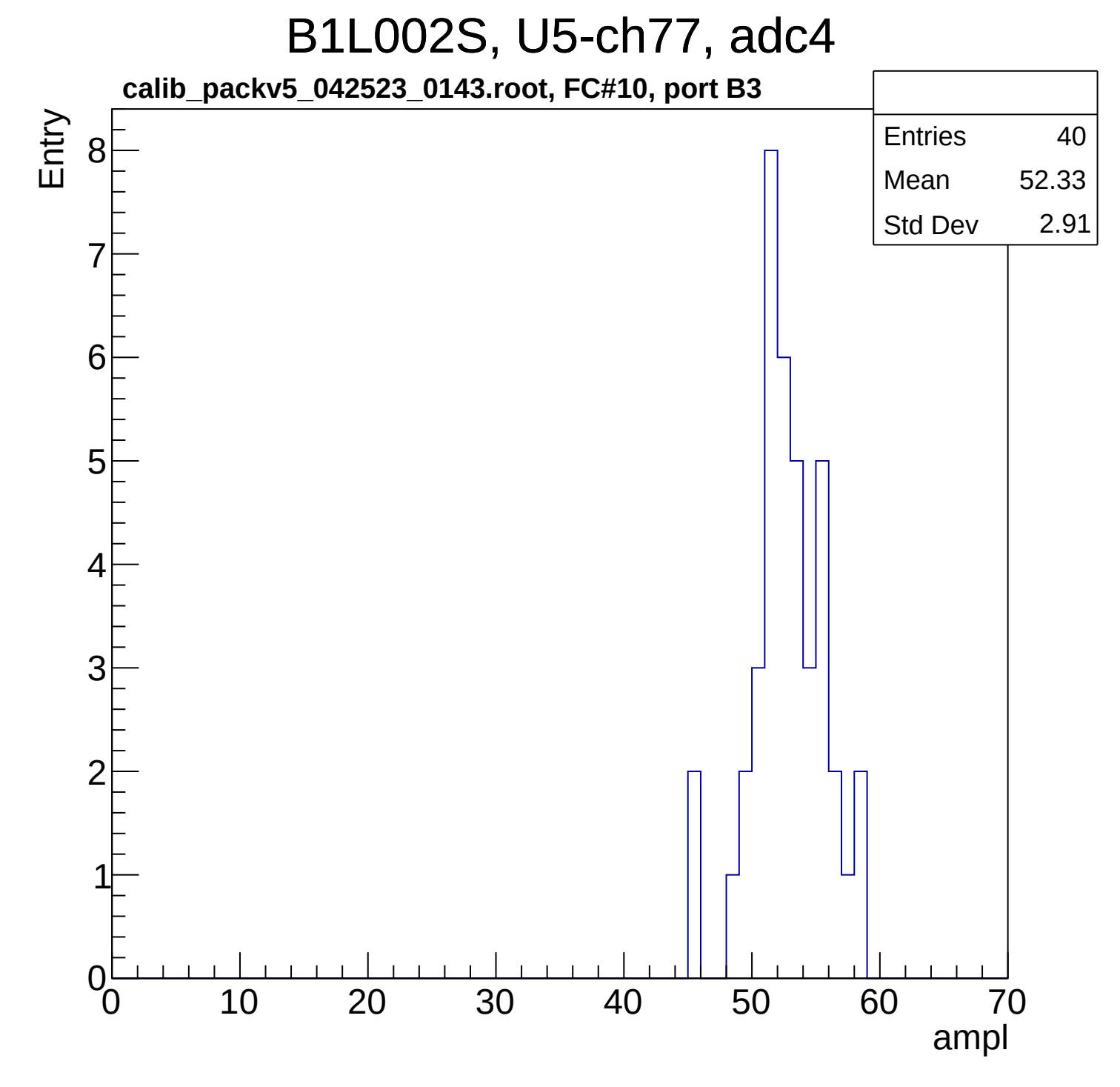
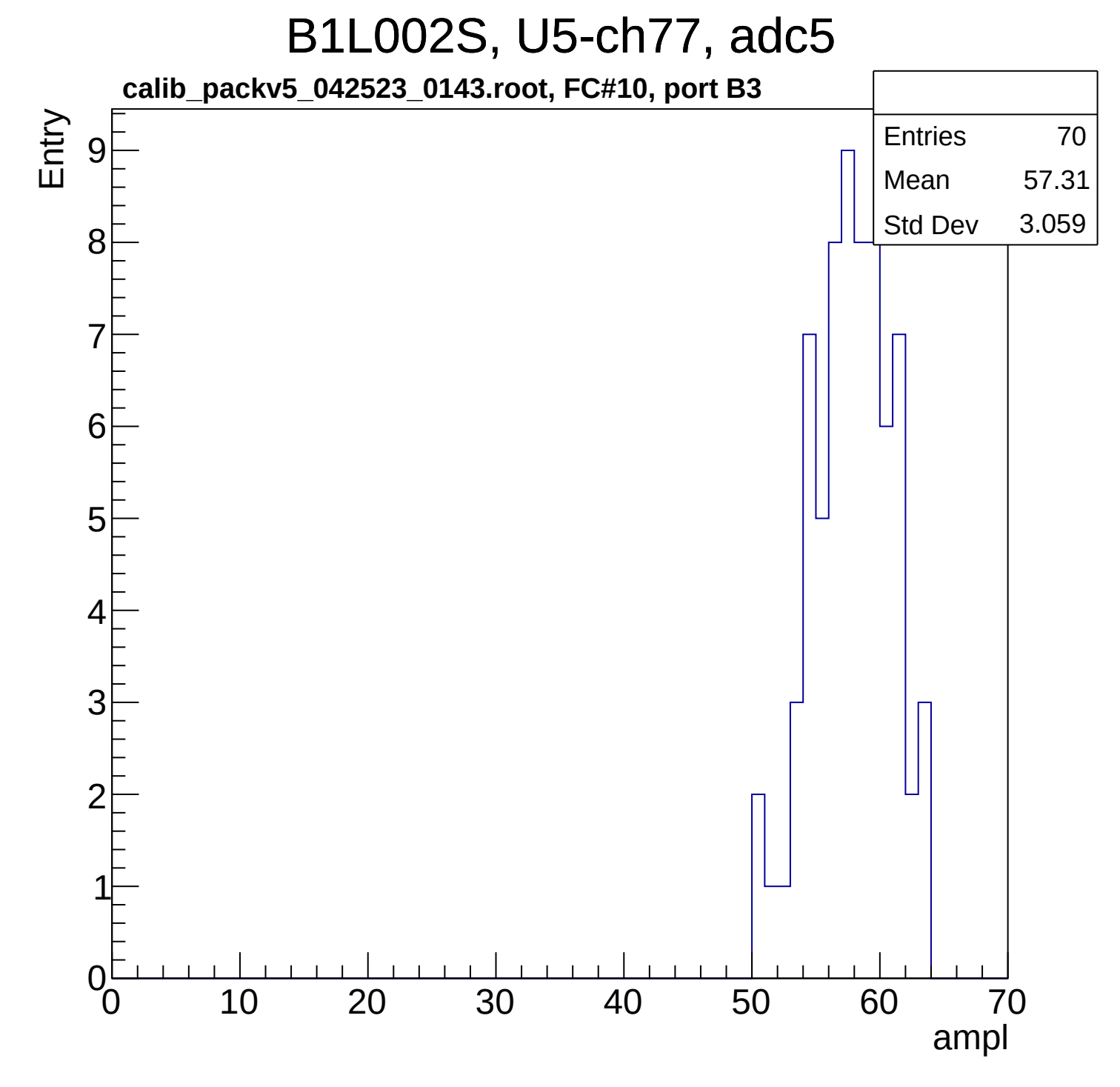

B1L002S, U5-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 52.33
Std Dev 2.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 57.31
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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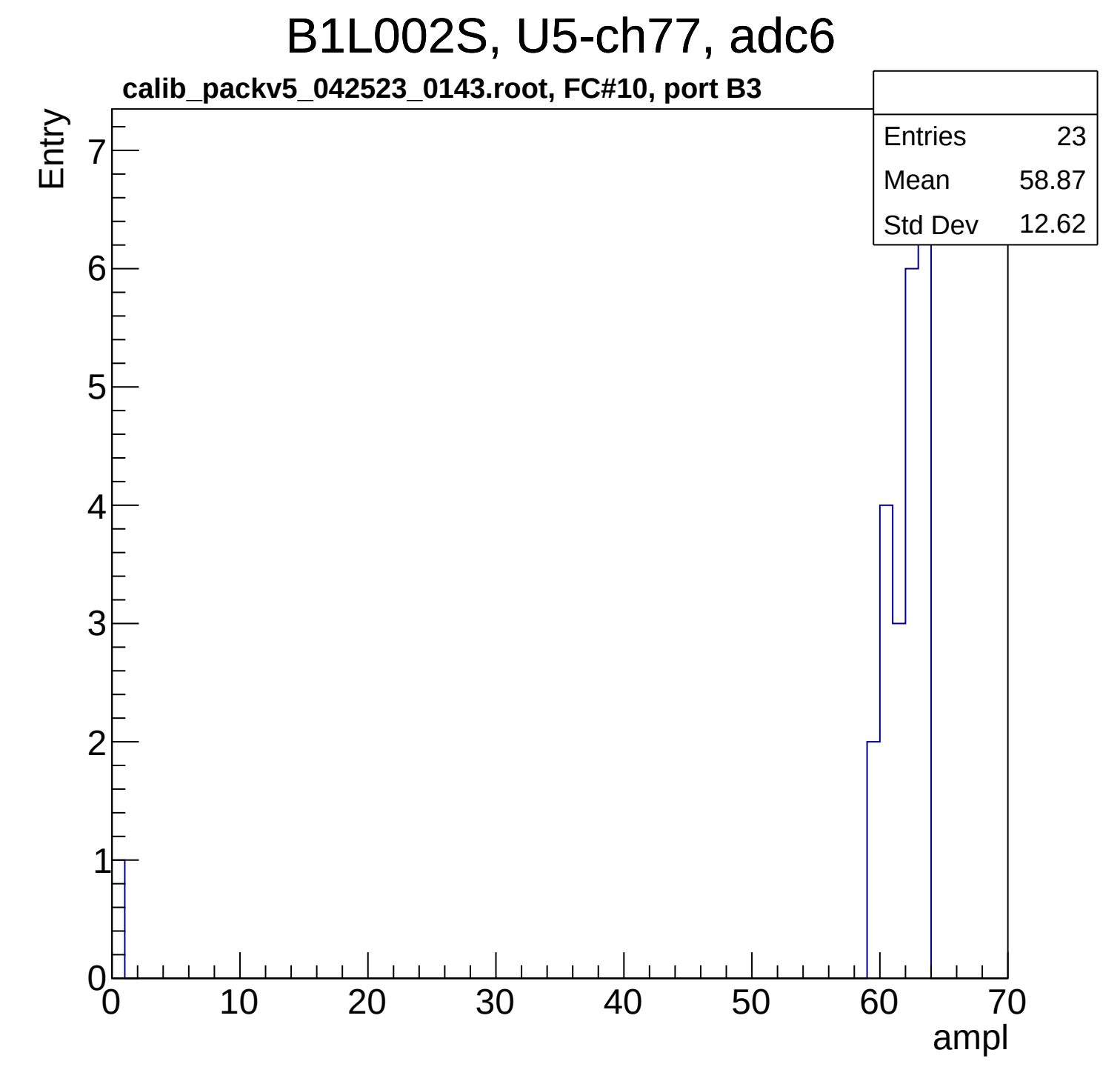
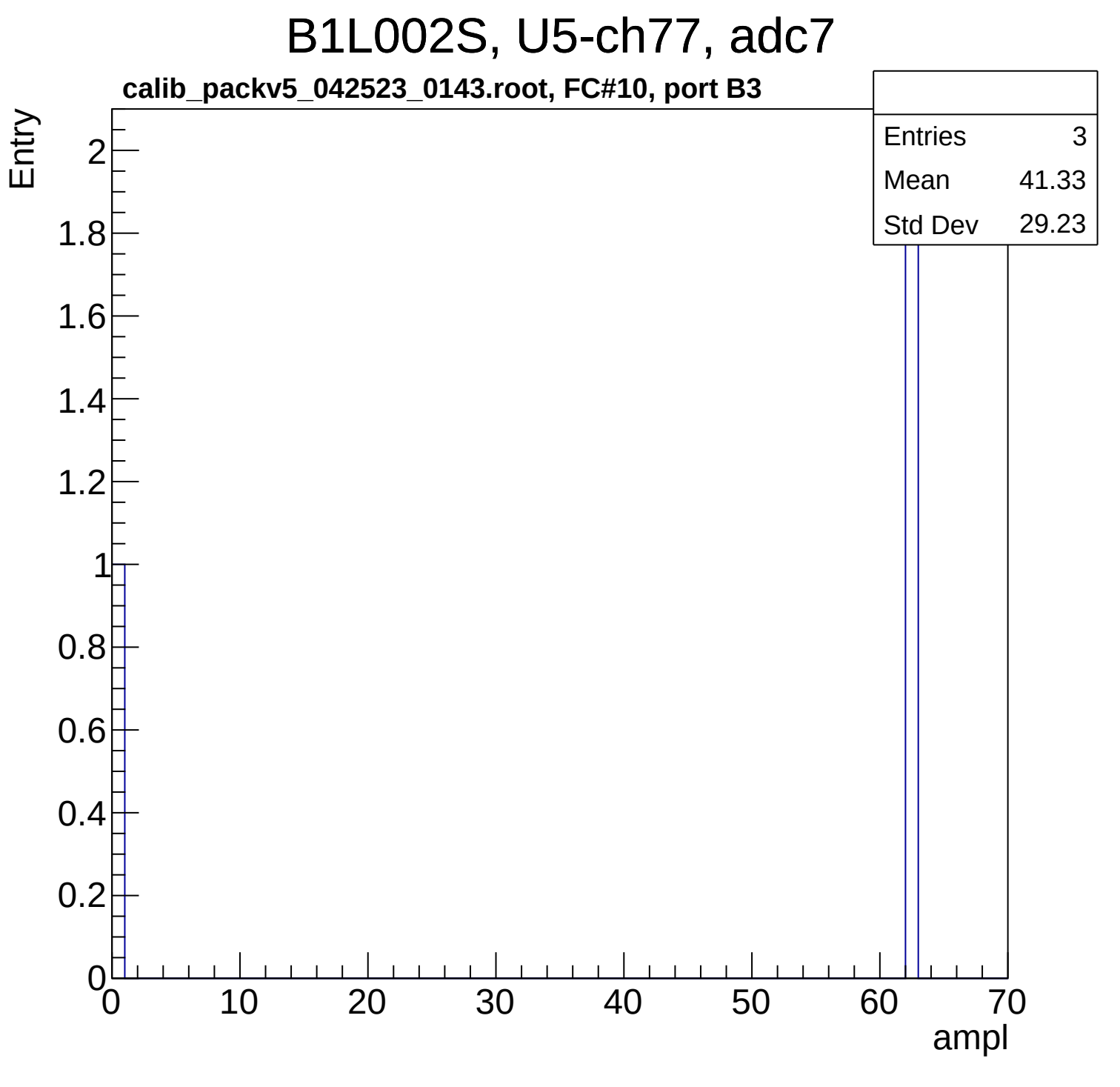

B1L002S, U5-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.87
Std Dev 12.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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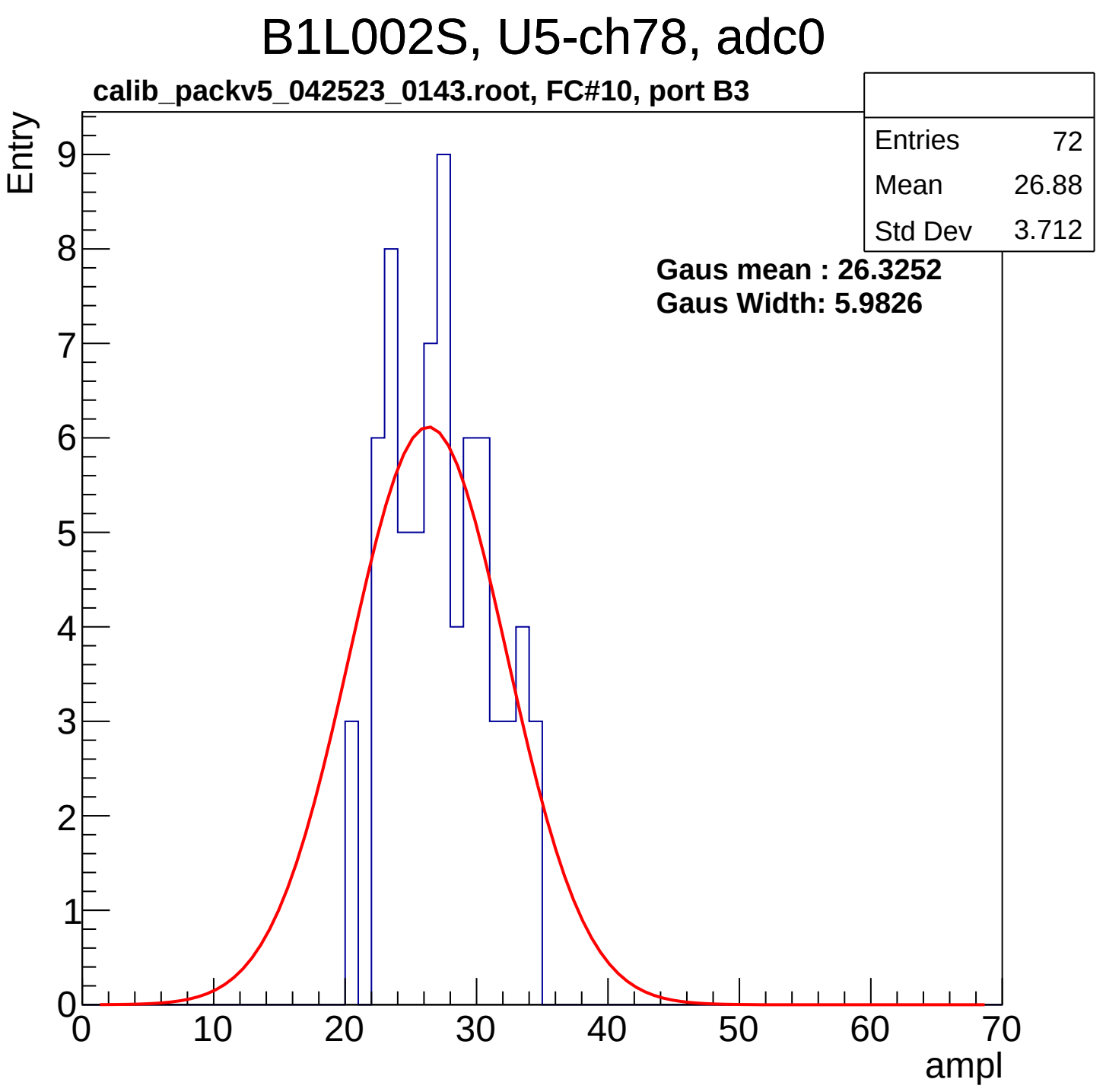
B1L002S, U5-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.88
Std Dev 3.712
Gaus mean : 26.3252
Gaus Width: 5.9826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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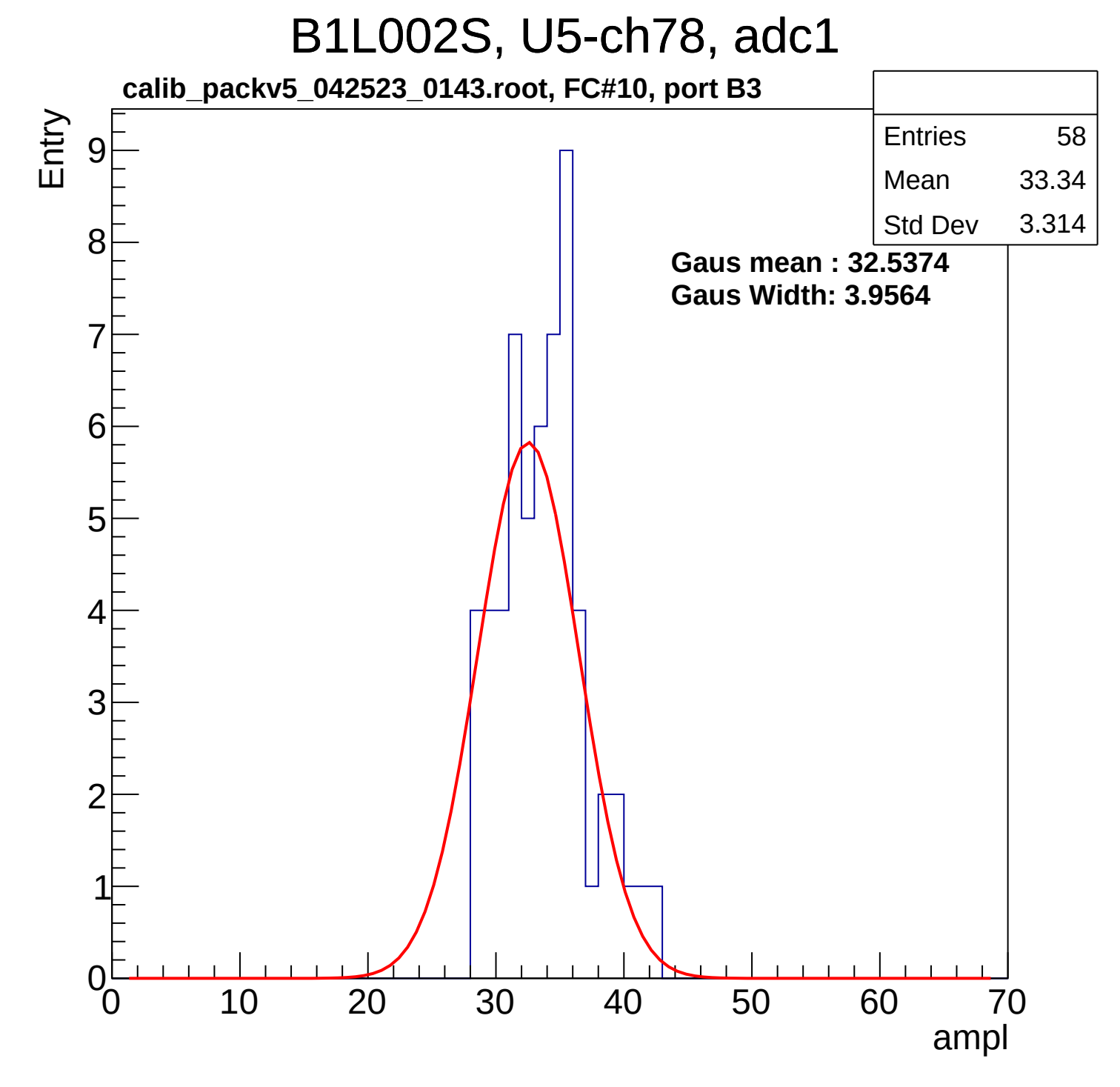
B1L002S, U5-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.34
Std Dev 3.314
Gaus mean : 32.5374
Gaus Width: 3.9564
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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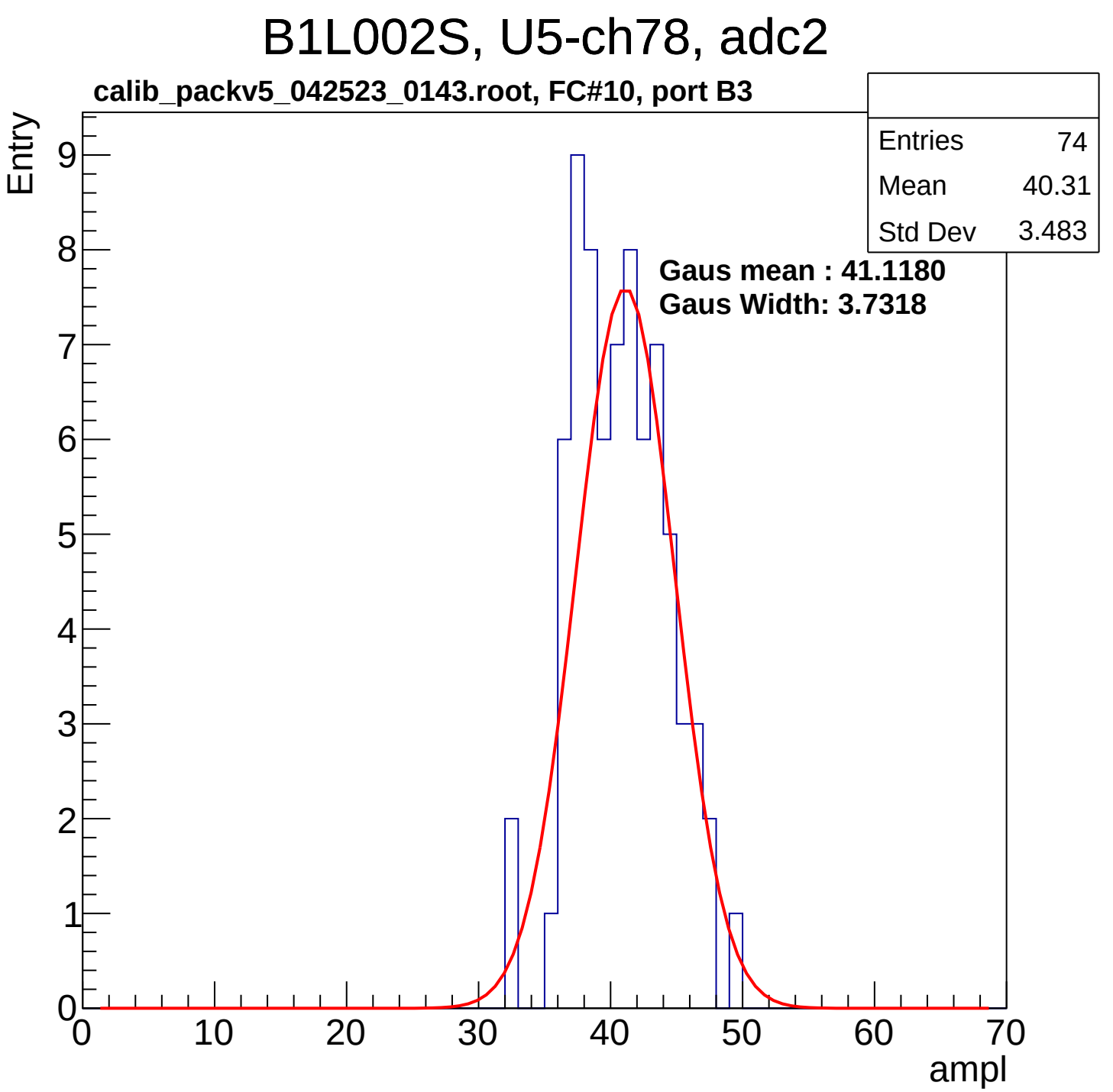
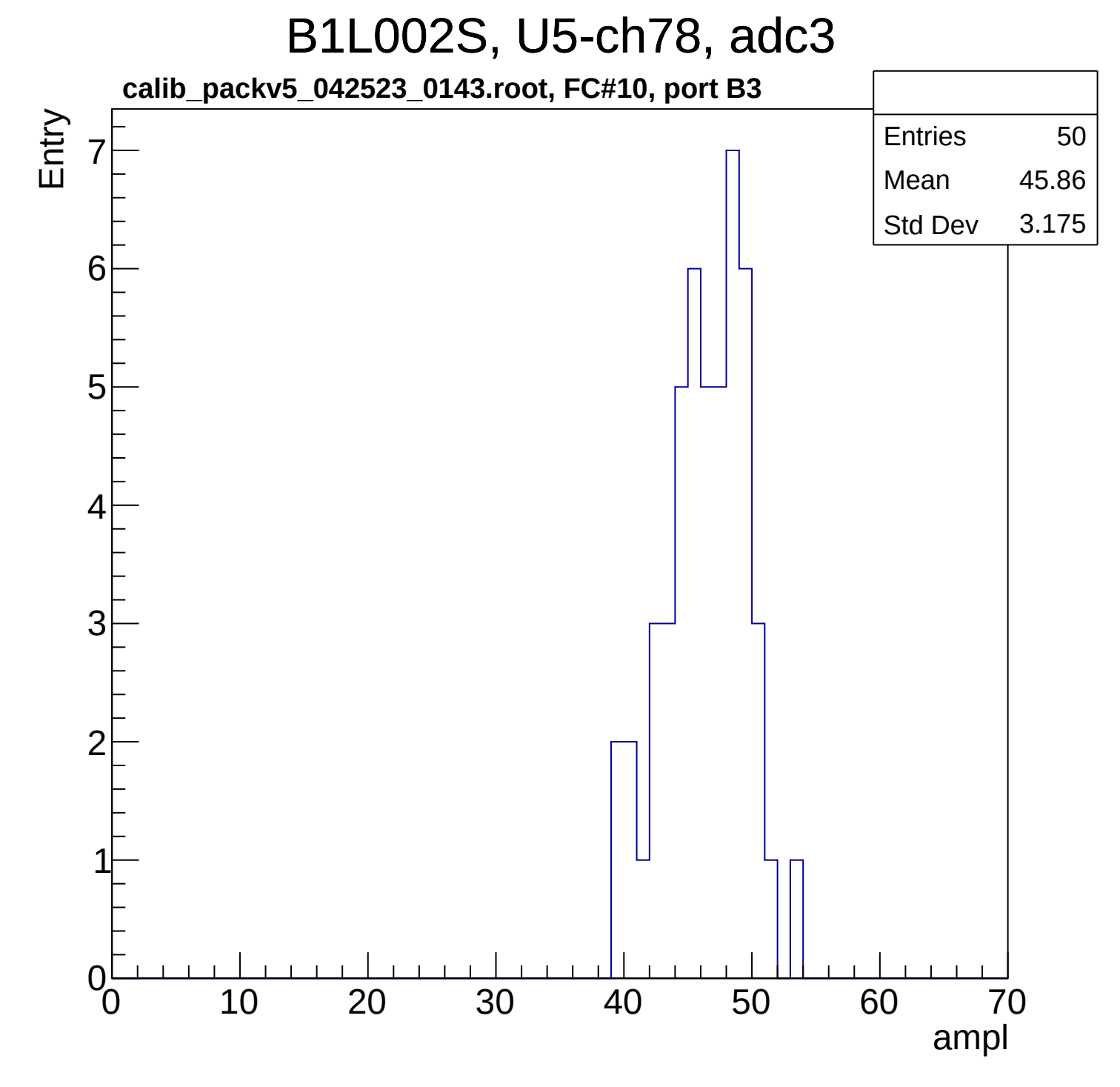

B1L002S, U5-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.31
Std Dev 3.483
Gaus mean : 41.1180
Gaus Width: 3.7318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 45.86
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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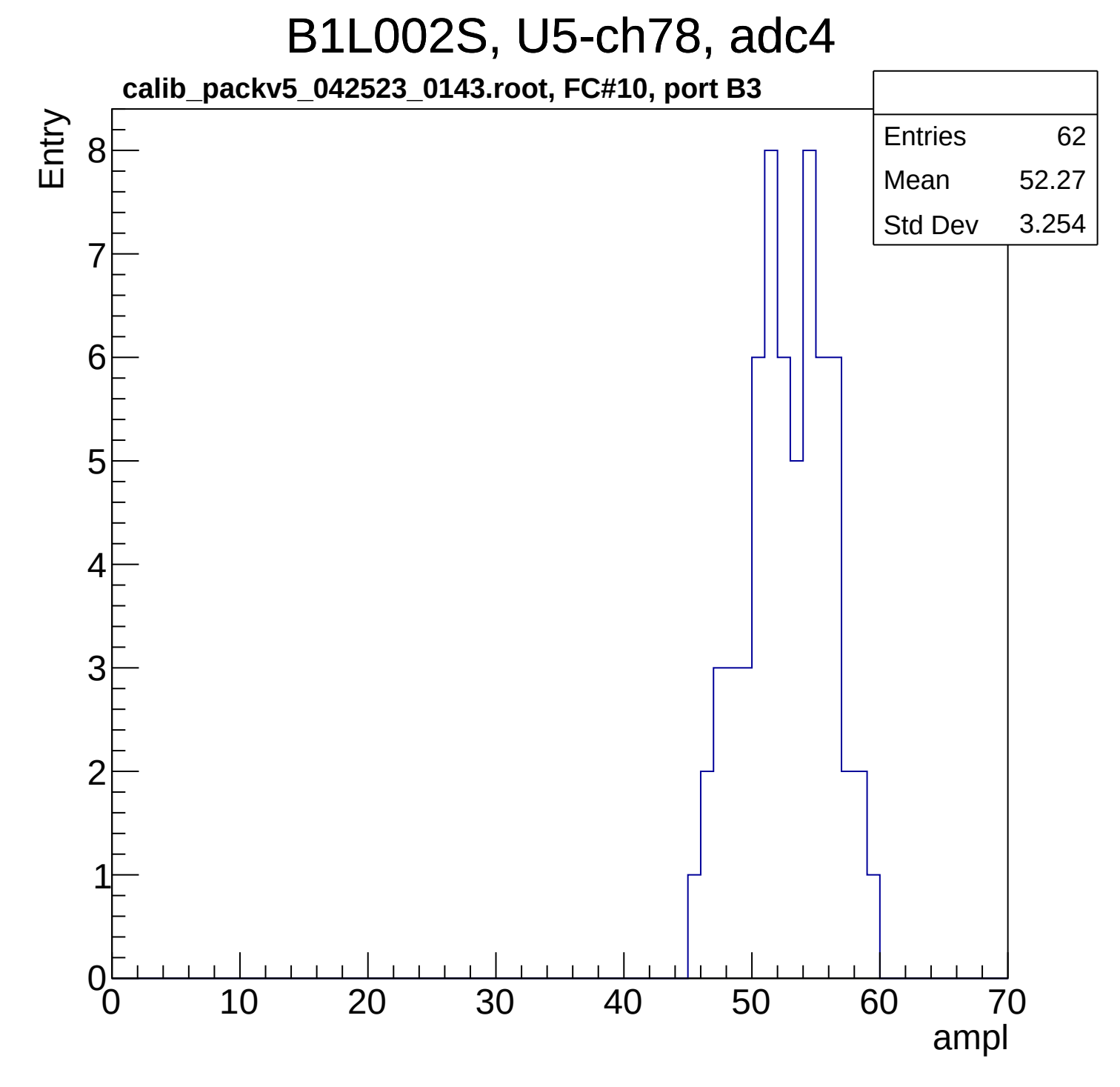
B1L002S, U5-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.27
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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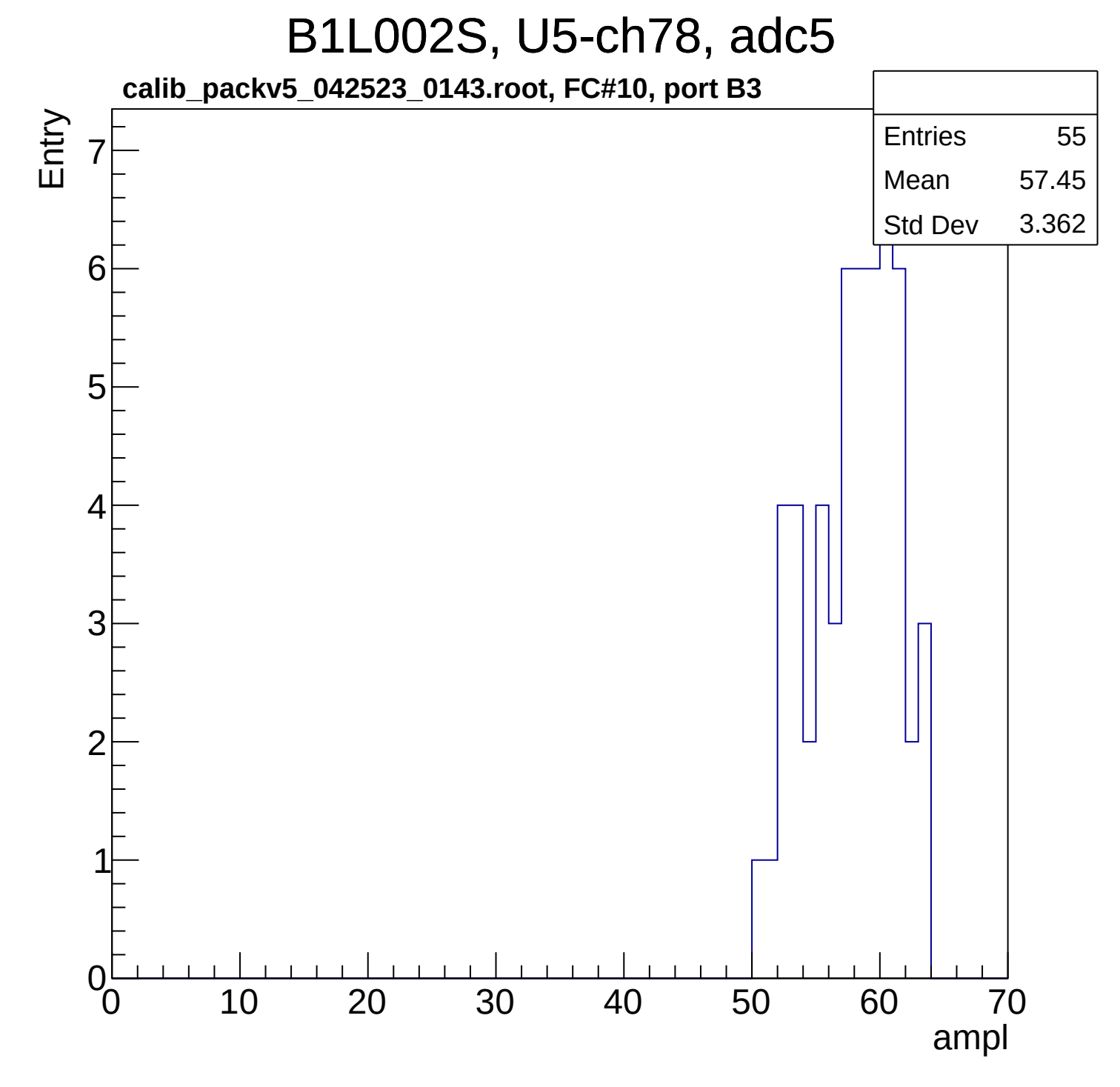
B1L002S, U5-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.45
Std Dev 3.362
Entry
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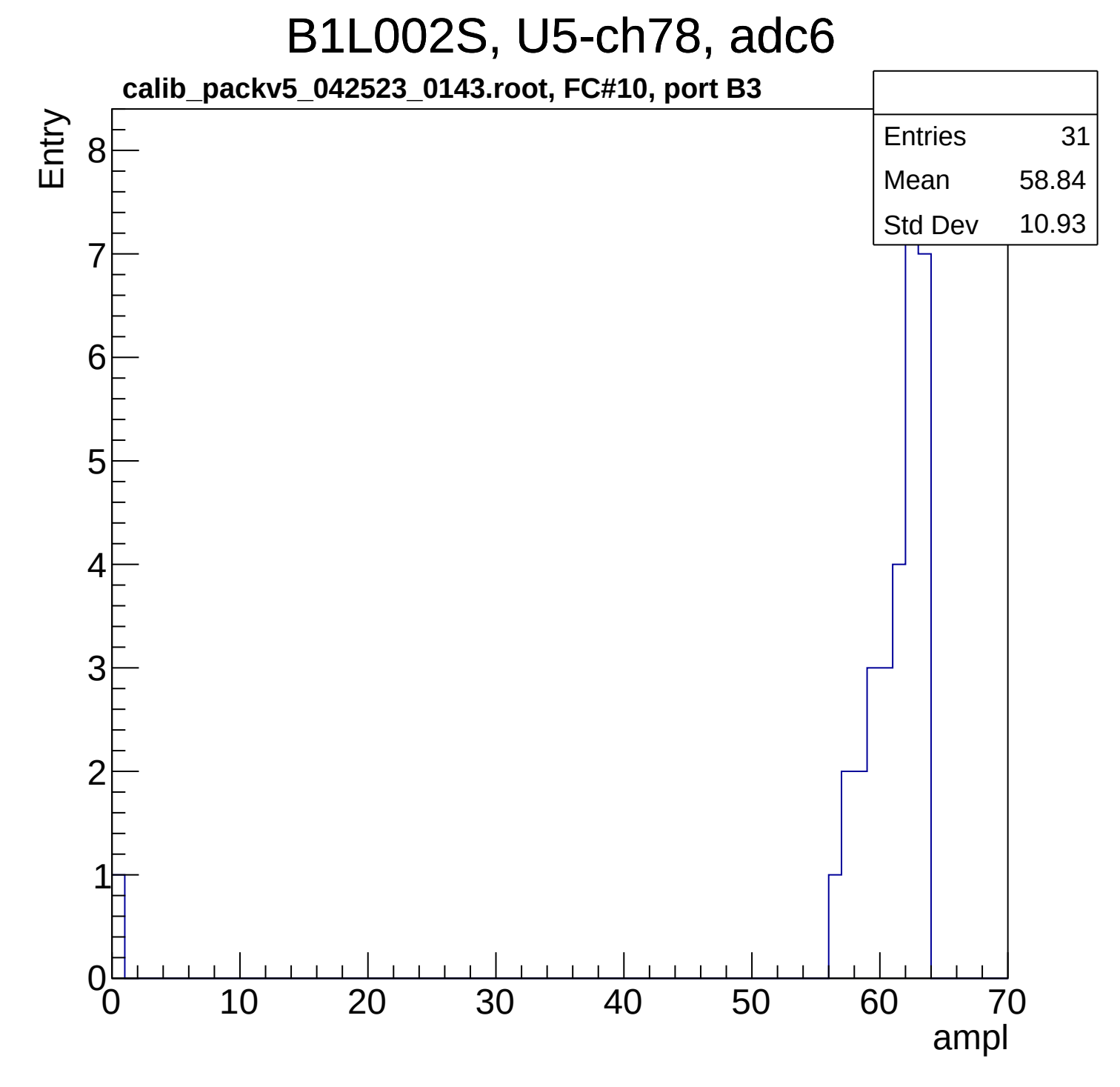
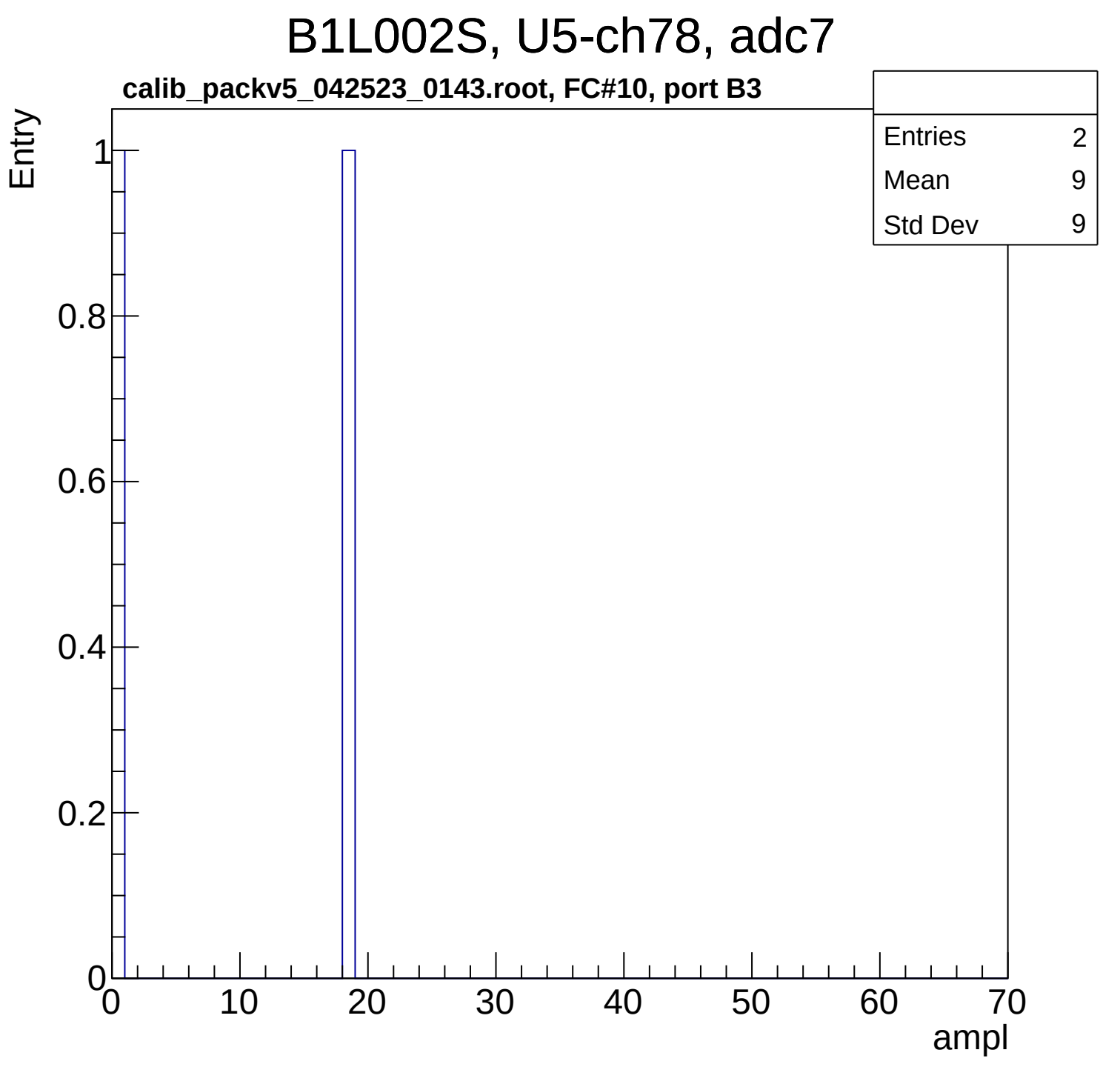
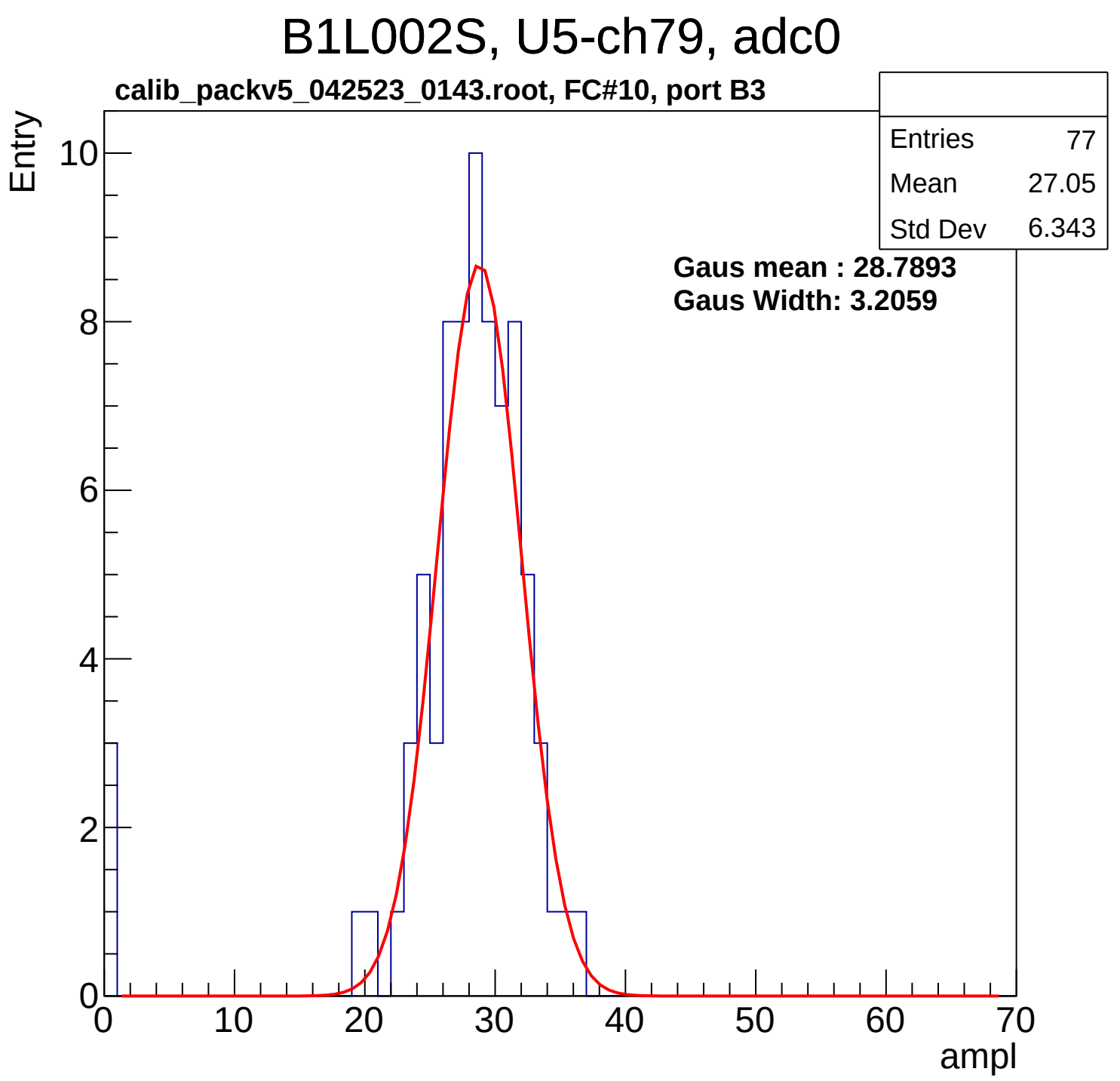
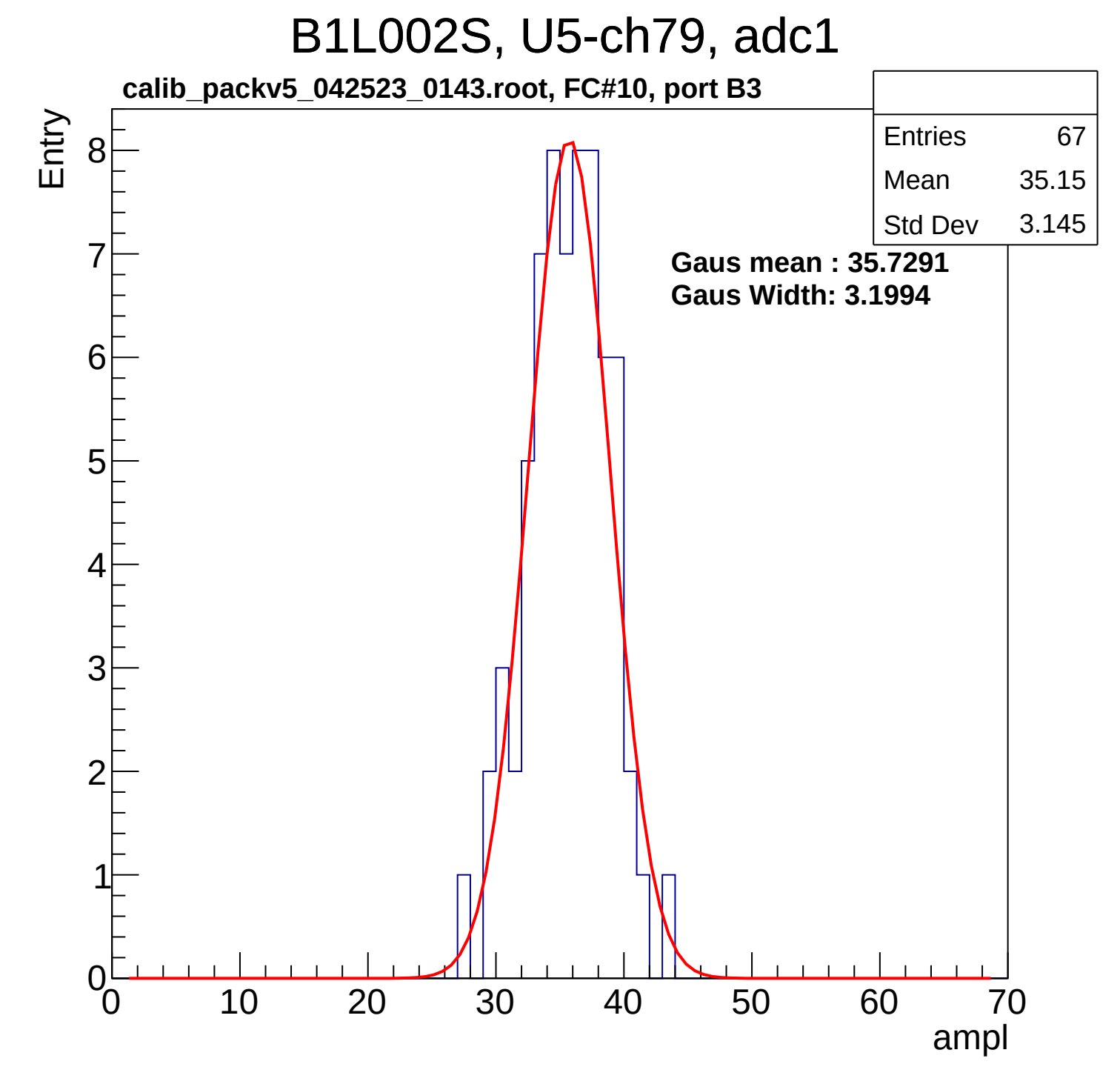
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.84
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9
Std Dev 9
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.05
Std Dev 6.343
Gaus mean : 28.7893
Gaus Width: 3.2059
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.15
Std Dev 3.145
Gaus mean : 35.7291
Gaus Width: 3.1994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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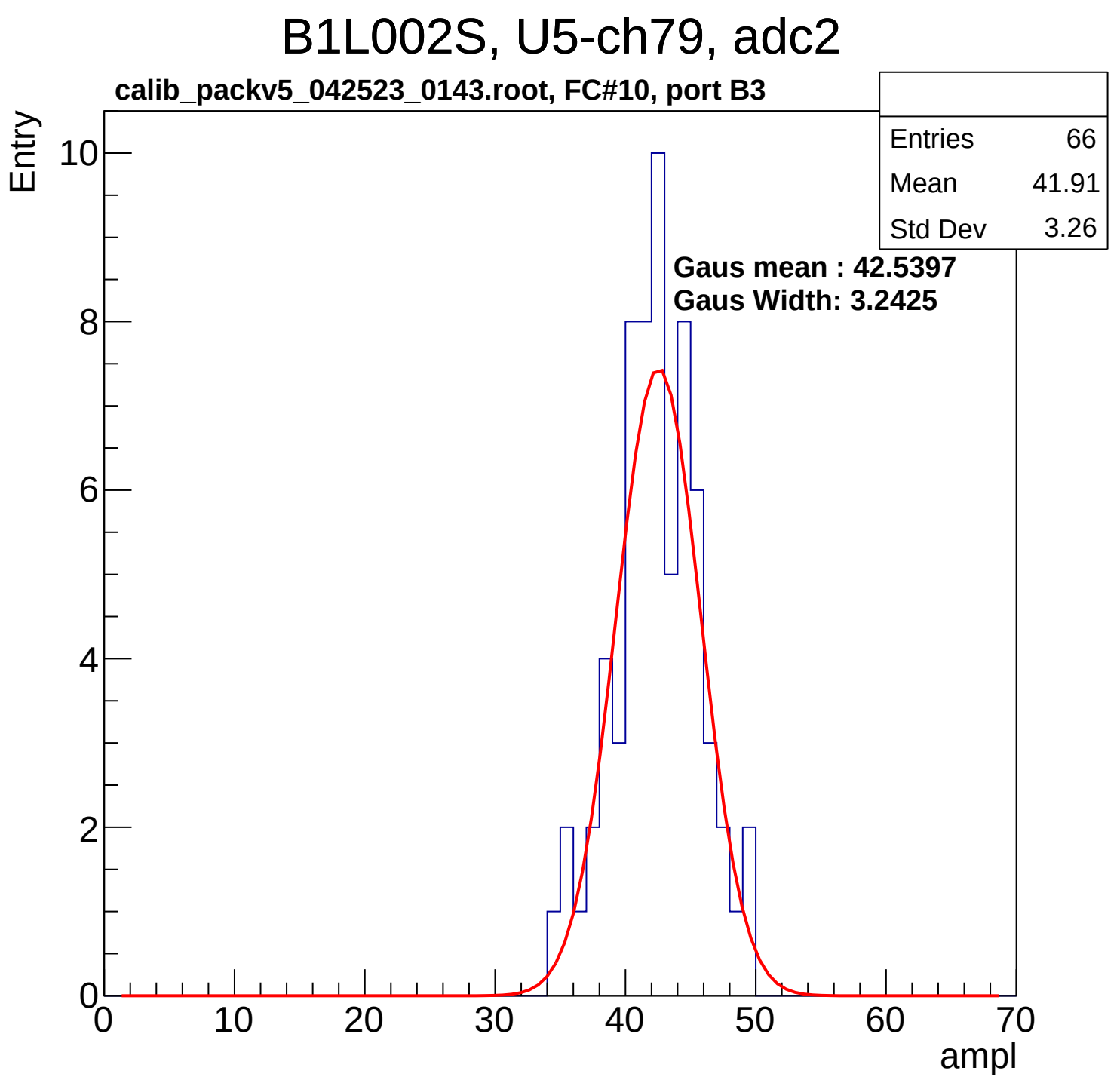
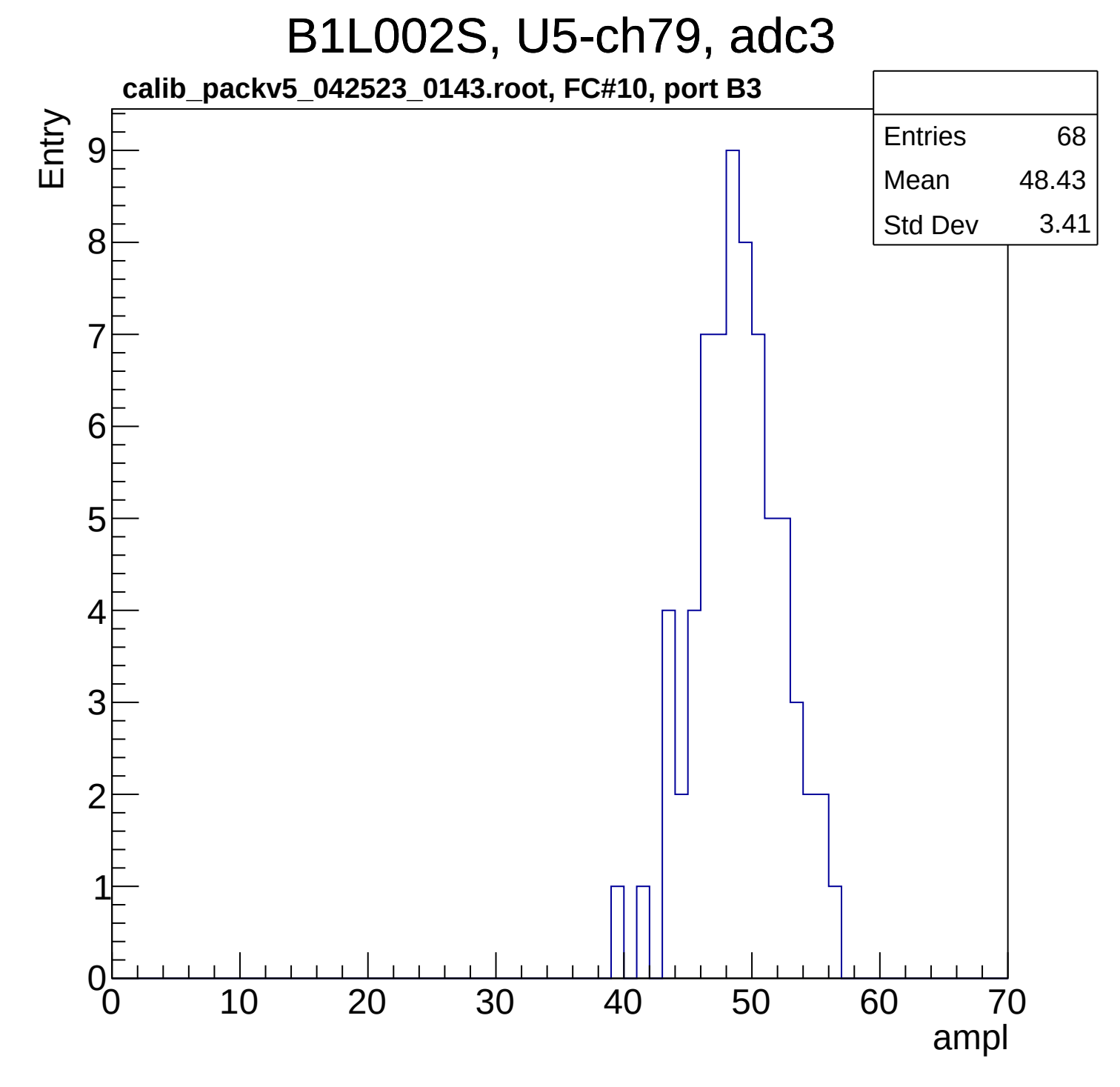

B1L002S, U5-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.91
Std Dev 3.26
Gaus mean : 42.5397
Gaus Width: 3.2425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.43
Std Dev 3.41
Entry
ampl
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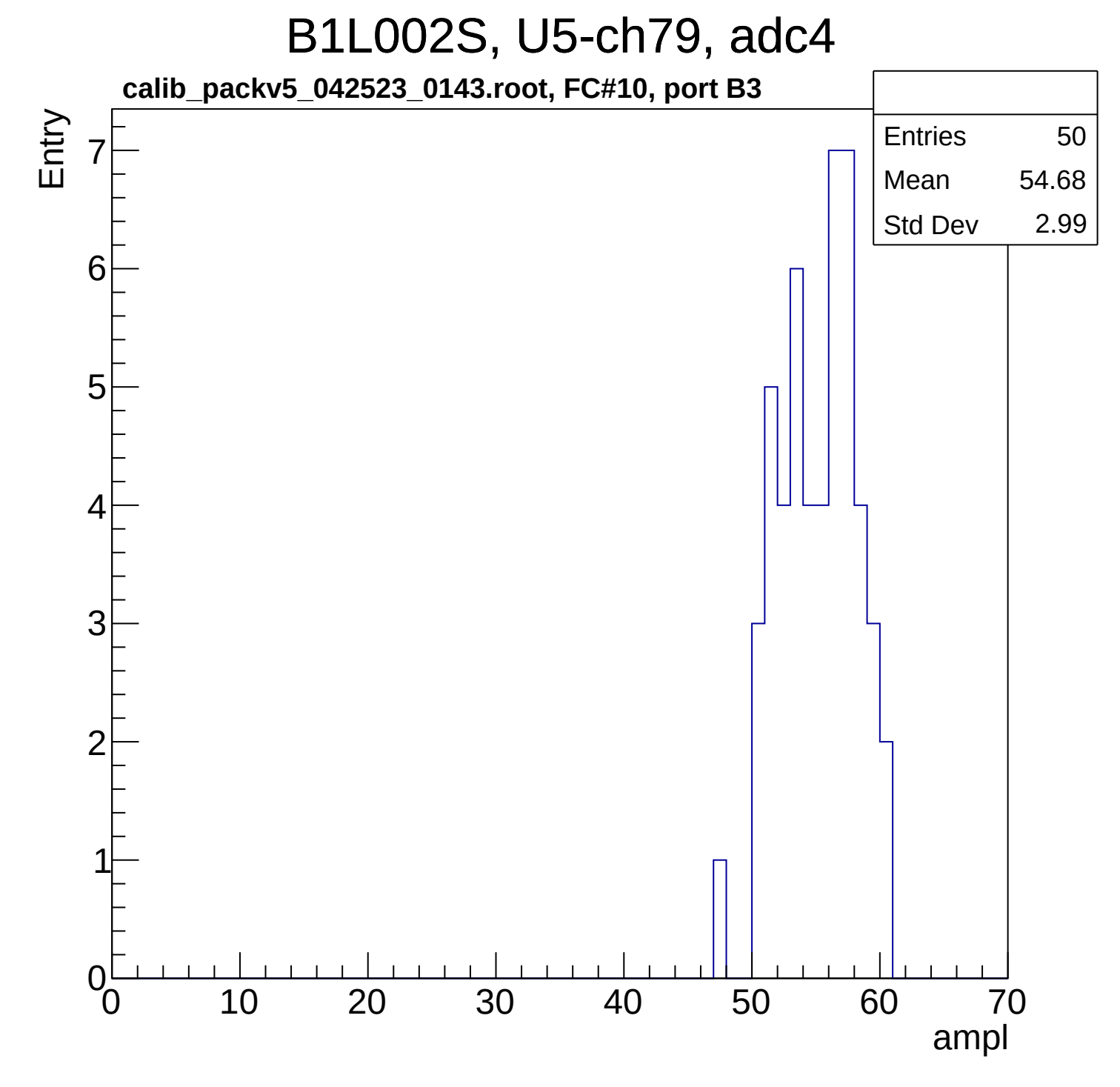
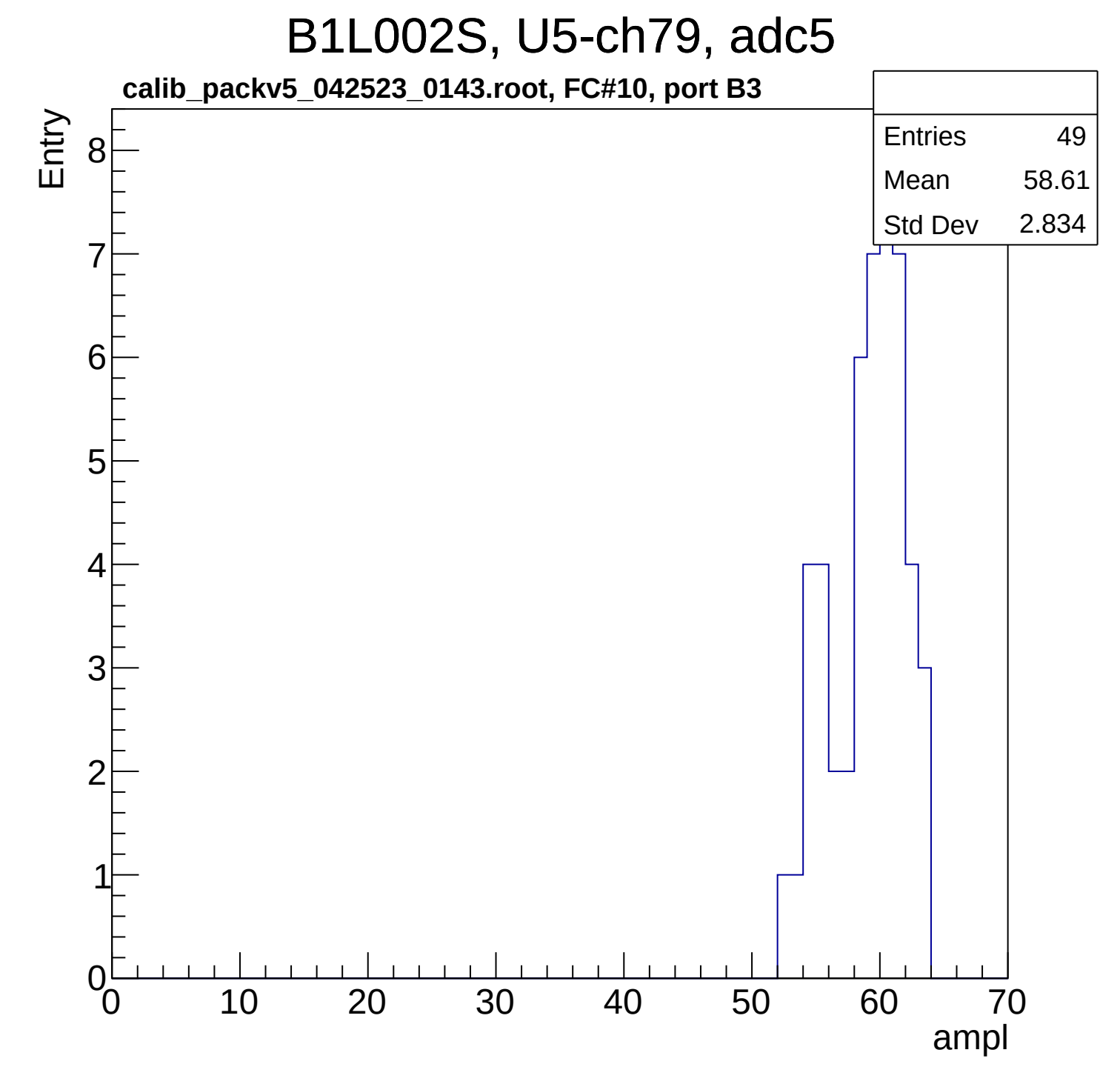
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 54.68
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.61
Std Dev 2.834
Entry
ampl
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1
2
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4
5
6
7
8
0
10
20
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60
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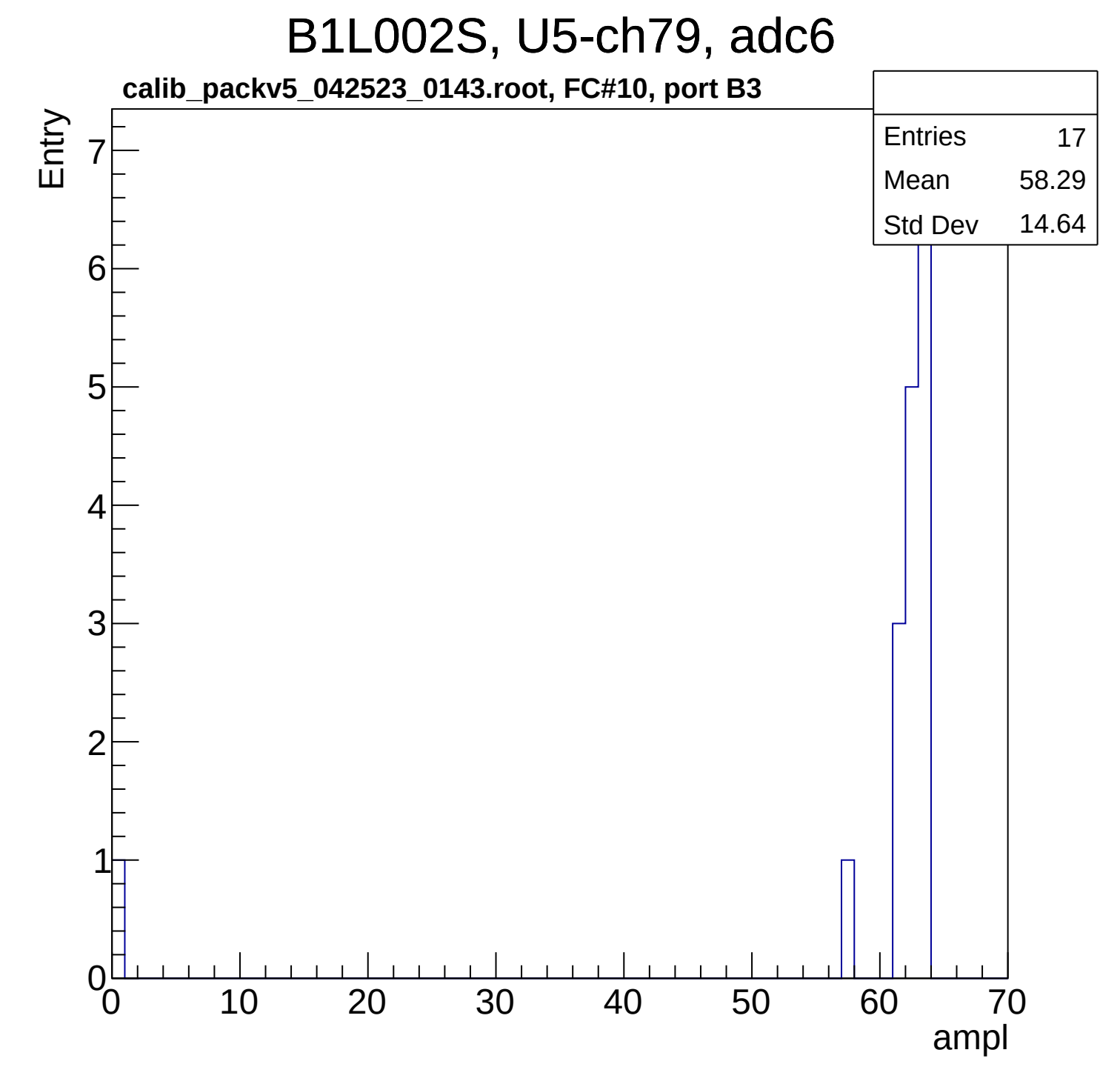
B1L002S, U5-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.29
Std Dev 14.64
Entry
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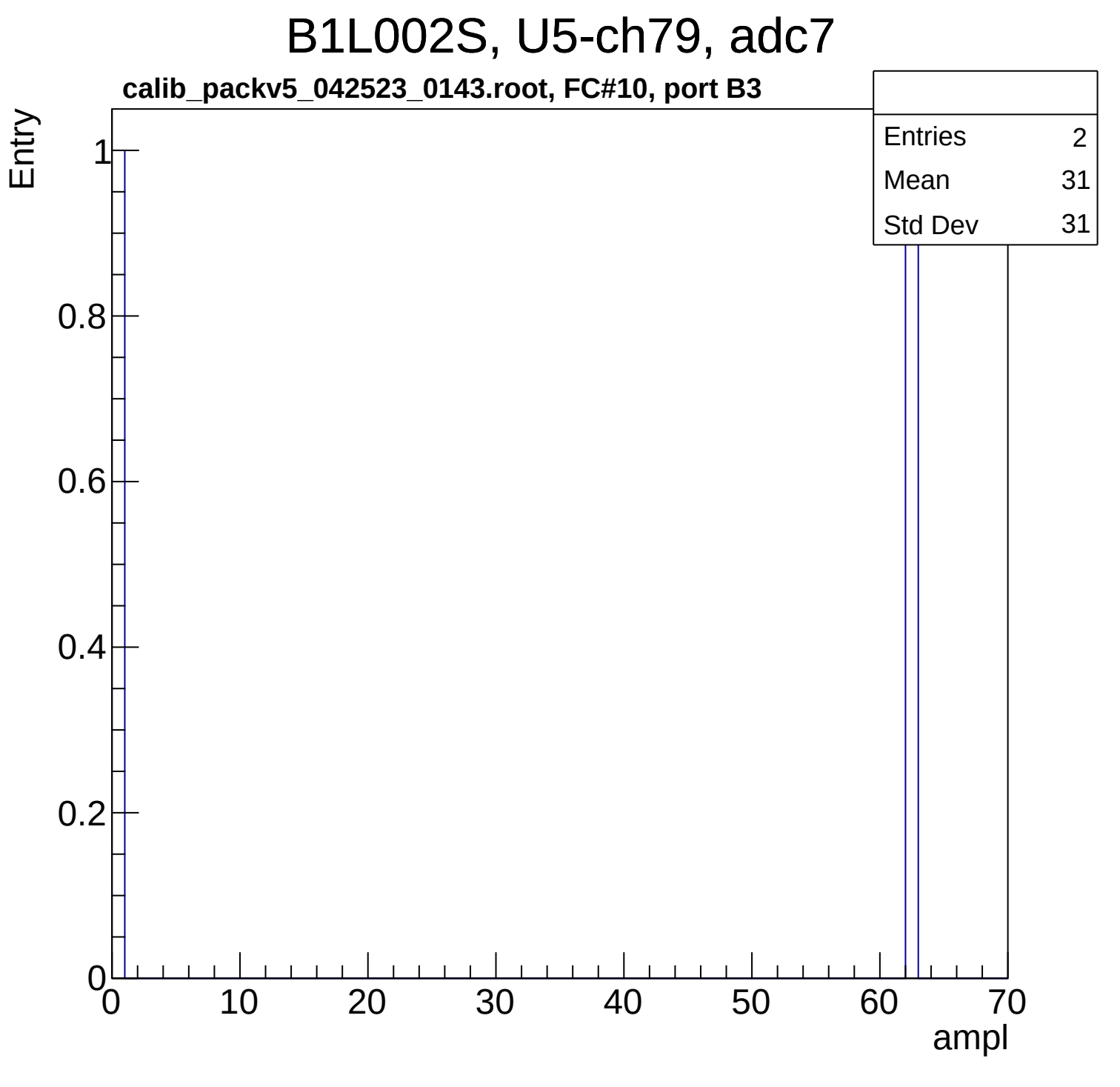
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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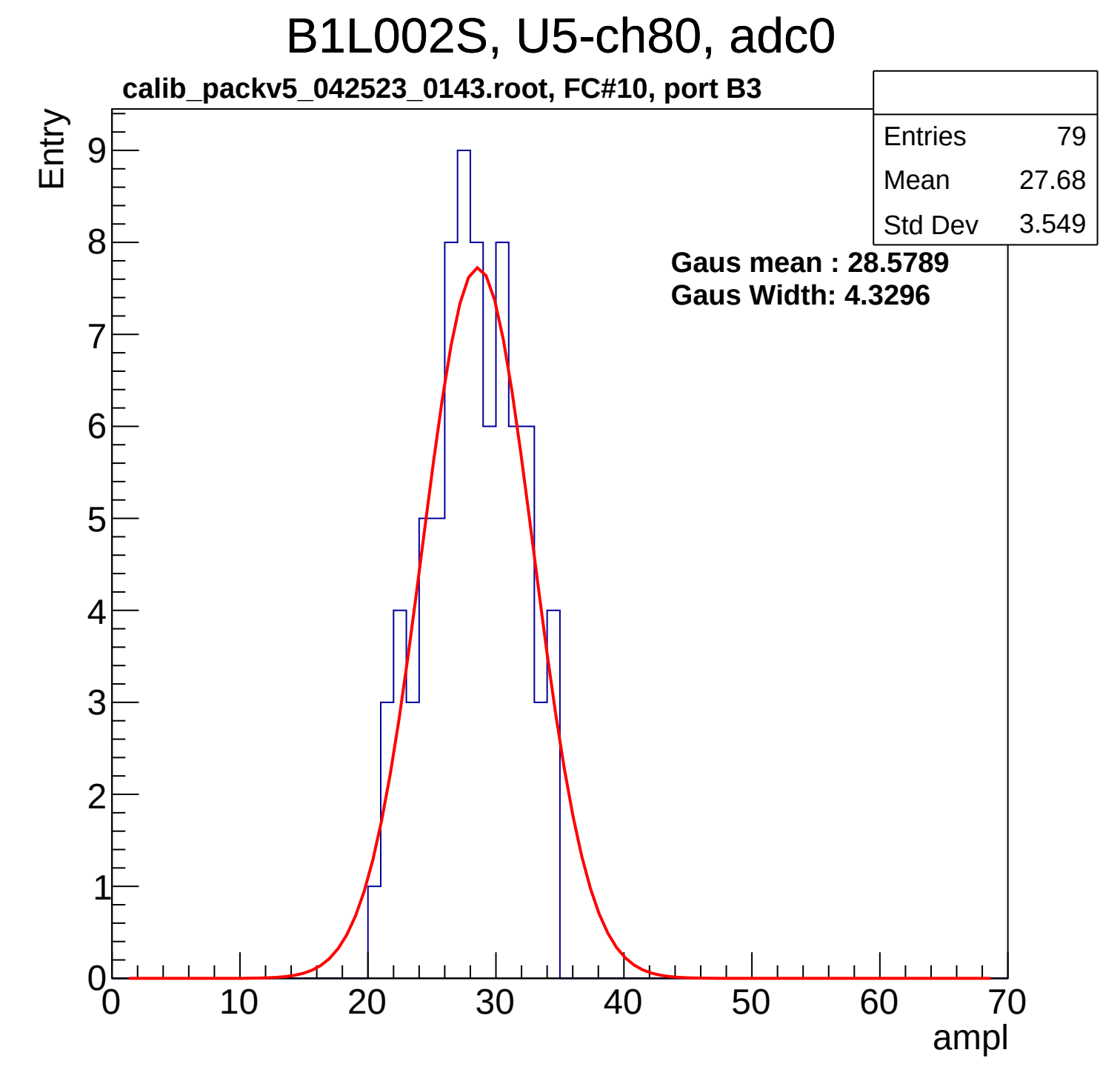
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.68
Std Dev 3.549
Gaus mean : 28.5789
Gaus Width: 4.3296
Entry
ampl
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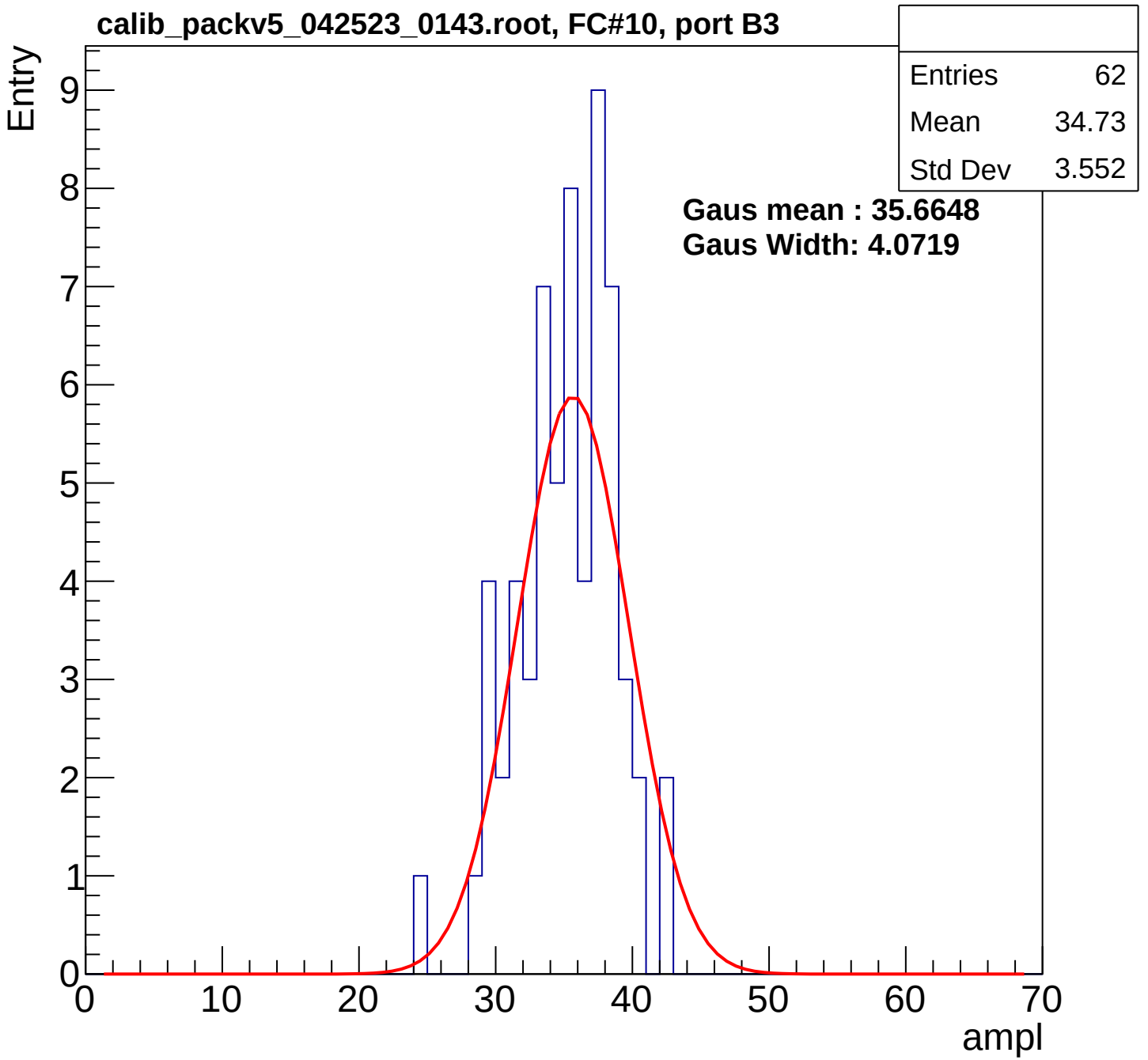
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.73
Std Dev 3.552
Gaus mean : 35.6648
Gaus Width: 4.0719
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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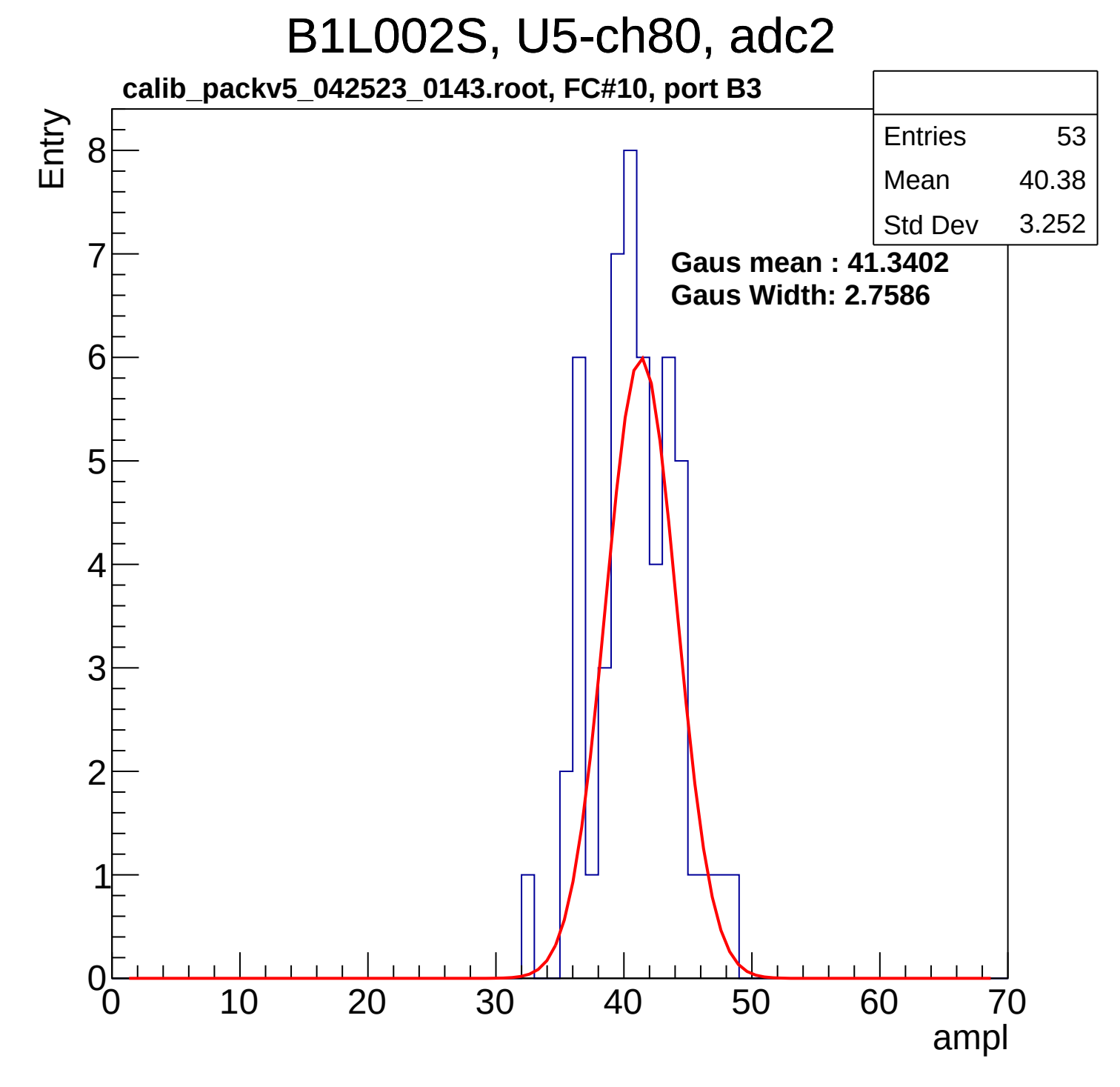
B1L002S, U5-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 40.38
Std Dev 3.252
Gaus mean : 41.3402
Gaus Width: 2.7586
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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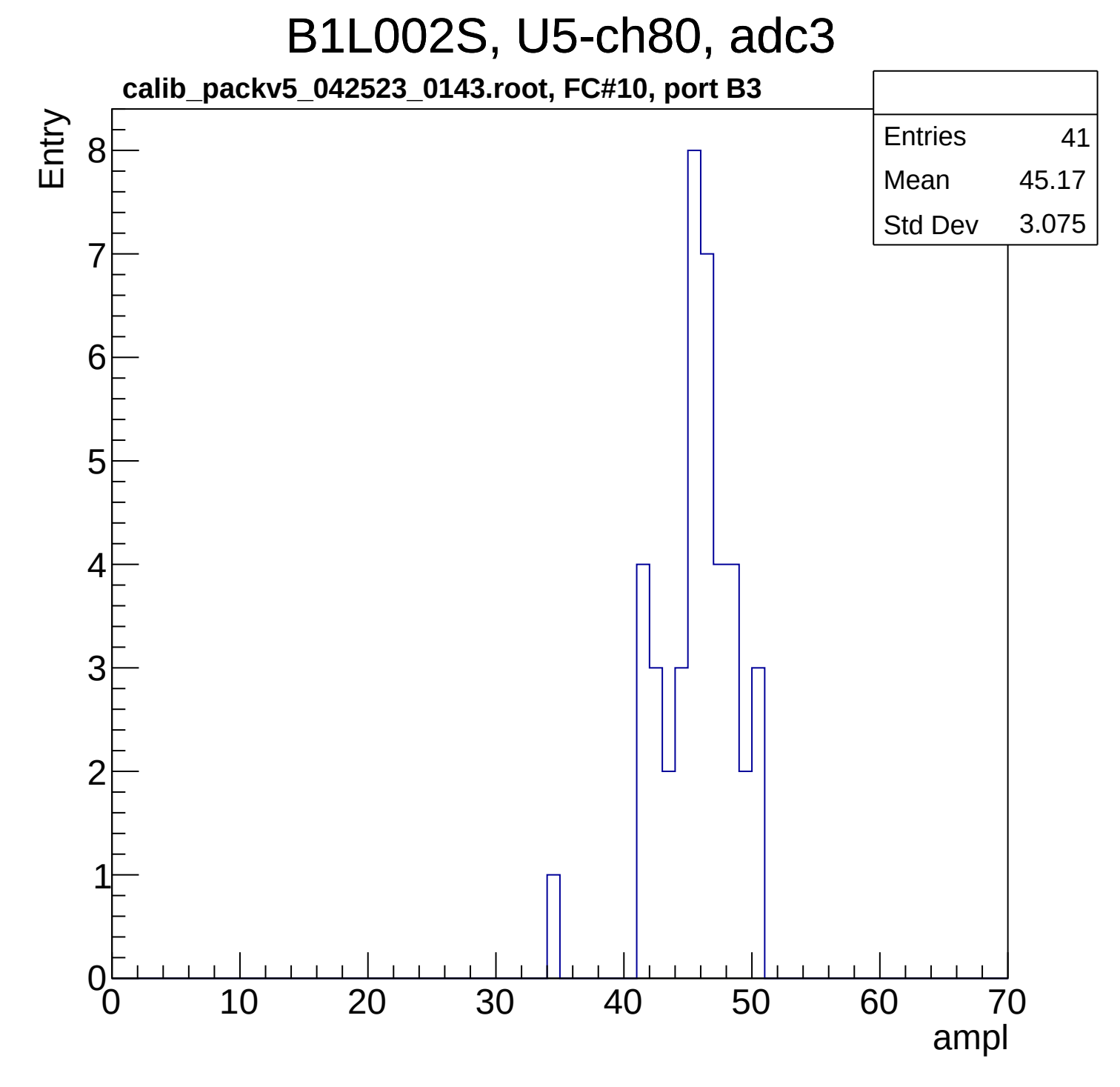
B1L002S, U5-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 45.17
Std Dev 3.075
Entry
ampl
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2
3
4
5
6
7
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0
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60
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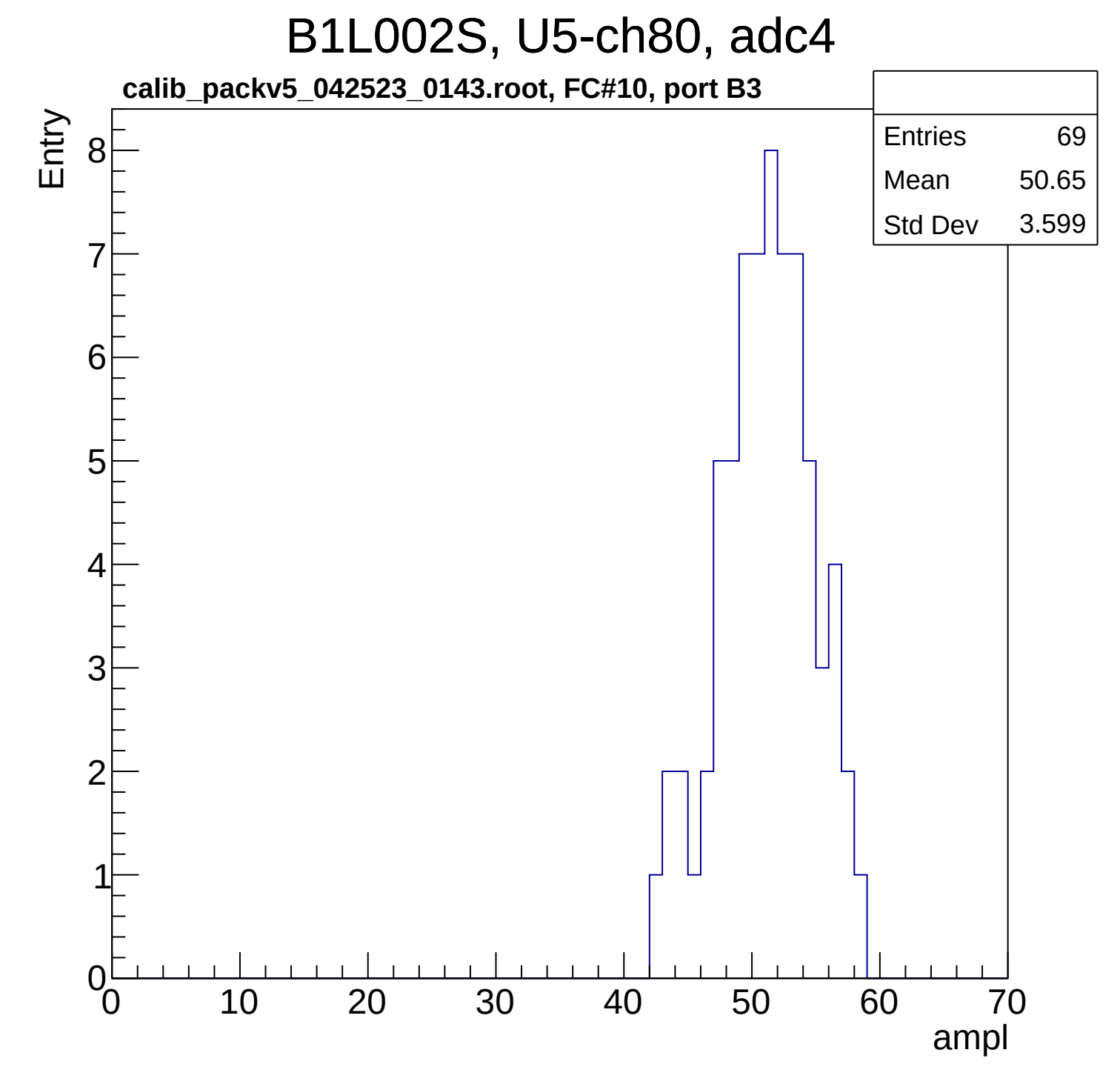
B1L002S, U5-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 50.65
Std Dev 3.599
Entry
ampl
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3
4
5
6
7
8
0
10
20
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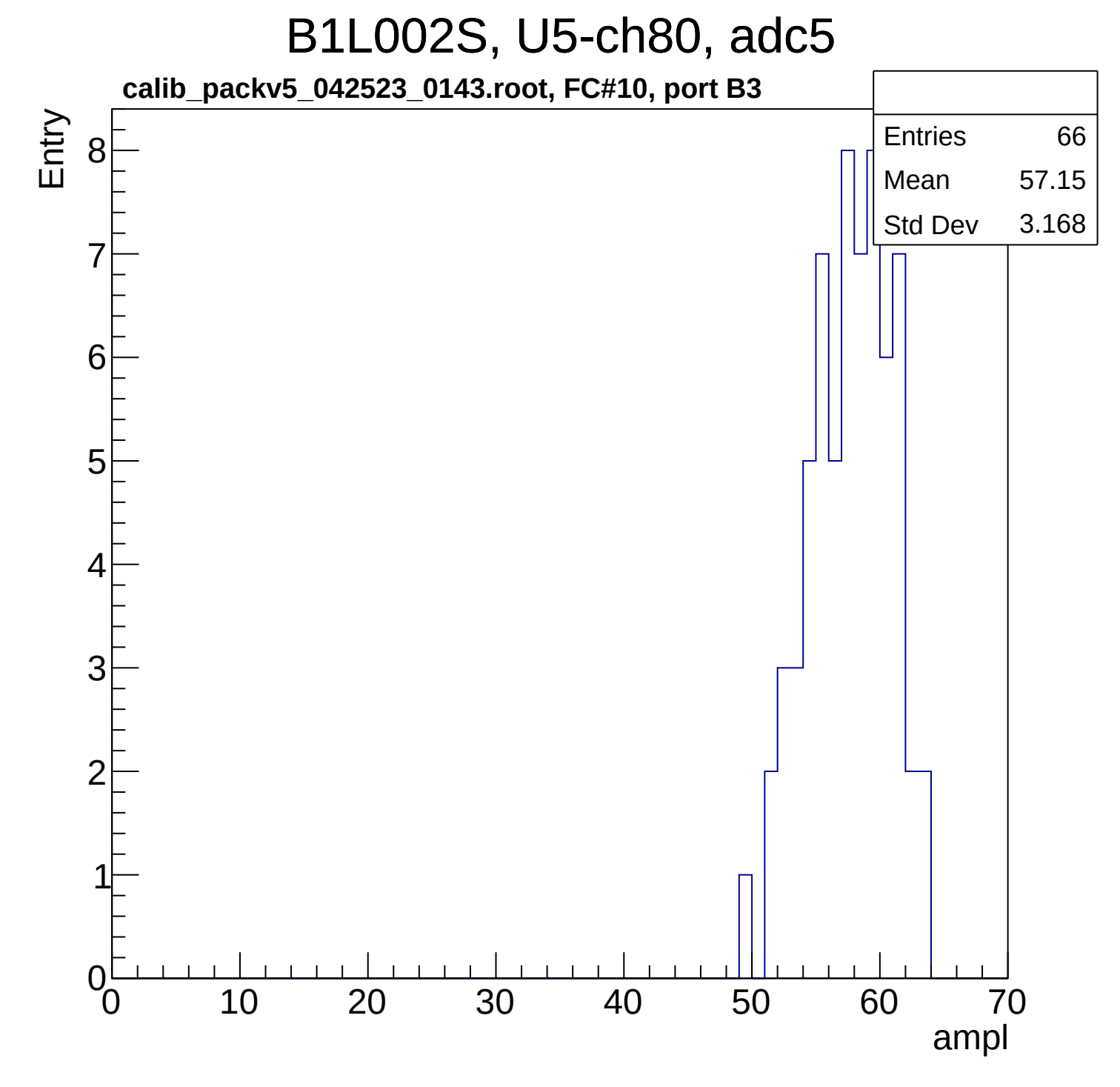
40
50
60
70


B1L002S, U5-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.15
Std Dev 3.168
Entry
ampl
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1
2
3
4
5
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7
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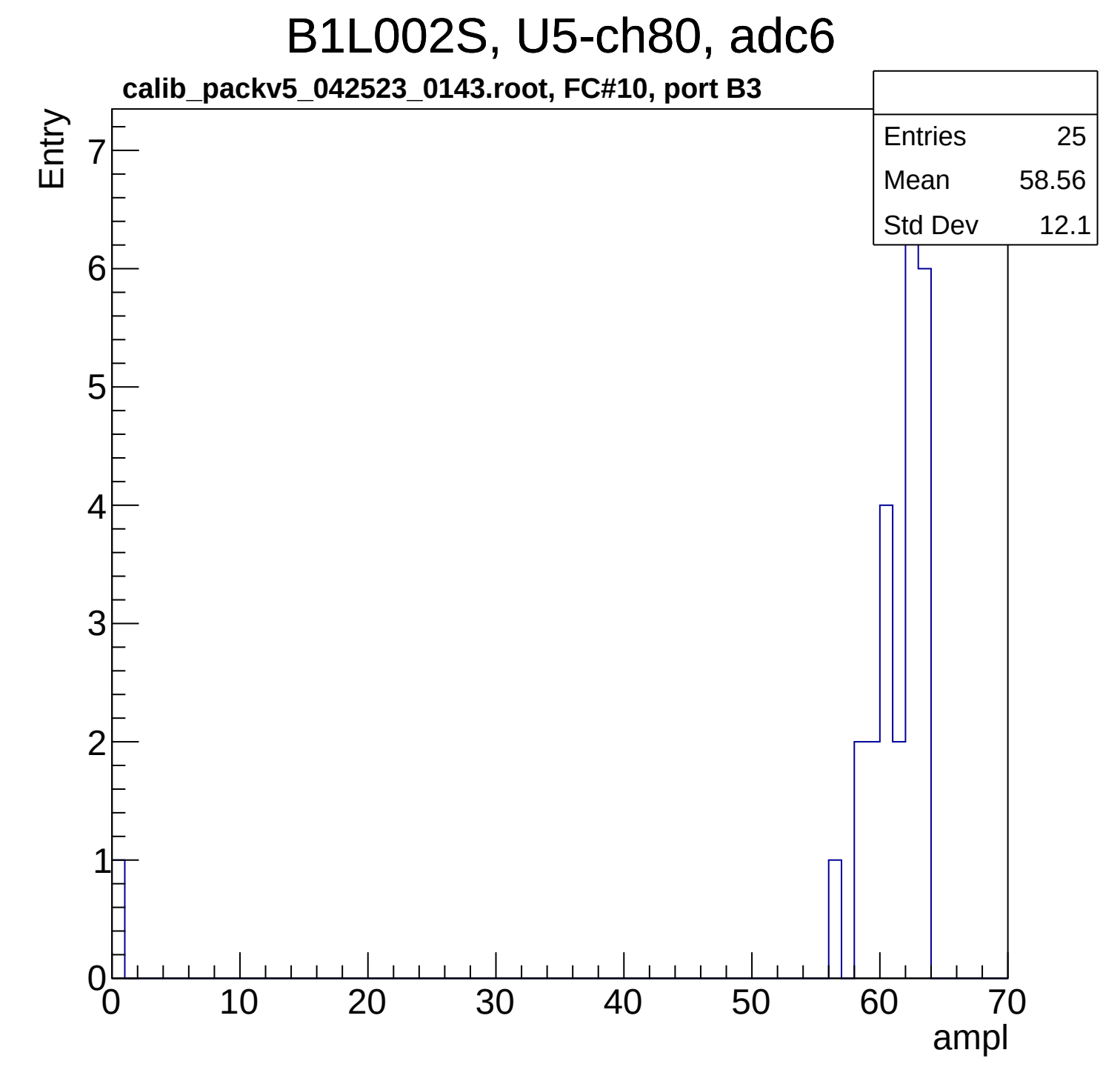
B1L002S, U5-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.56
Std Dev 12.1
Entry
ampl
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3
4
5
6
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40
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60
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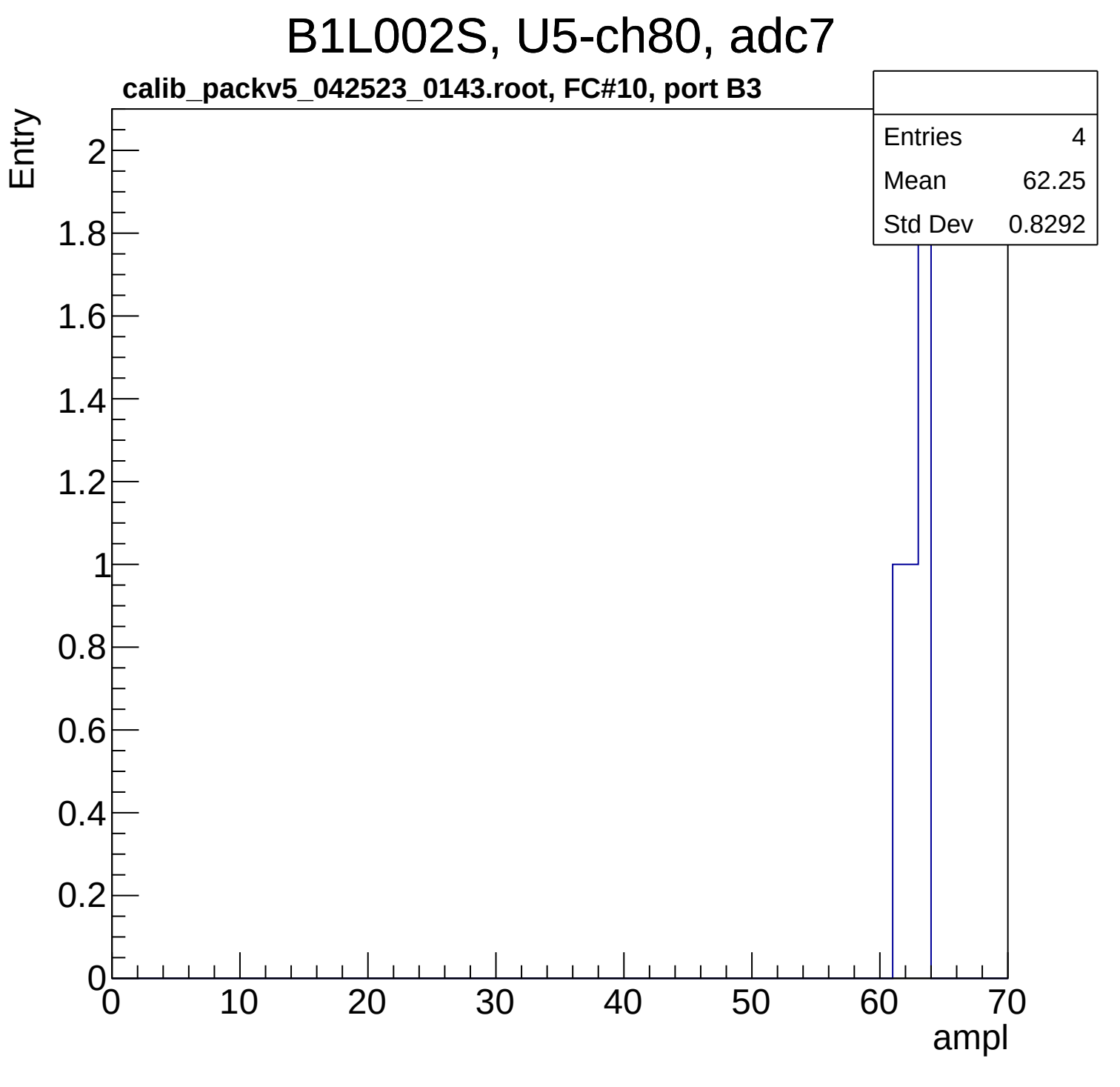
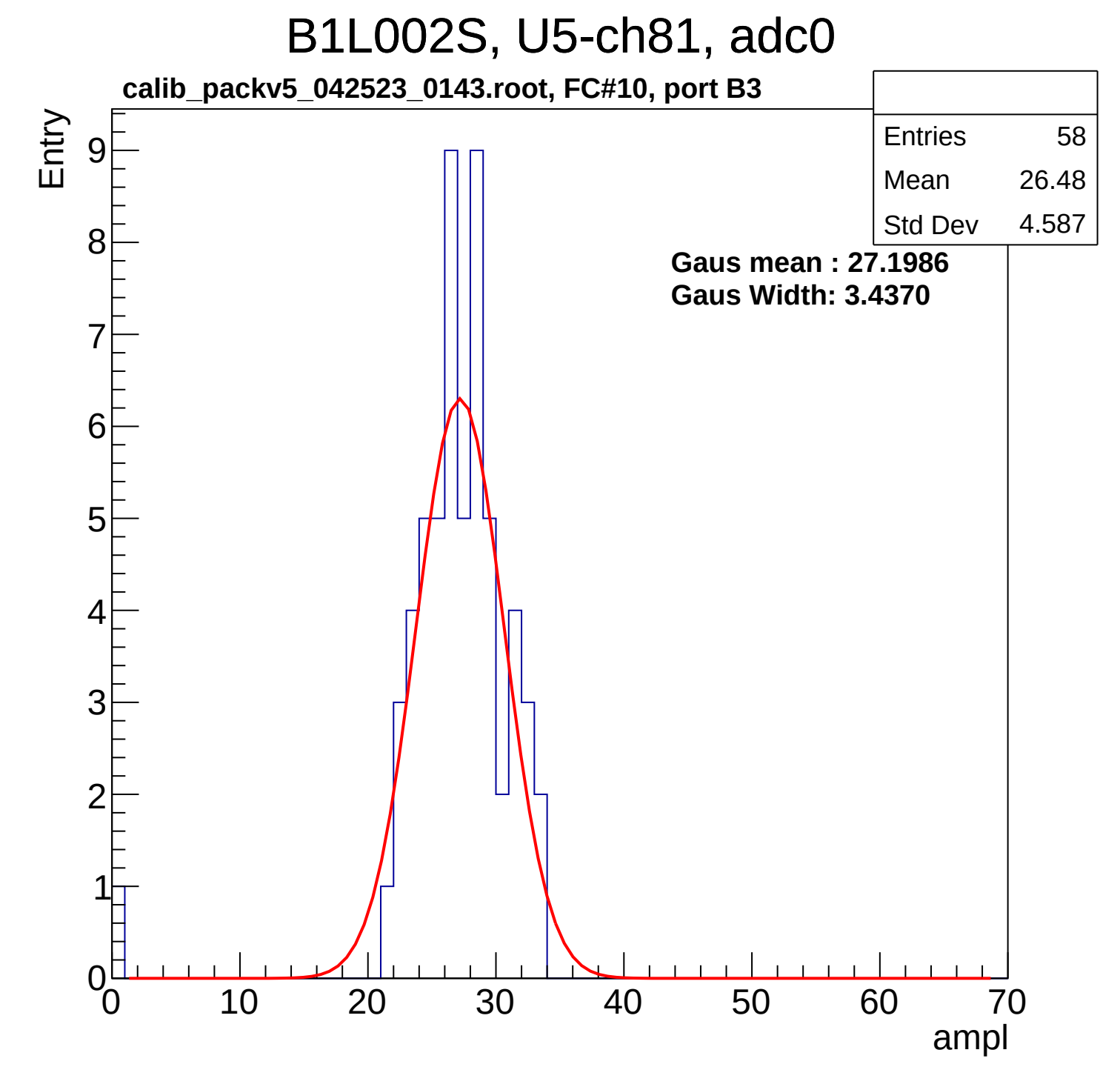

B1L002S, U5-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.48
Std Dev 4.587
Gaus mean : 27.1986
Gaus Width: 3.4370
Entry
ampl
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2
3
4
5
6
7
8
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0
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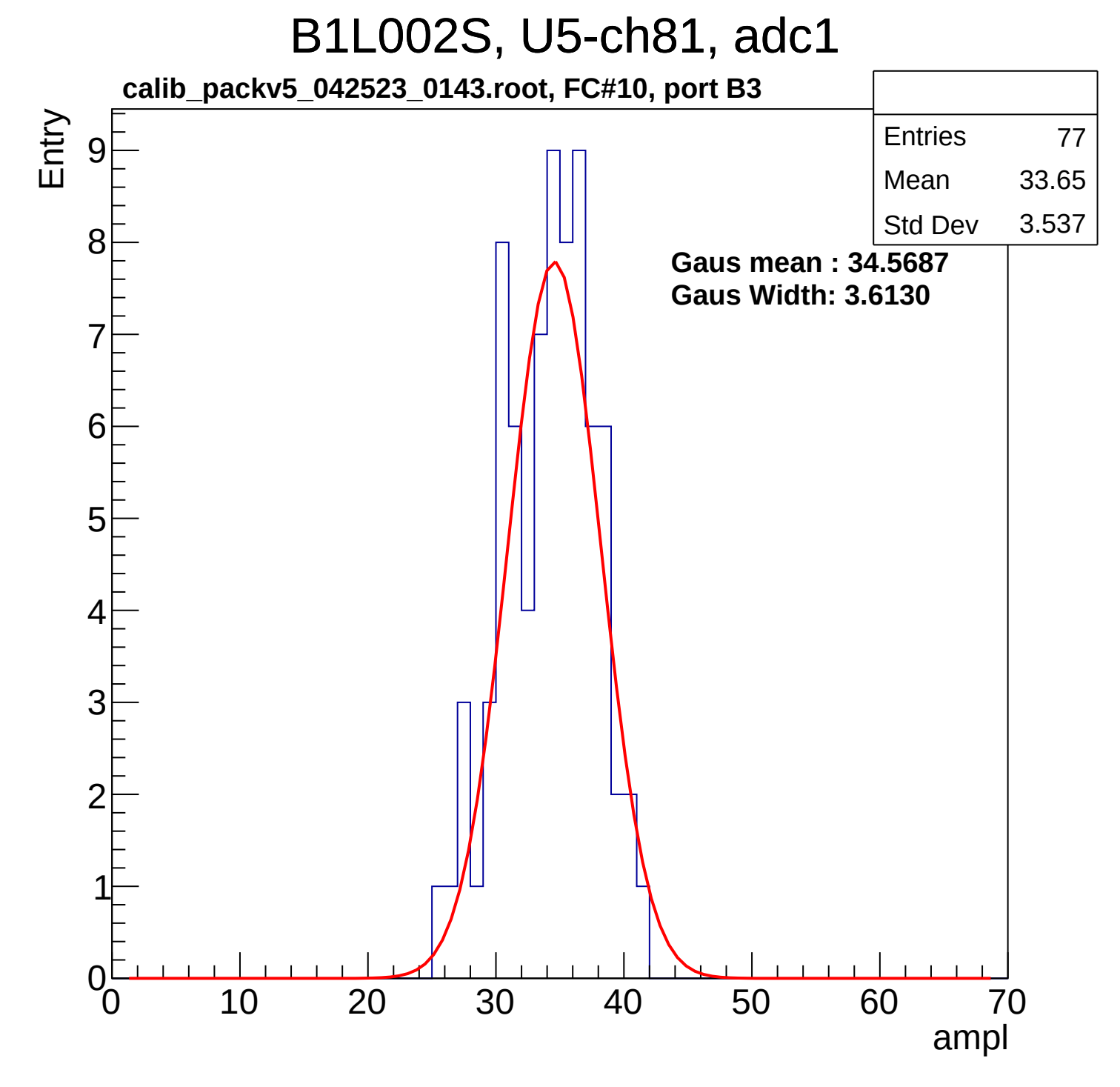
B1L002S, U5-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 33.65
Std Dev 3.537
Gaus mean : 34.5687
Gaus Width: 3.6130
Entry
ampl
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2
3
4
5
6
7
8
9
0
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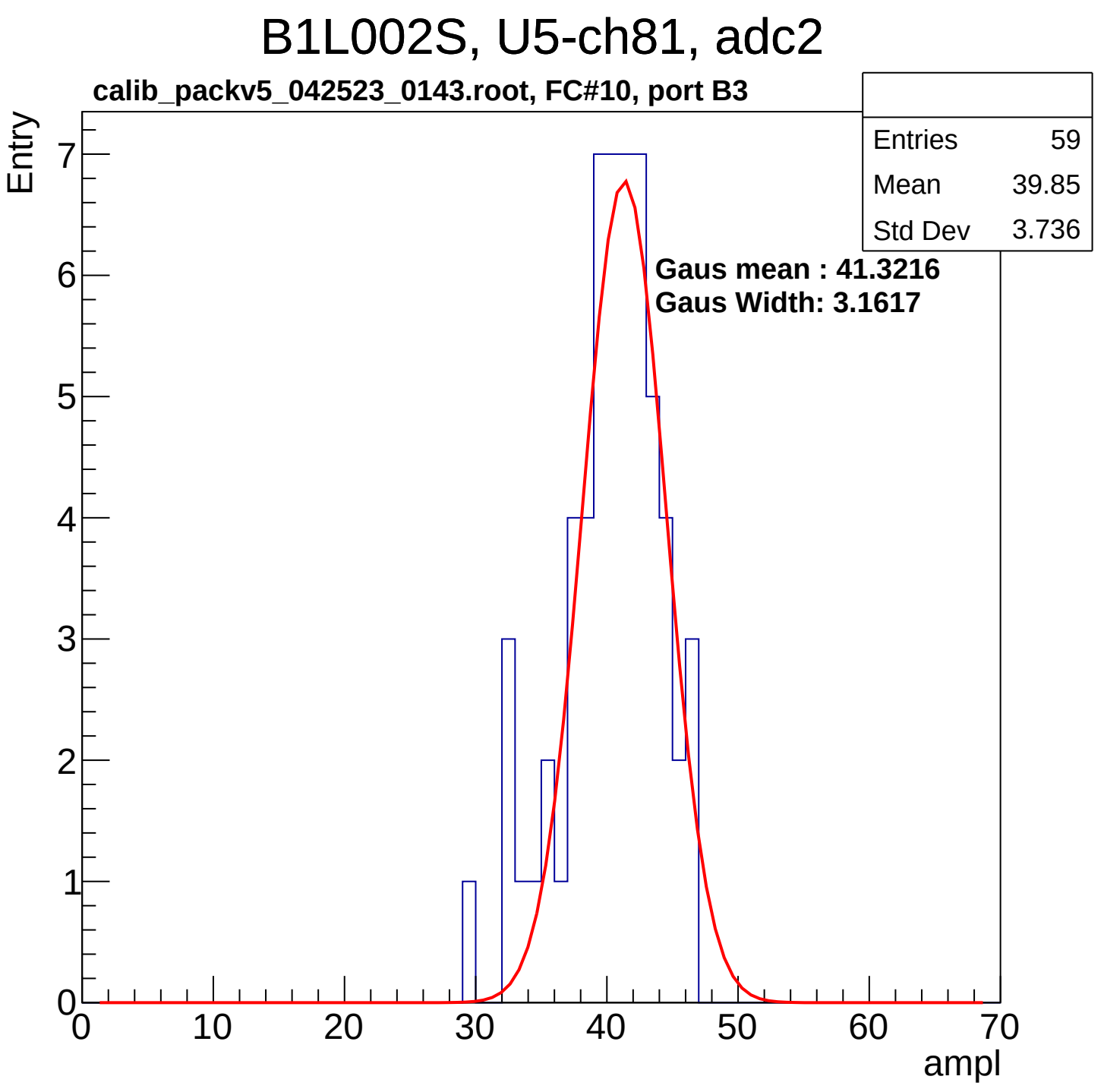
B1L002S, U5-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.85
Std Dev 3.736
Gaus mean : 41.3216
Gaus Width: 3.1617
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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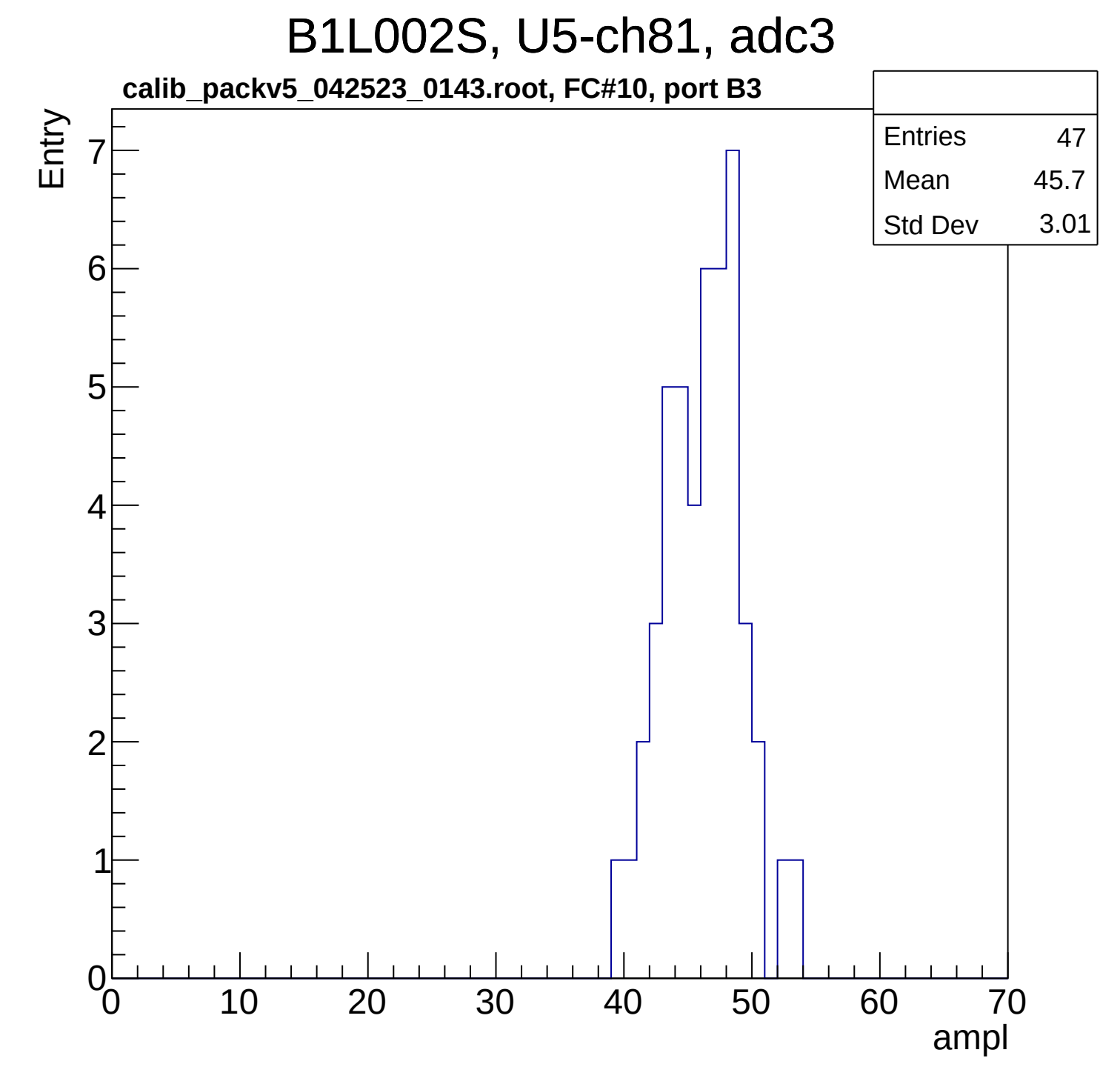
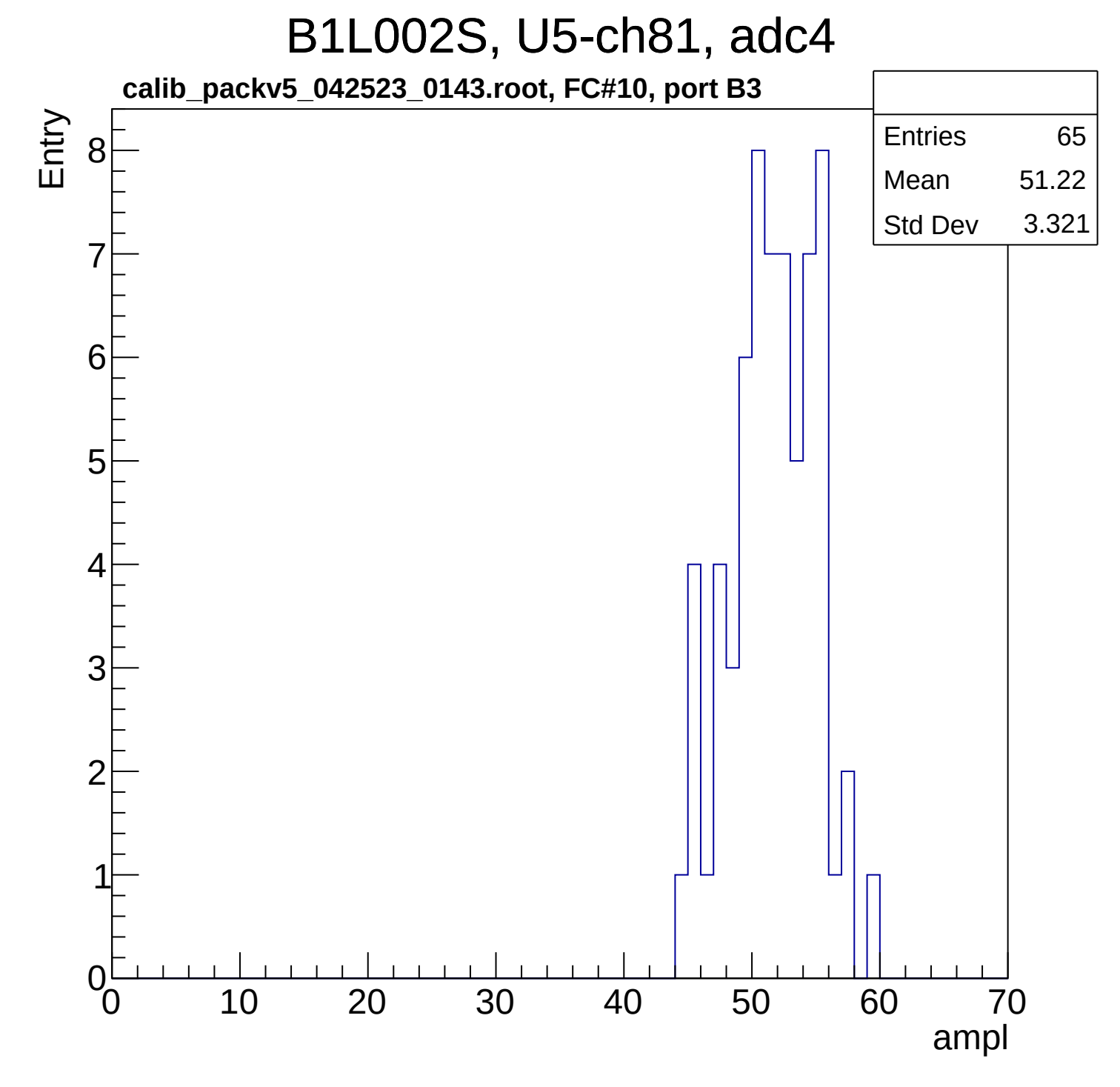

B1L002S, U5-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 45.7
Std Dev 3.01
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 51.22
Std Dev 3.321
Entry
ampl
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2
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4
5
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7
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0
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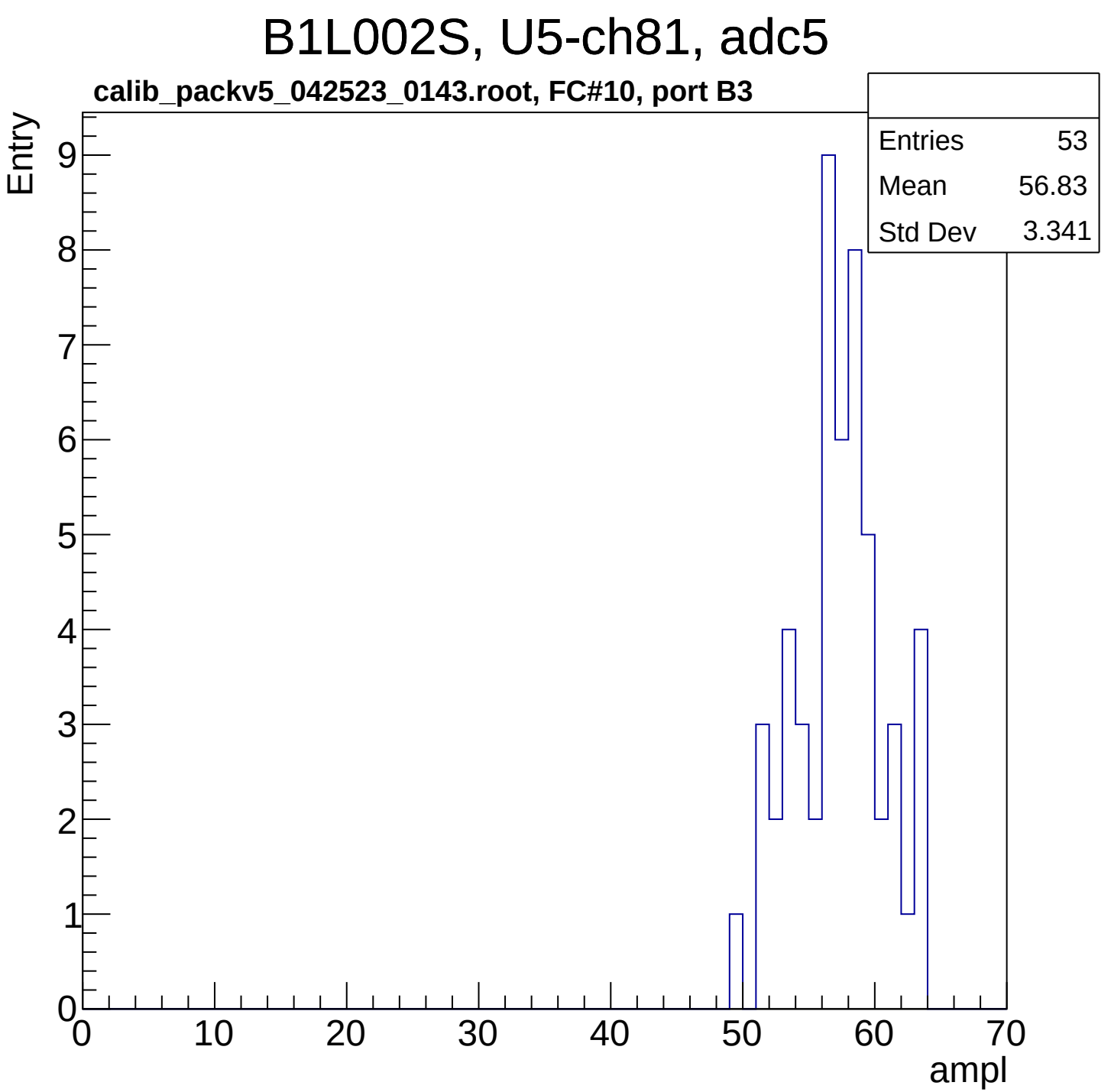
B1L002S, U5-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.83
Std Dev 3.341
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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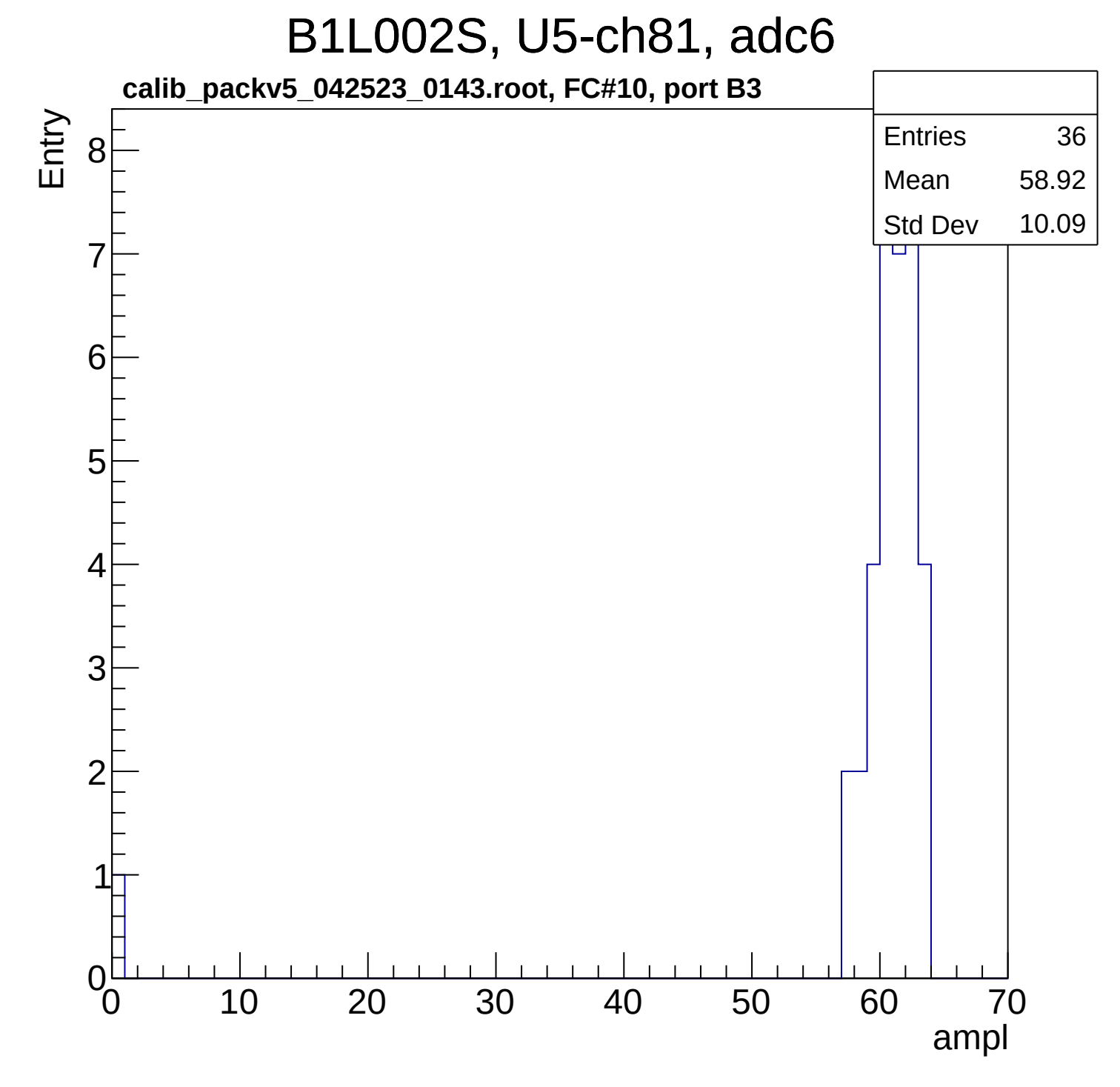
B1L002S, U5-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 58.92
Std Dev 10.09
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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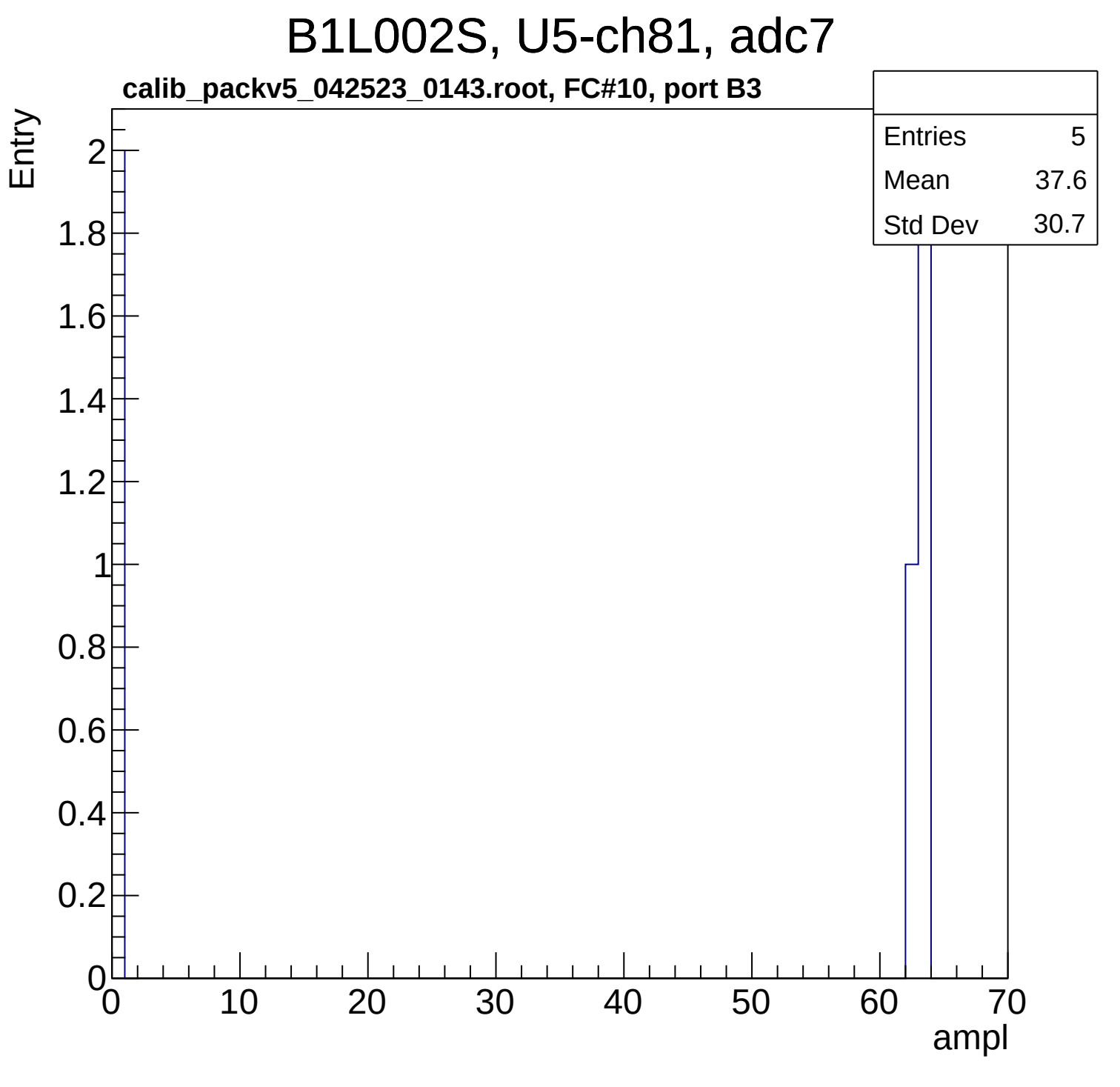
B1L002S, U5-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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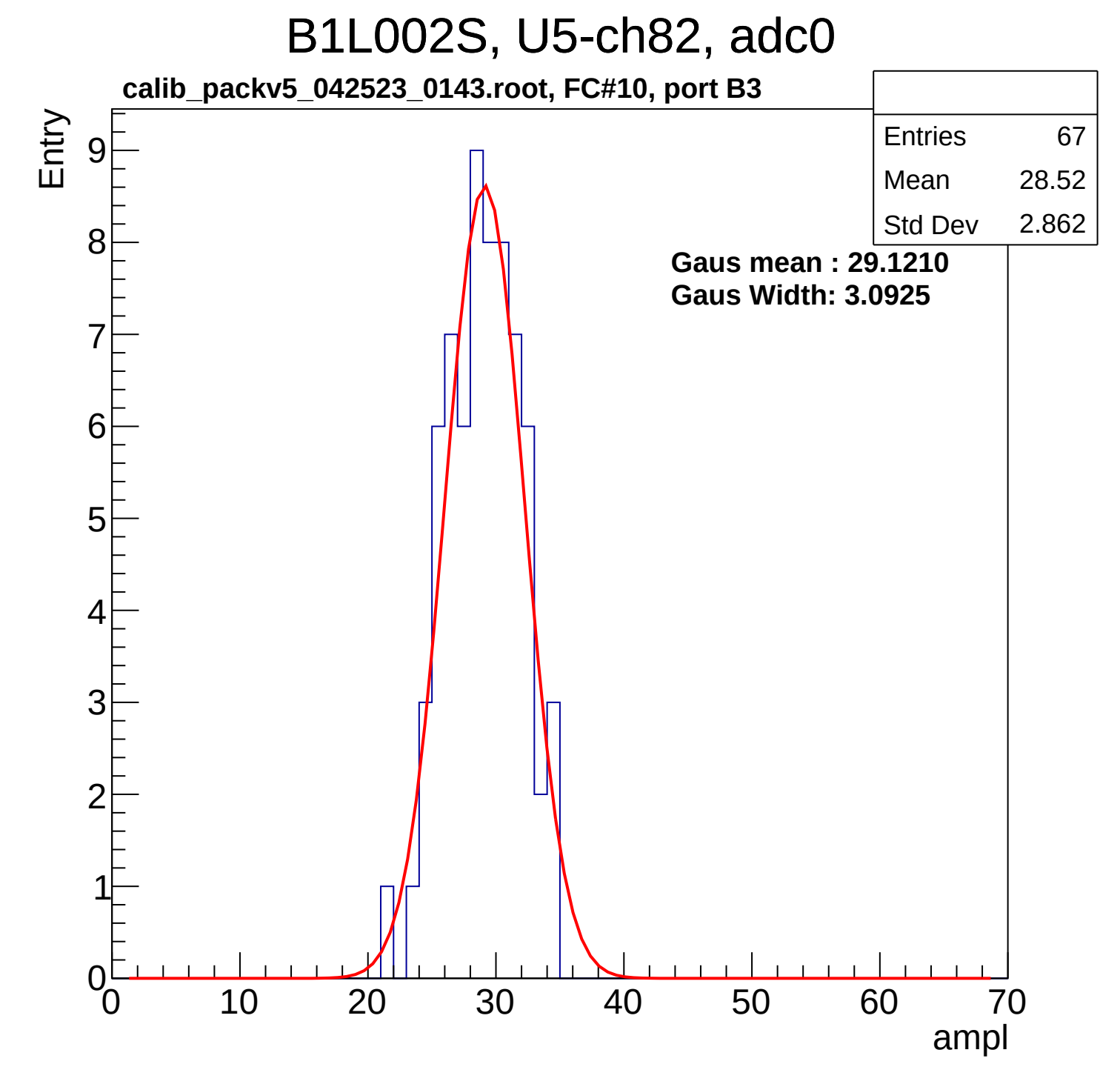
B1L002S, U5-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.52
Std Dev 2.862
Gaus mean : 29.1210
Gaus Width: 3.0925
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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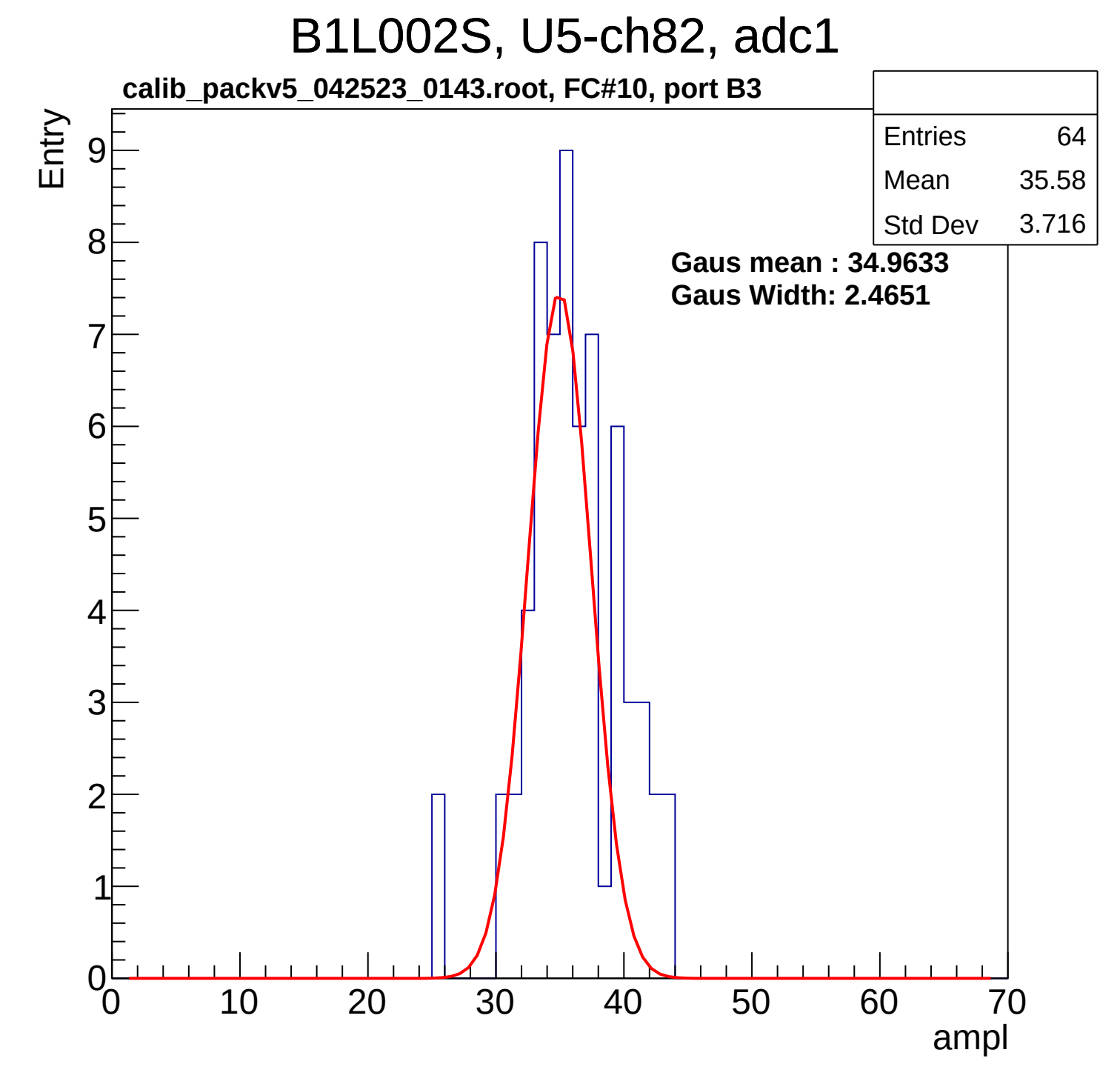
B1L002S, U5-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.58
Std Dev 3.716
Gaus mean : 34.9633
Gaus Width: 2.4651
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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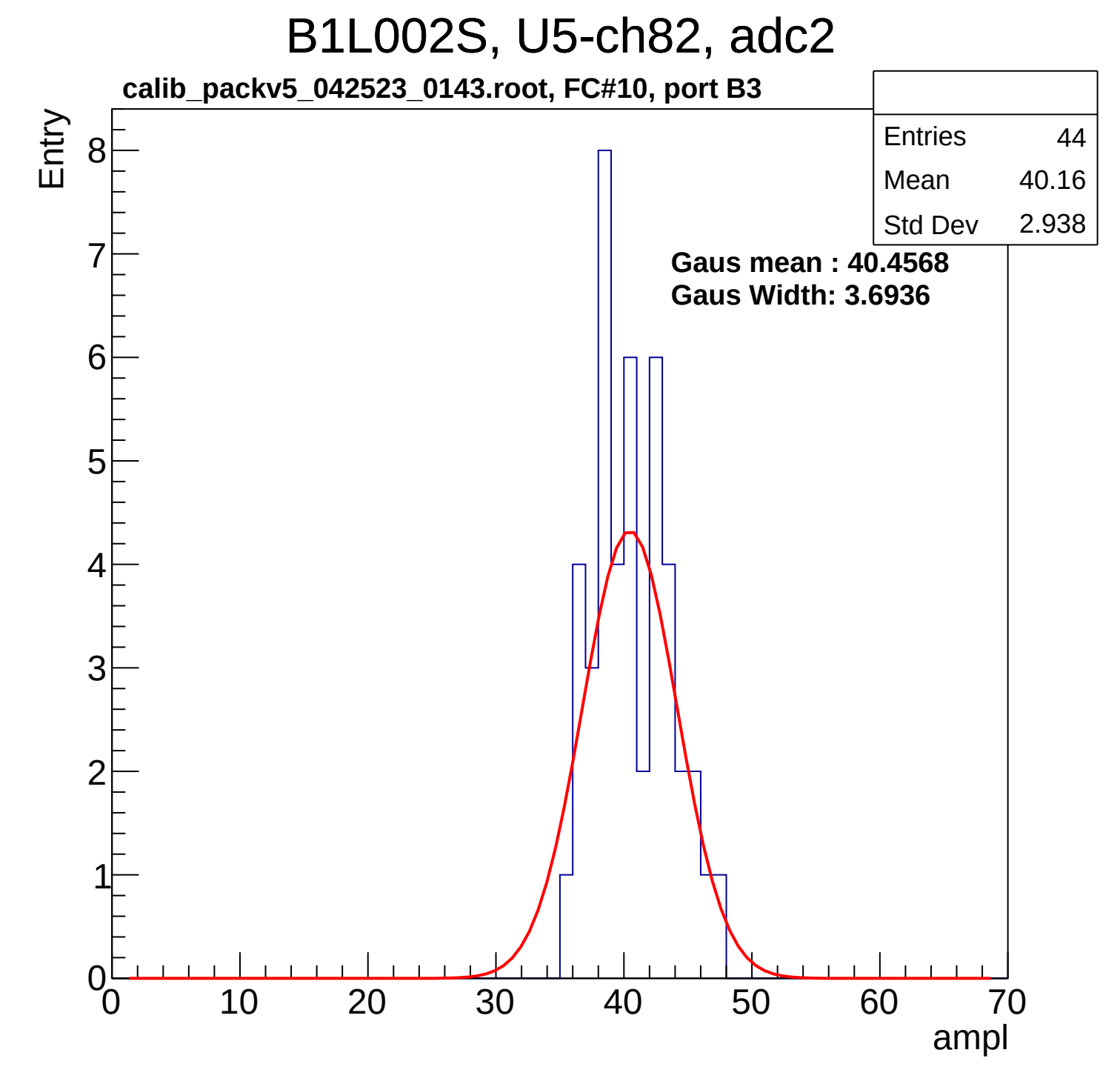
B1L002S, U5-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 40.16
Std Dev 2.938
Gaus mean : 40.4568
Gaus Width: 3.6936
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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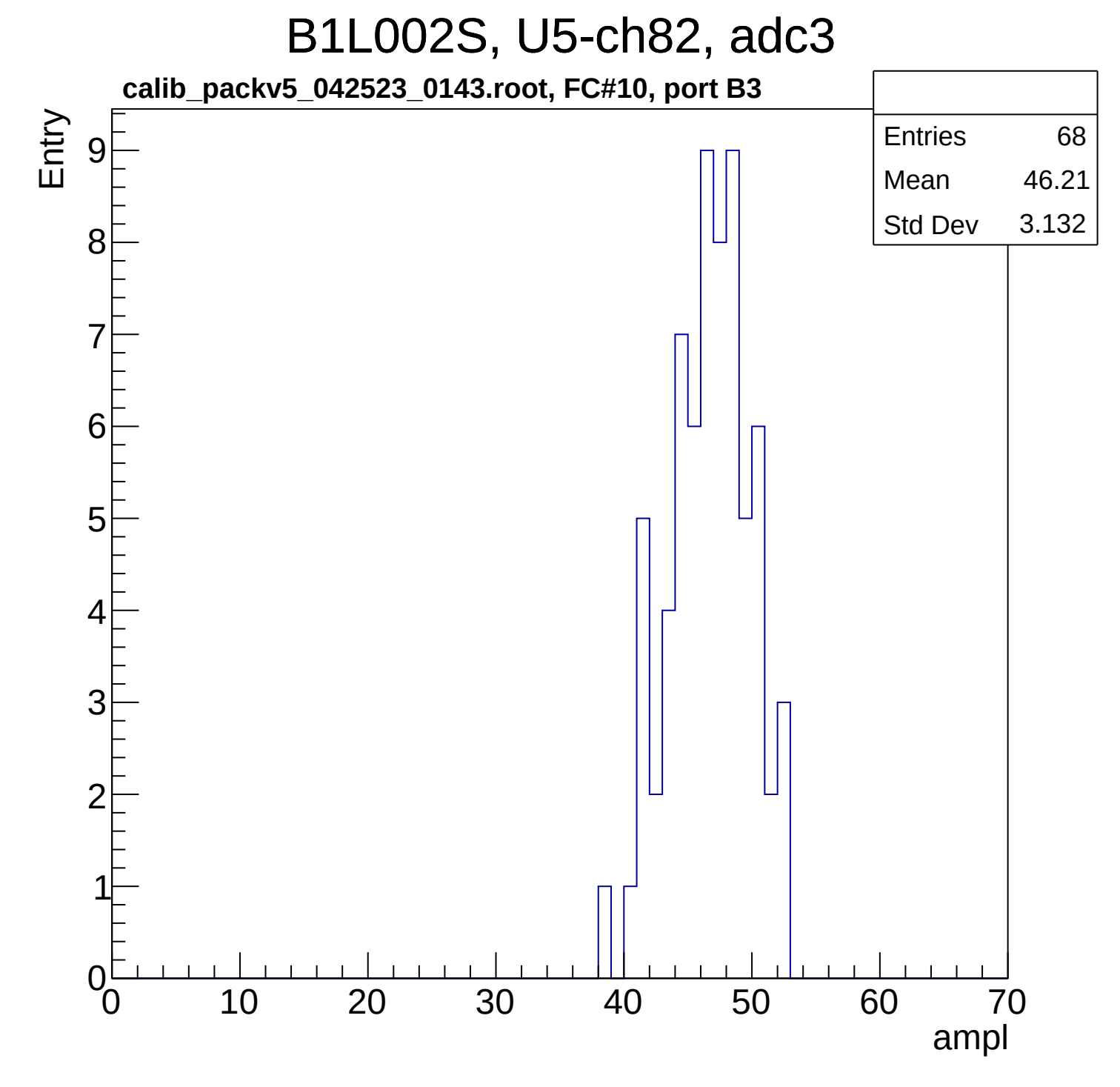
B1L002S, U5-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.21
Std Dev 3.132
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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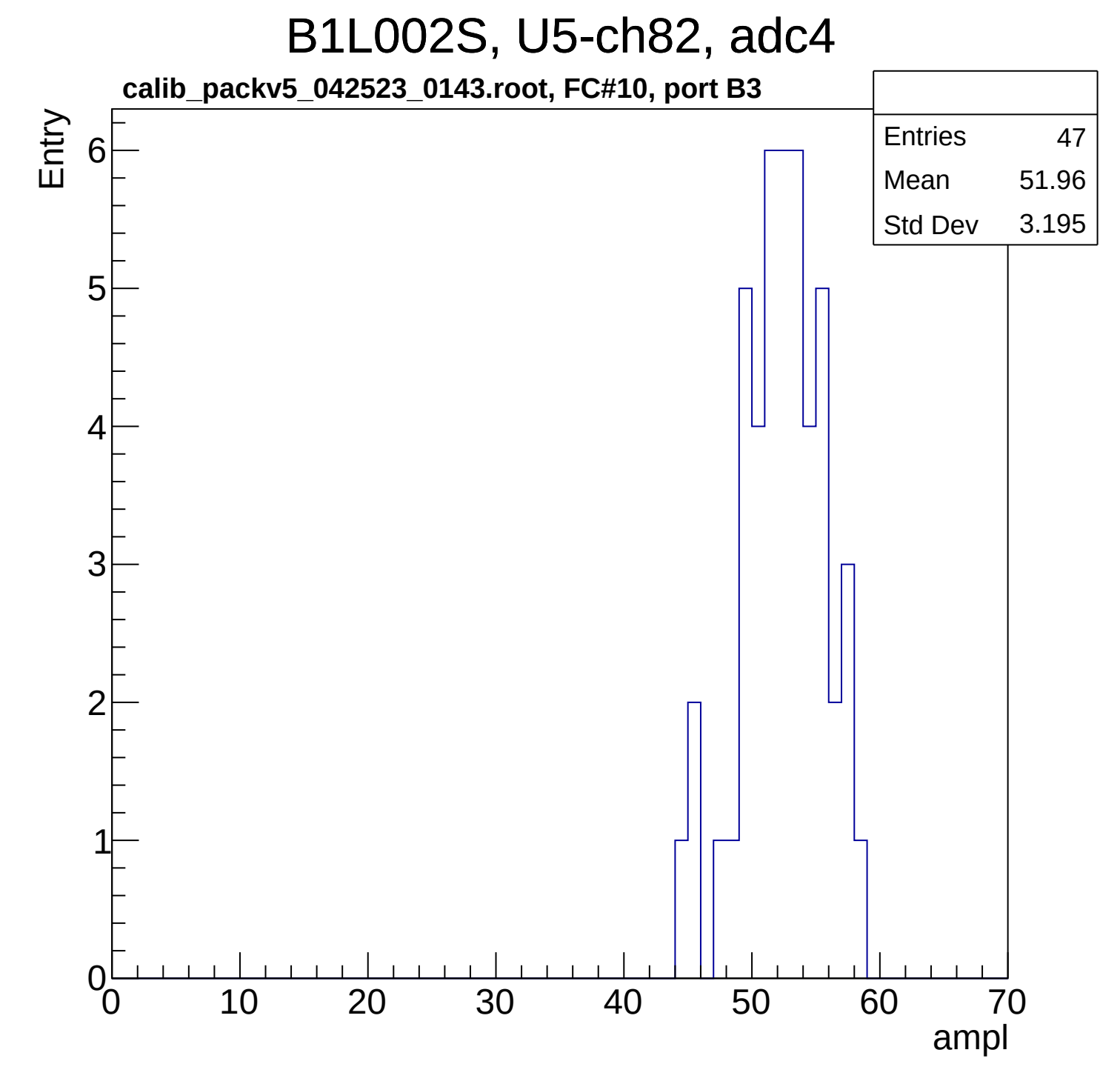
B1L002S, U5-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 51.96
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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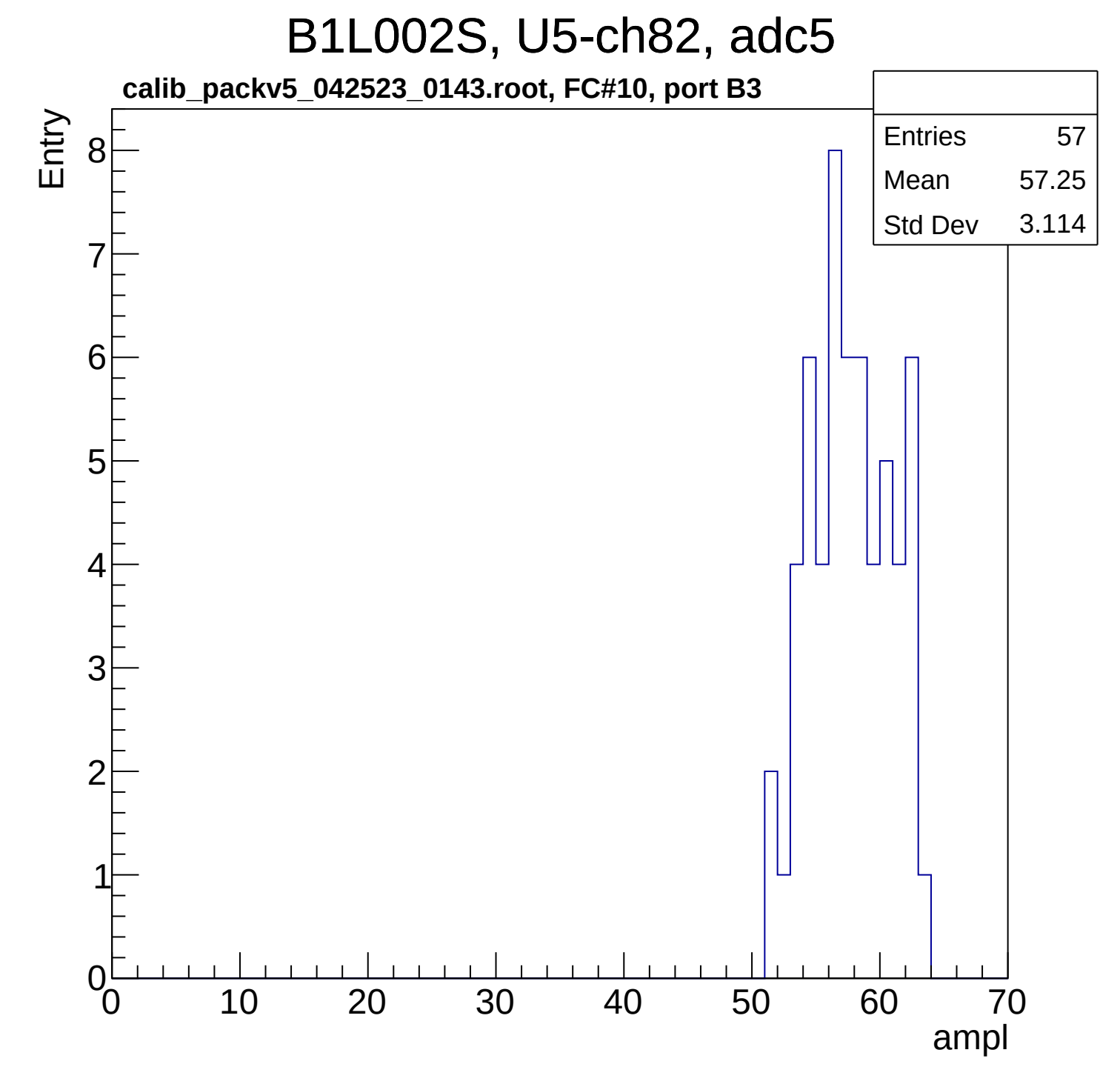
B1L002S, U5-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.25
Std Dev 3.114
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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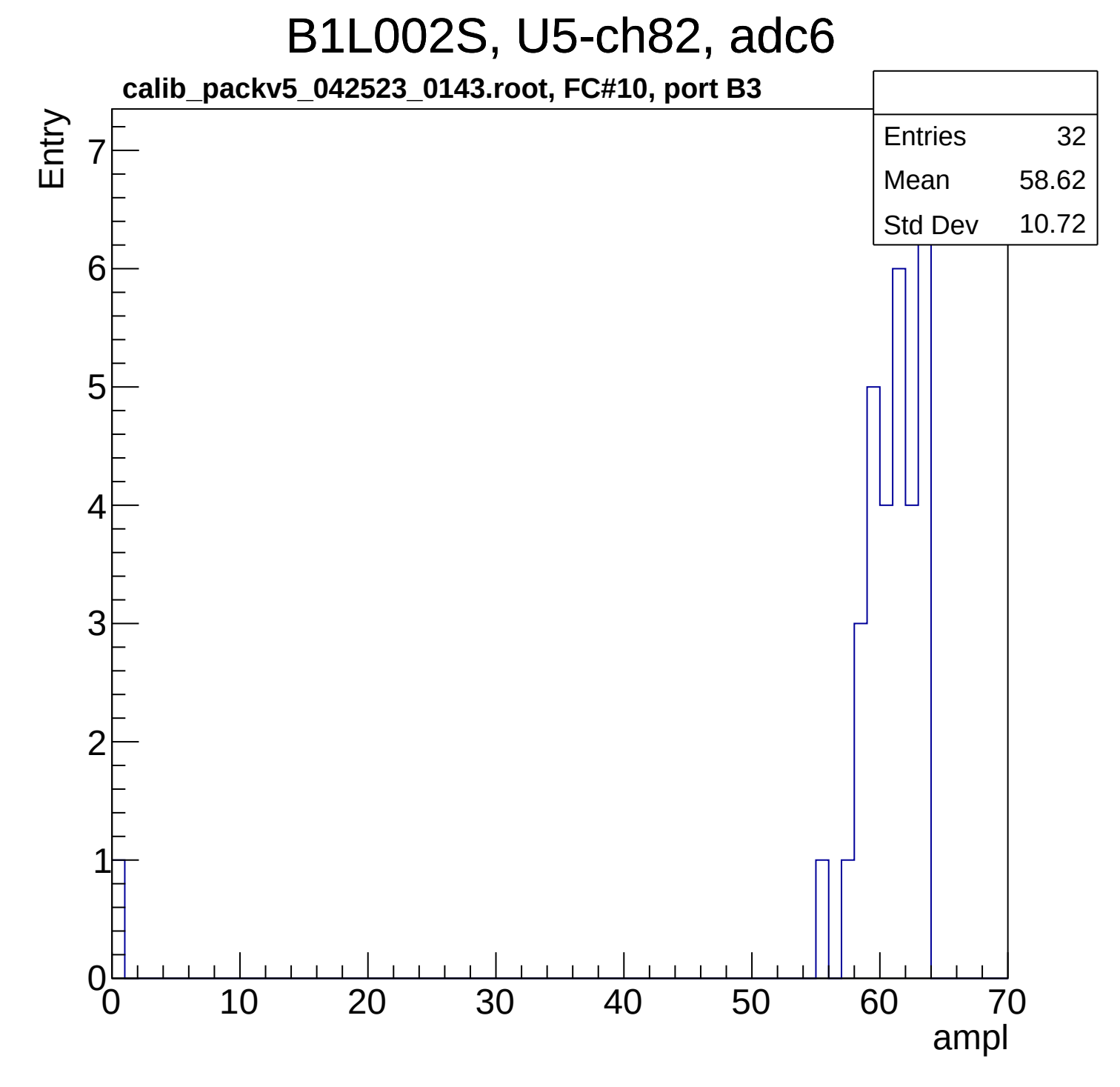
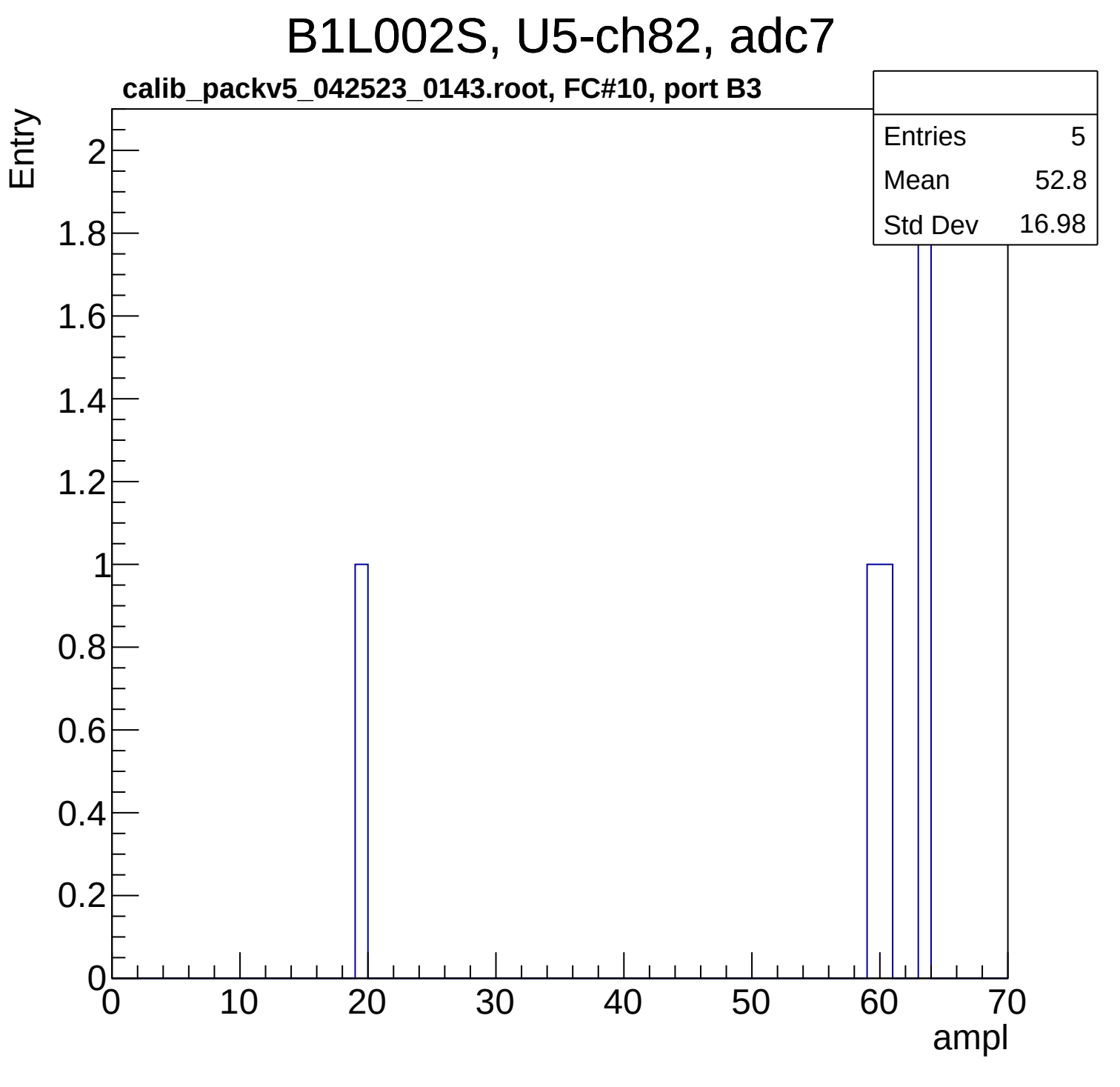

B1L002S, U5-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.62
Std Dev 10.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 52.8
Std Dev 16.98
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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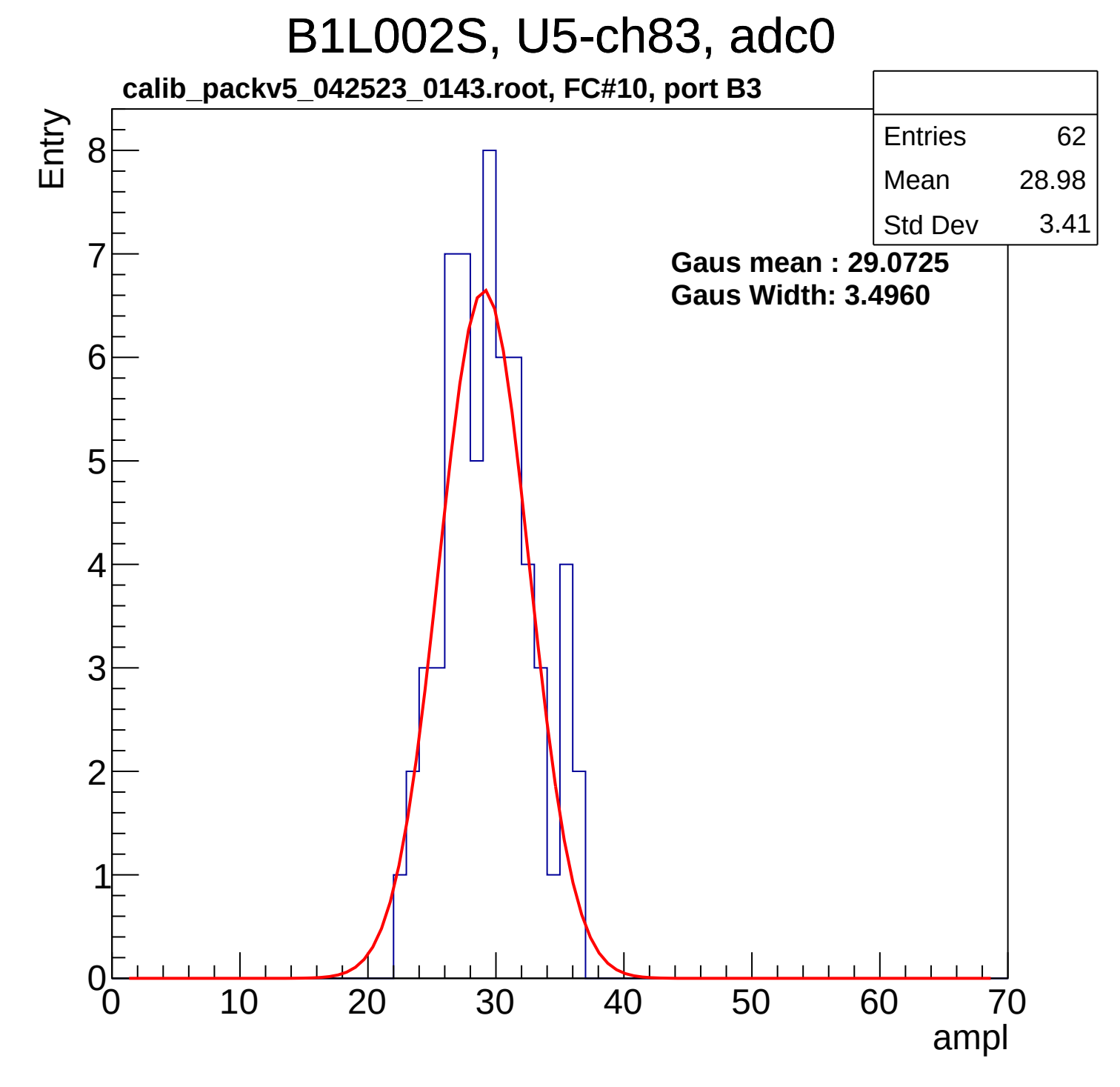
B1L002S, U5-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.98
Std Dev 3.41
Gaus mean : 29.0725
Gaus Width: 3.4960
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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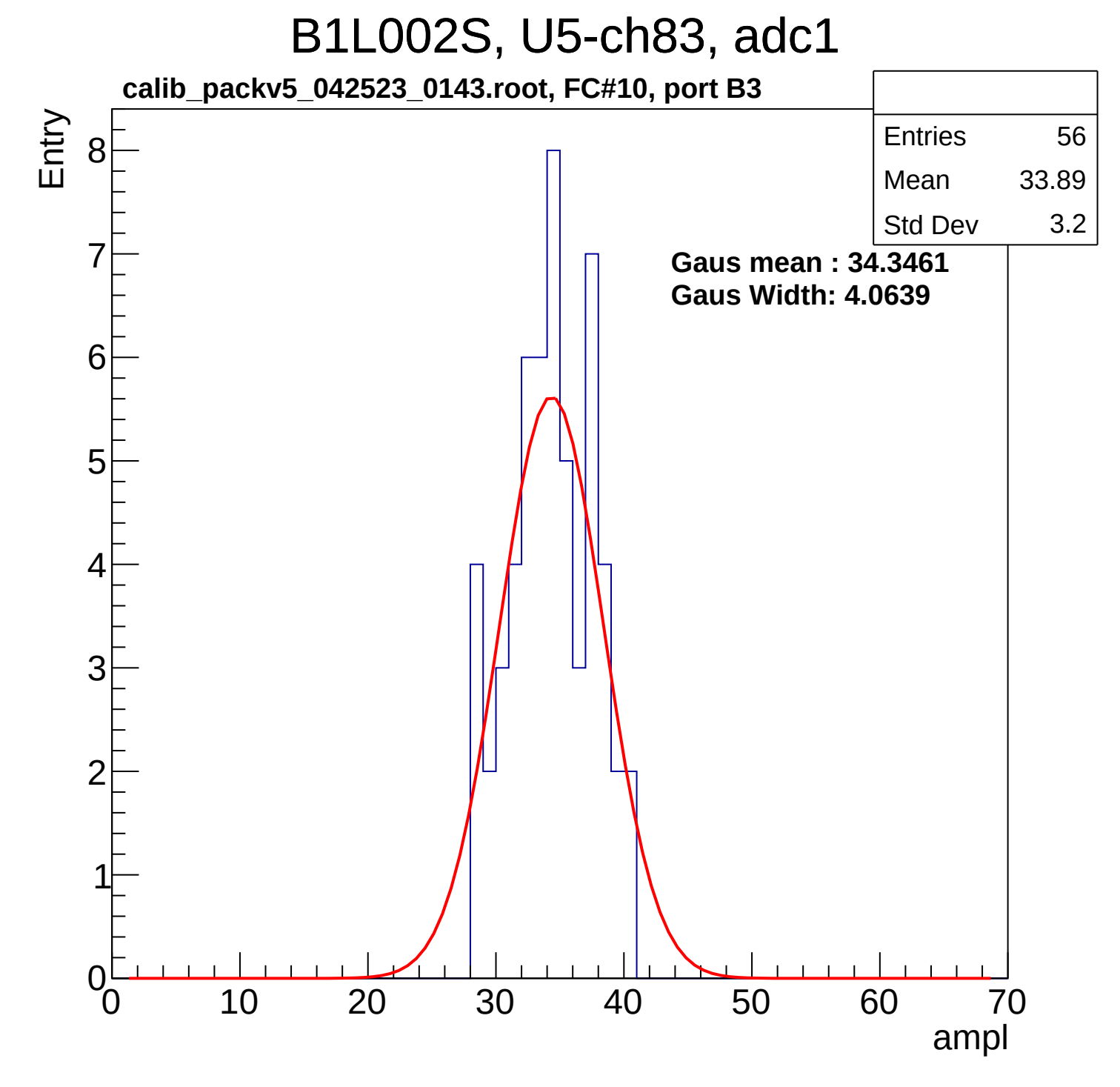
B1L002S, U5-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.89
Std Dev 3.2
Gaus mean : 34.3461
Gaus Width: 4.0639
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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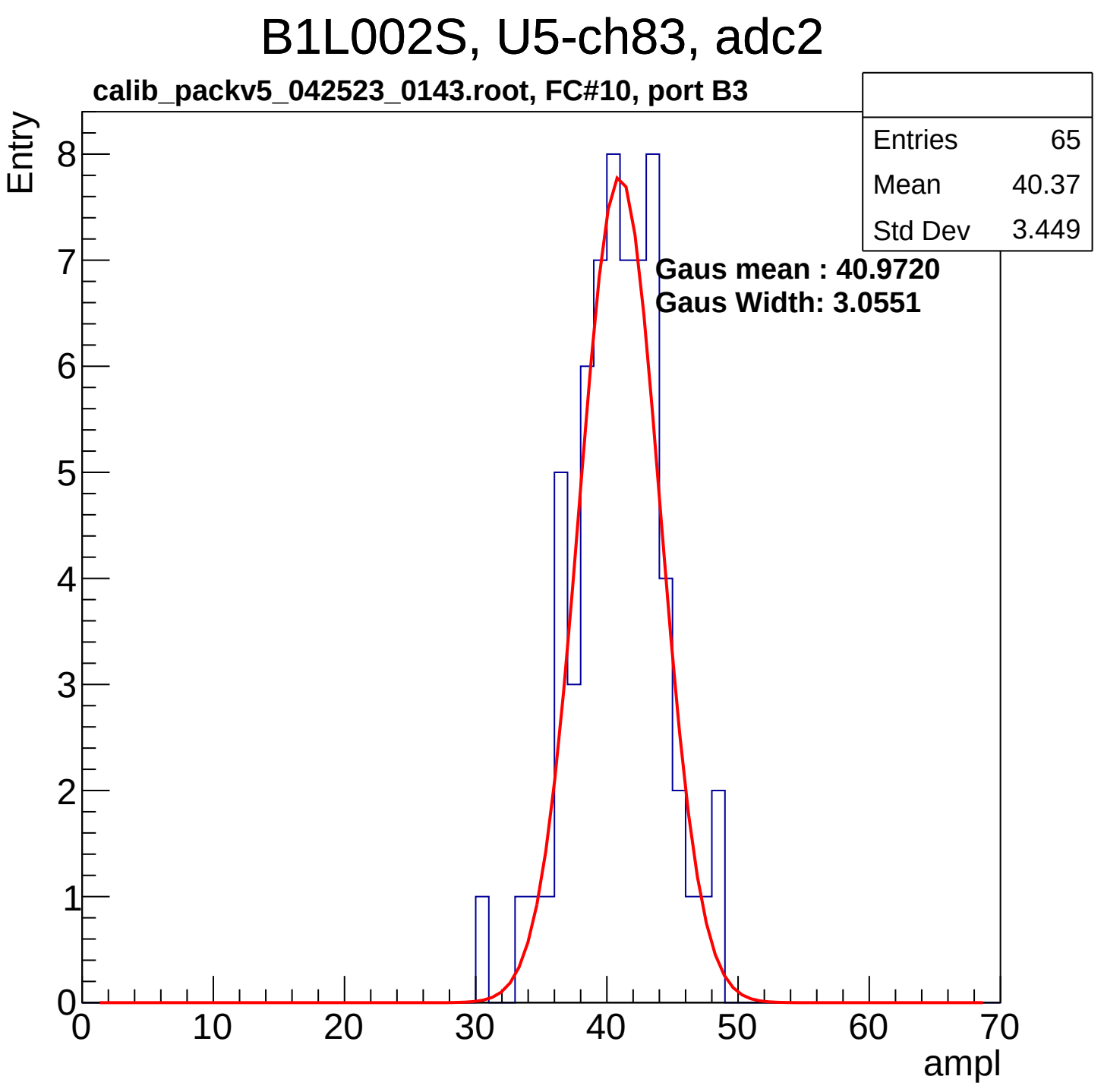
B1L002S, U5-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.37
Std Dev 3.449
Gaus mean : 40.9720
Gaus Width: 3.0551
Entry
ampl
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2
3
4
5
6
7
8
0
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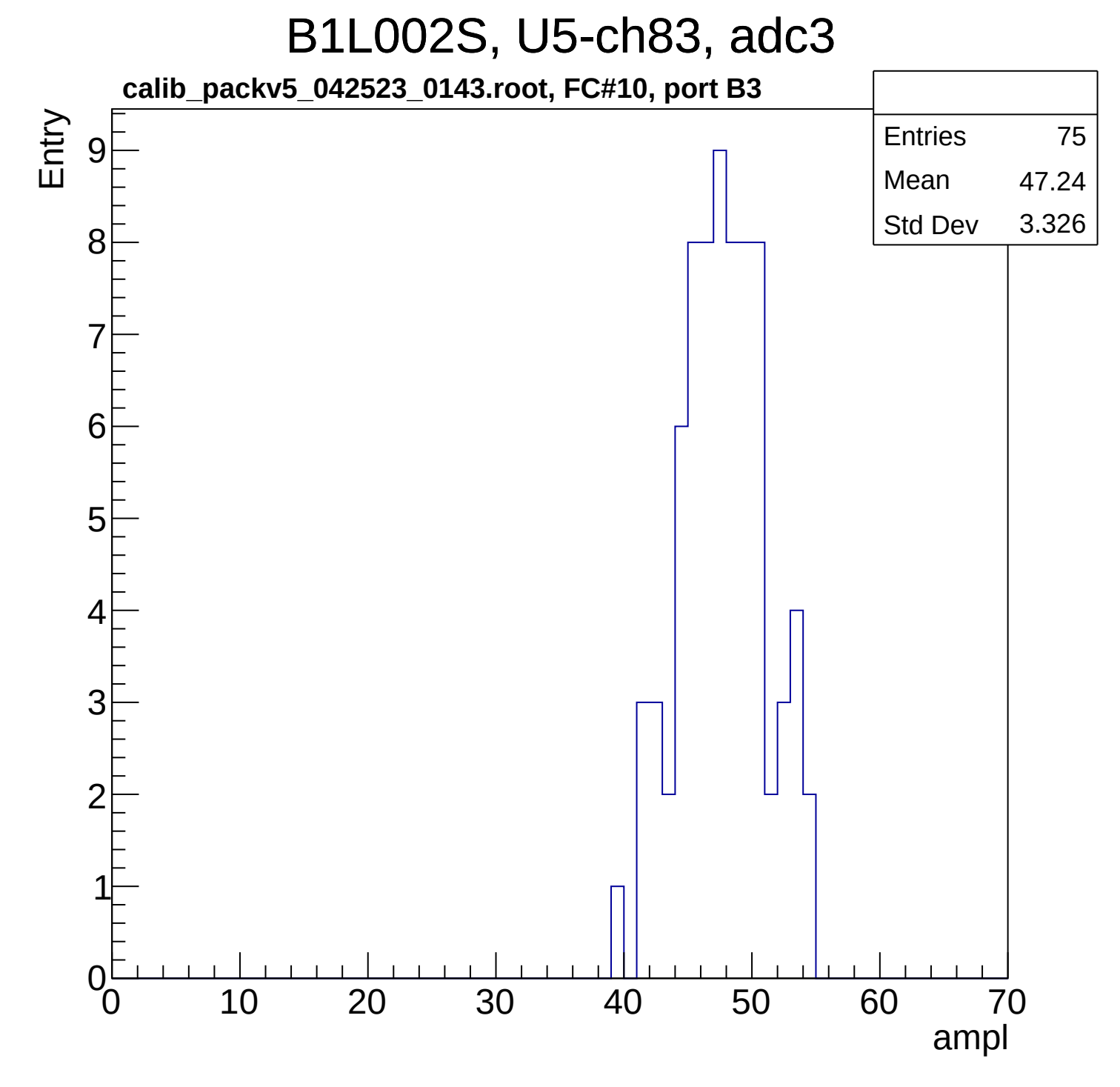
B1L002S, U5-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 47.24
Std Dev 3.326
Entry
ampl
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4
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6
7
8
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0
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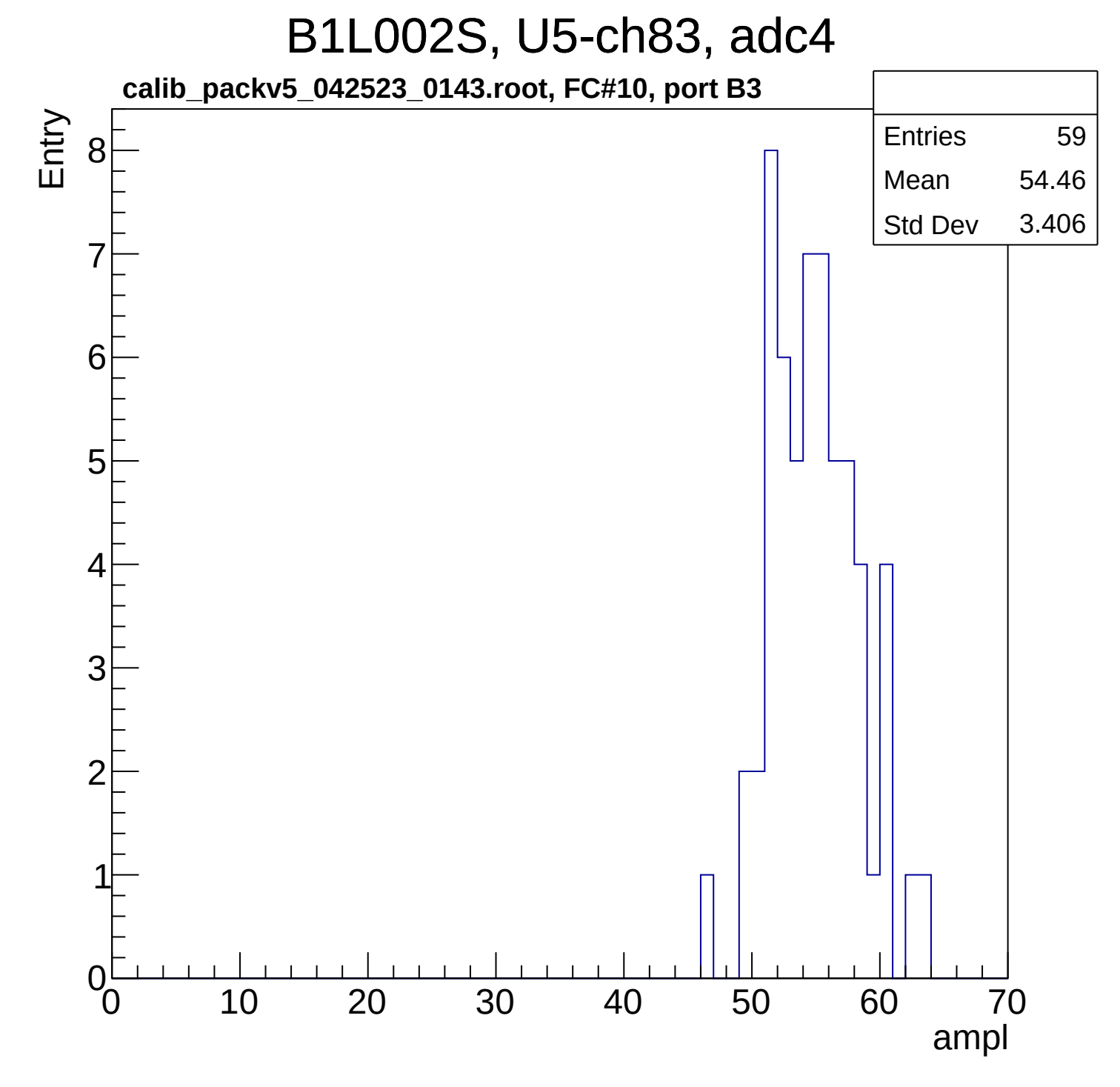
B1L002S, U5-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.46
Std Dev 3.406
Entry
ampl
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4
5
6
7
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0
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20
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40
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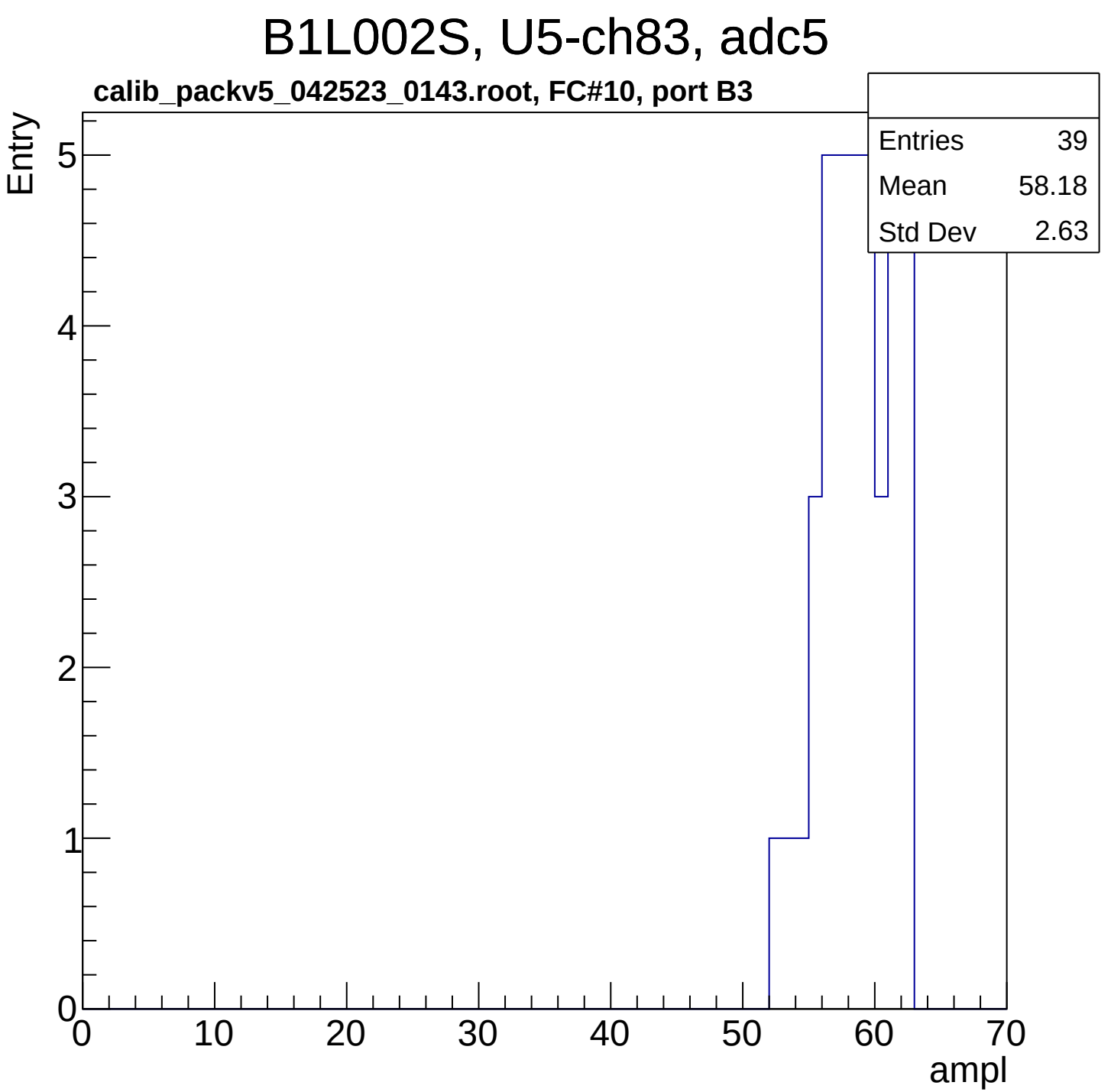
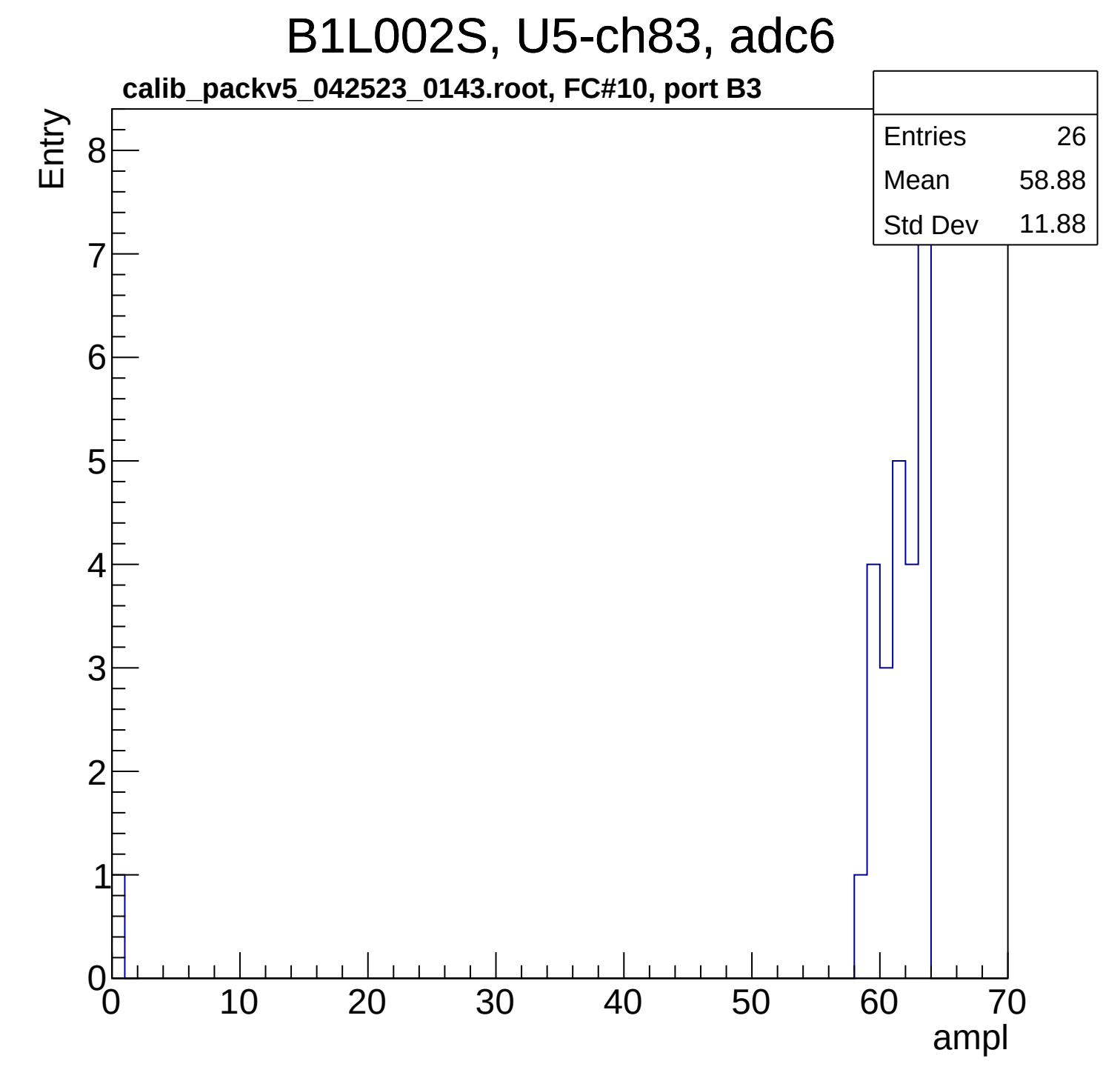

B1L002S, U5-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 58.18
Std Dev 2.63
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.88
Std Dev 11.88
Entry
ampl
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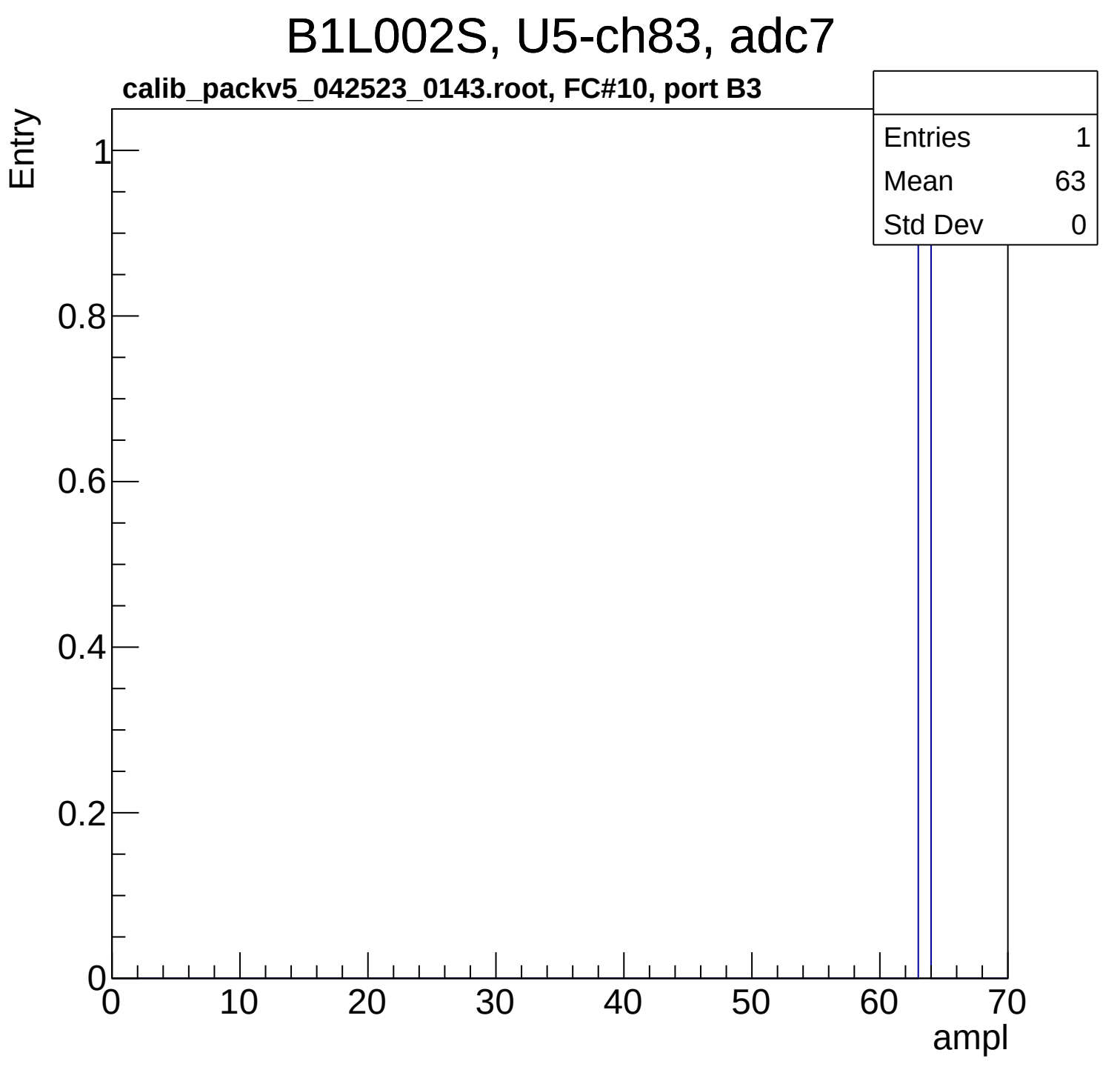
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
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10
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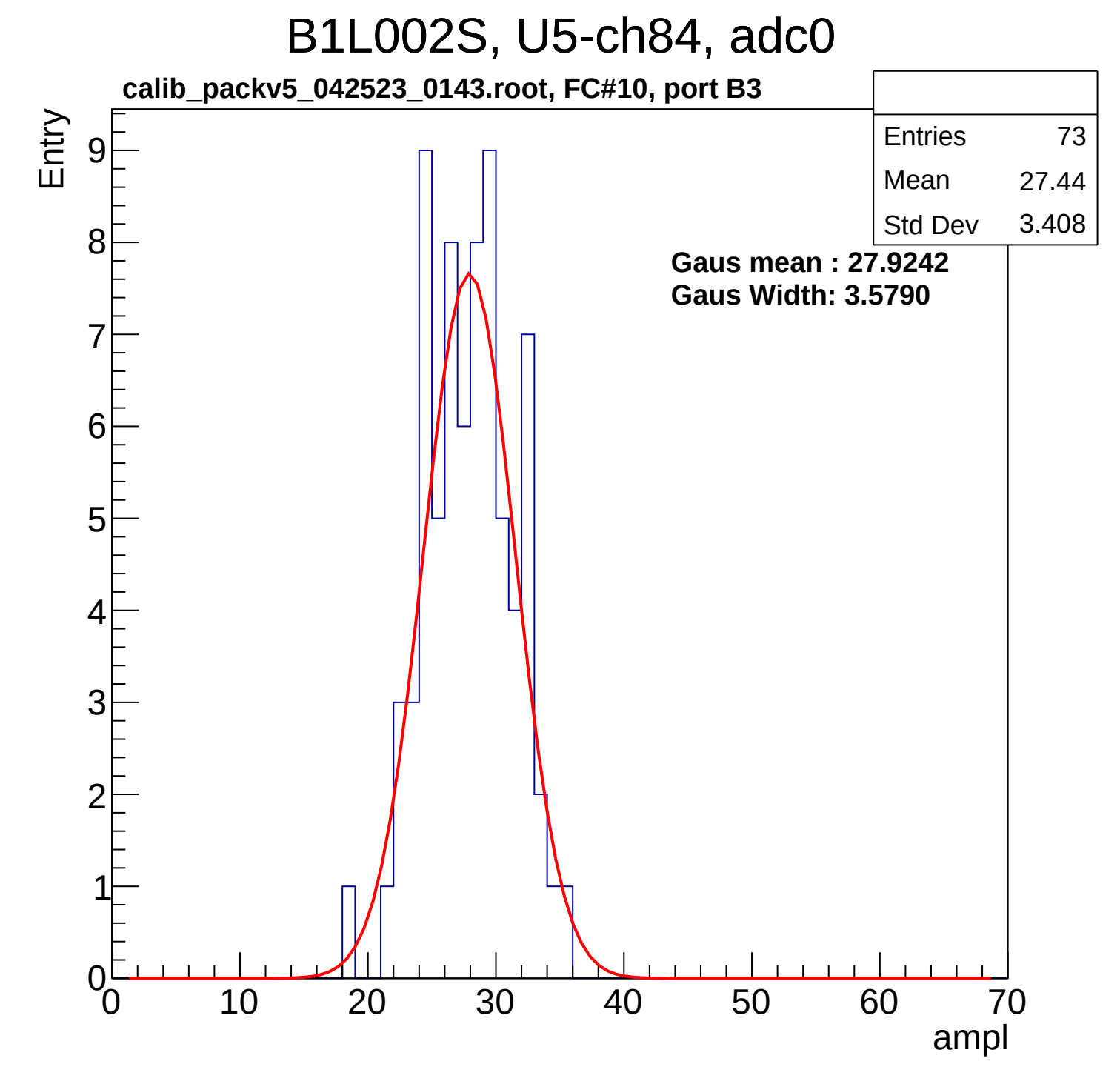
50
60
70
ampl


B1L002S, U5-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.44
Std Dev 3.408
Gaus mean : 27.9242
Gaus Width: 3.5790
Entry
ampl
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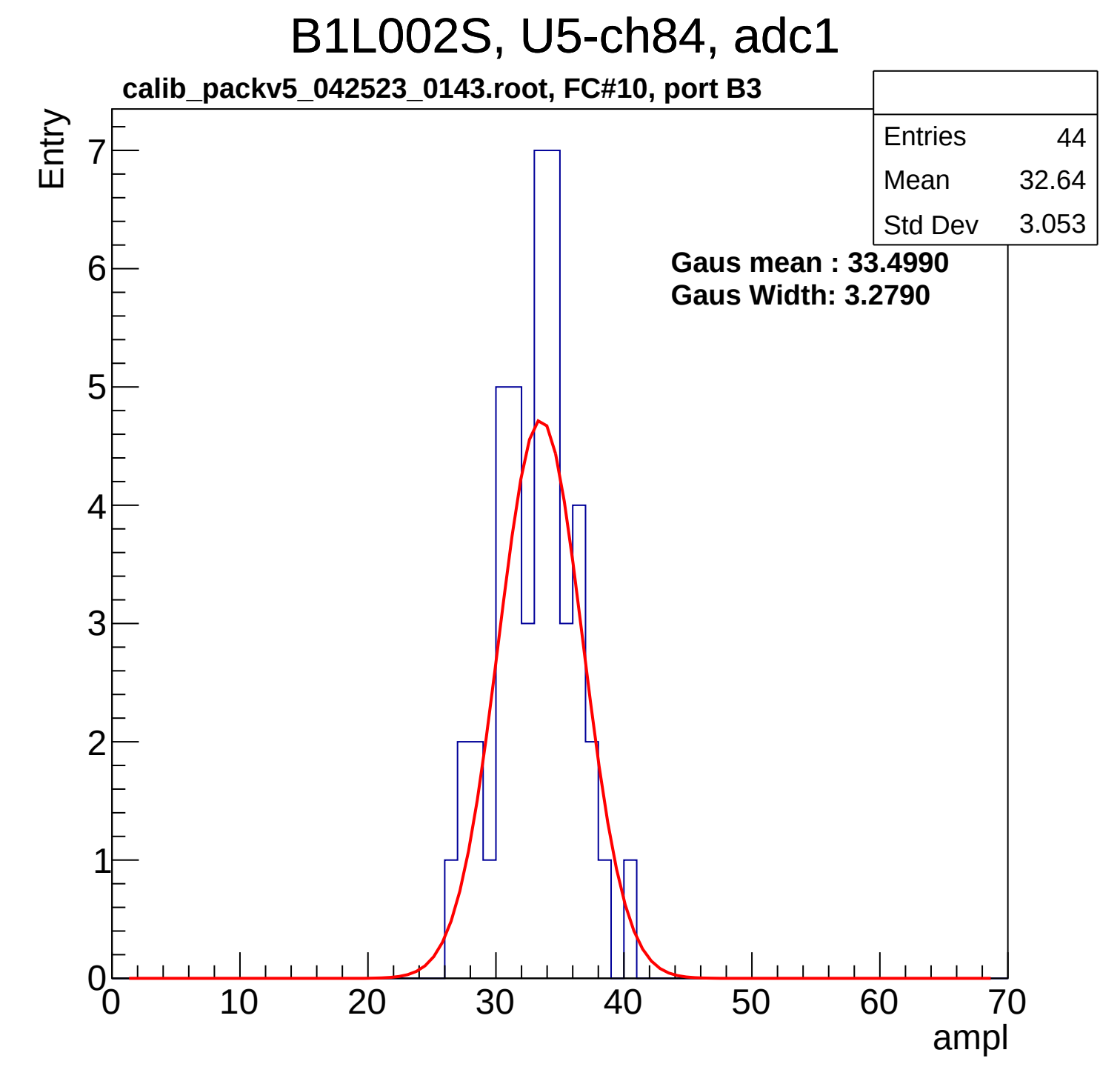
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 32.64
Std Dev 3.053
Gaus mean : 33.4990
Gaus Width: 3.2790
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
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40
50
60
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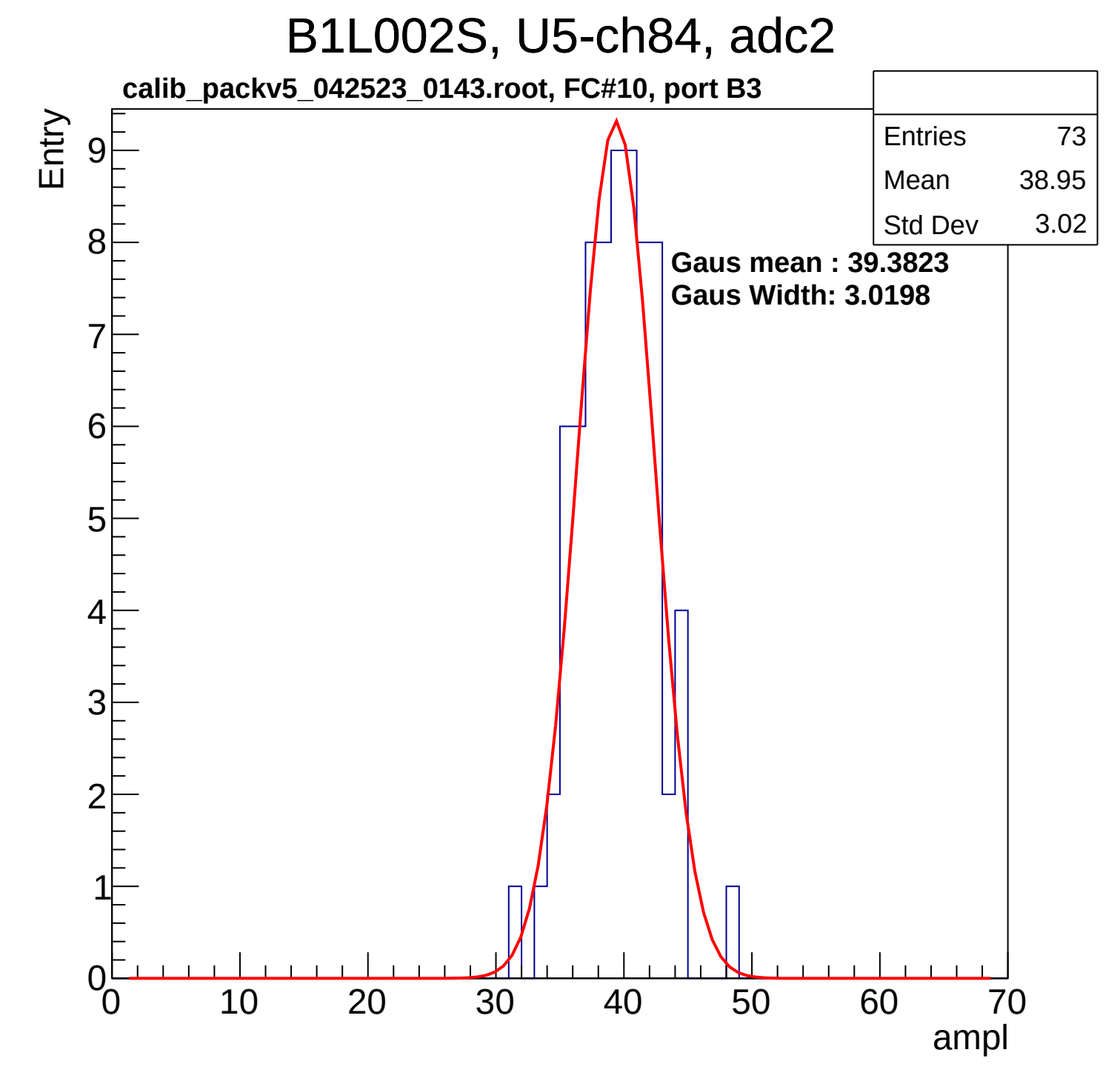
B1L002S, U5-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 38.95
Std Dev 3.02
Gaus mean : 39.3823
Gaus Width: 3.0198
Entry
ampl
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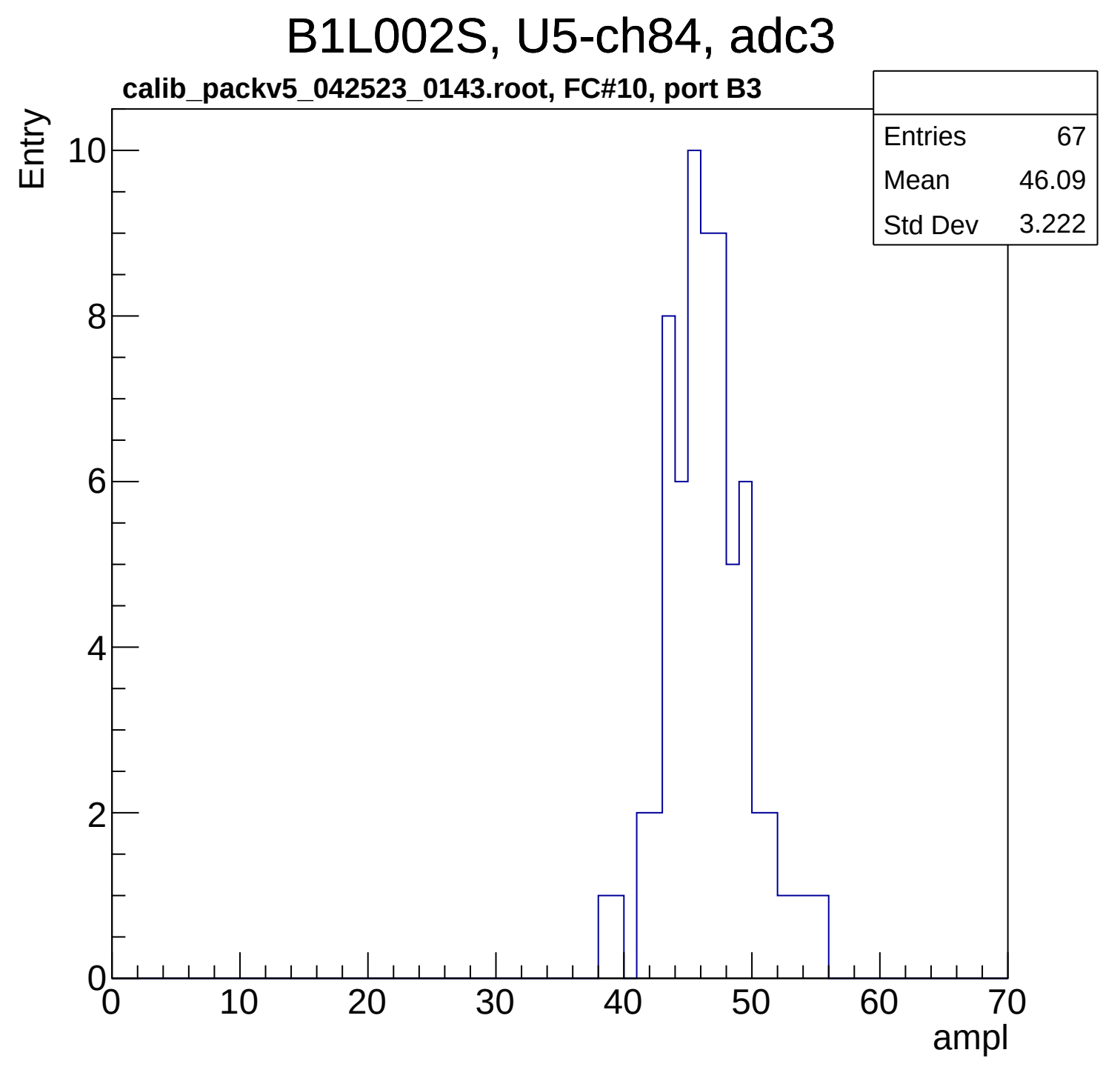
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.09
Std Dev 3.222
Entry
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6
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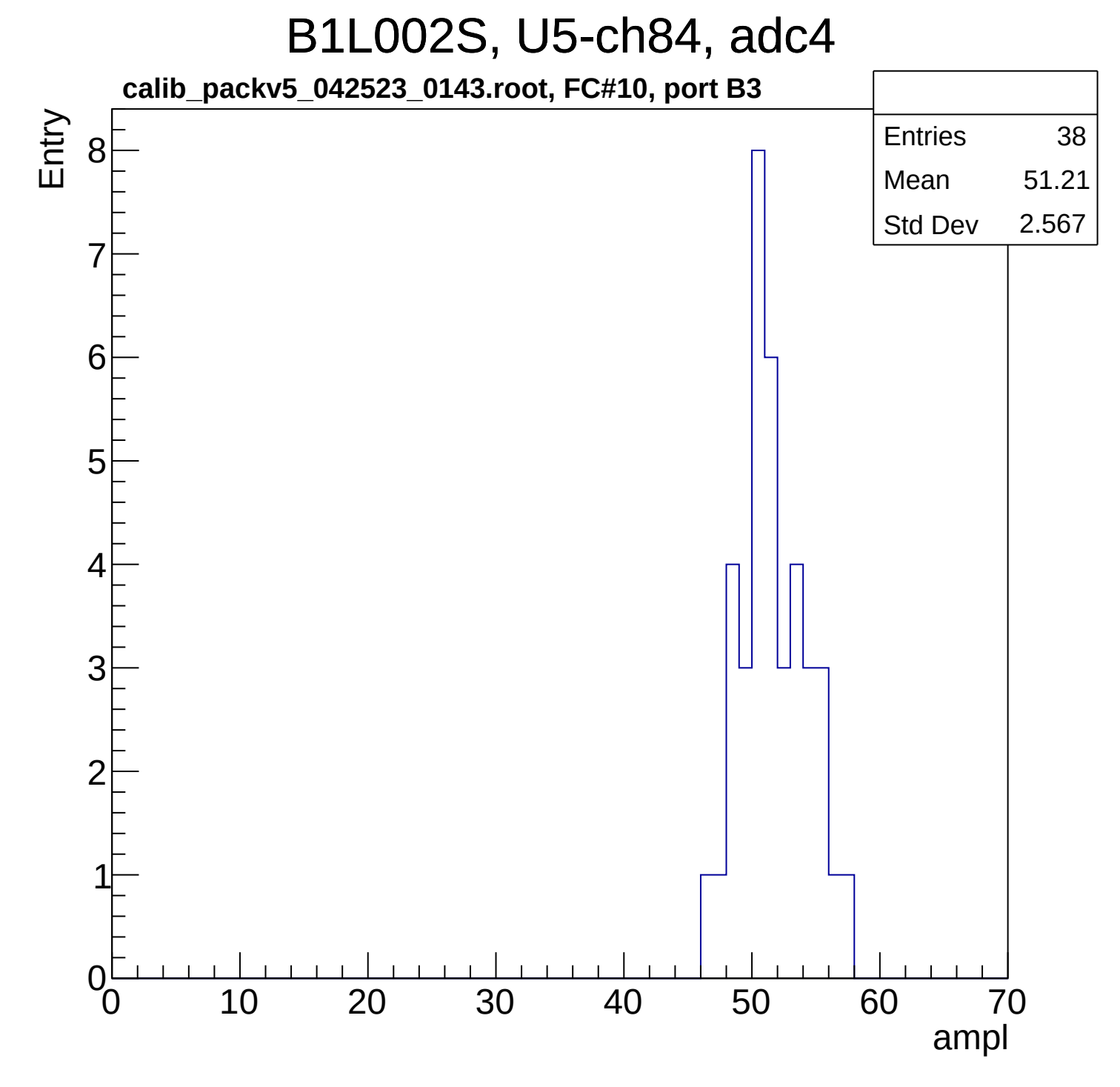
70
ampl


B1L002S, U5-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 51.21
Std Dev 2.567
Entry
ampl
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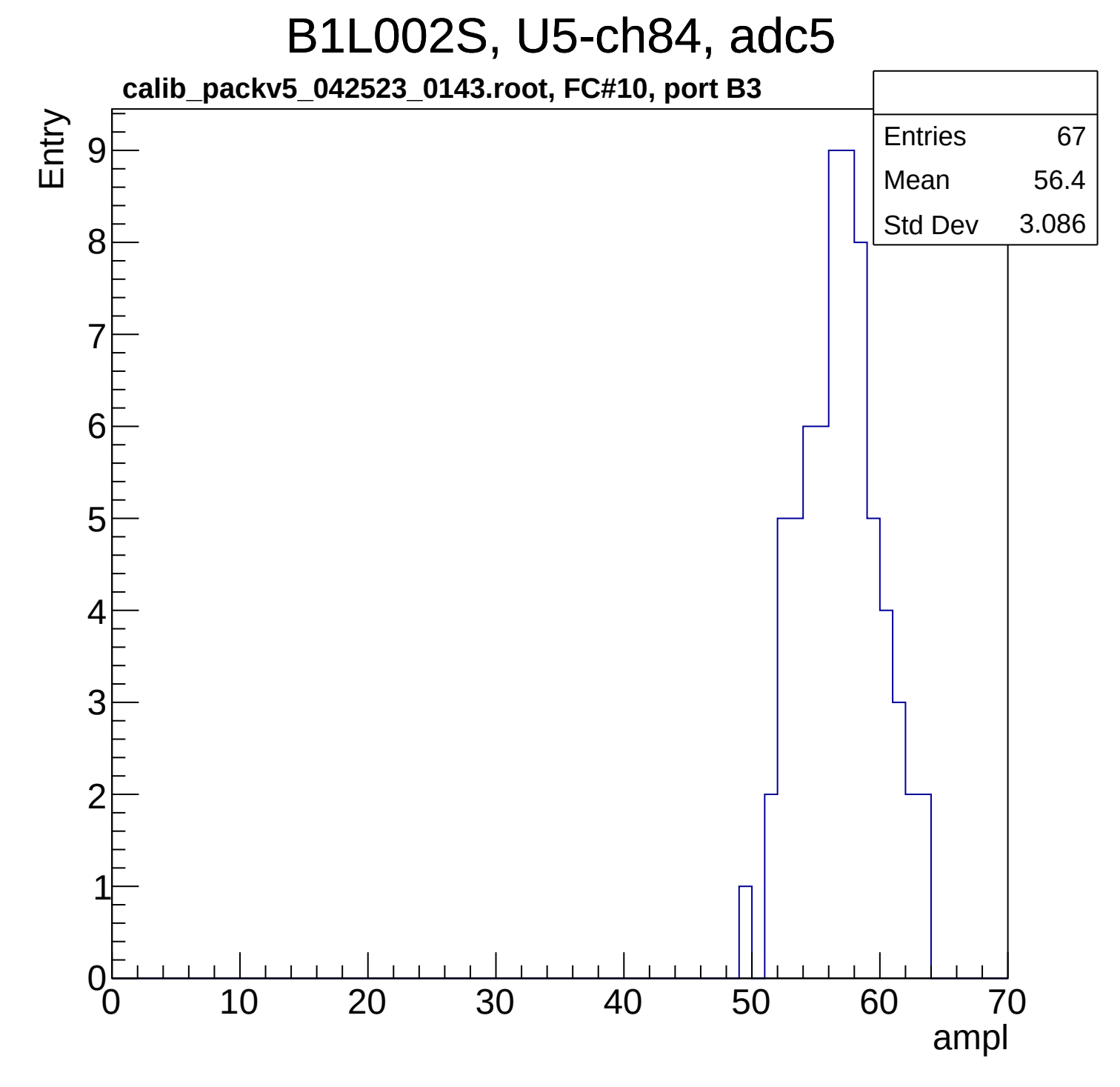
B1L002S, U5-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.4
Std Dev 3.086
Entry
ampl
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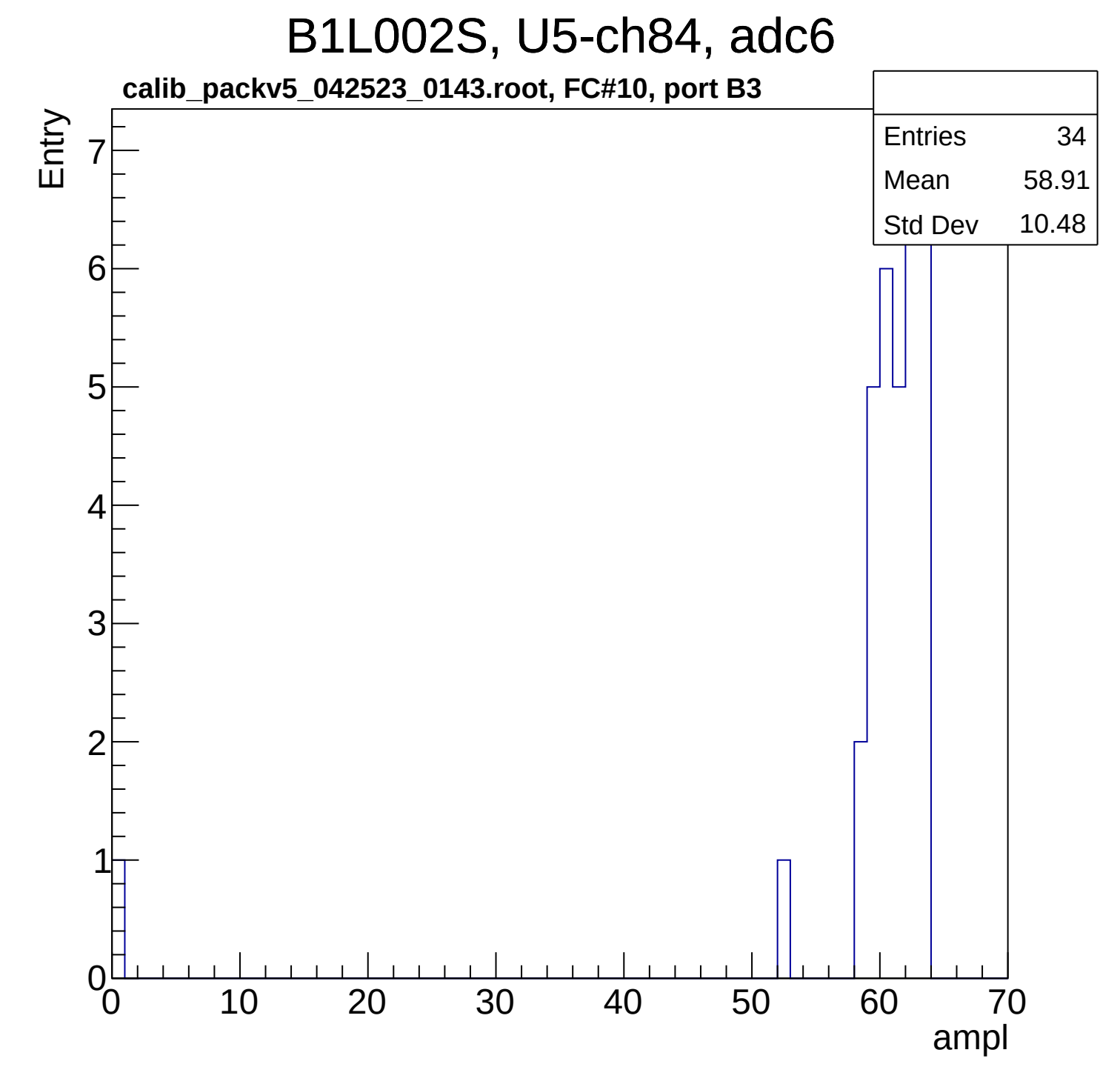
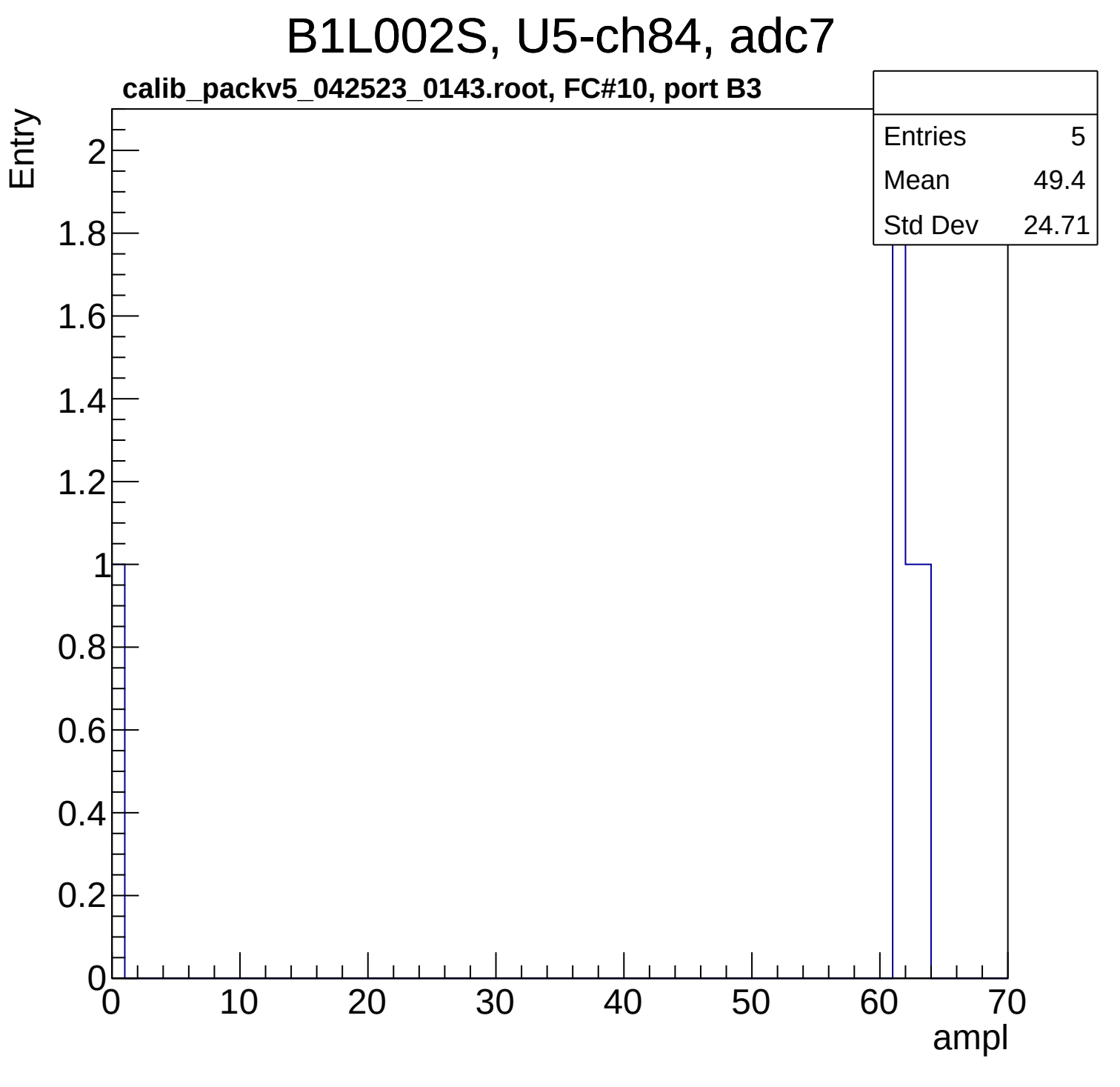

B1L002S, U5-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.91
Std Dev 10.48
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.4
Std Dev 24.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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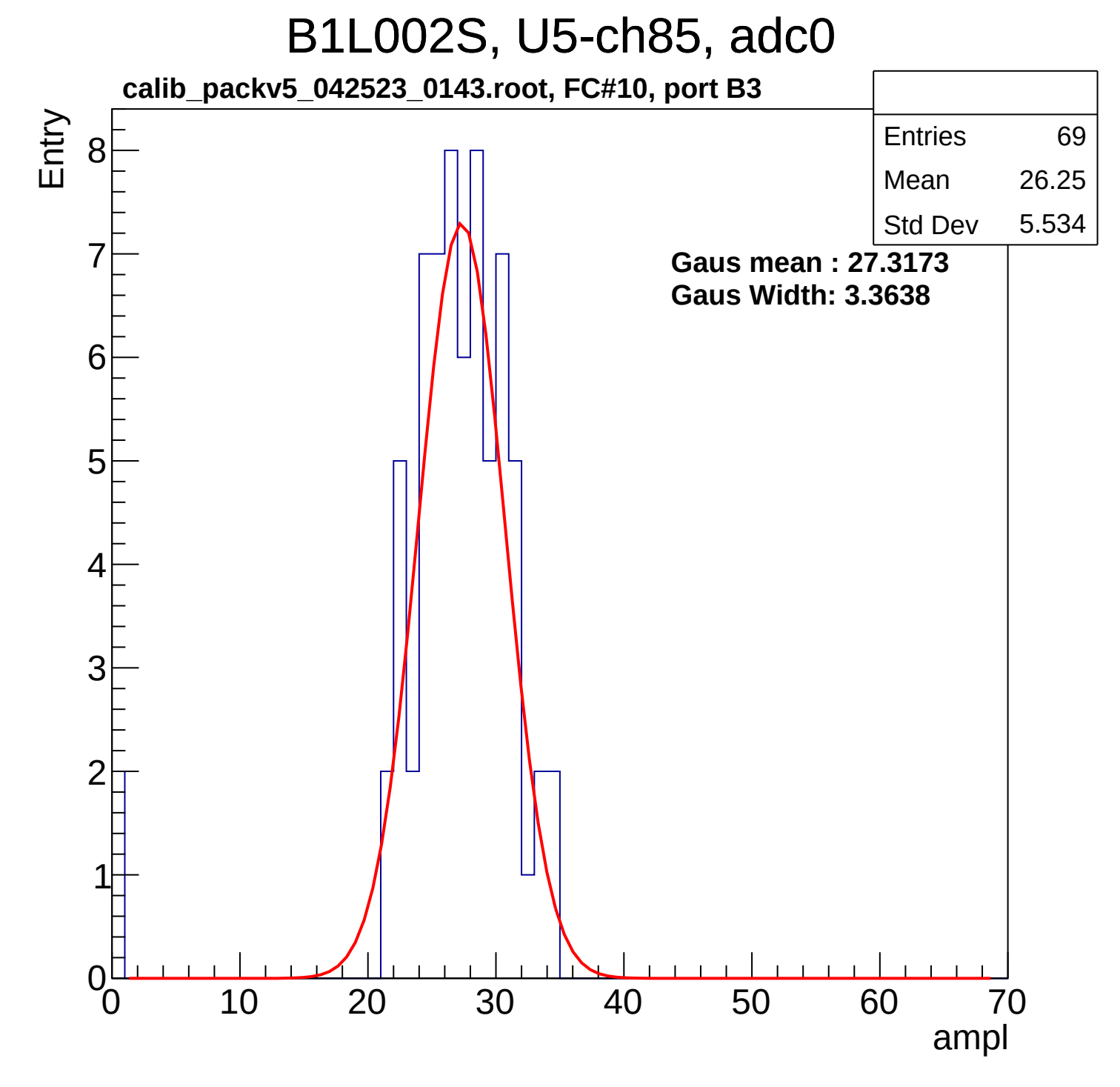
B1L002S, U5-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 26.25
Std Dev 5.534
Gaus mean : 27.3173
Gaus Width: 3.3638
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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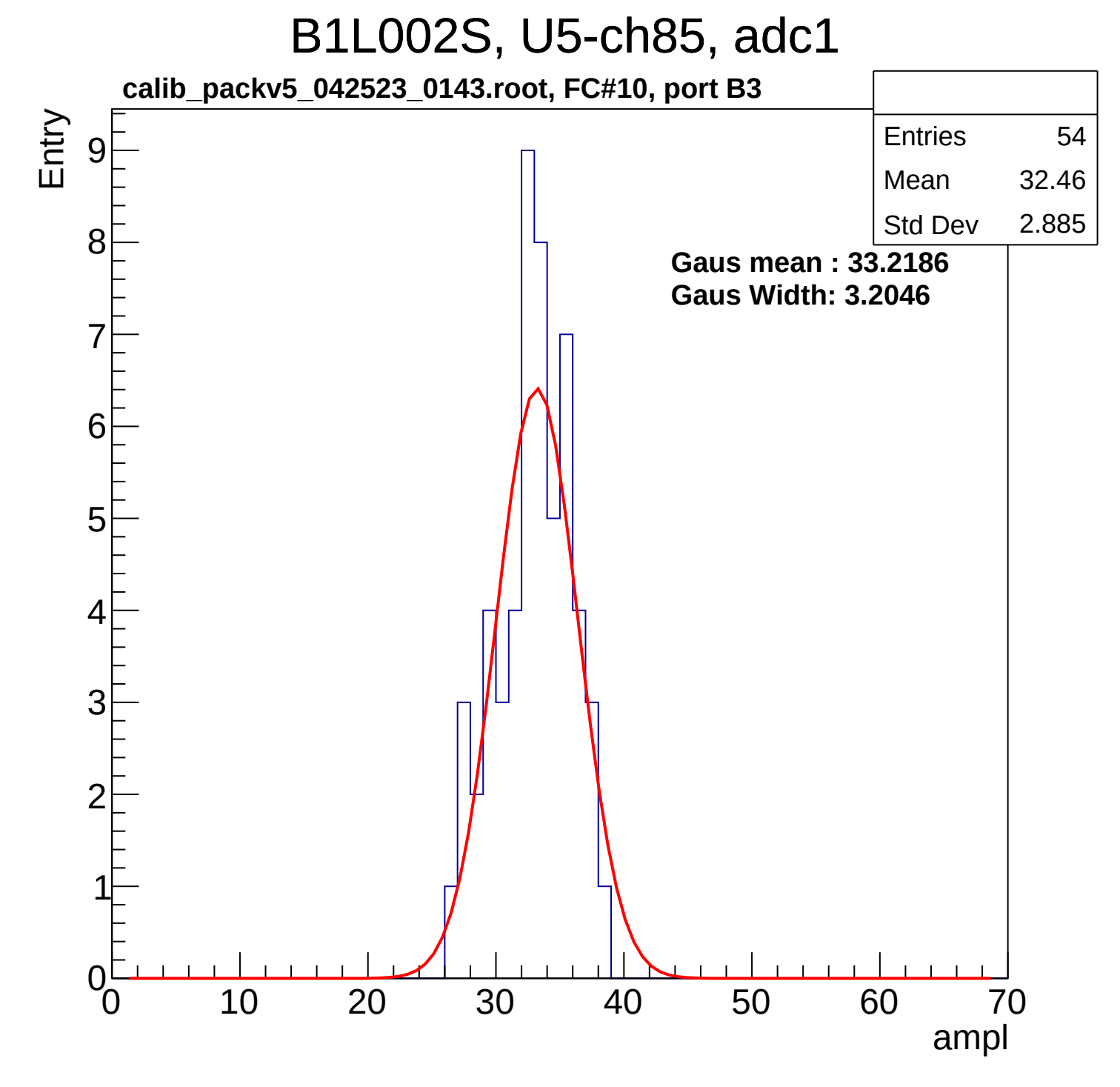
B1L002S, U5-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 32.46
Std Dev 2.885
Gaus mean : 33.2186
Gaus Width: 3.2046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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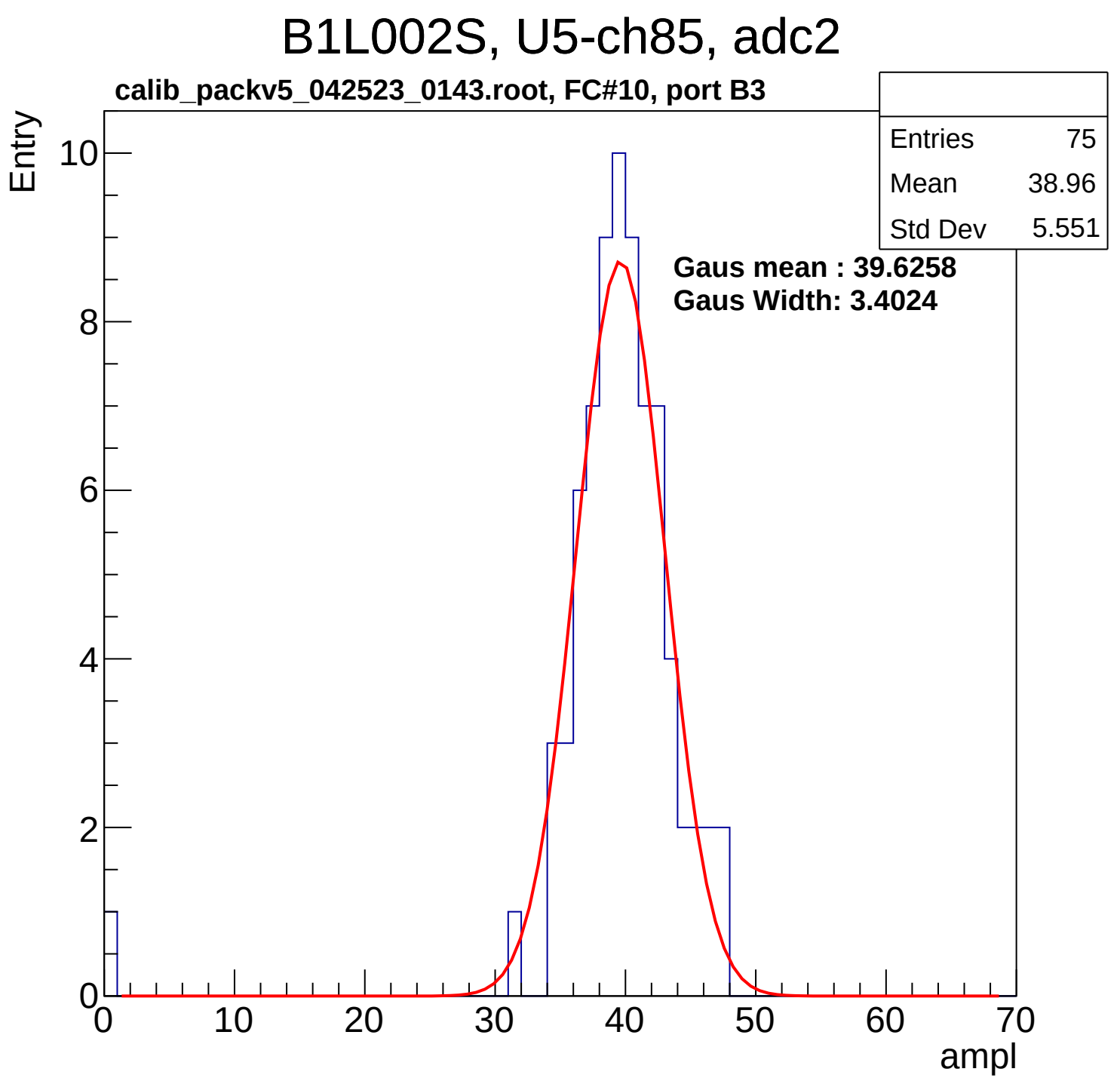
B1L002S, U5-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 38.96
Std Dev 5.551
Gaus mean : 39.6258
Gaus Width: 3.4024
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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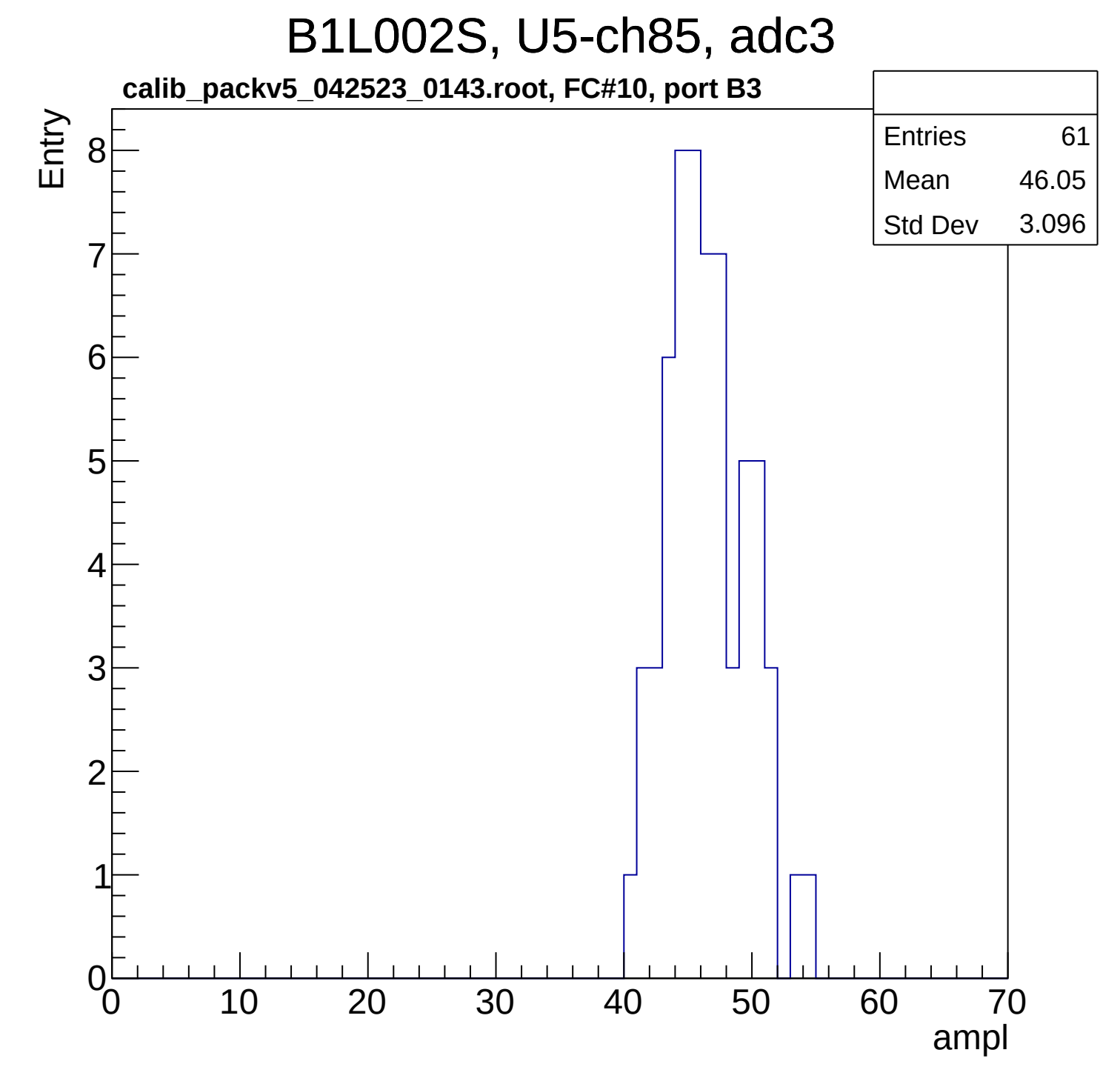
B1L002S, U5-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.05
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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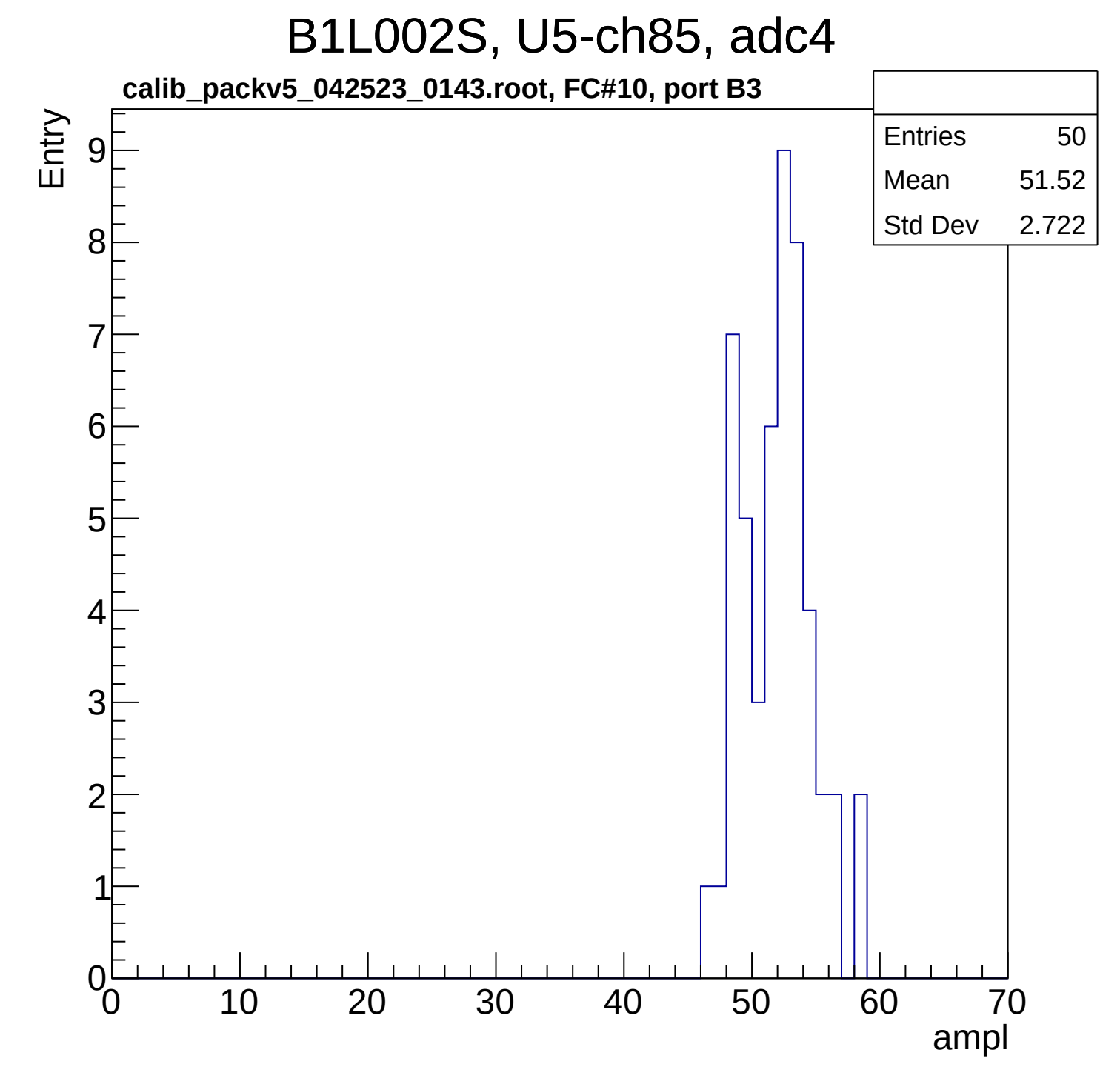
B1L002S, U5-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.52
Std Dev 2.722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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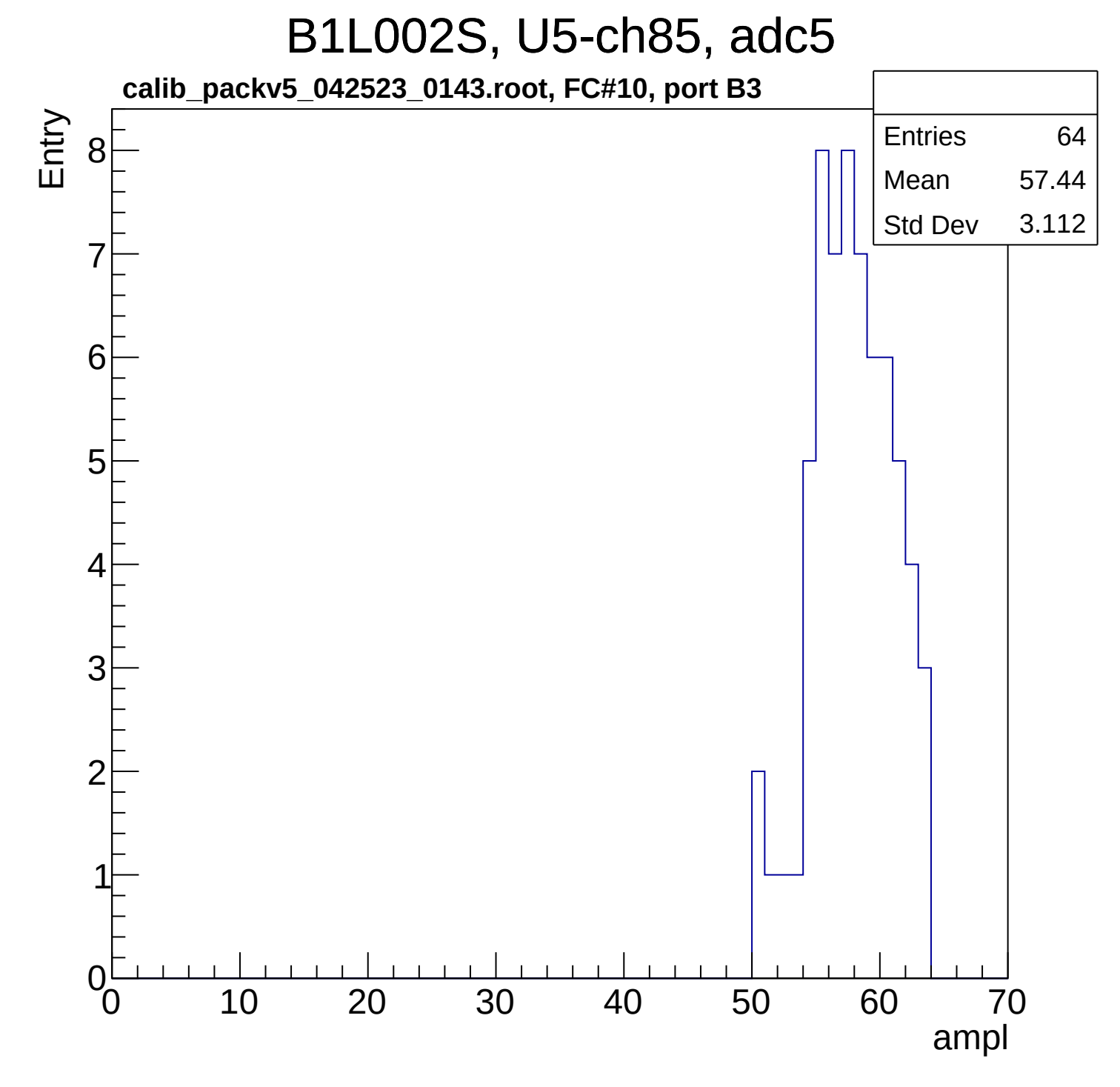
B1L002S, U5-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.44
Std Dev 3.112
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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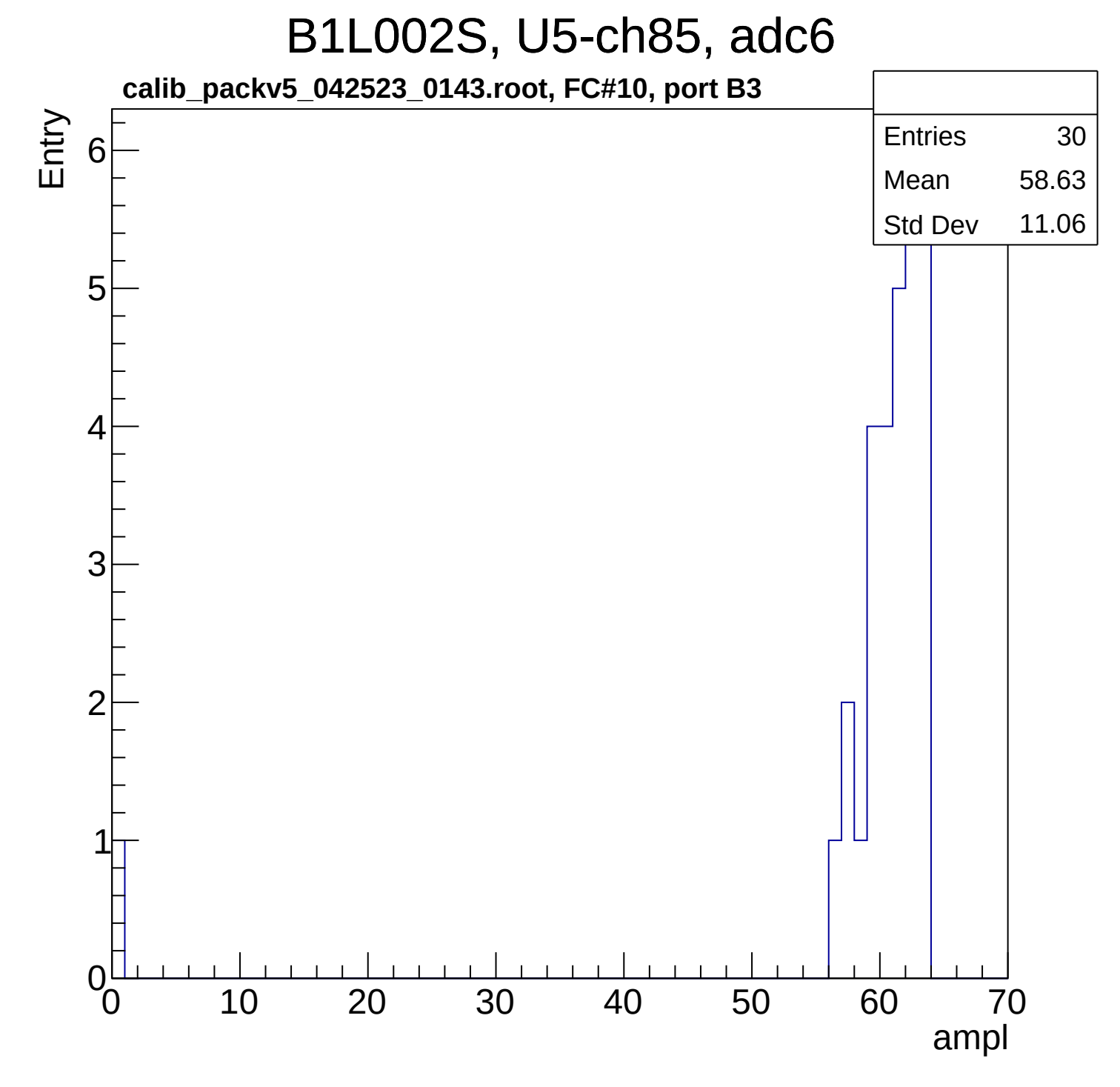
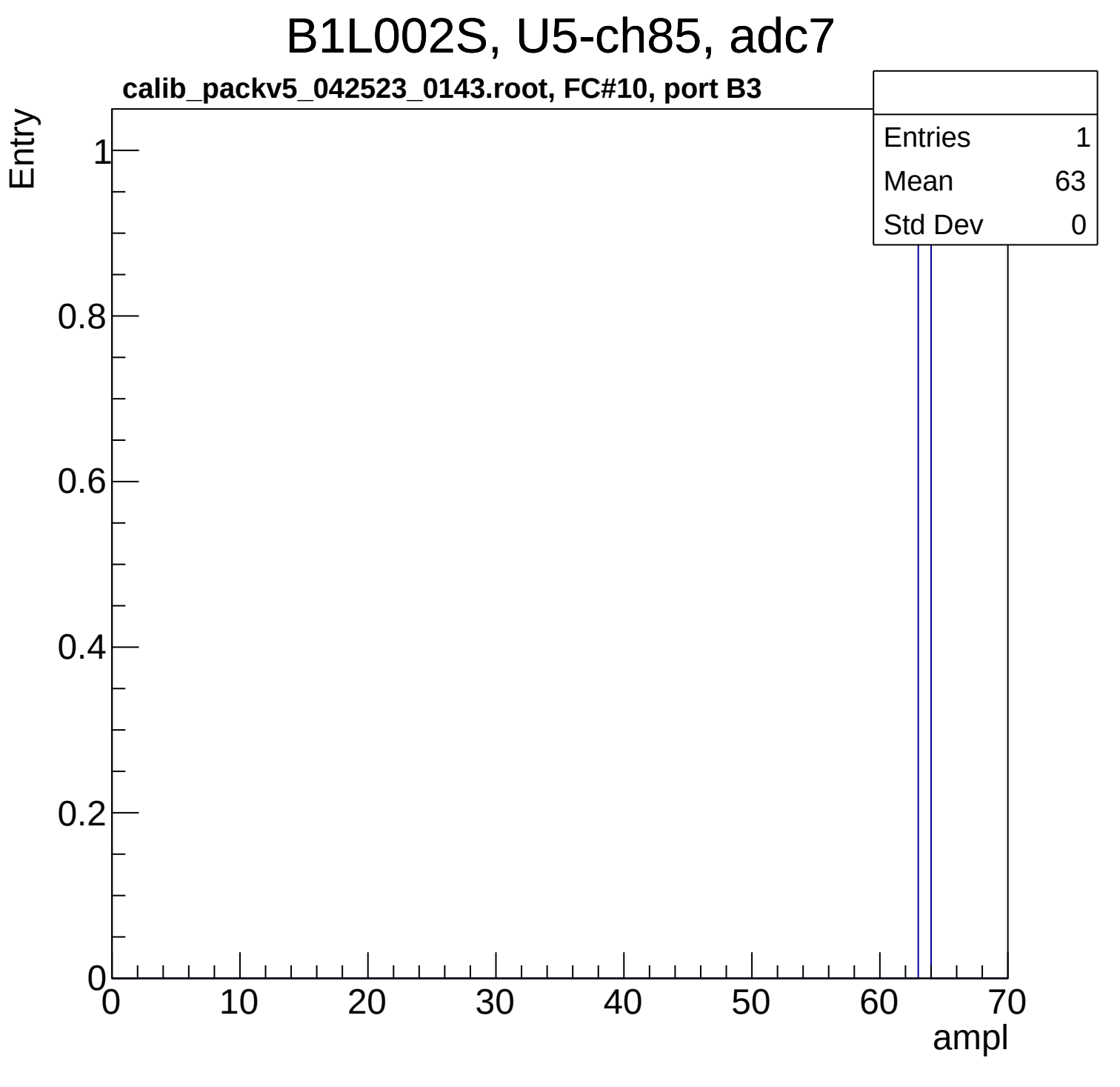
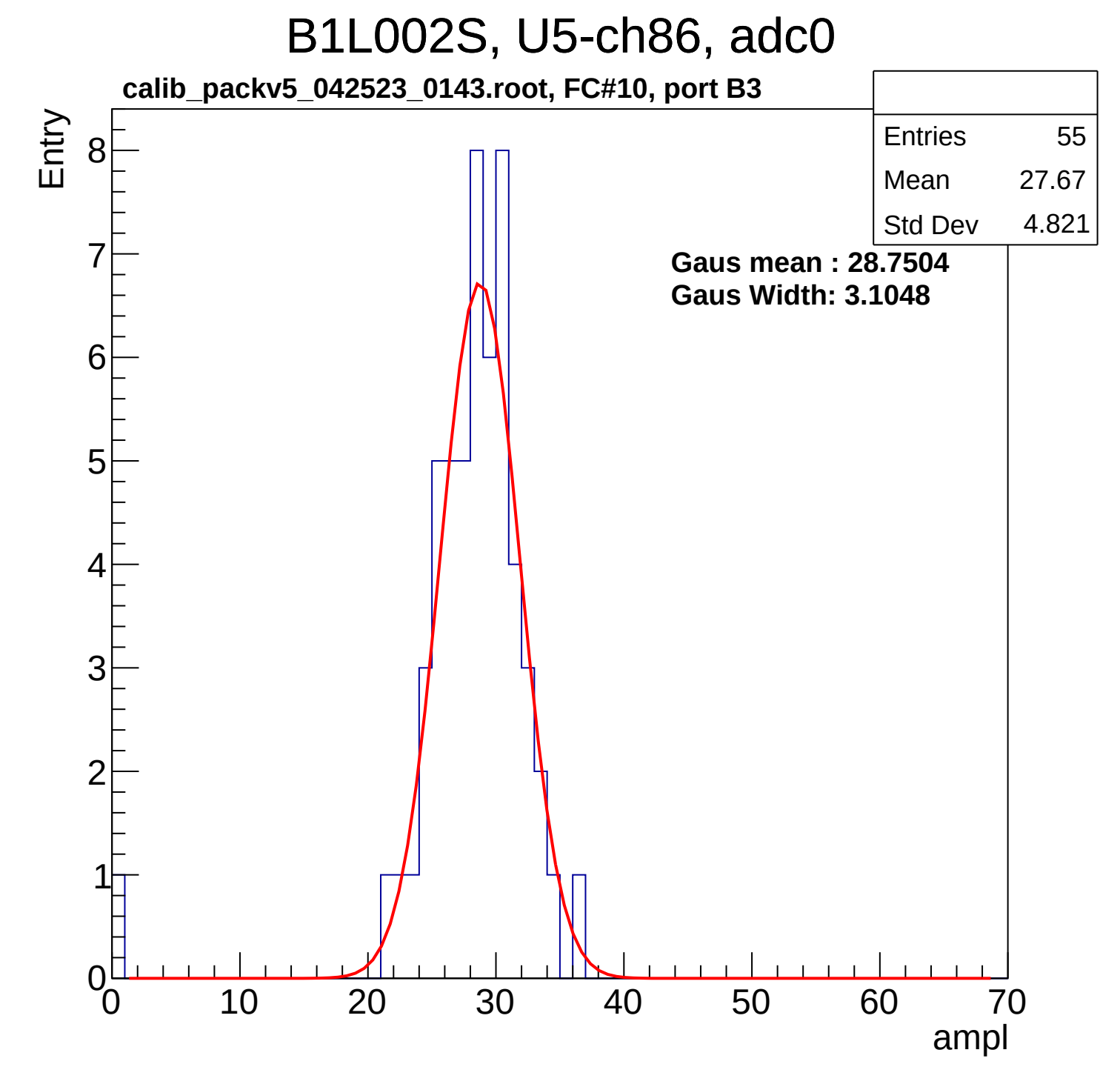

B1L002S, U5-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 58.63
Std Dev 11.06
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.67
Std Dev 4.821
Gaus mean : 28.7504
Gaus Width: 3.1048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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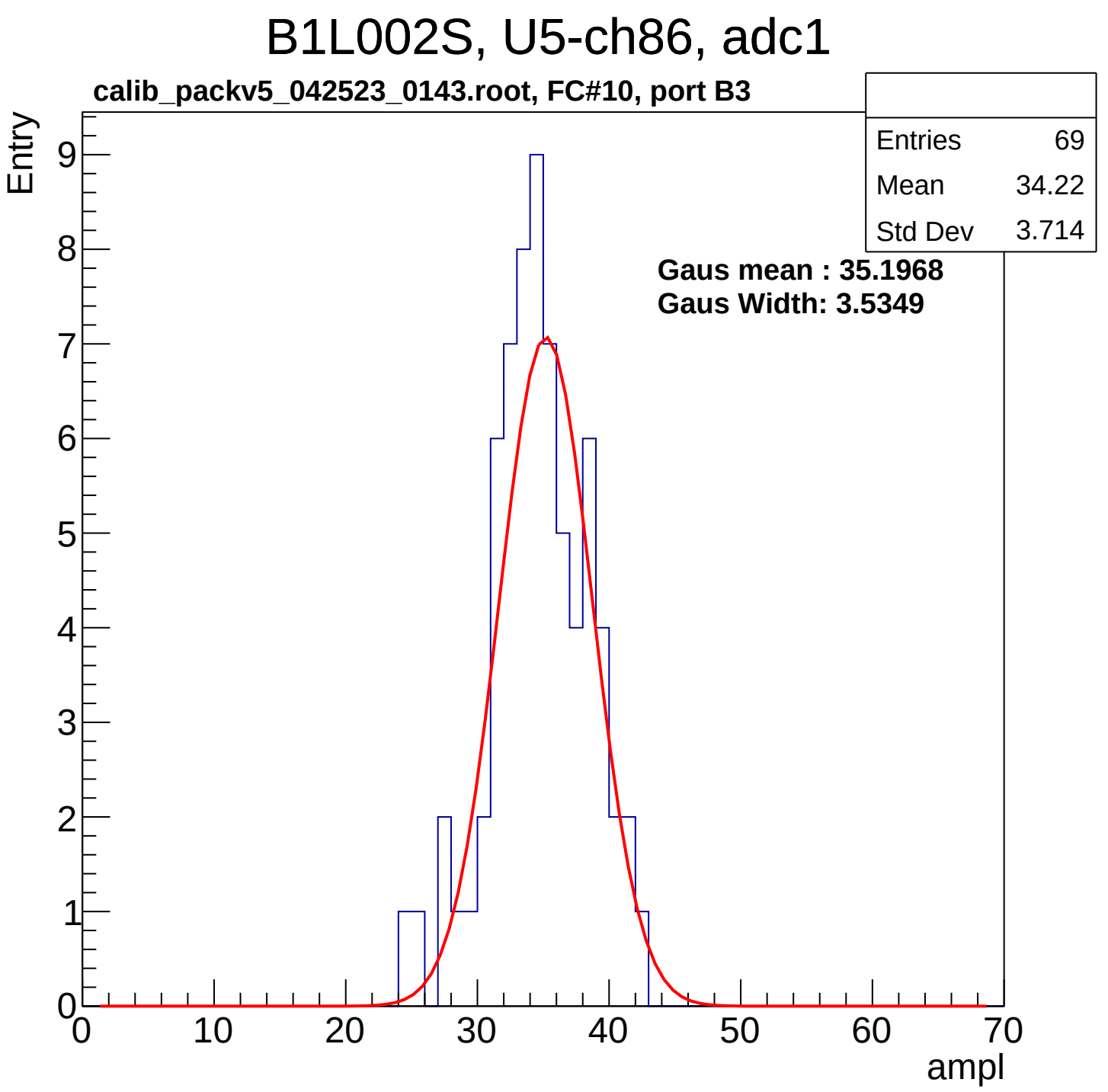
B1L002S, U5-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.22
Std Dev 3.714
Gaus mean : 35.1968
Gaus Width: 3.5349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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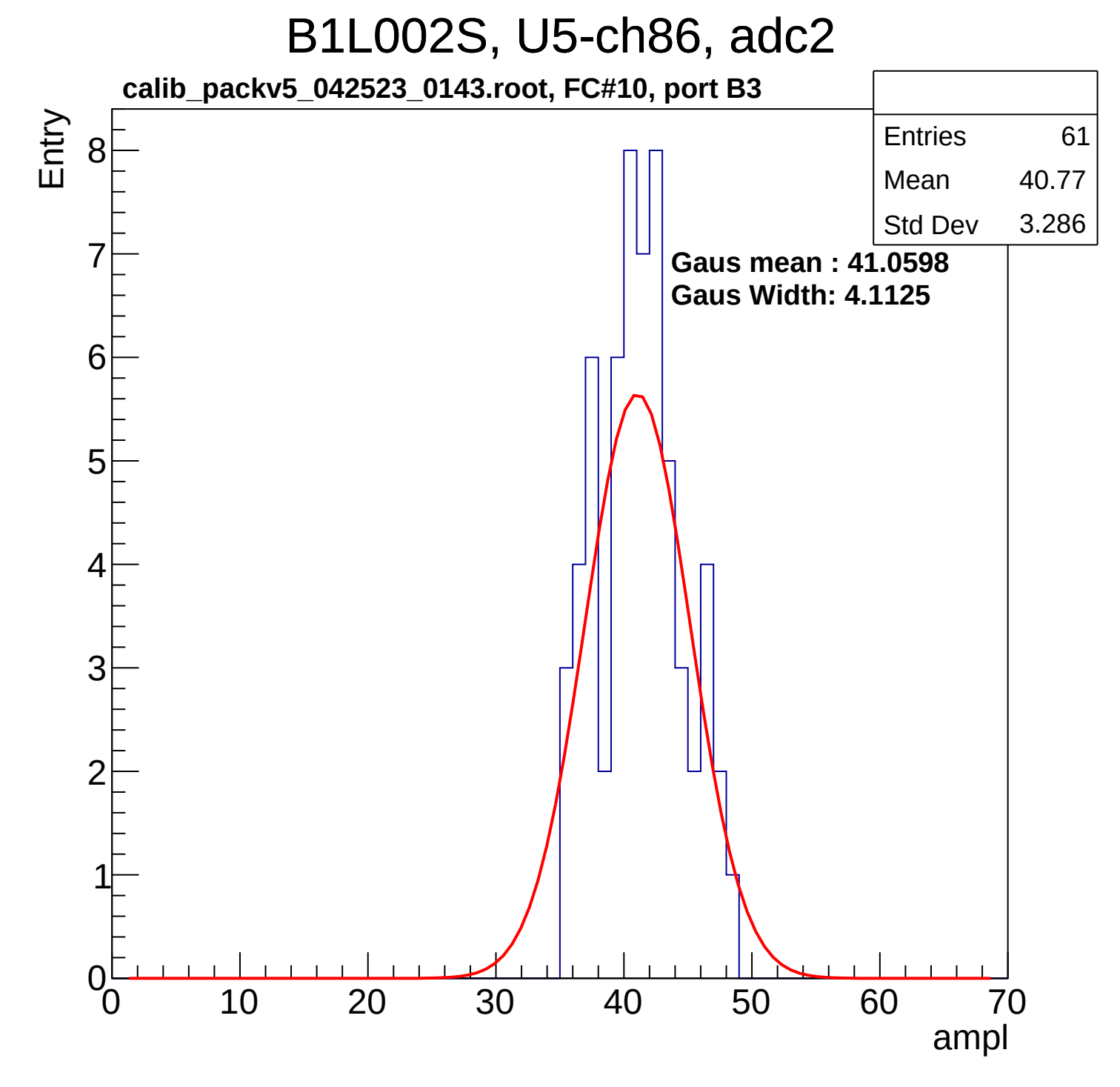
B1L002S, U5-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.77
Std Dev 3.286
Gaus mean : 41.0598
Gaus Width: 4.1125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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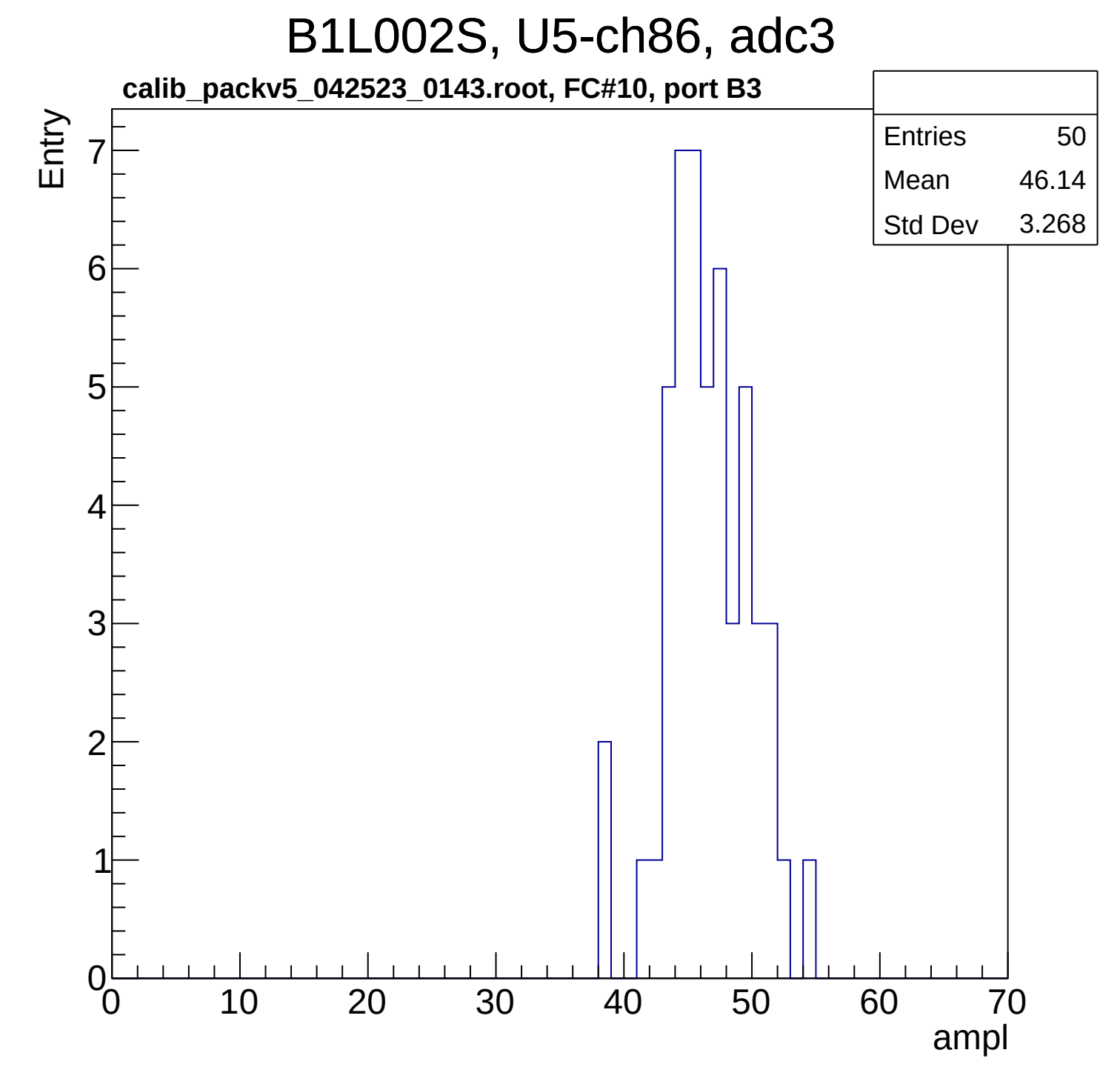
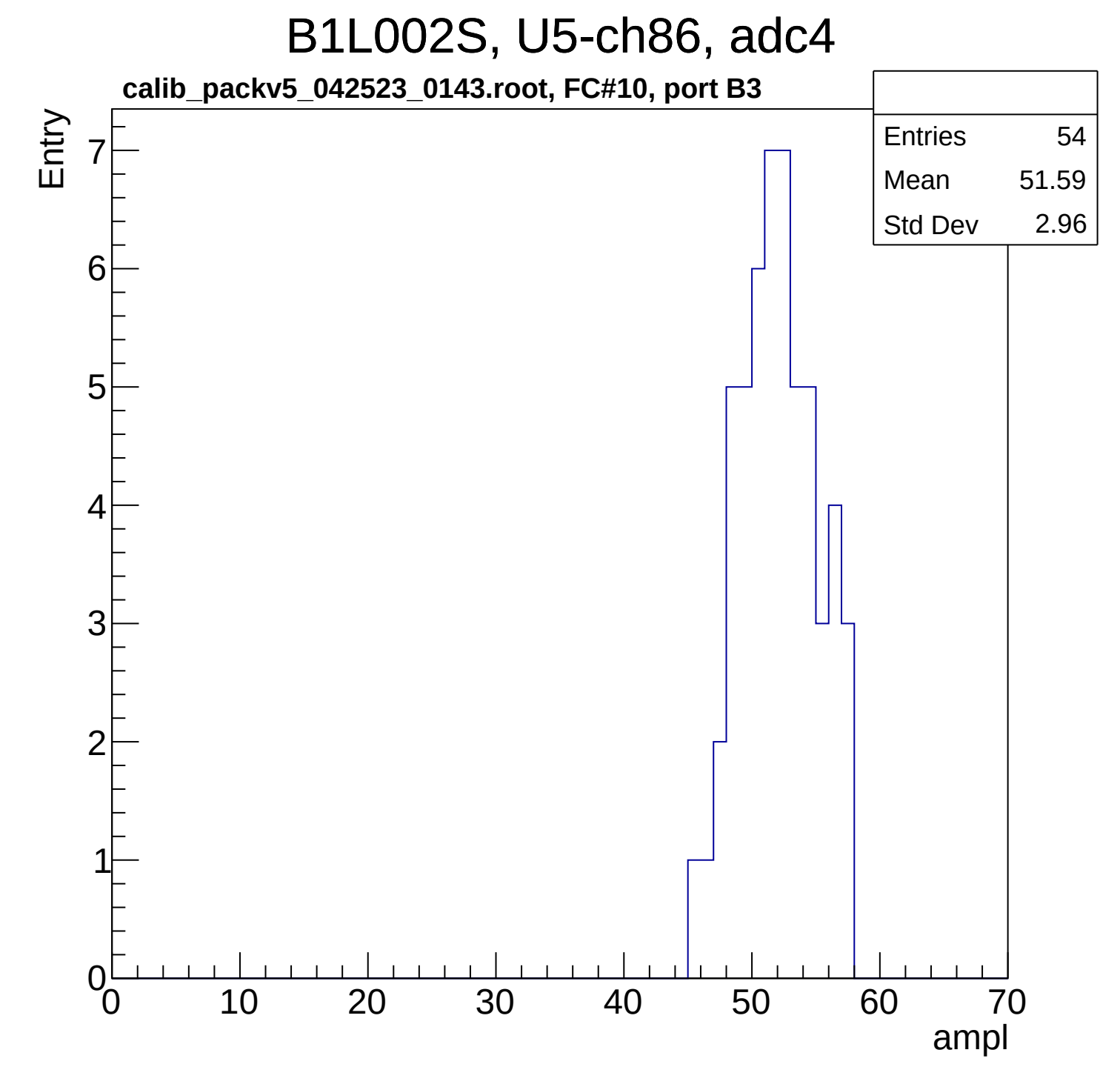
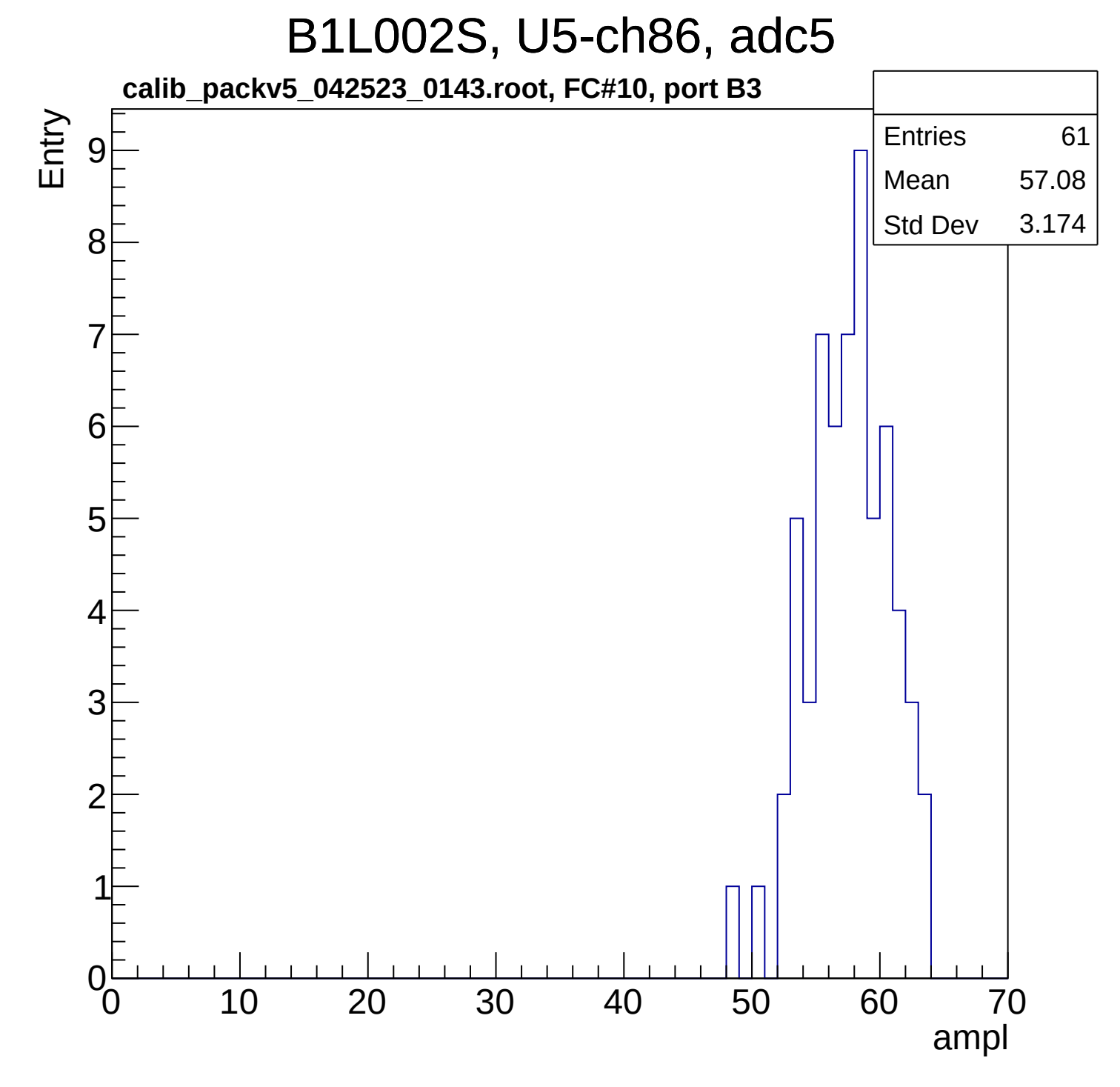

B1L002S, U5-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 46.14
Std Dev 3.268
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.59
Std Dev 2.96
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.08
Std Dev 3.174
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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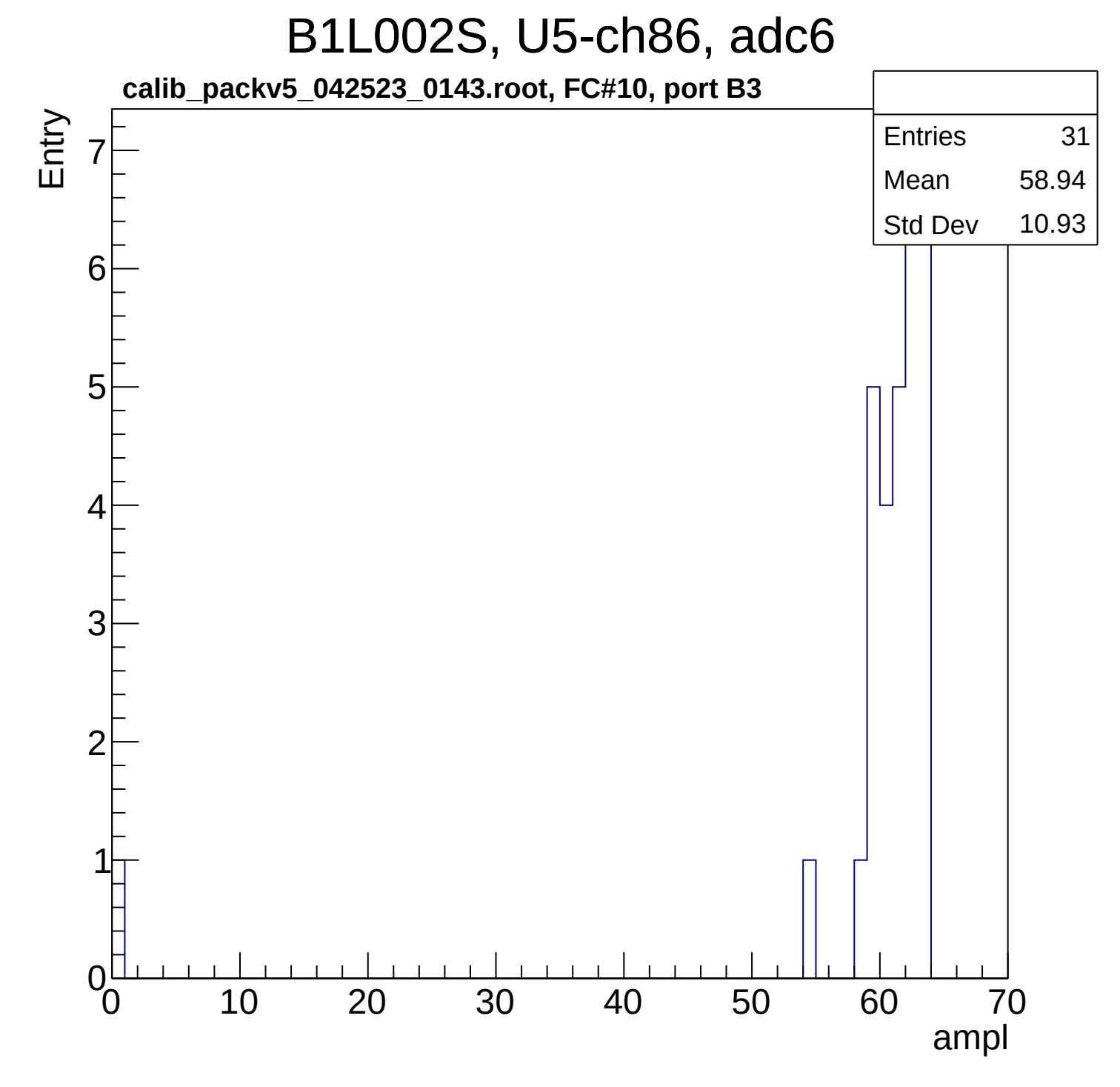
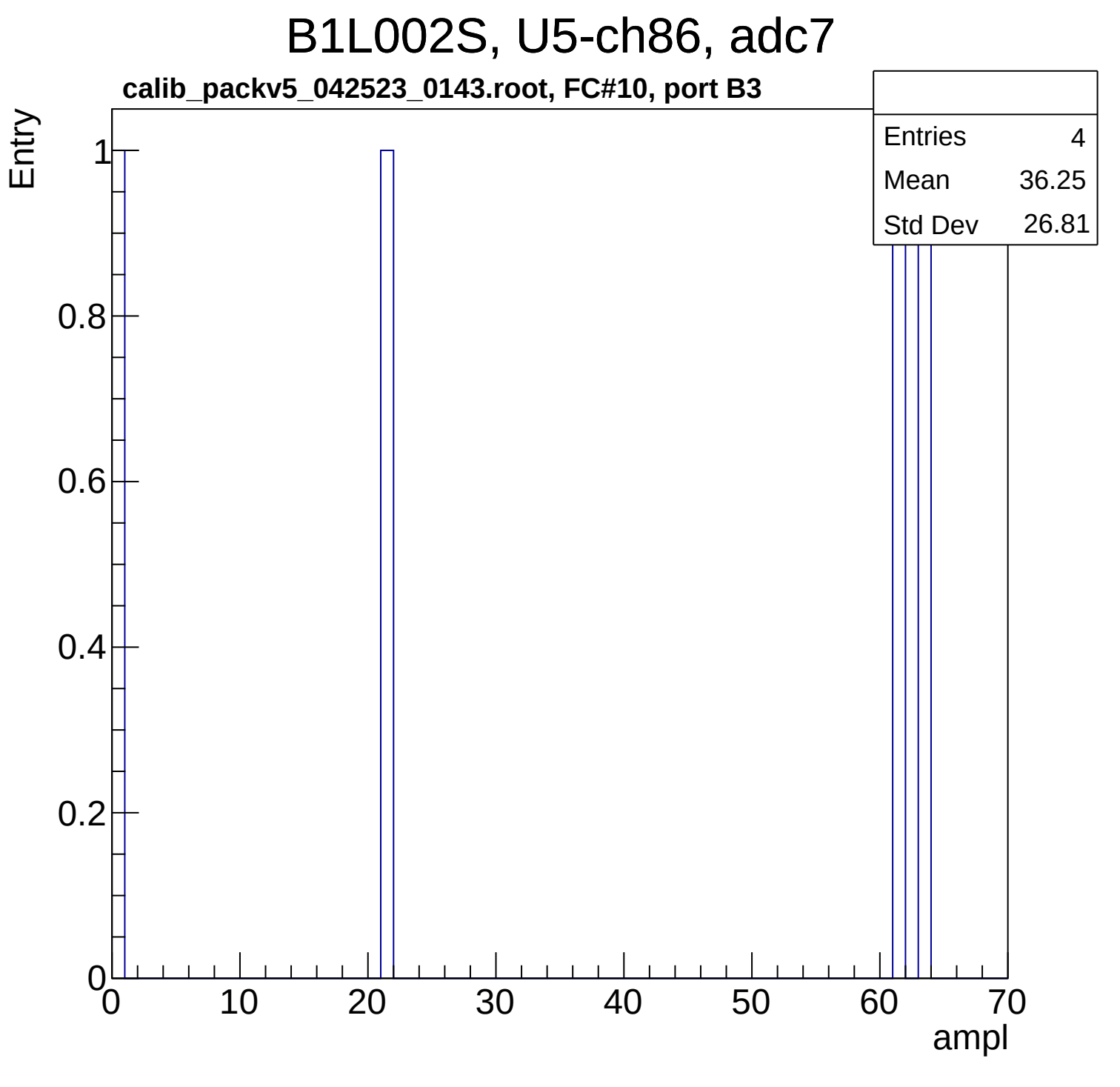
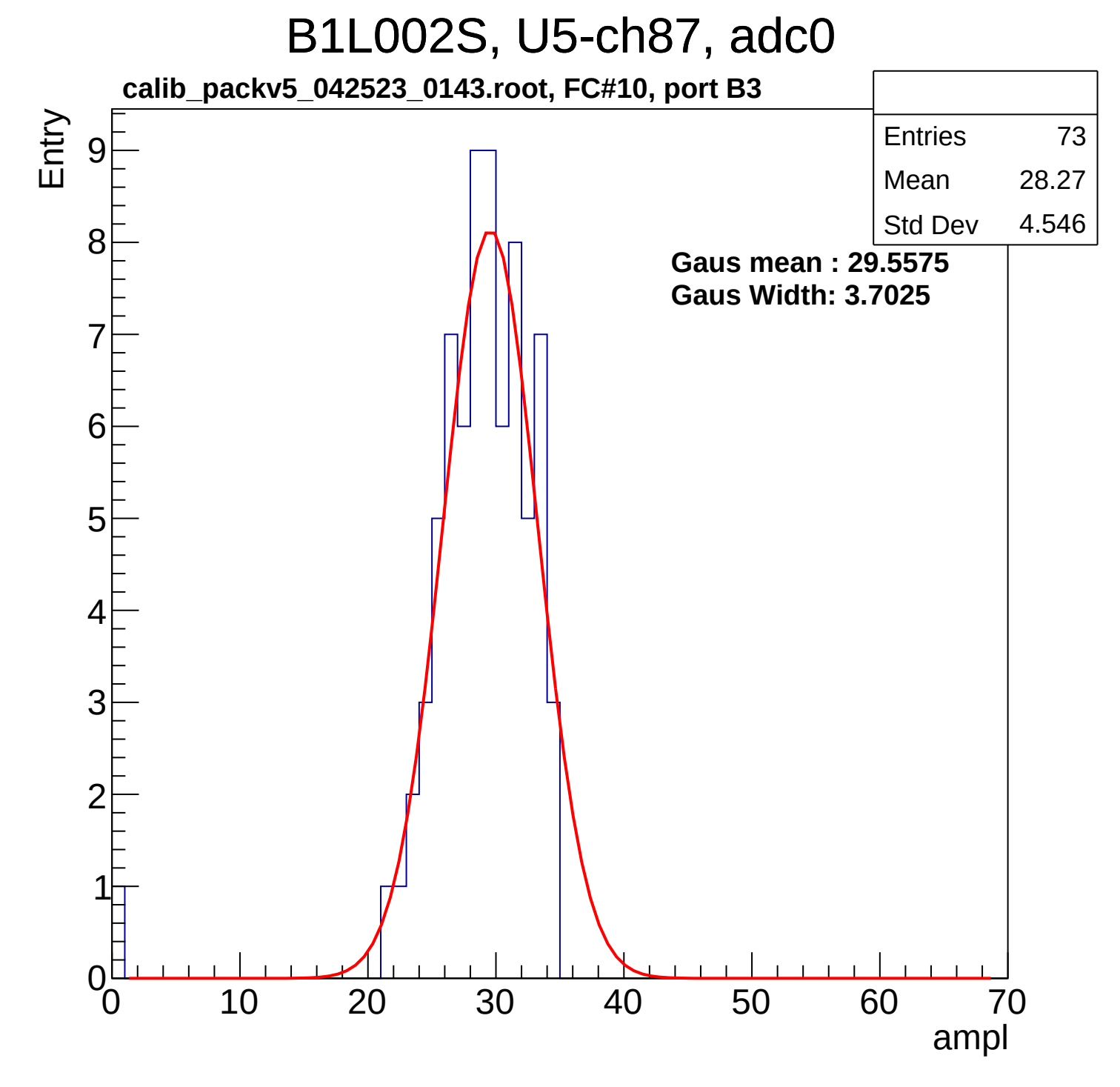
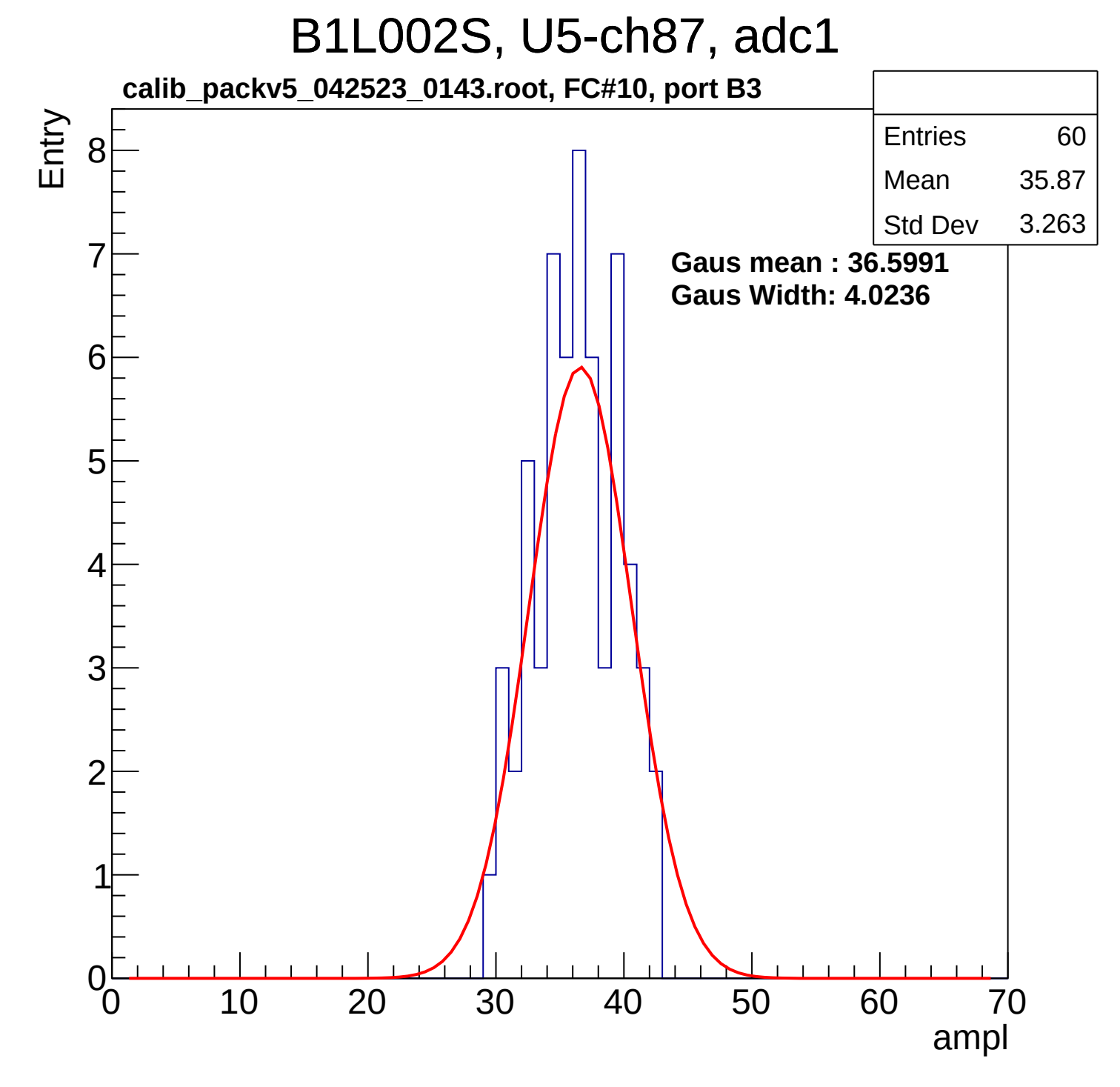

B1L002S, U5-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.94
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.25
Std Dev 26.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U5-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.27
Std Dev 4.546
Gaus mean : 29.5575
Gaus Width: 3.7025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.87
Std Dev 3.263
Gaus mean : 36.5991
Gaus Width: 4.0236
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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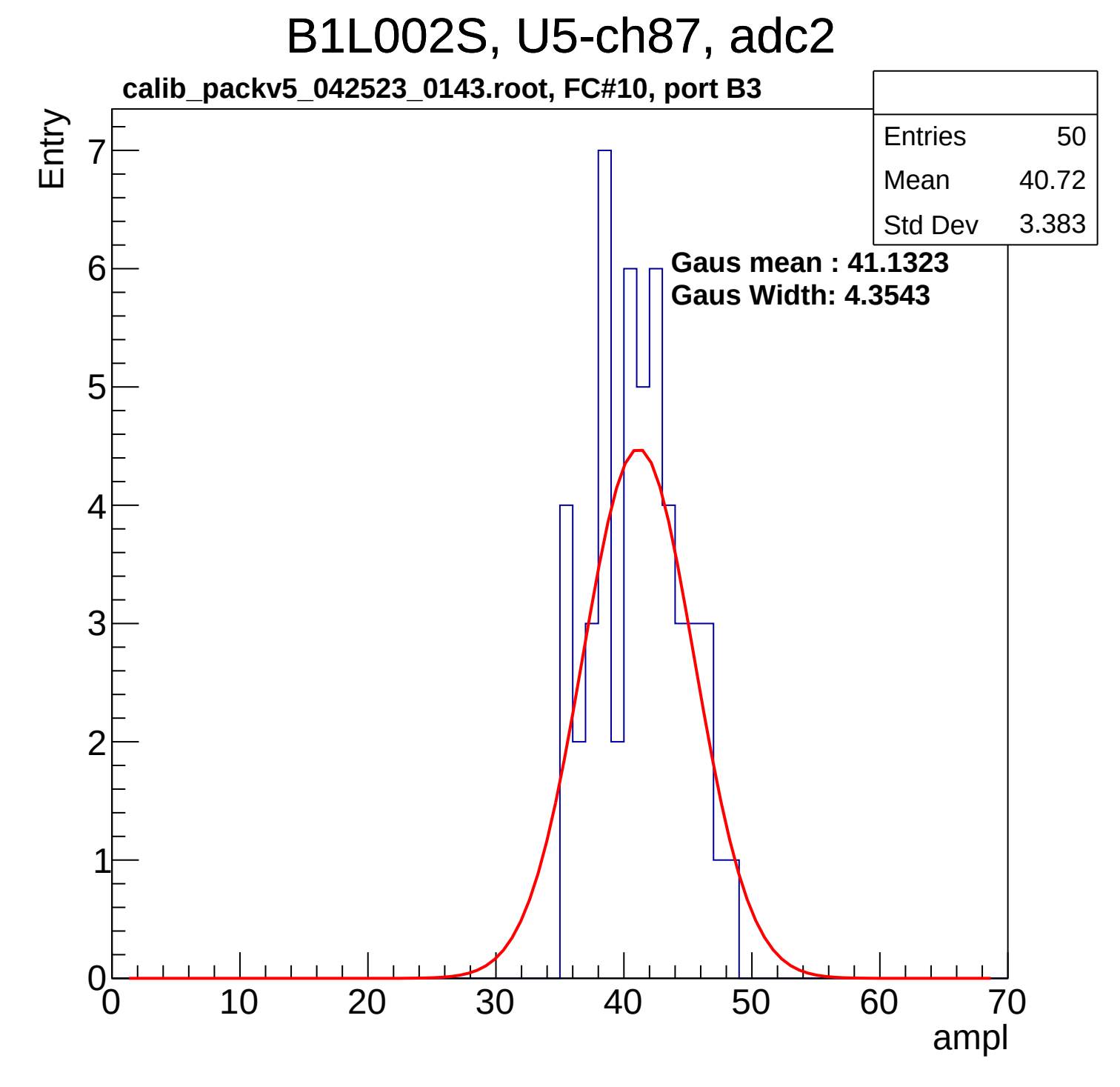
B1L002S, U5-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.72
Std Dev 3.383
Gaus mean : 41.1323
Gaus Width: 4.3543
Entry
ampl
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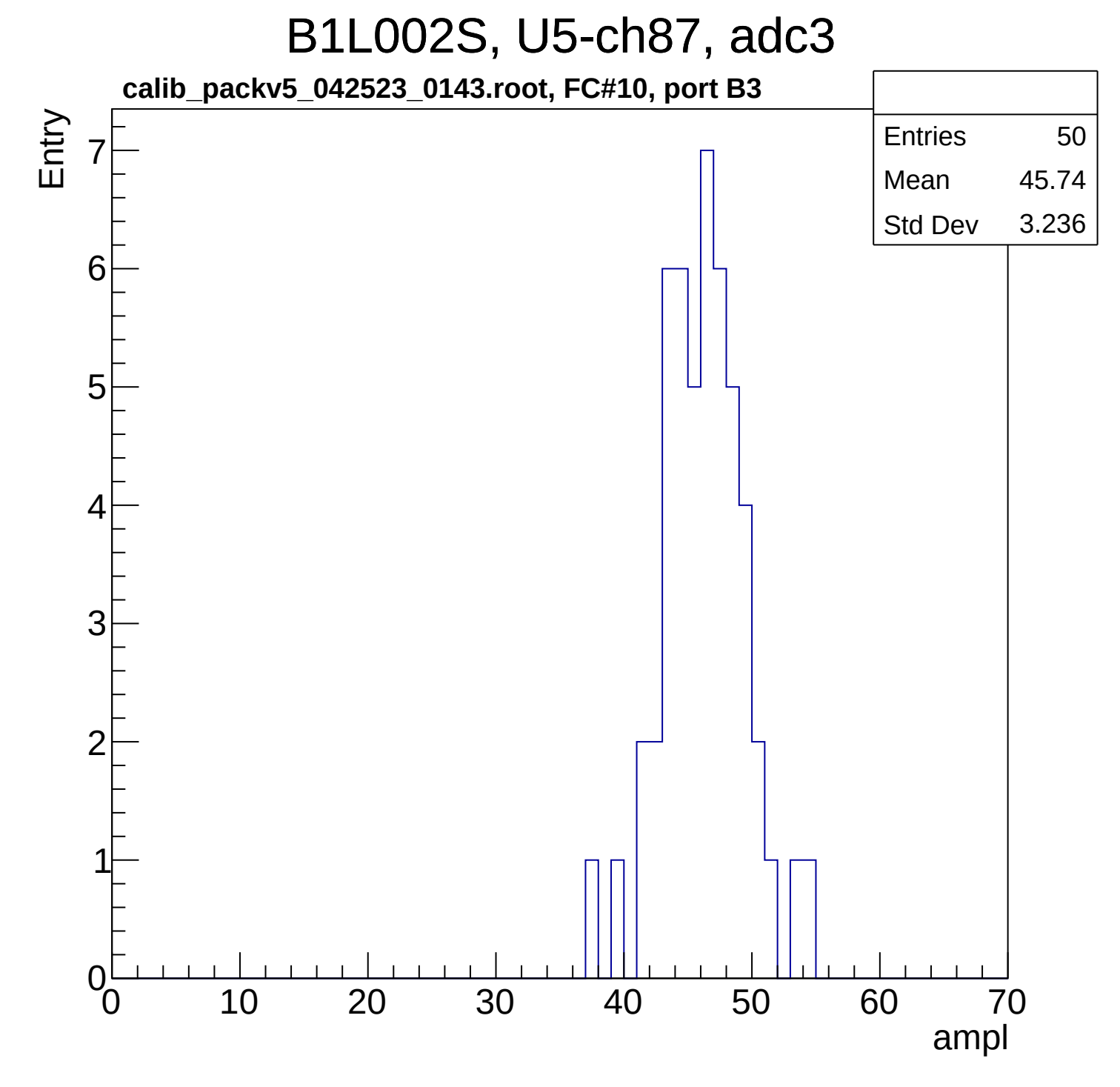
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 45.74
Std Dev 3.236
Entry
ampl
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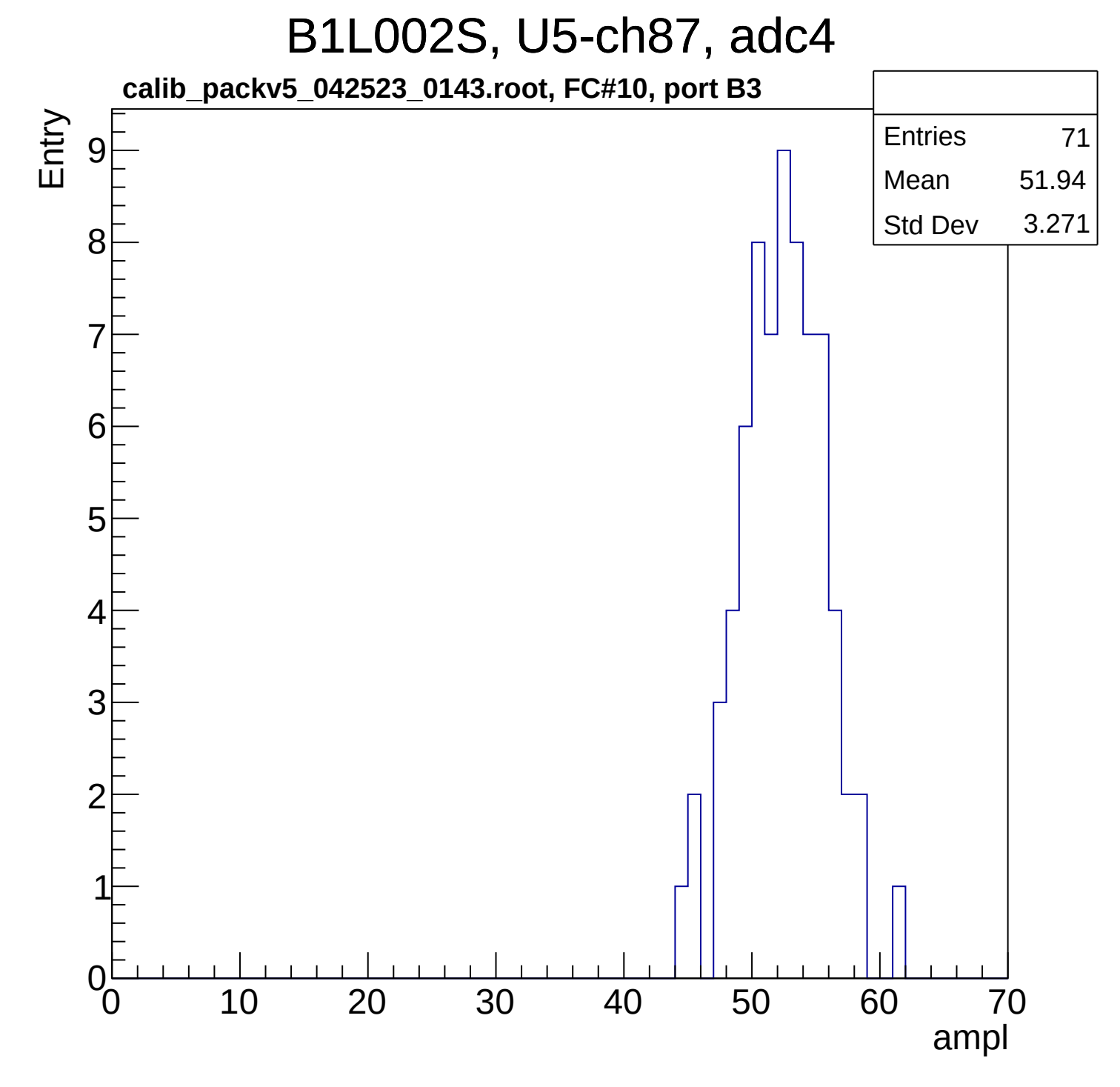
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 51.94
Std Dev 3.271
Entry
ampl
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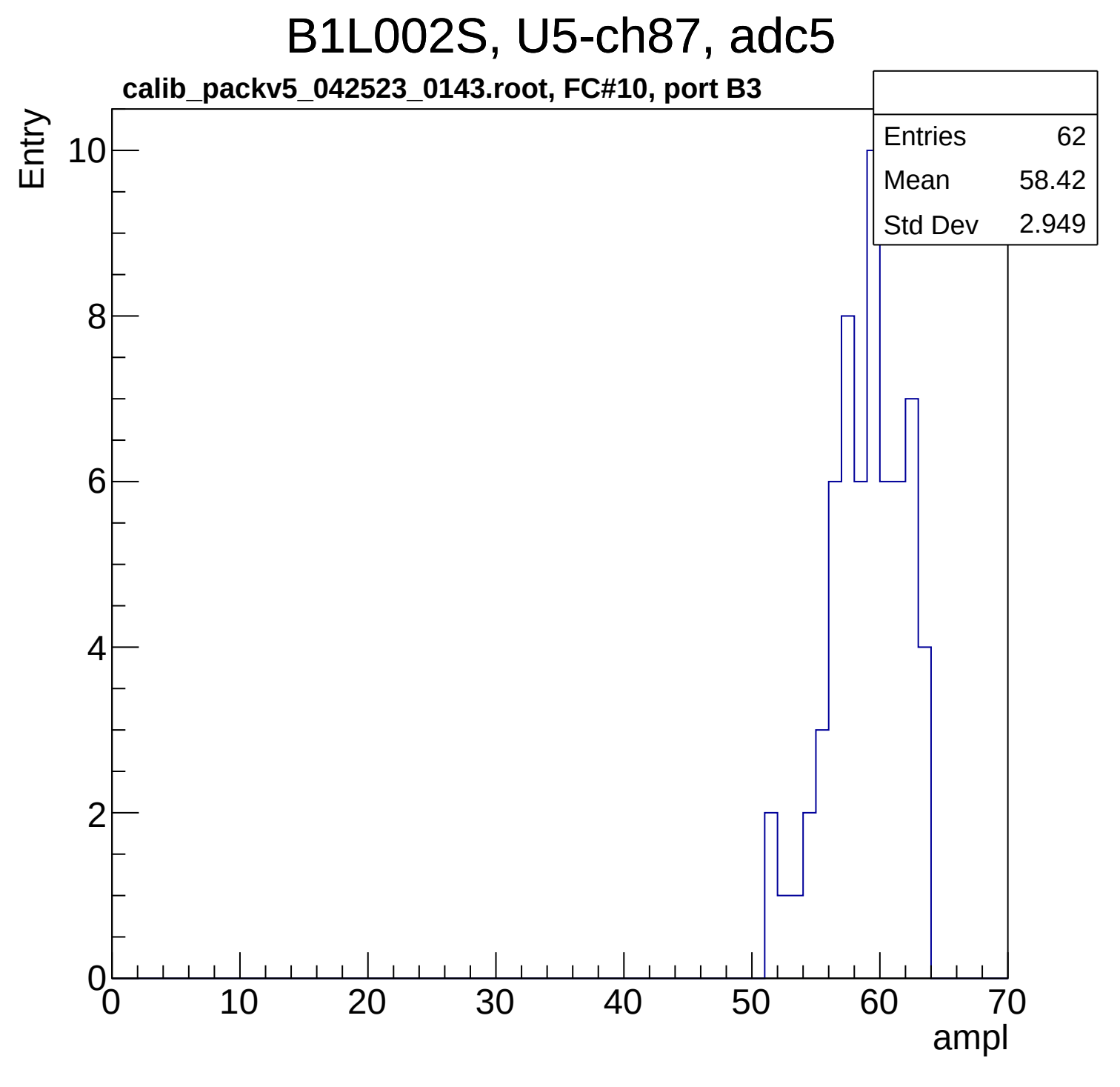
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.42
Std Dev 2.949
Entry
ampl
0
2
4
6
8
10
0
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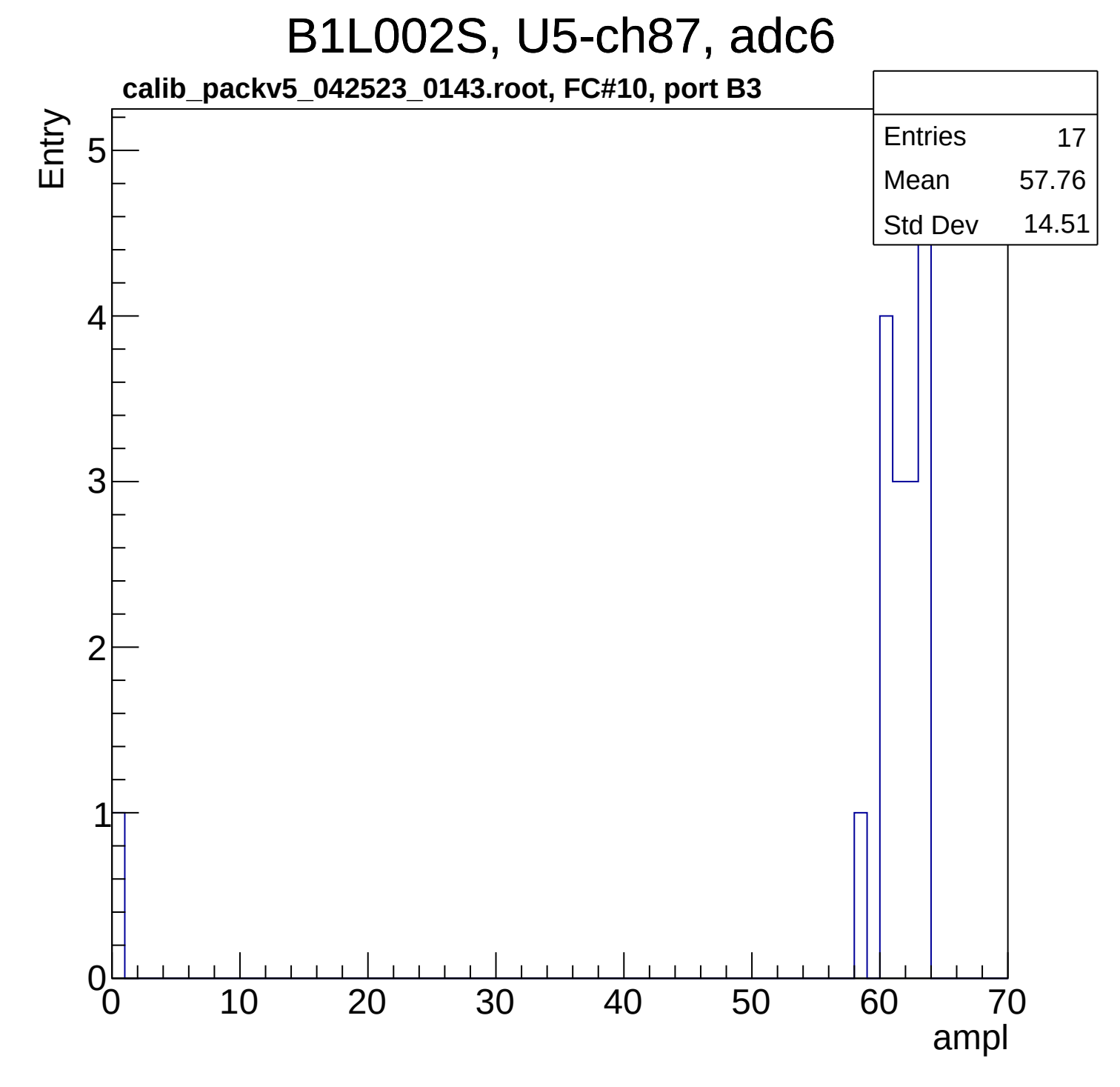
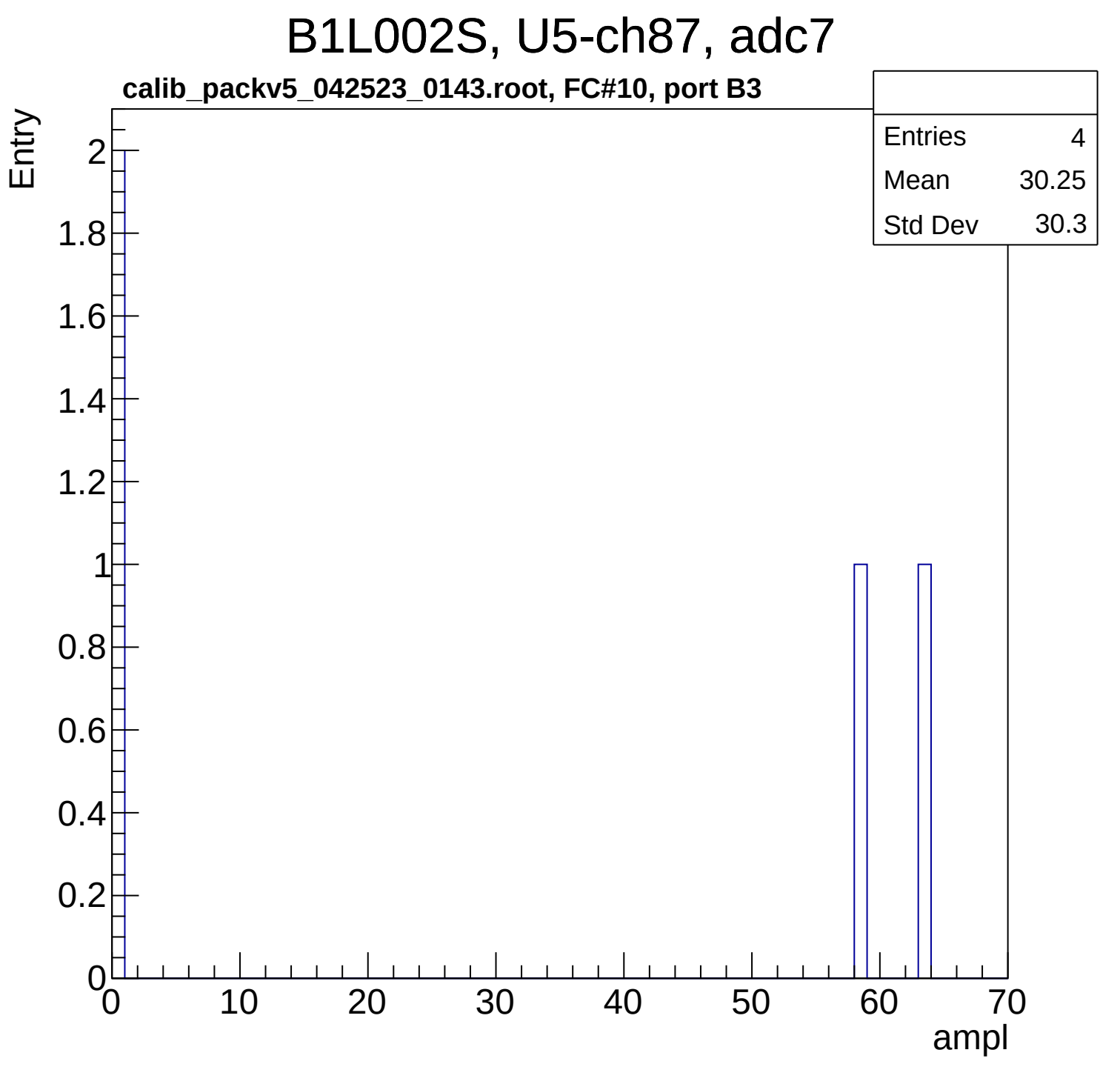
20
30
40
50
60
70


B1L002S, U5-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.76
Std Dev 14.51
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.25
Std Dev 30.3
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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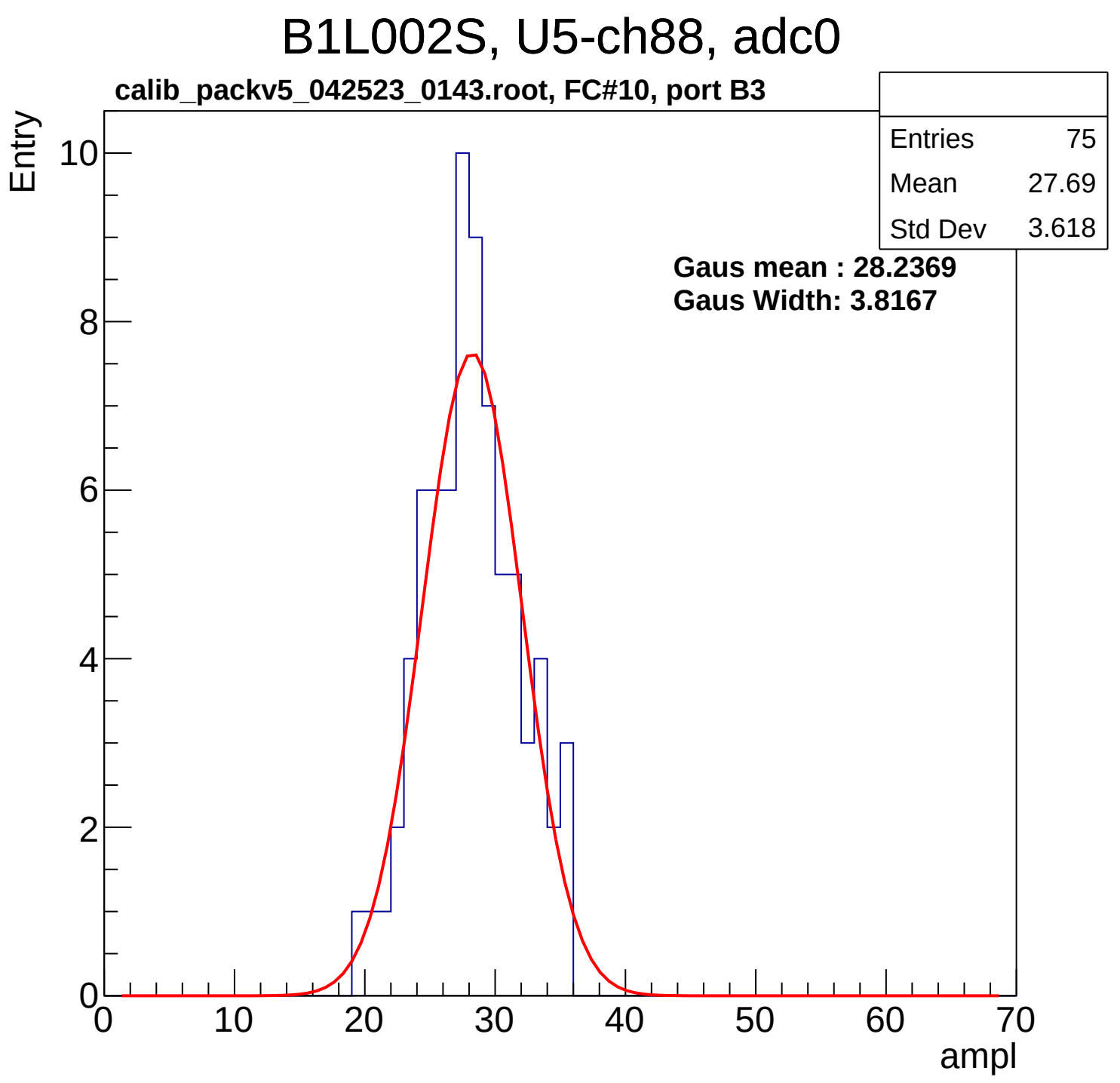
B1L002S, U5-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.69
Std Dev 3.618
Gaus mean : 28.2369
Gaus Width: 3.8167
Entry
ampl
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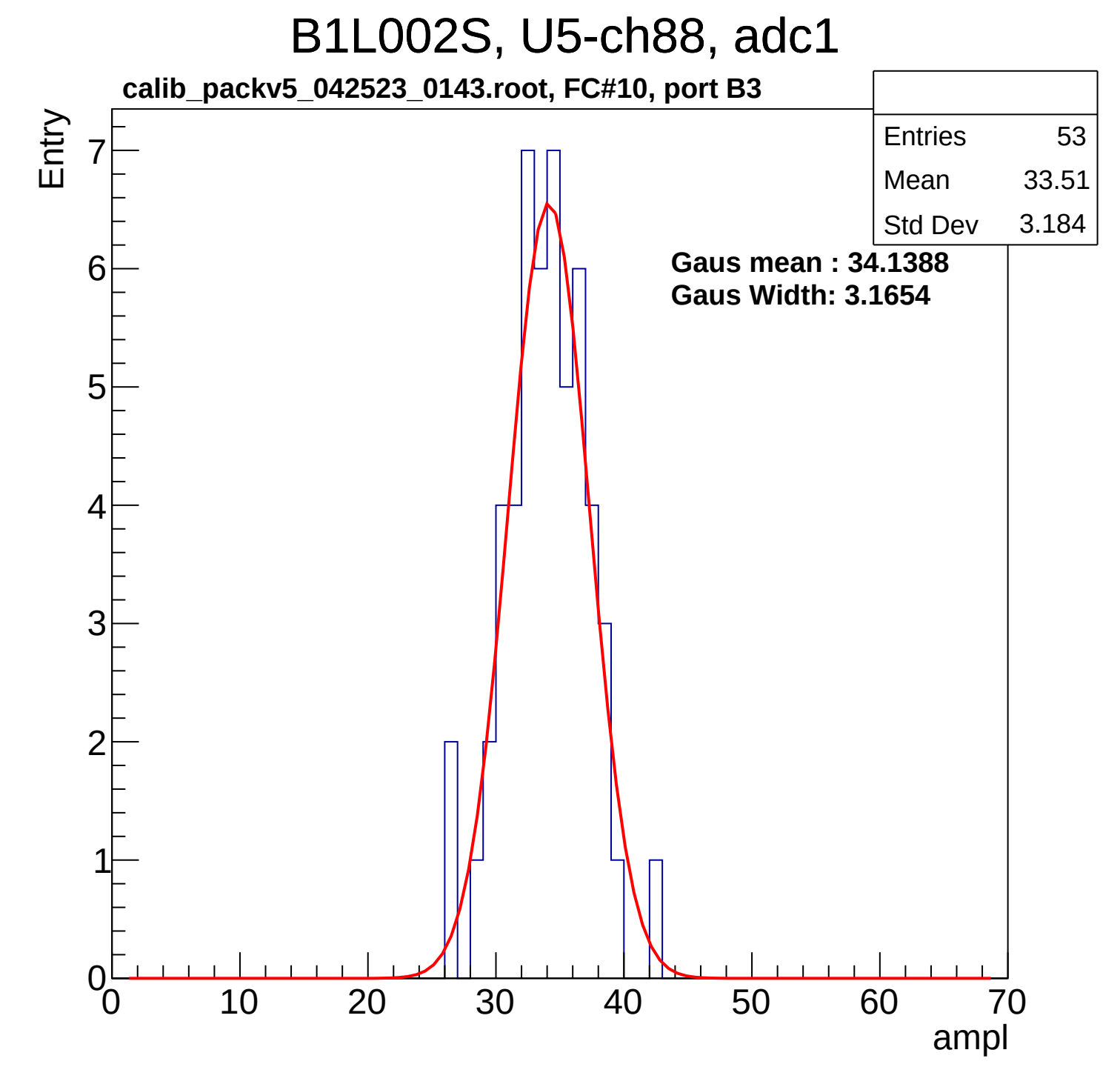
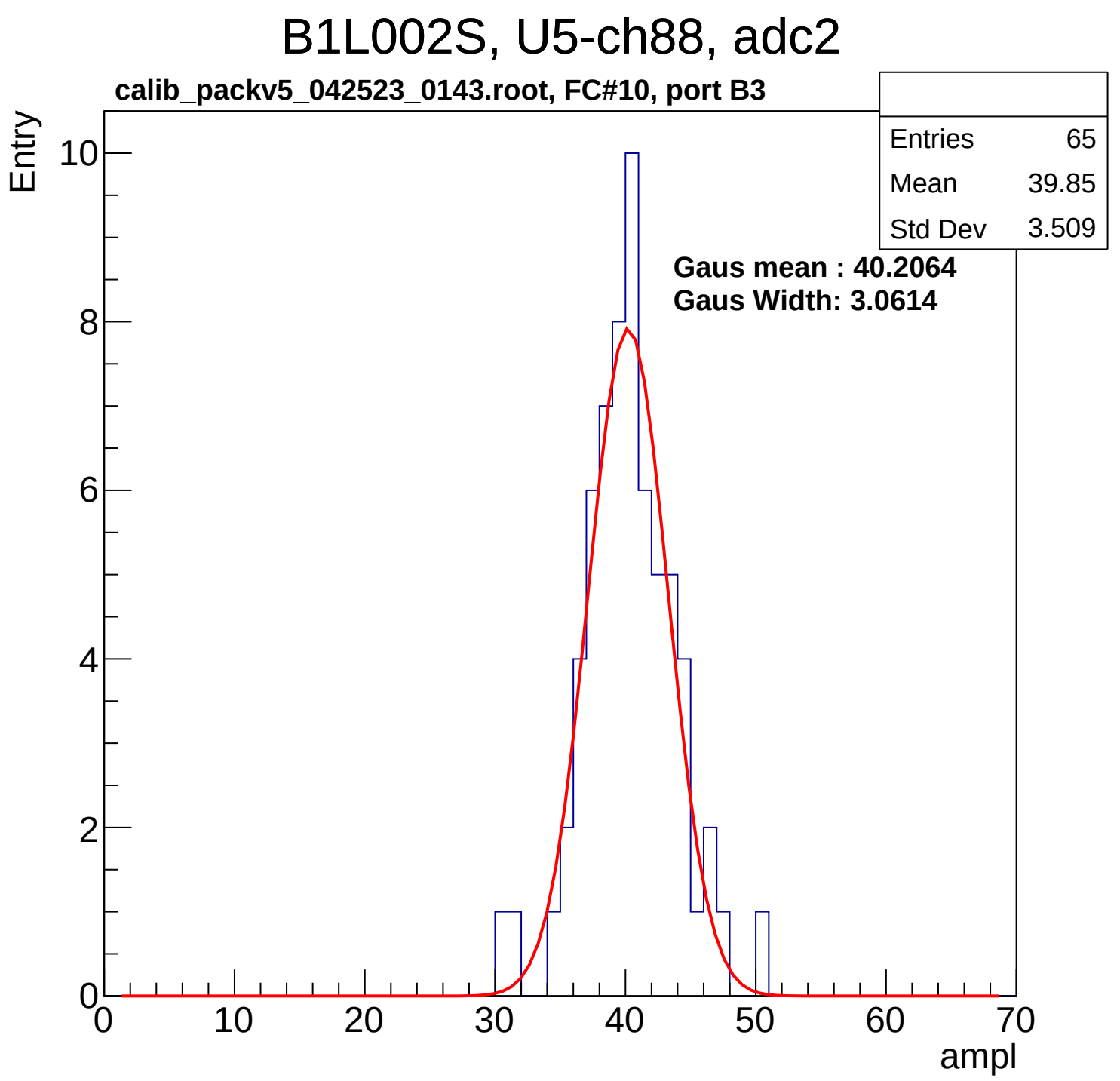
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.51
Std Dev 3.184
Gaus mean : 34.1388
Gaus Width: 3.1654
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 39.85
Std Dev 3.509
Gaus mean : 40.2064
Gaus Width: 3.0614
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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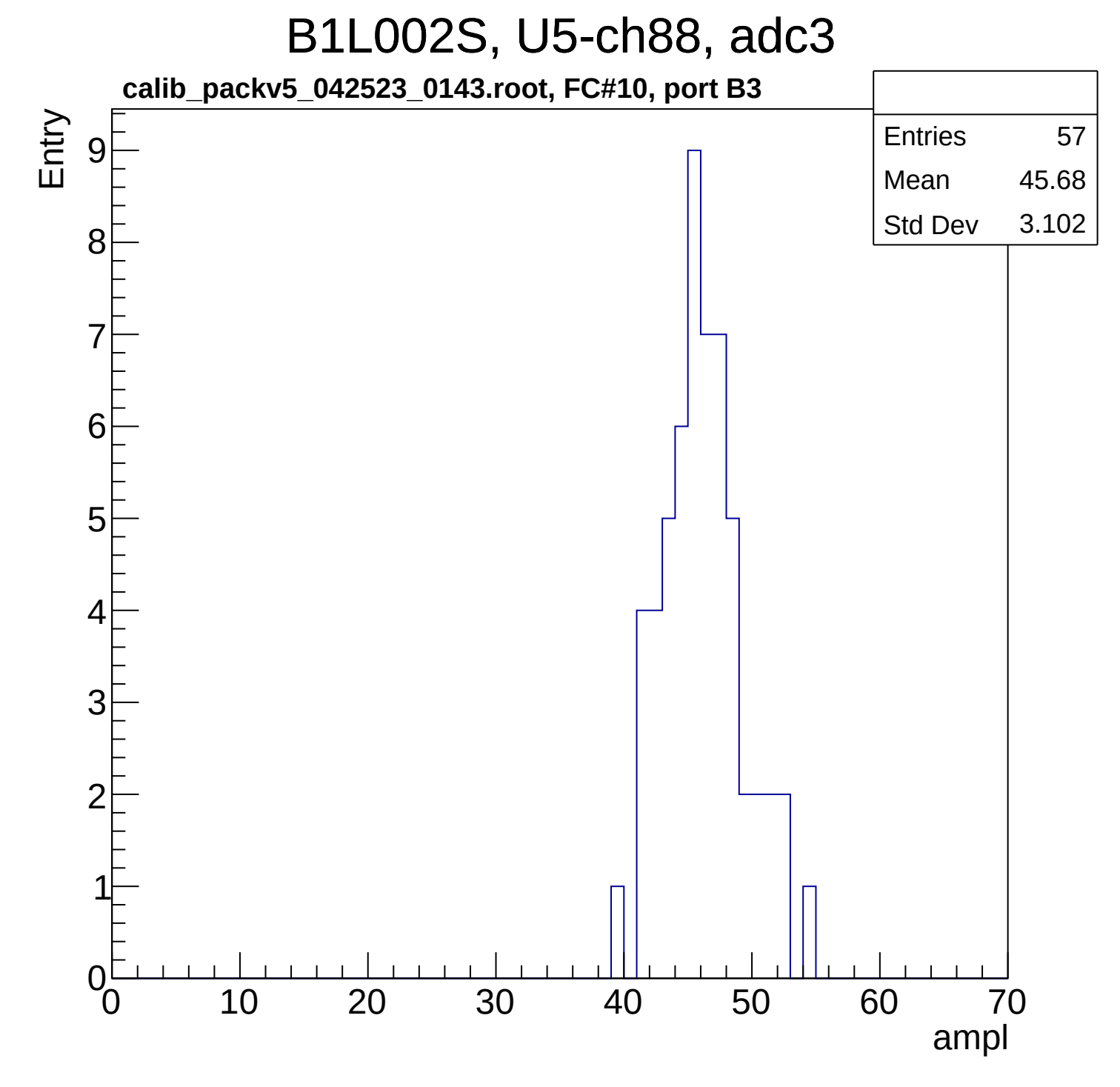
B1L002S, U5-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 45.68
Std Dev 3.102
Entry
ampl
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3
4
5
6
7
8
9
0
10
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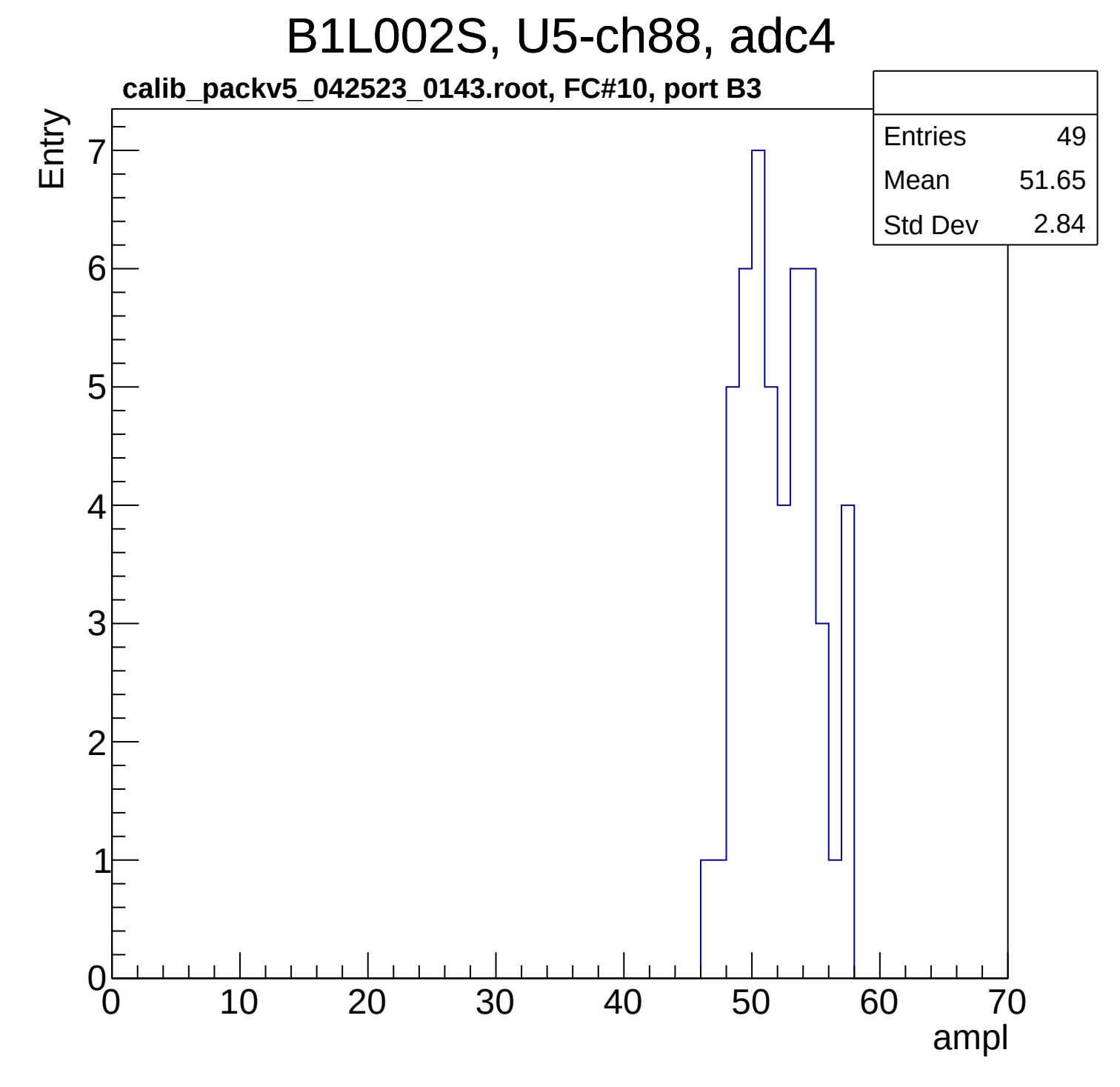
B1L002S, U5-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.65
Std Dev 2.84
Entry
ampl
0
1
2
3
4
5
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0
10
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40
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60
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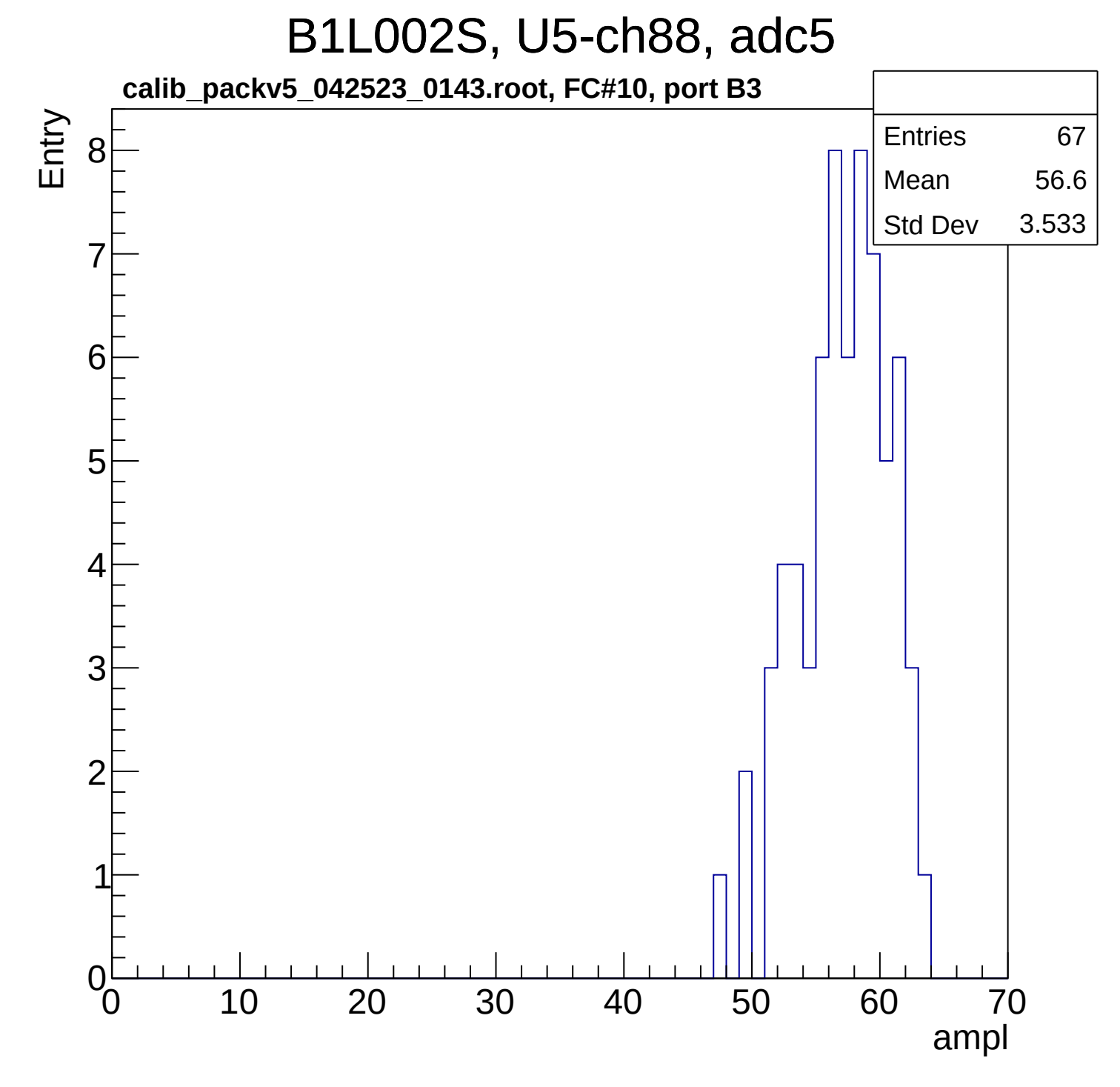
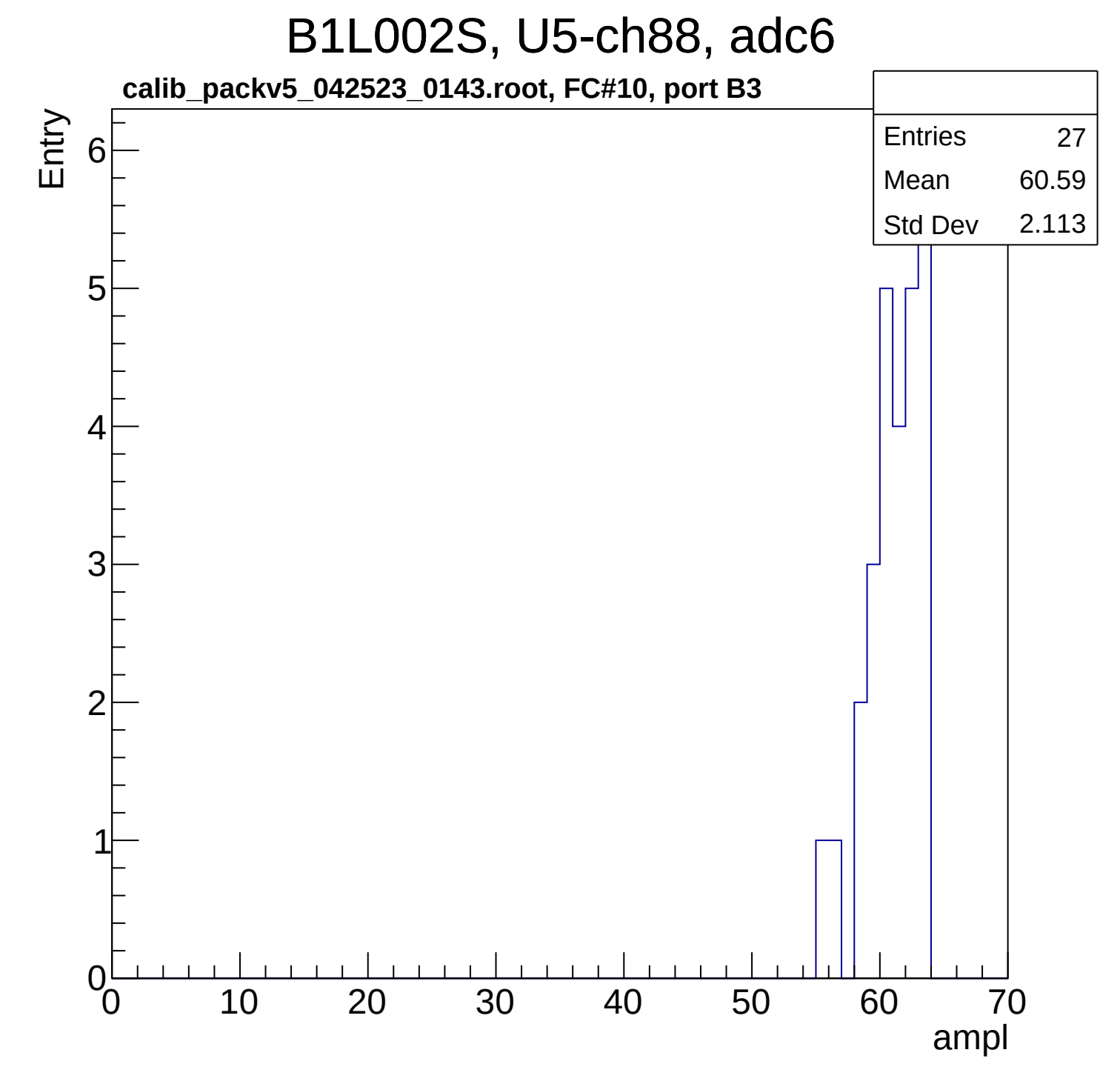

B1L002S, U5-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.6
Std Dev 3.533
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.59
Std Dev 2.113
Entry
0
1
2
3
4
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6
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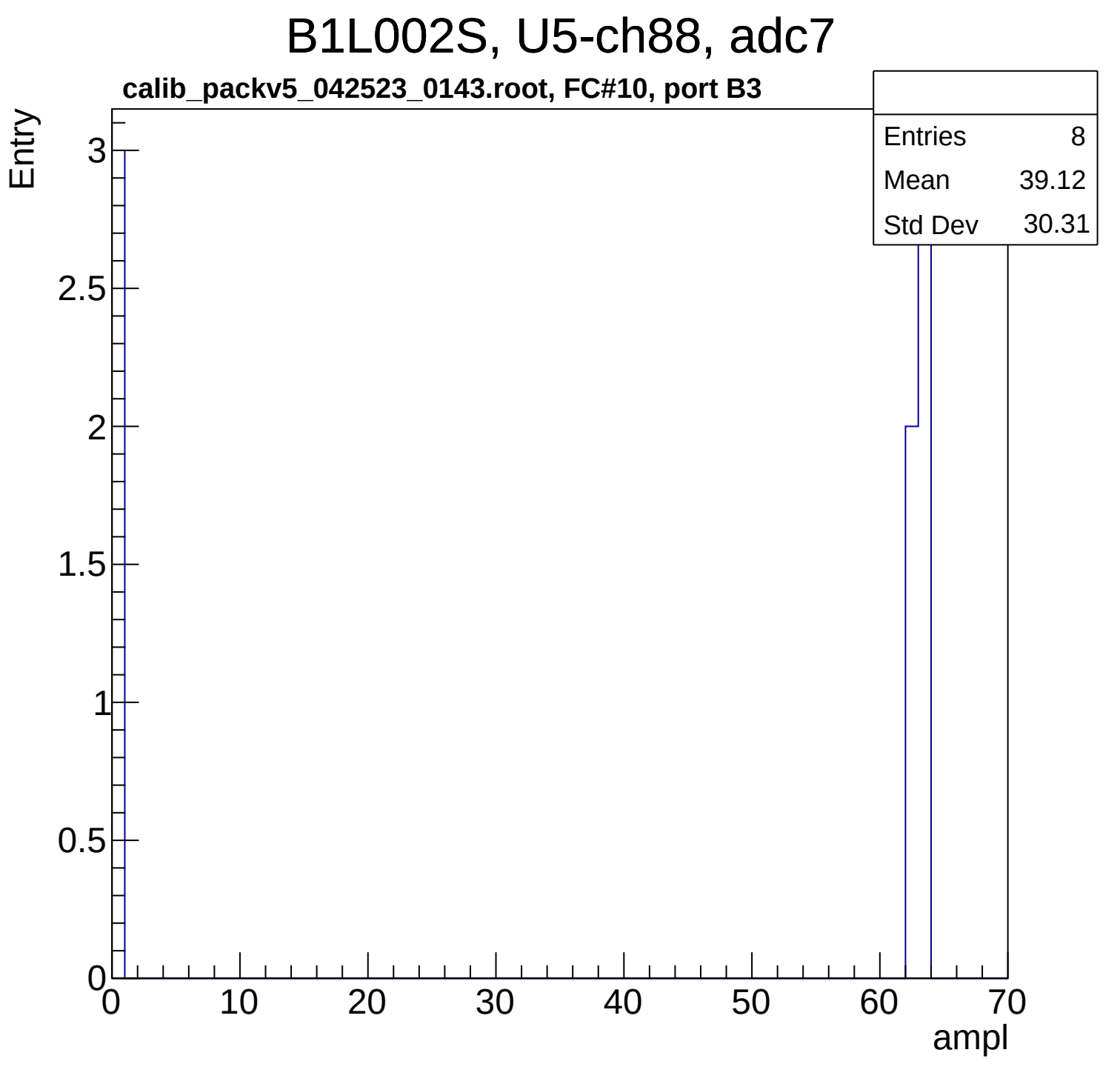
60
70
ampl


B1L002S, U5-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 39.12
Std Dev 30.31
Entry
0
0.5
1
1.5
2
2.5
3
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10
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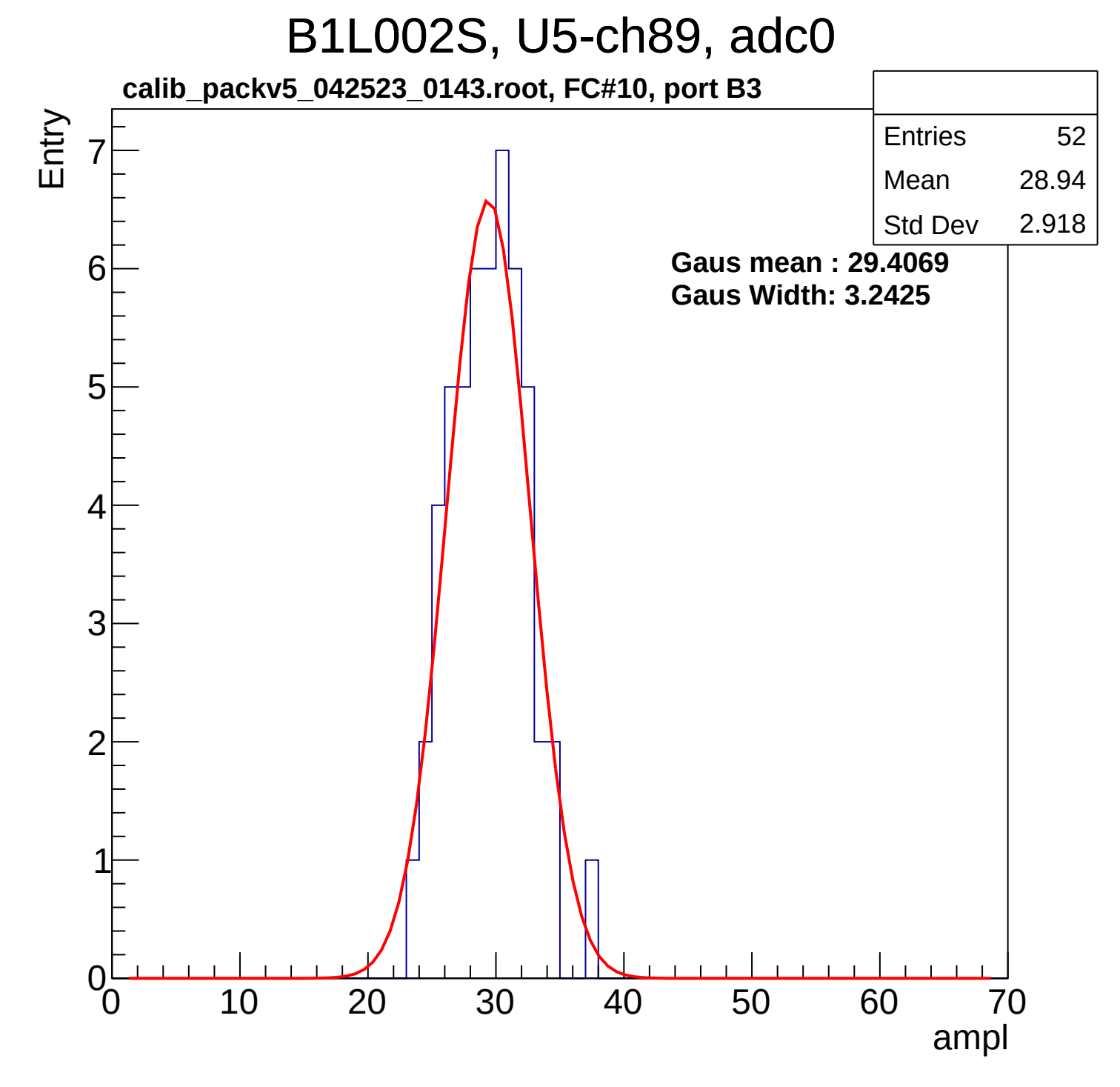
50
60
70
ampl


B1L002S, U5-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28.94
Std Dev 2.918
Gaus mean : 29.4069
Gaus Width: 3.2425
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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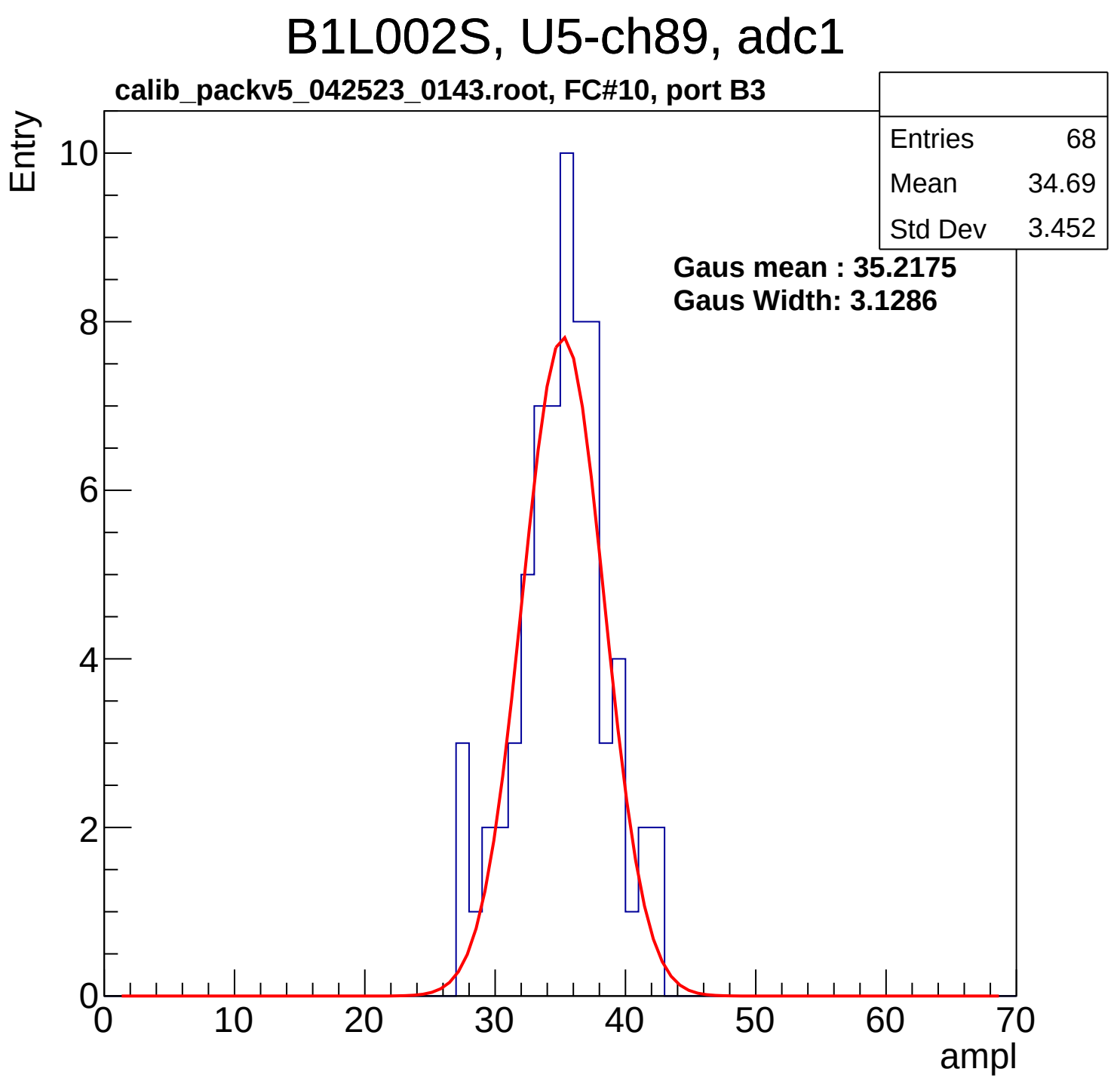
B1L002S, U5-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.69
Std Dev 3.452
Gaus mean : 35.2175
Gaus Width: 3.1286
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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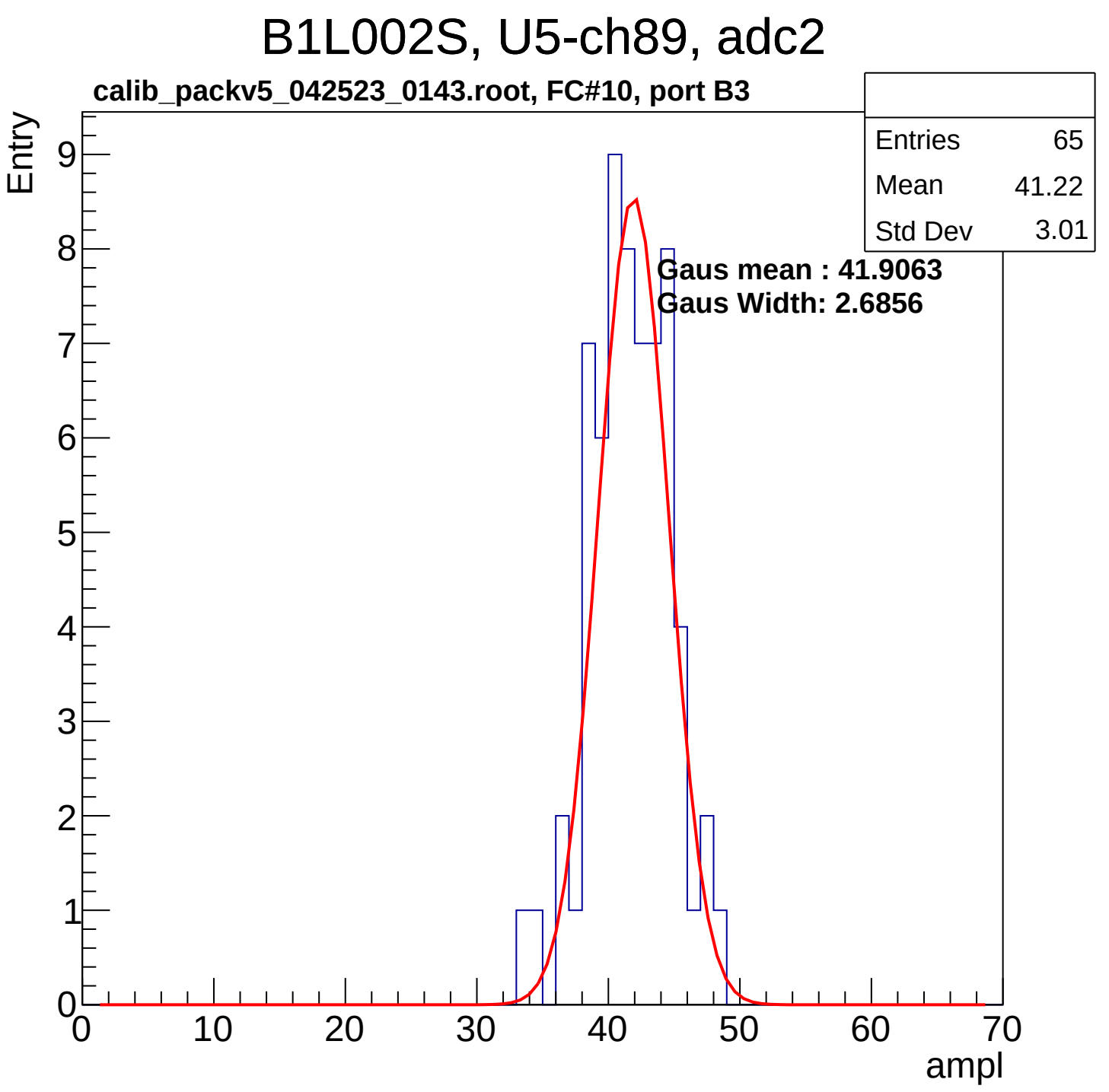
B1L002S, U5-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.22
Std Dev 3.01
Gaus mean : 41.9063
Gaus Width: 2.6856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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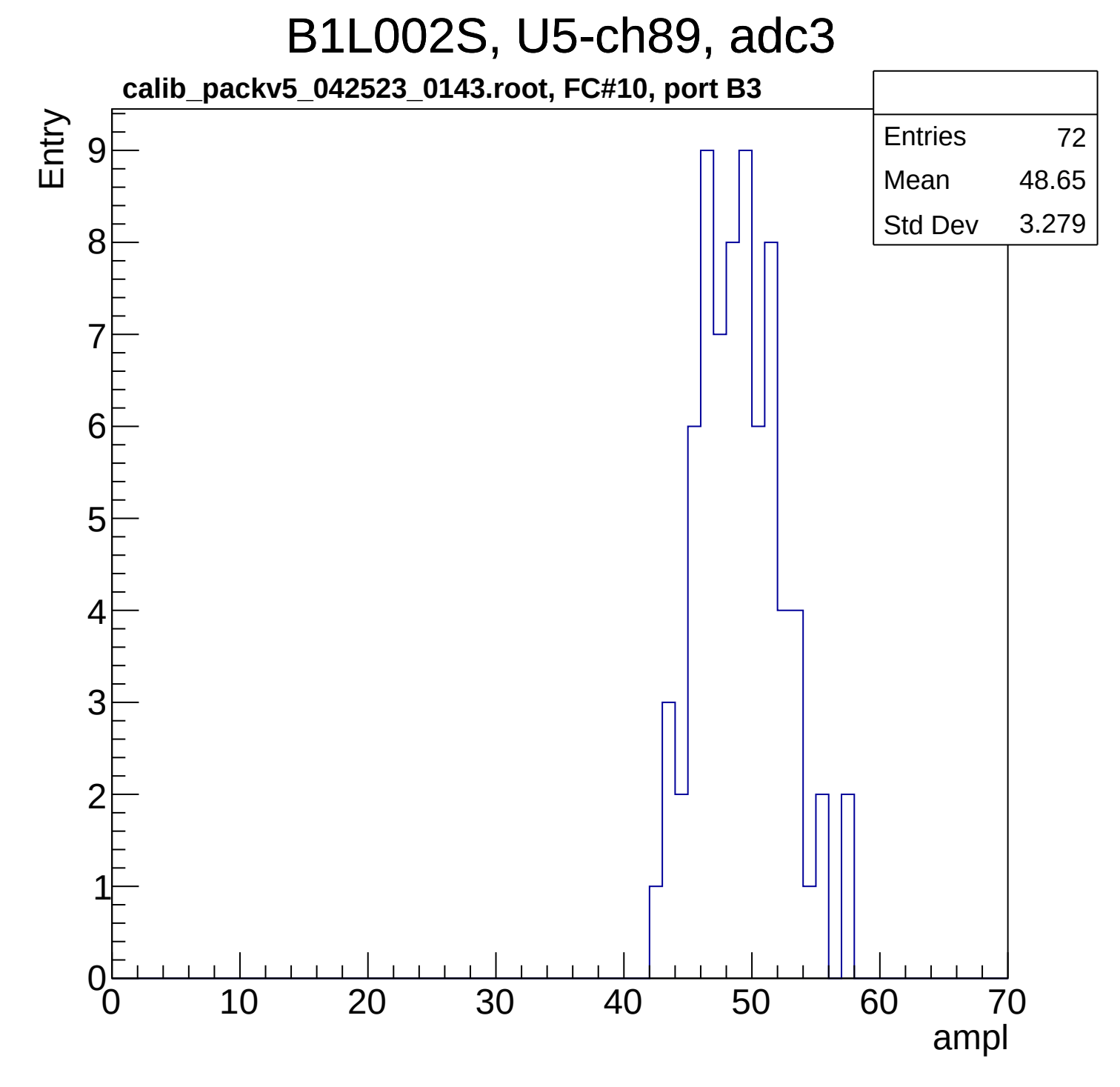
B1L002S, U5-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 48.65
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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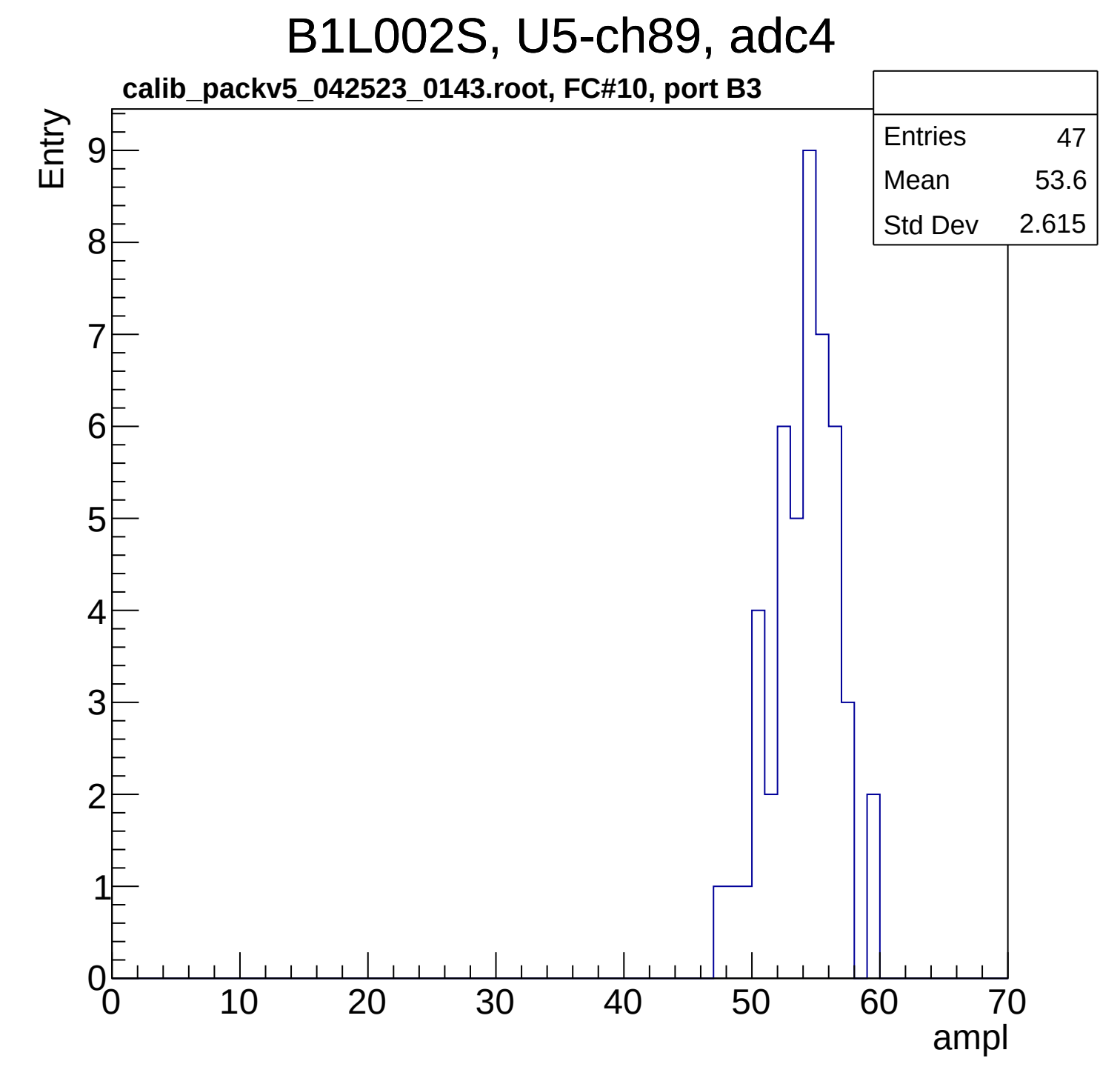
B1L002S, U5-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.6
Std Dev 2.615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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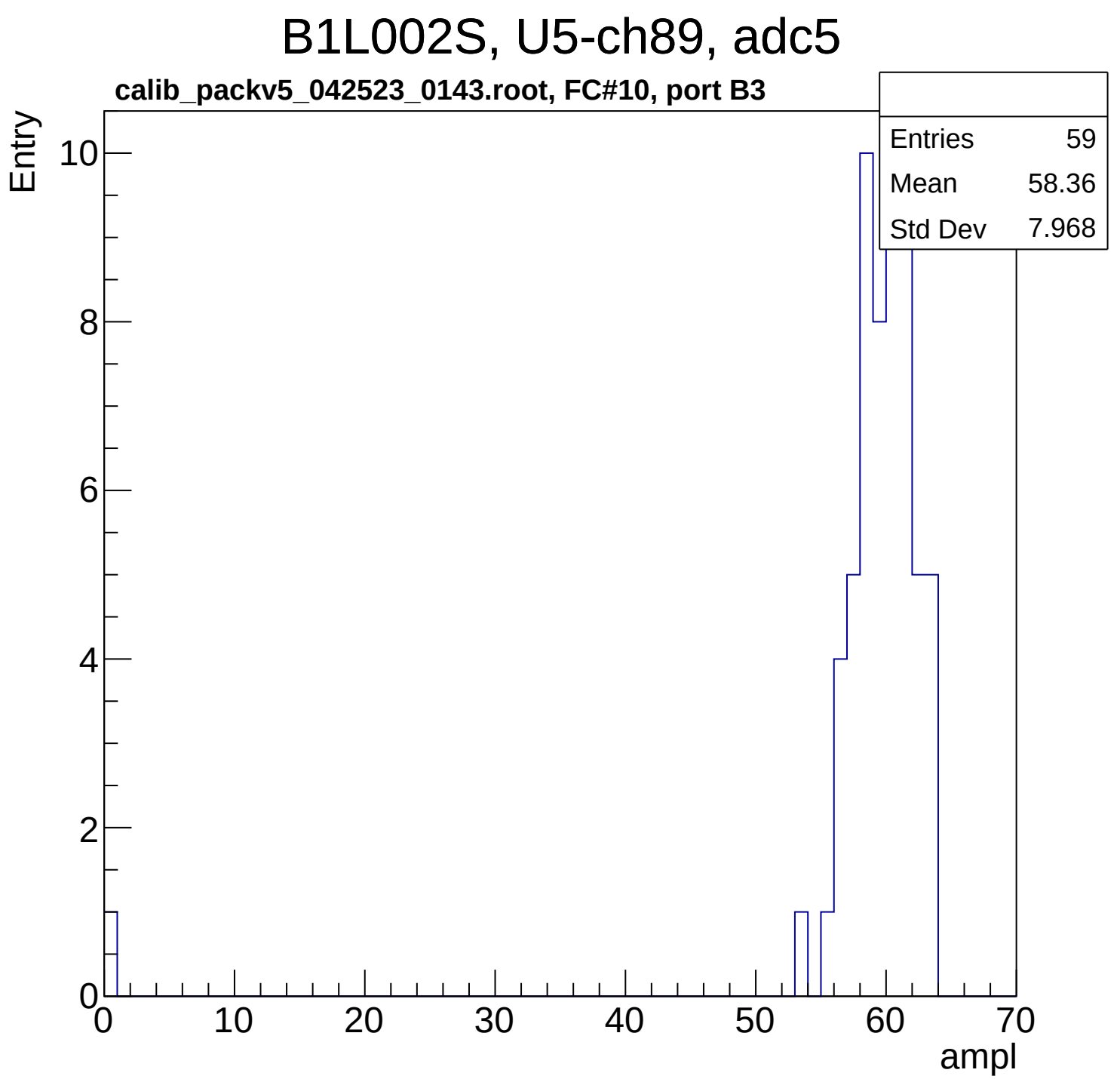
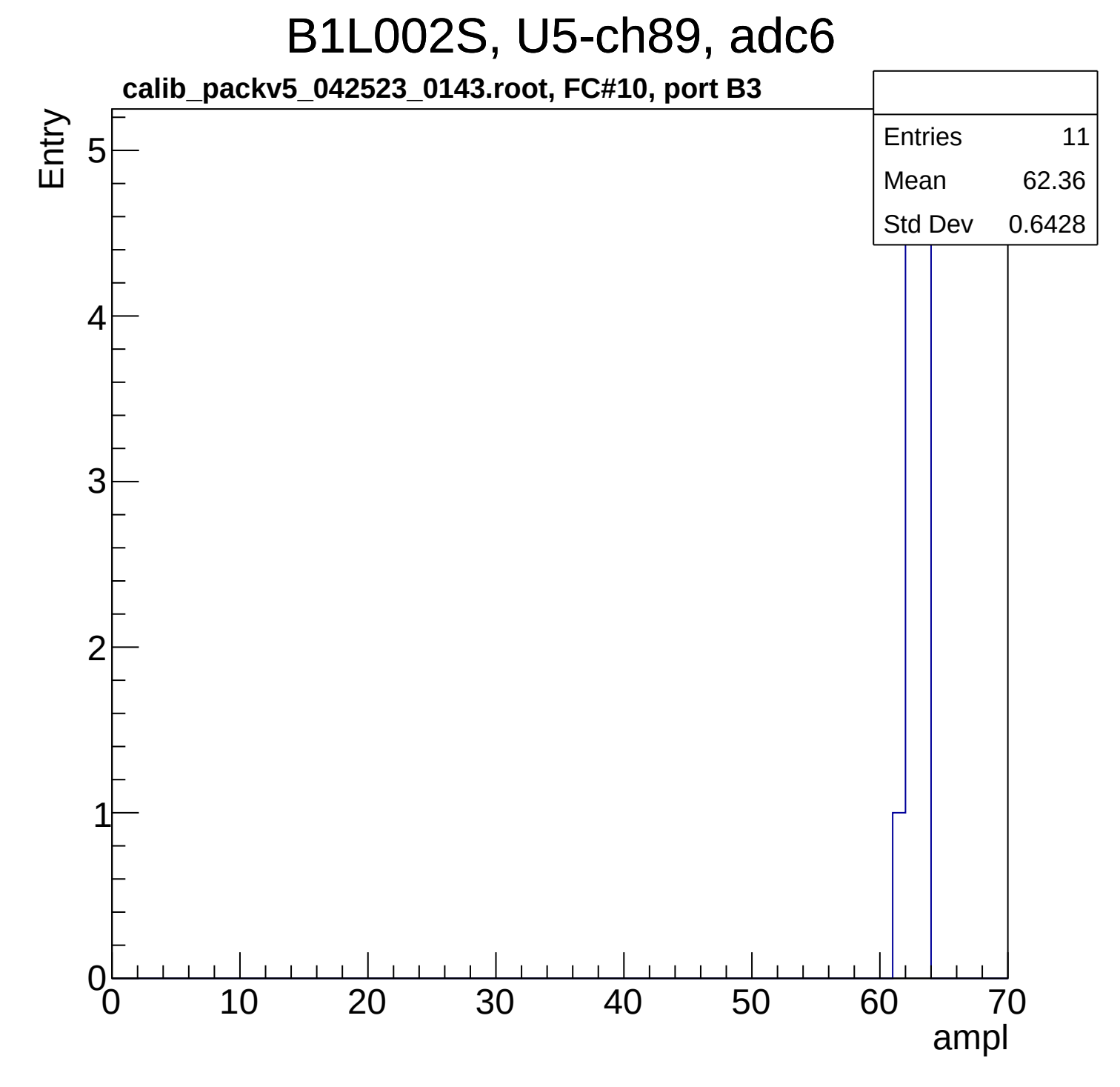
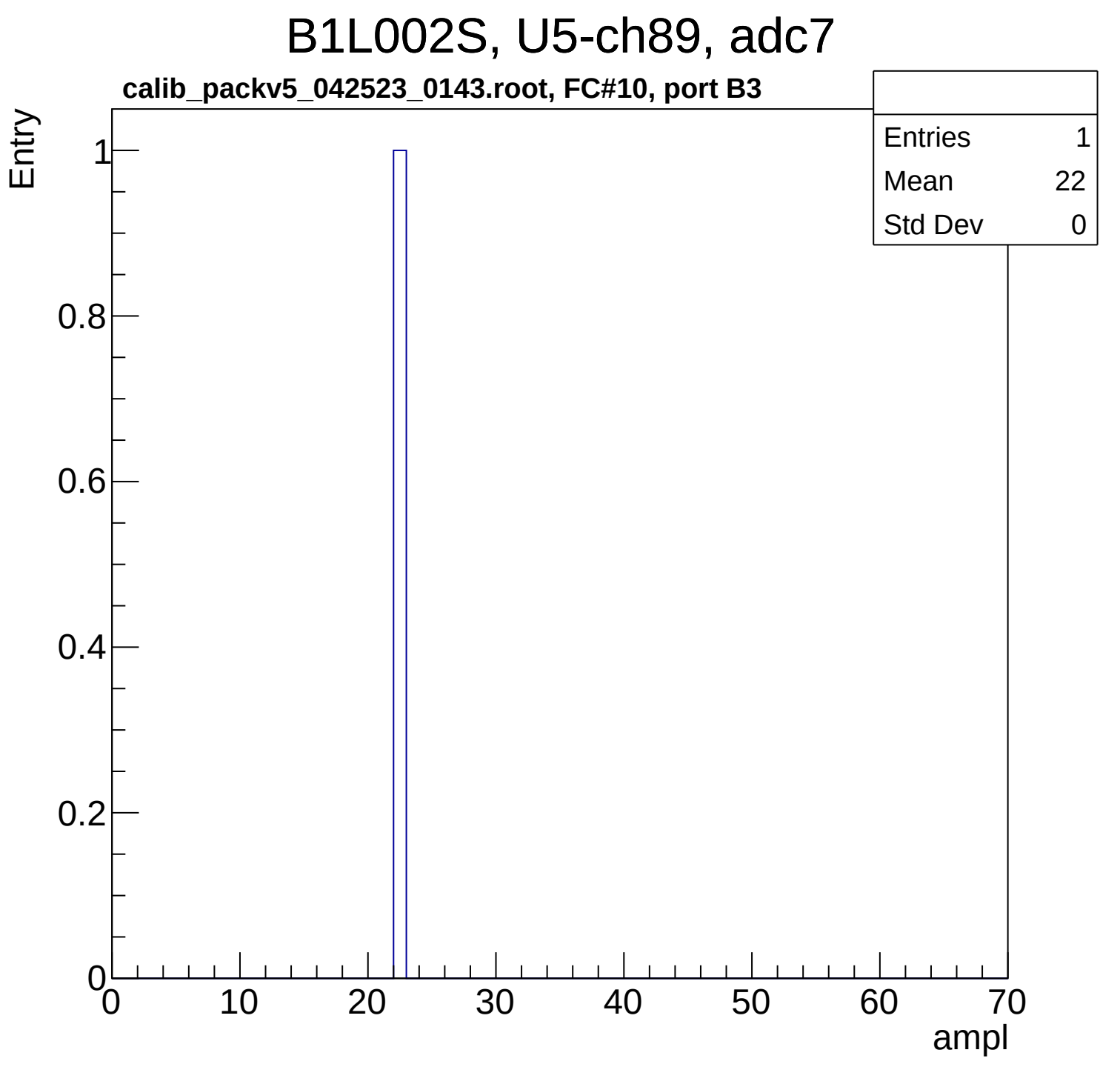

B1L002S, U5-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.36
Std Dev 7.968
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.36
Std Dev 0.6428
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
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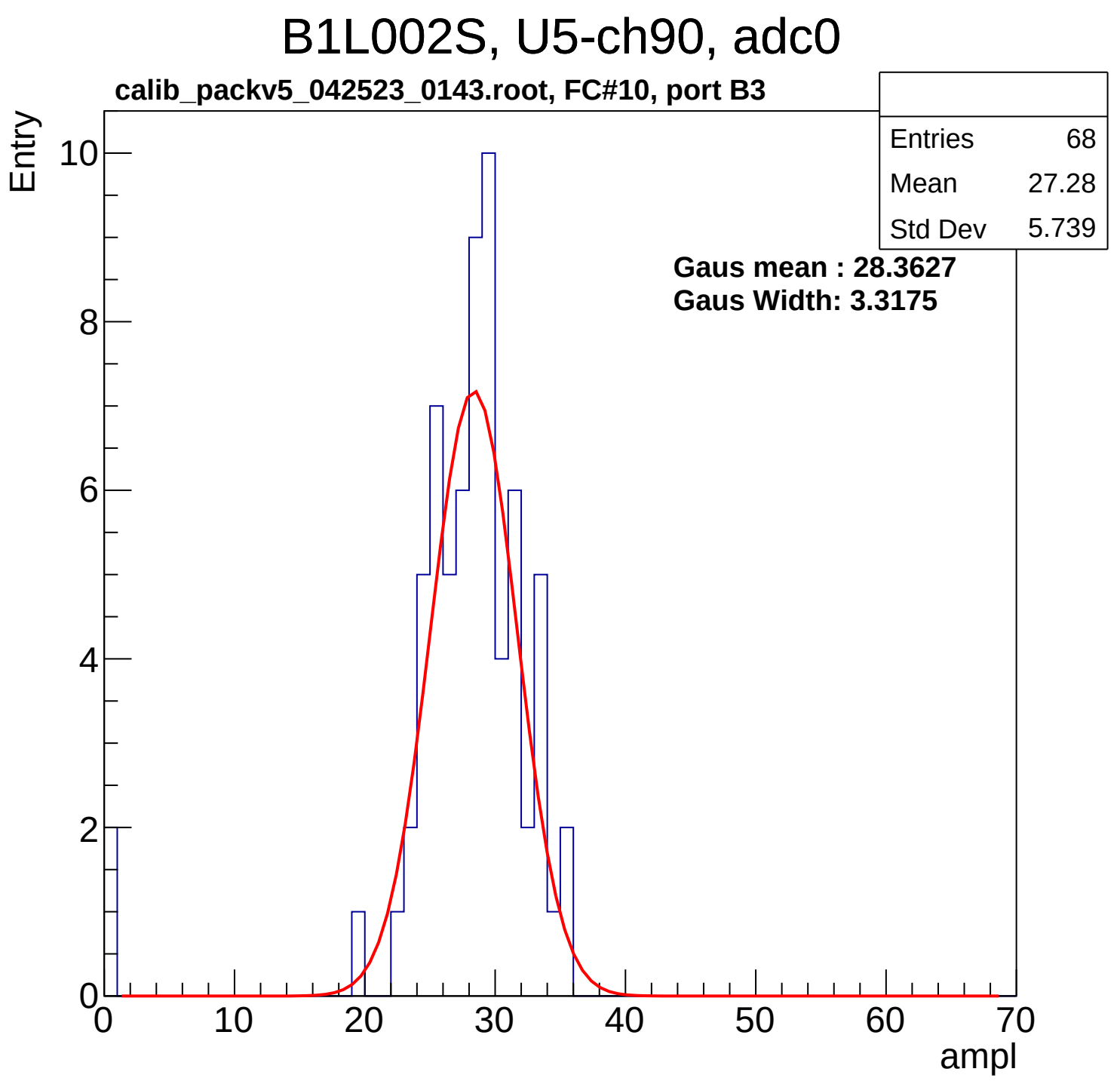
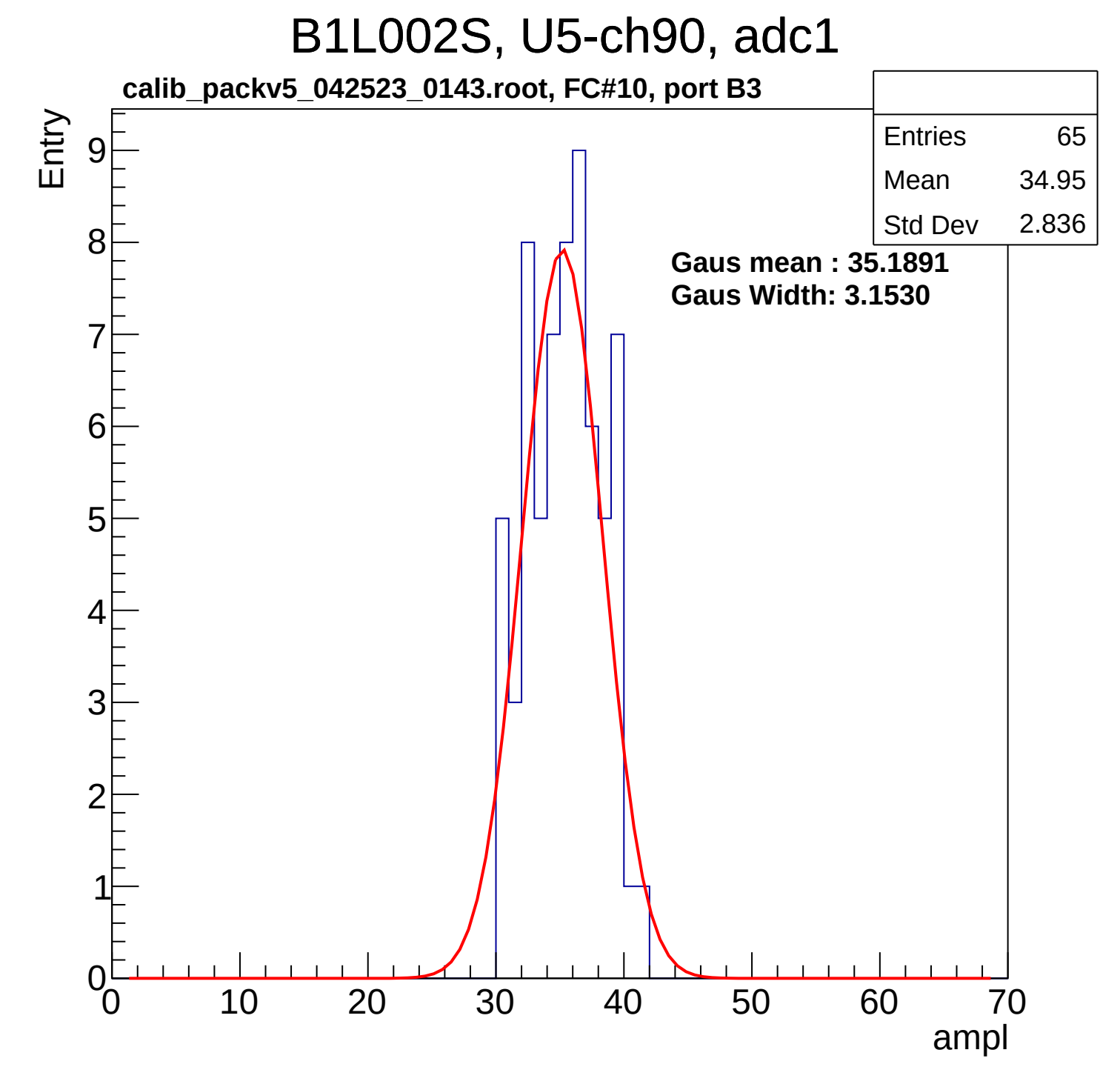
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.28
Std Dev 5.739
Gaus mean : 28.3627
Gaus Width: 3.3175
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.95
Std Dev 2.836
Gaus mean : 35.1891
Gaus Width: 3.1530
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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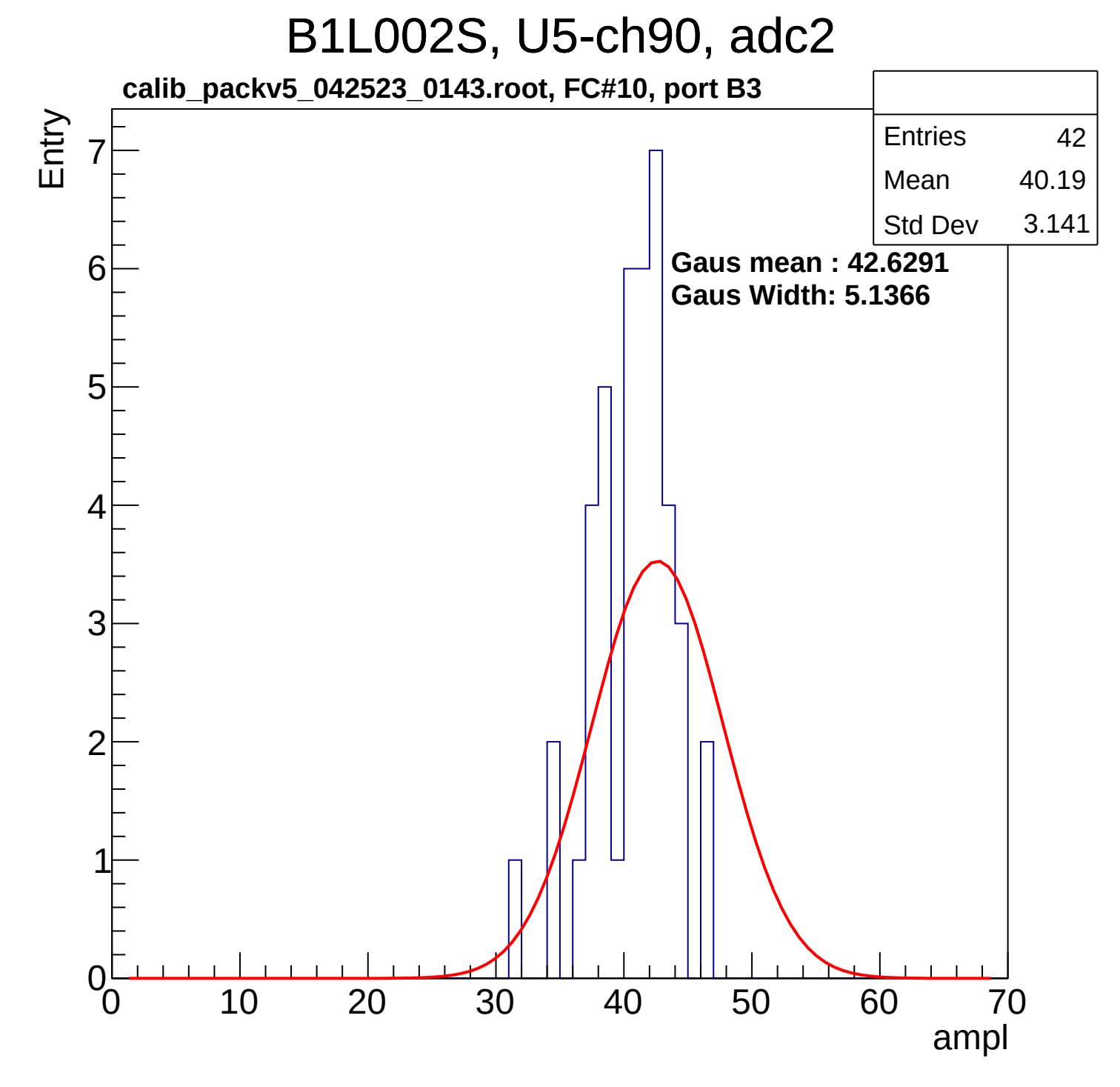
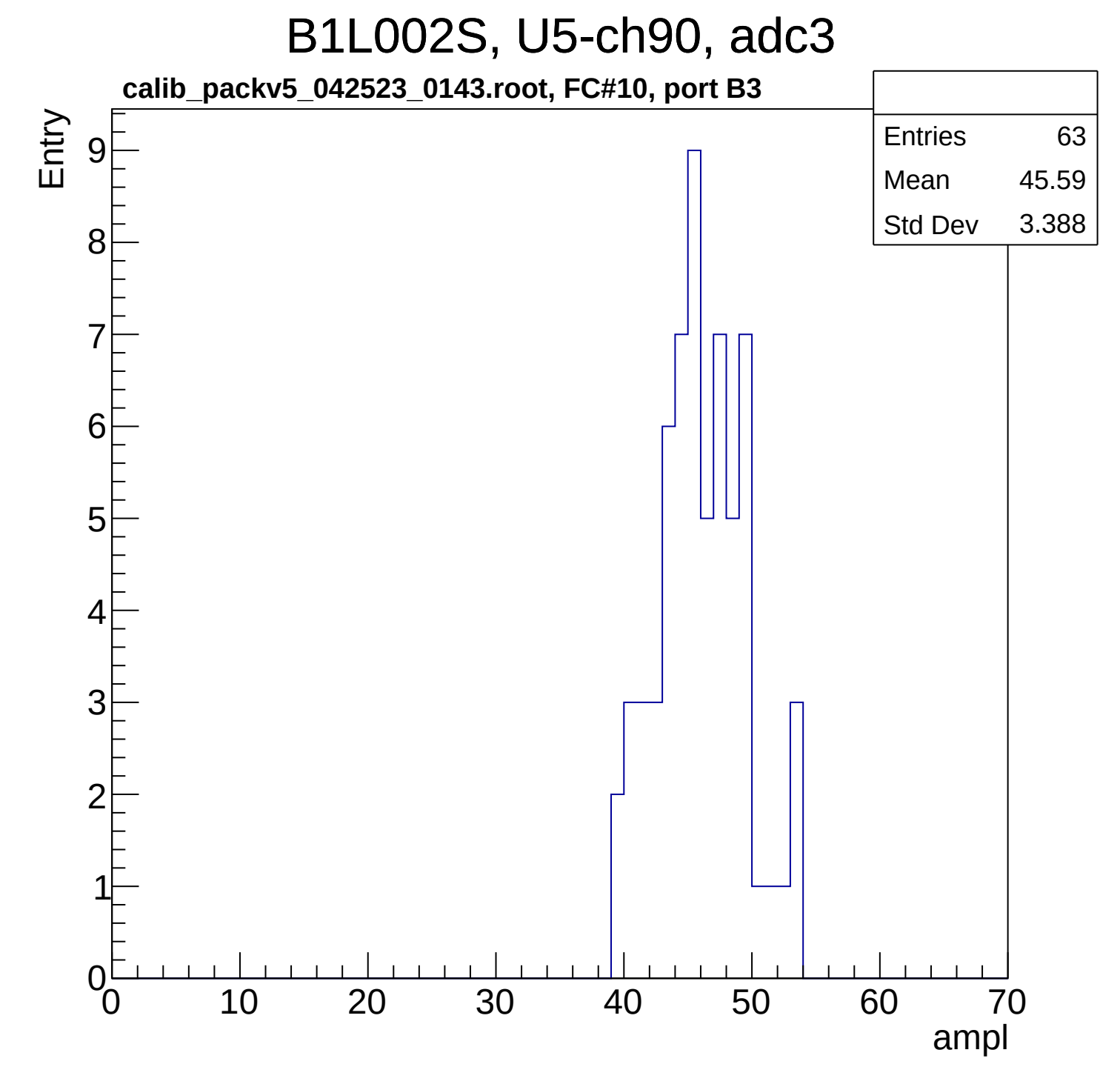

B1L002S, U5-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 40.19
Std Dev 3.141
Gaus mean : 42.6291
Gaus Width: 5.1366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 45.59
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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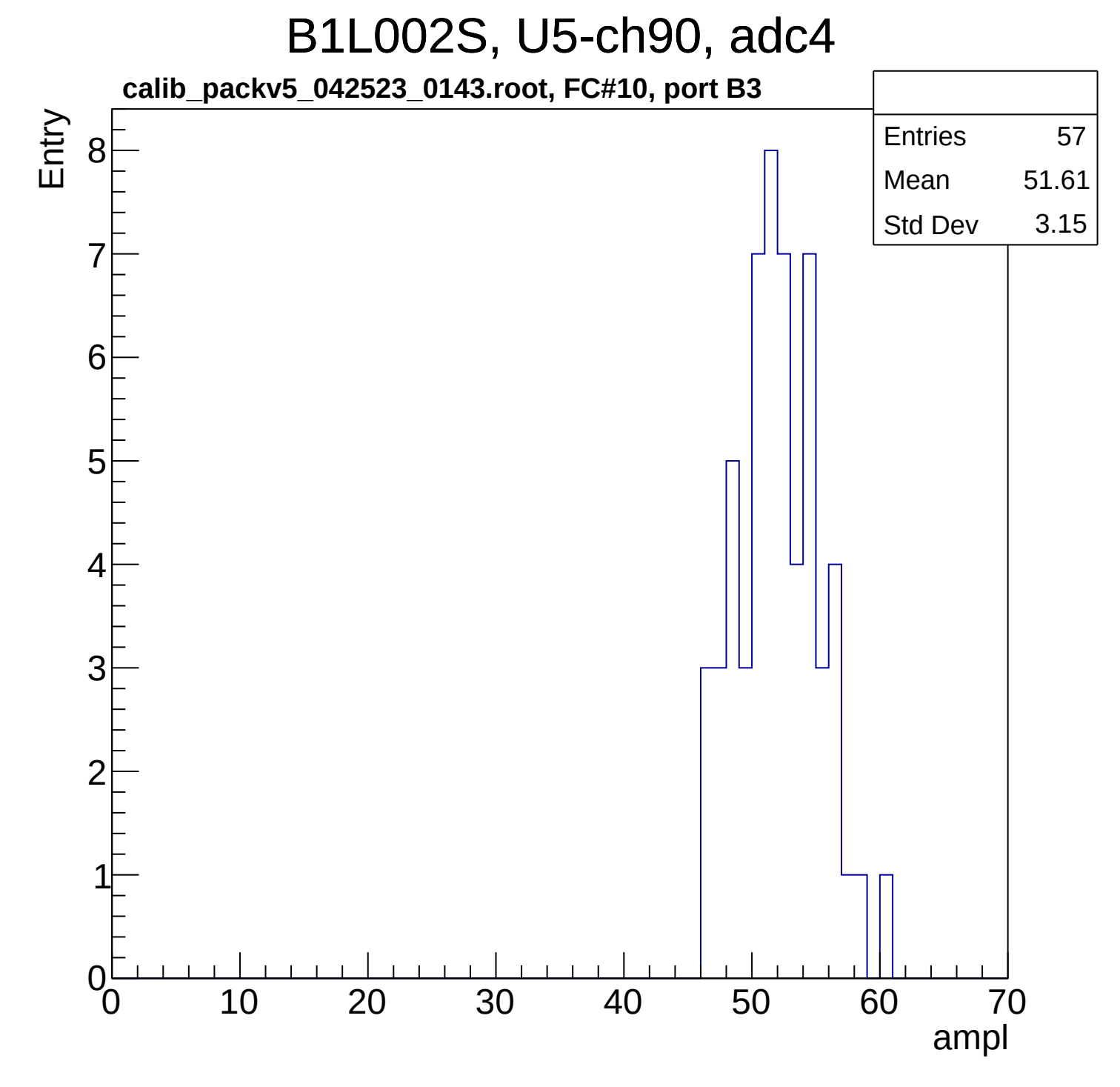
B1L002S, U5-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.61
Std Dev 3.15
Entry
ampl
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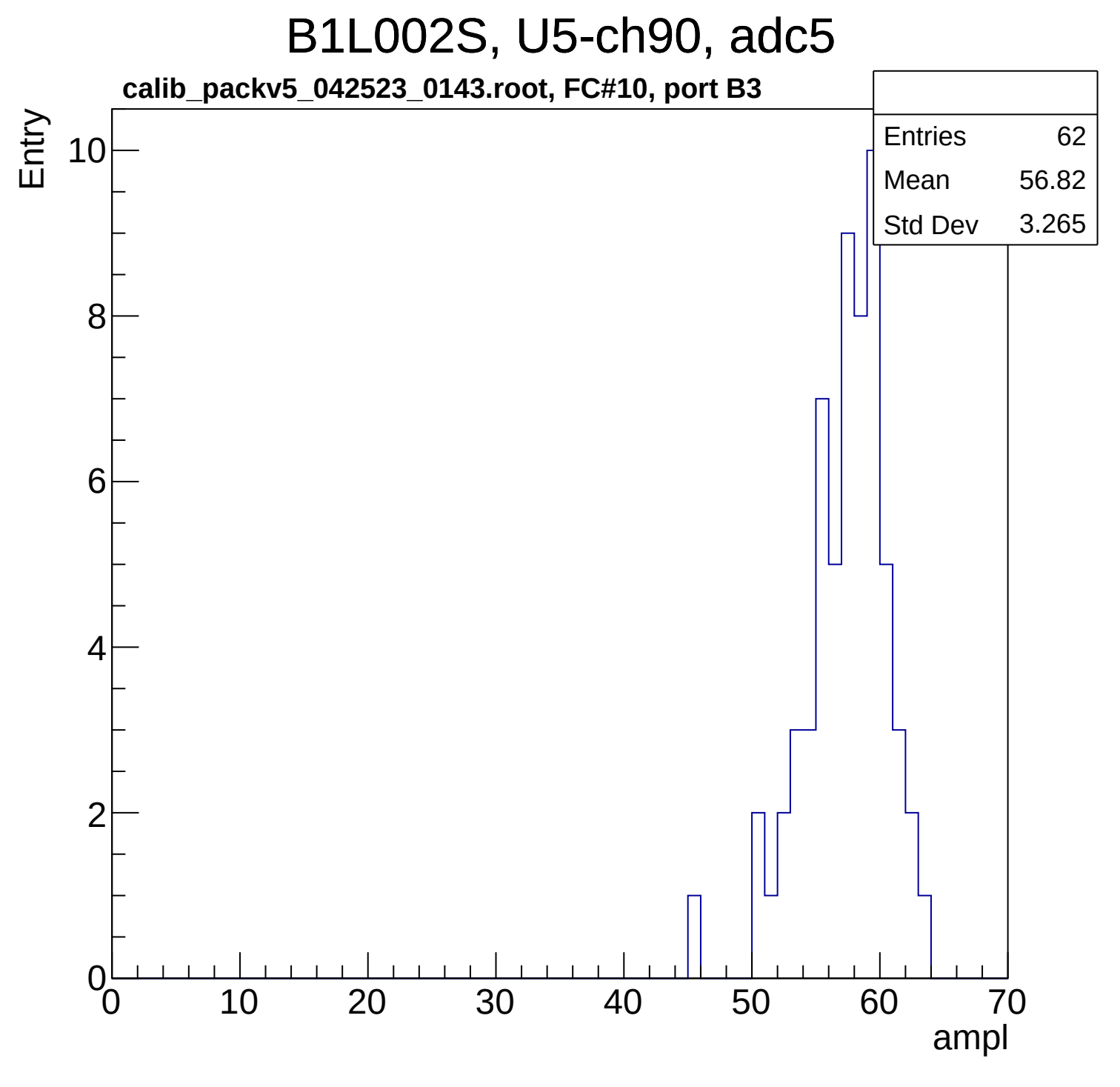
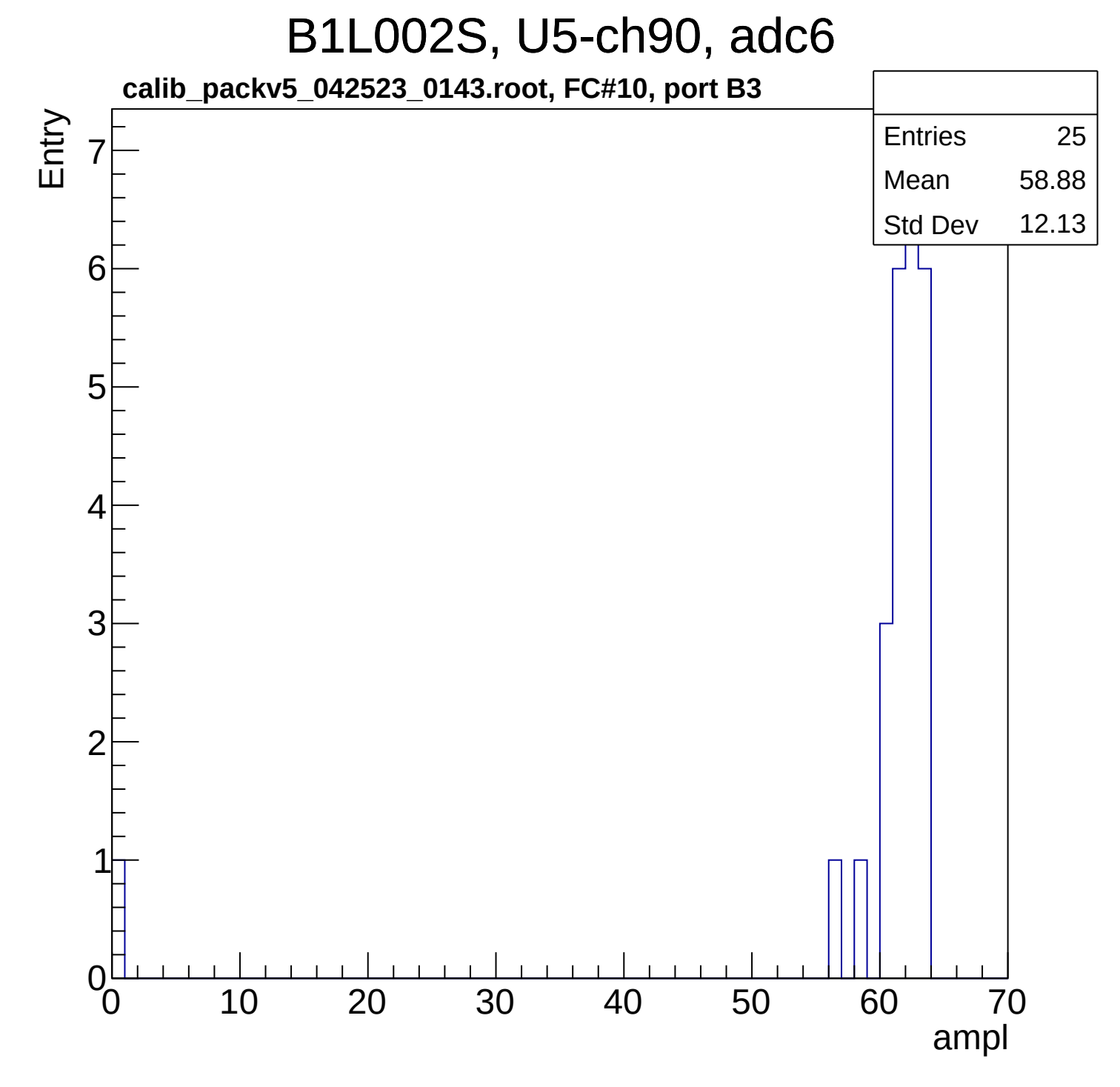
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.82
Std Dev 3.265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.88
Std Dev 12.13
Entry
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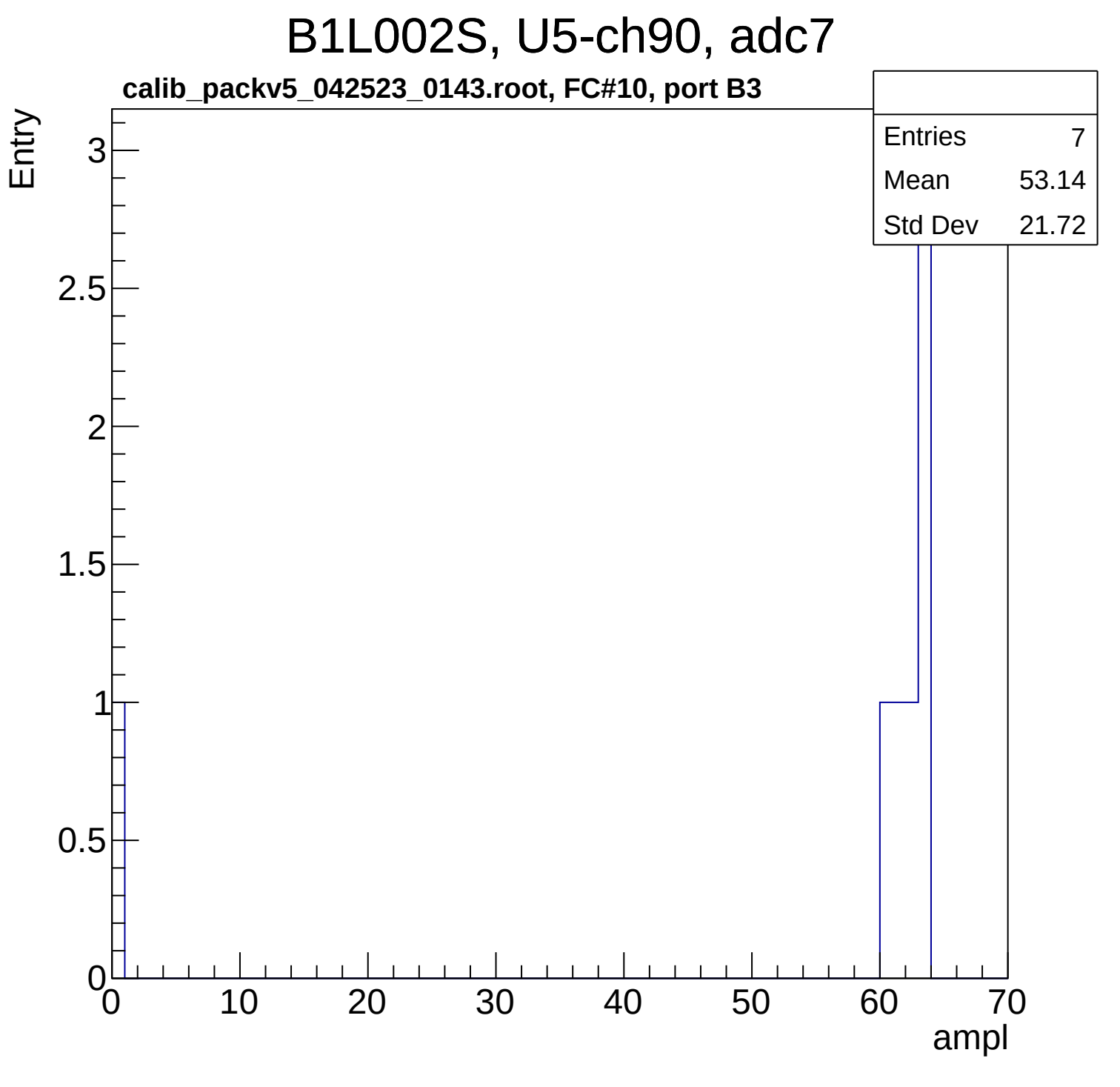
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.14
Std Dev 21.72
Entry
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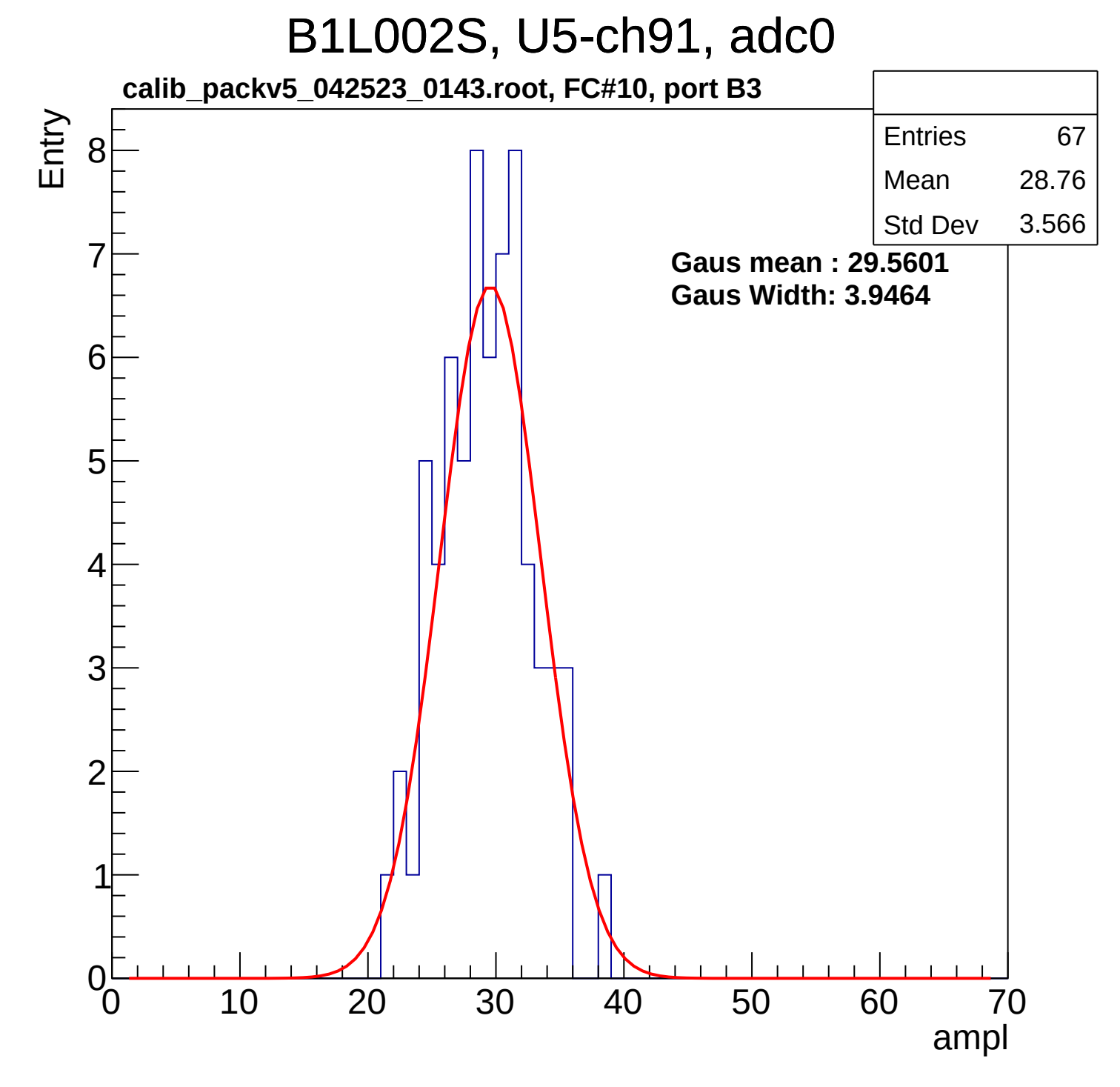
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.76
Std Dev 3.566
Gaus mean : 29.5601
Gaus Width: 3.9464
Entry
ampl
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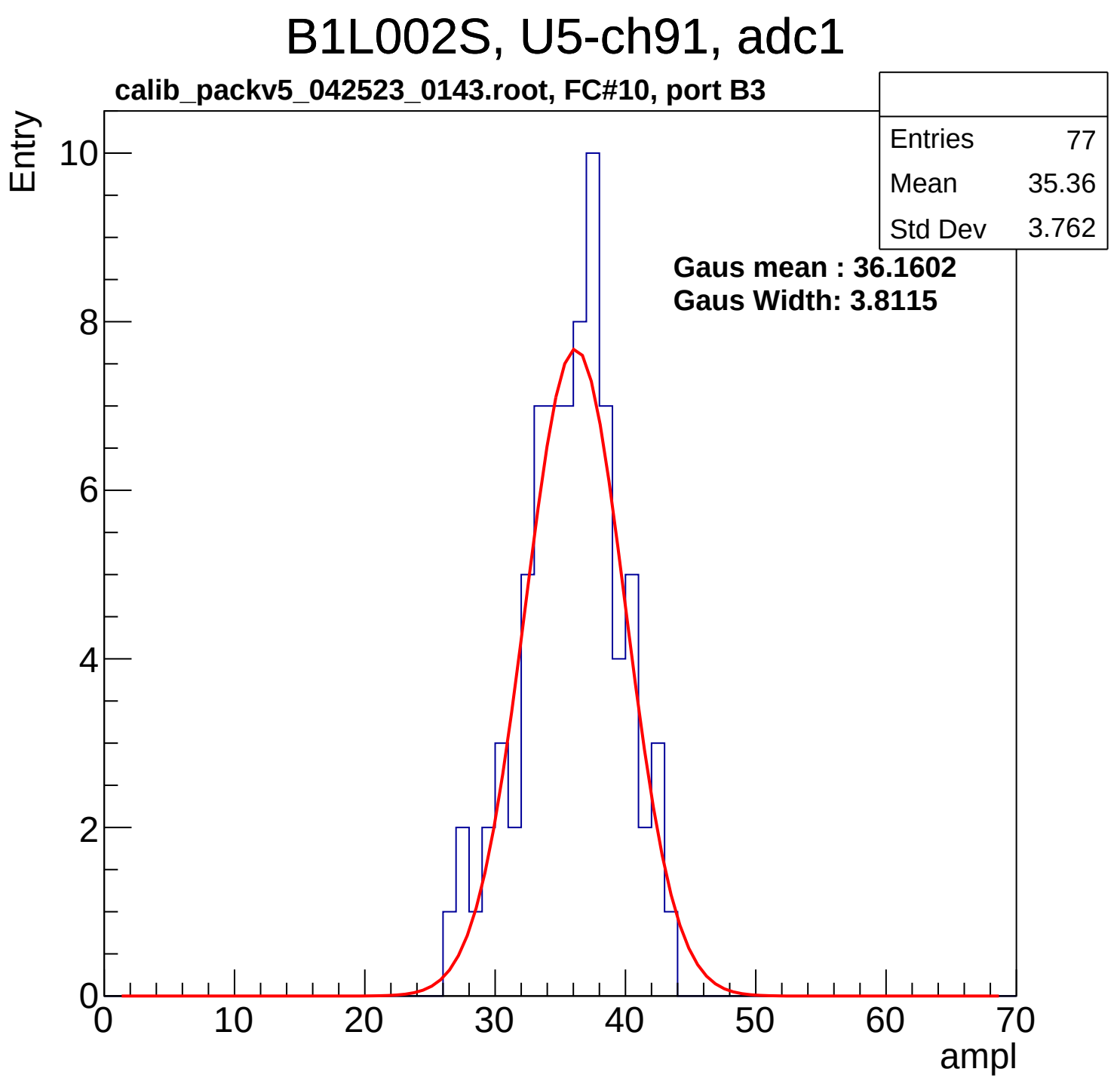
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.36
Std Dev 3.762
Gaus mean : 36.1602
Gaus Width: 3.8115
Entry
ampl
0
2
4
6
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10
0
10
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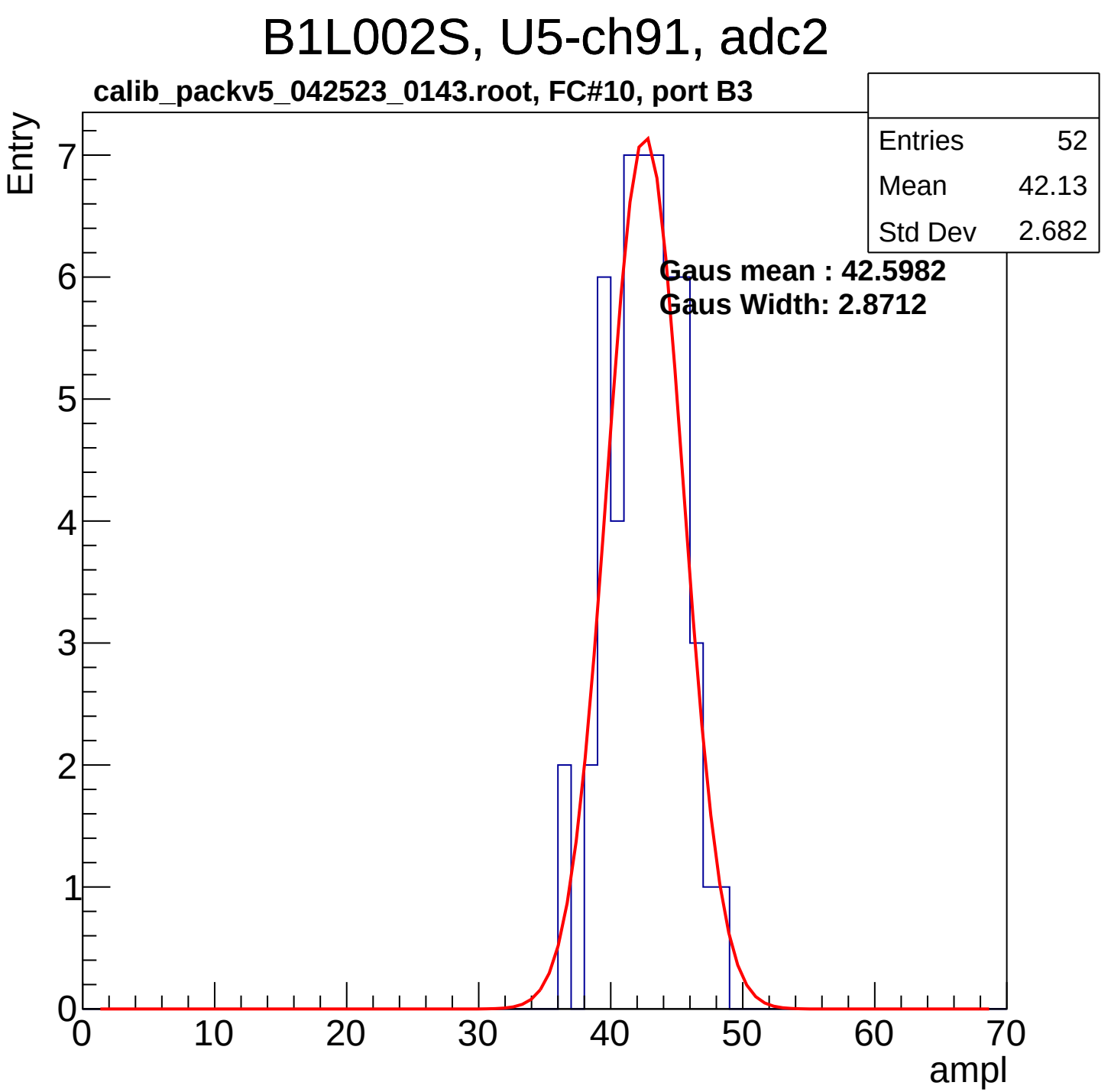
30
40
50
60
70


B1L002S, U5-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 42.13
Std Dev 2.682
Gaus mean : 42.5982
Gaus Width: 2.8712
Entry
ampl
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1
2
3
4
5
6
7
0
10
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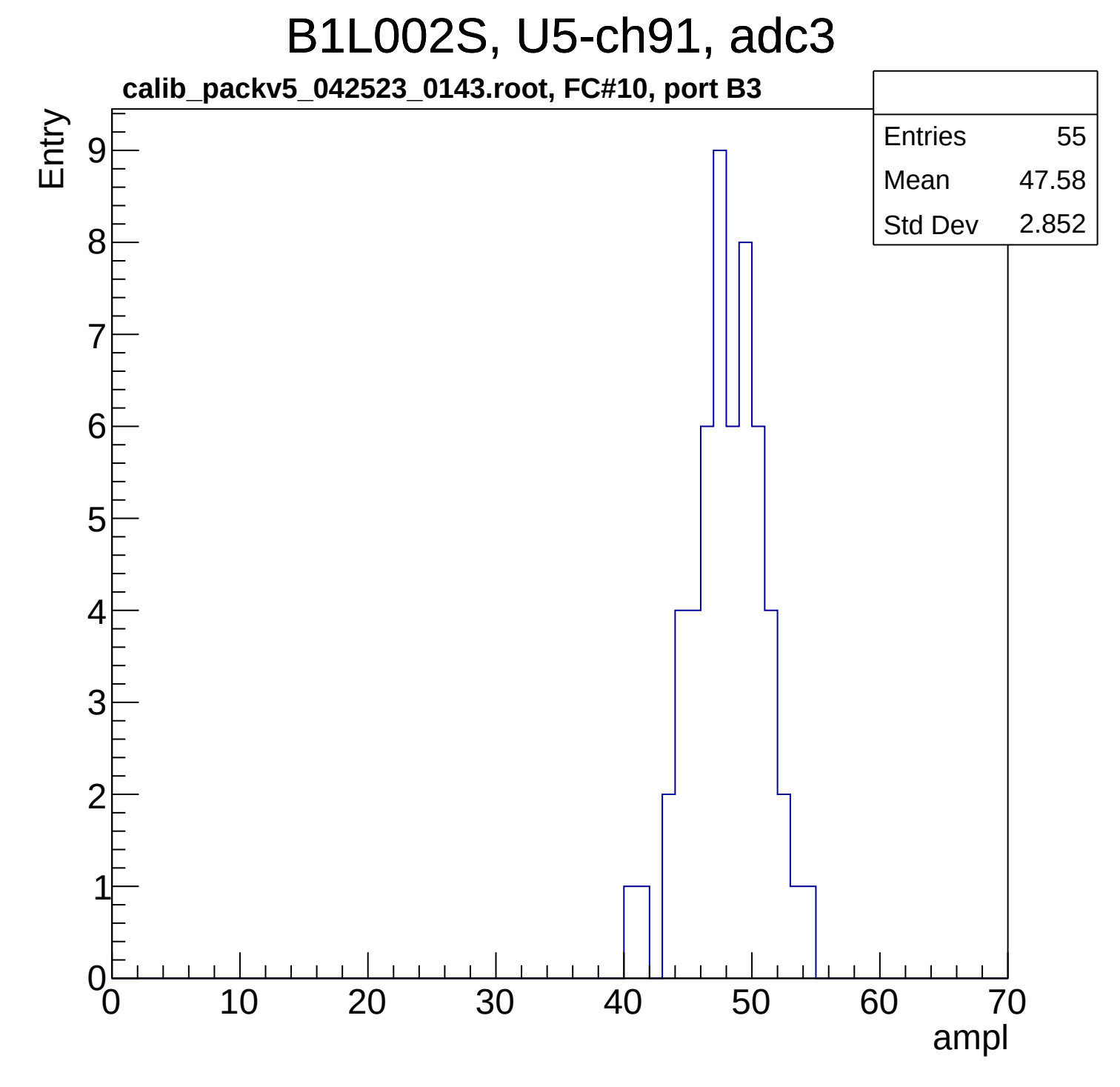
50
60
70


B1L002S, U5-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.58
Std Dev 2.852
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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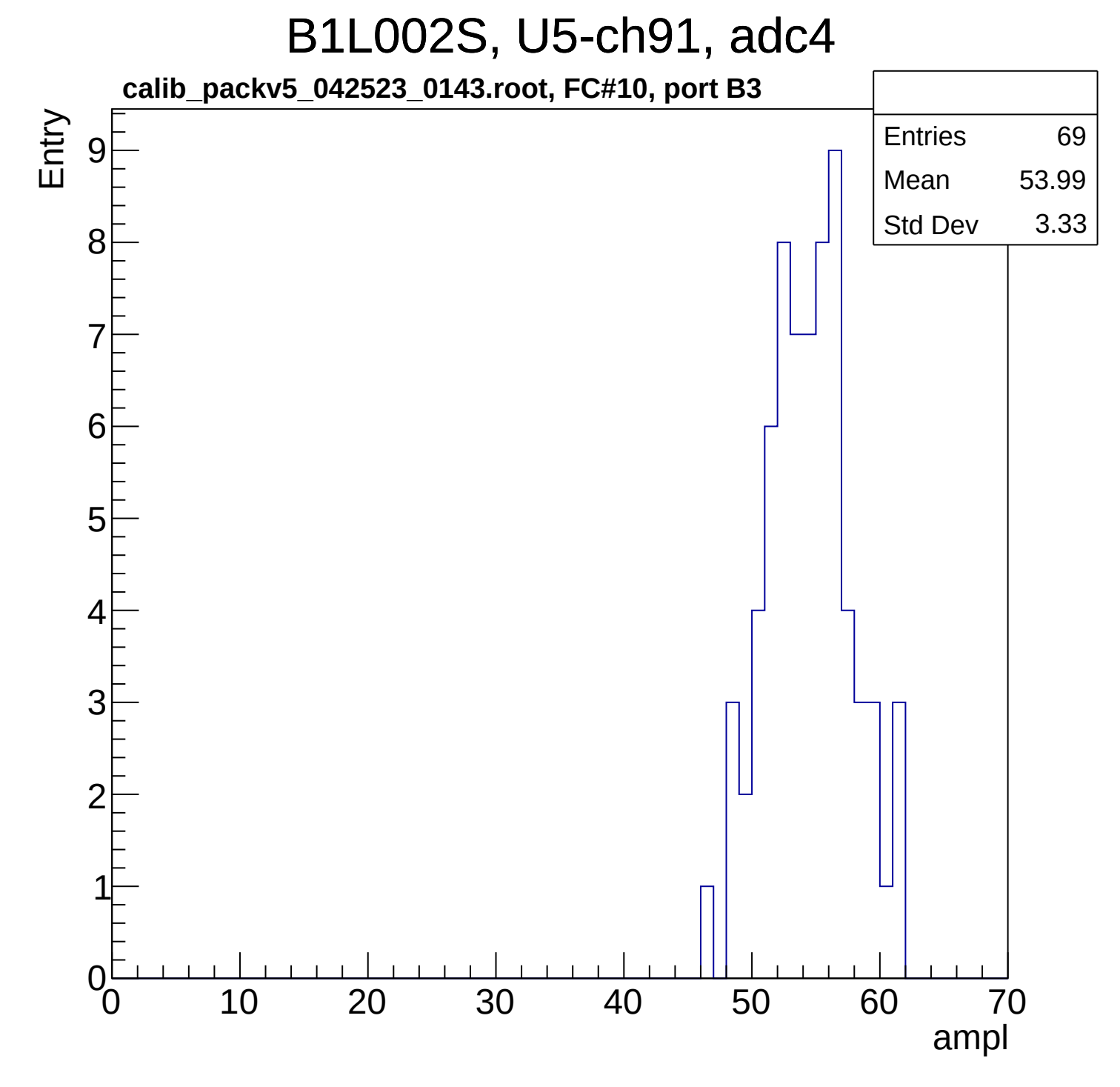
B1L002S, U5-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.99
Std Dev 3.33
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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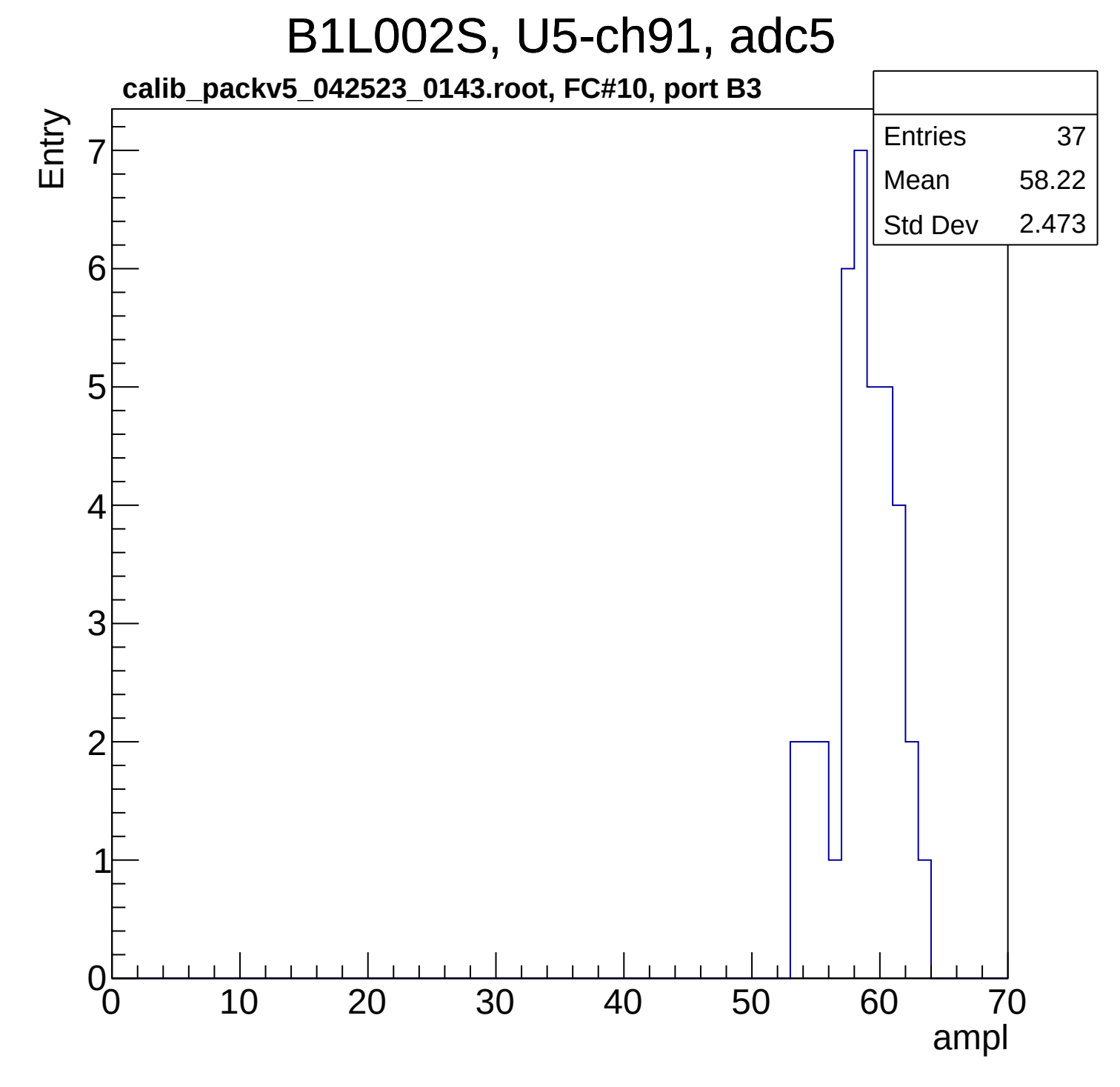
B1L002S, U5-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.22
Std Dev 2.473
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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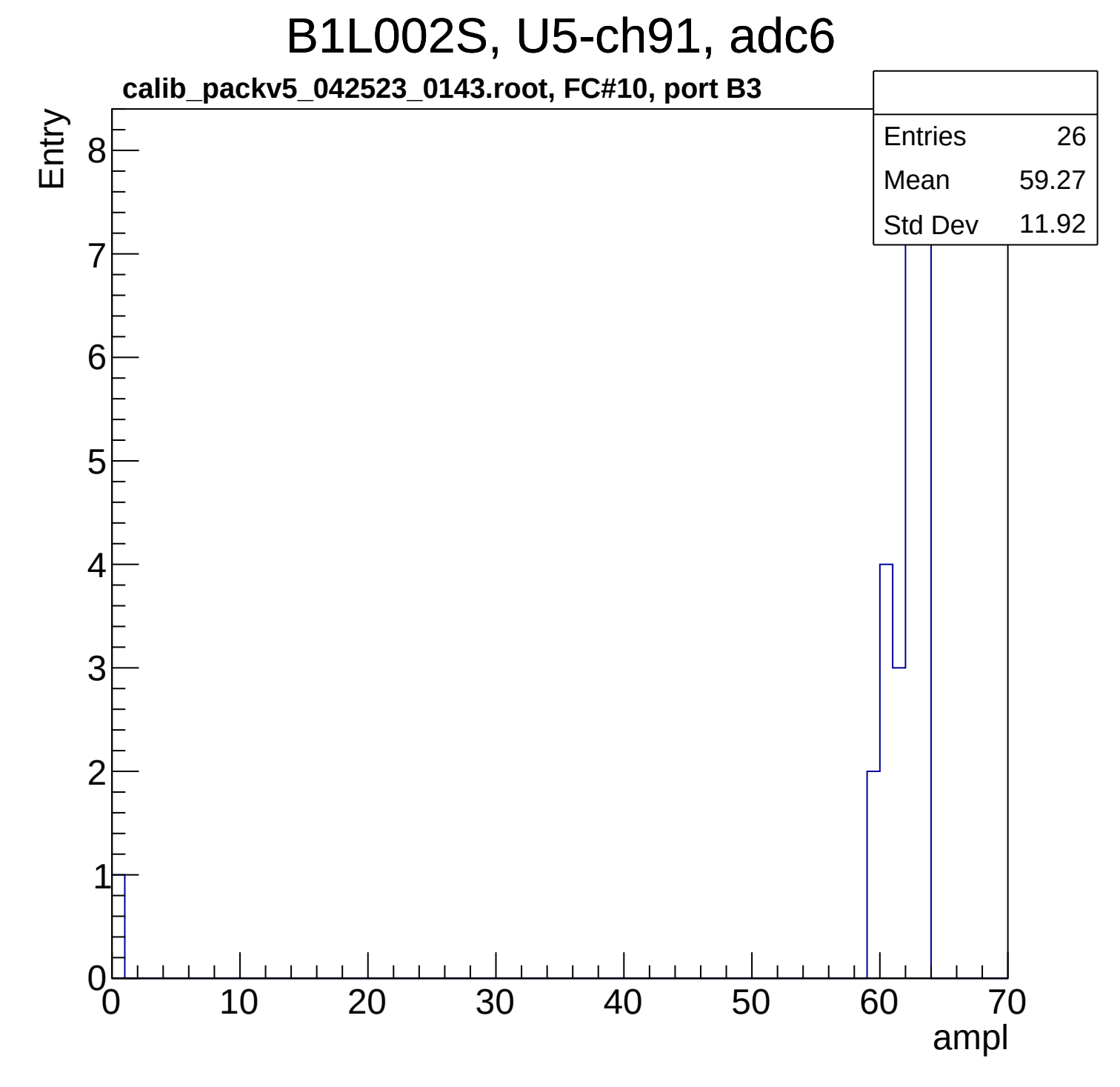
B1L002S, U5-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.27
Std Dev 11.92
Entry
ampl
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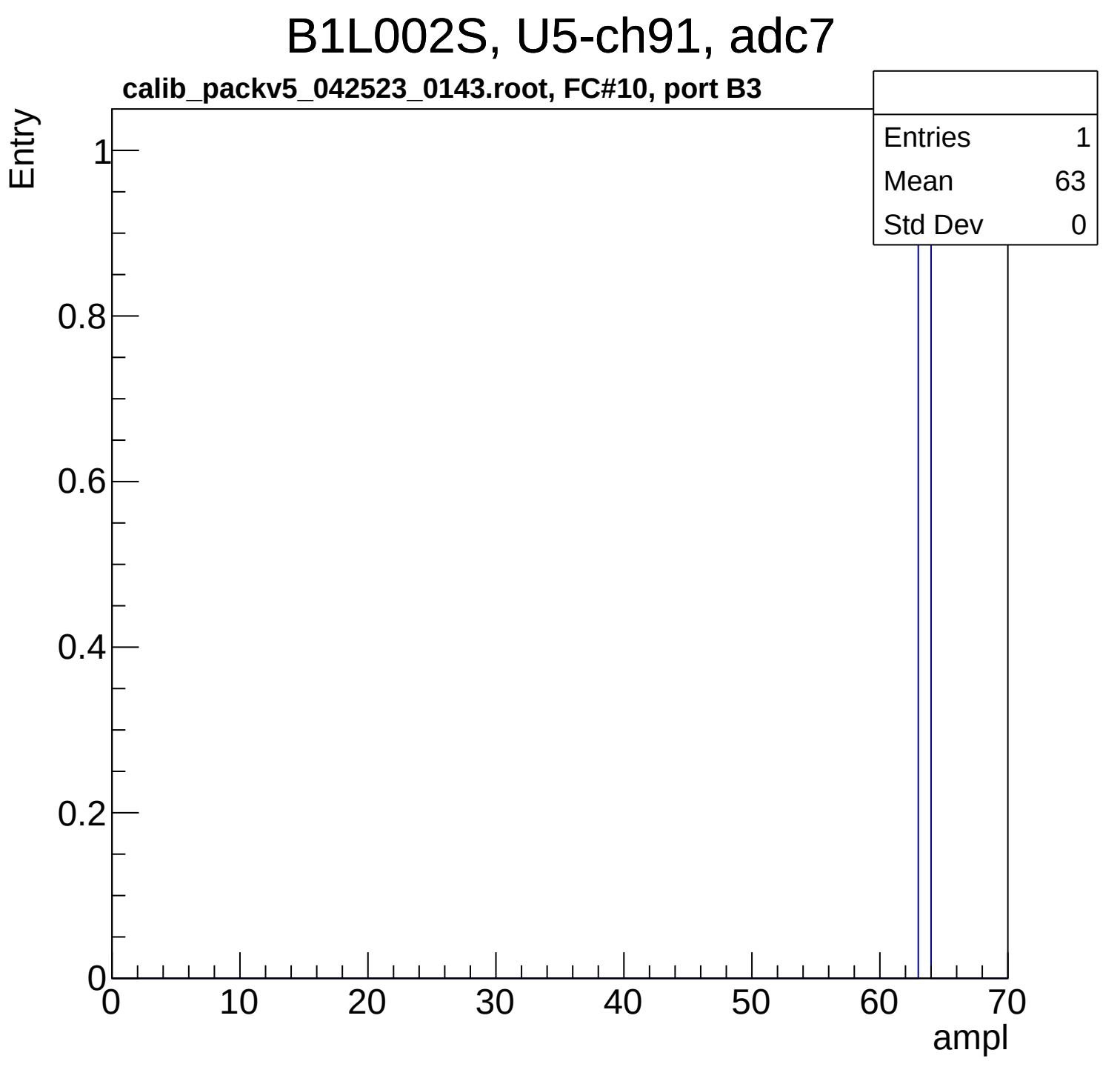
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
30
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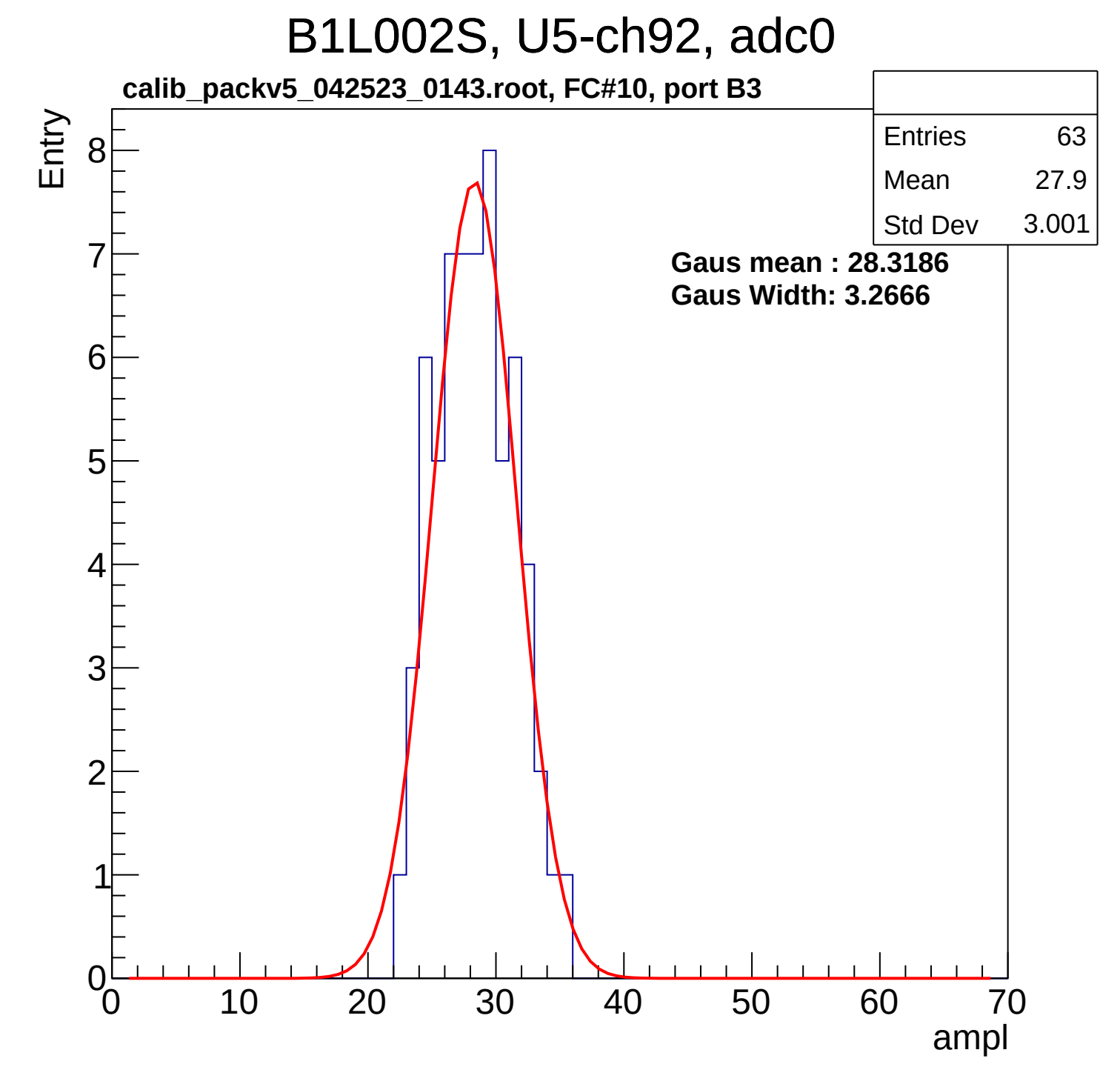
50
60
70
ampl


B1L002S, U5-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.9
Std Dev 3.001
Gaus mean : 28.3186
Gaus Width: 3.2666
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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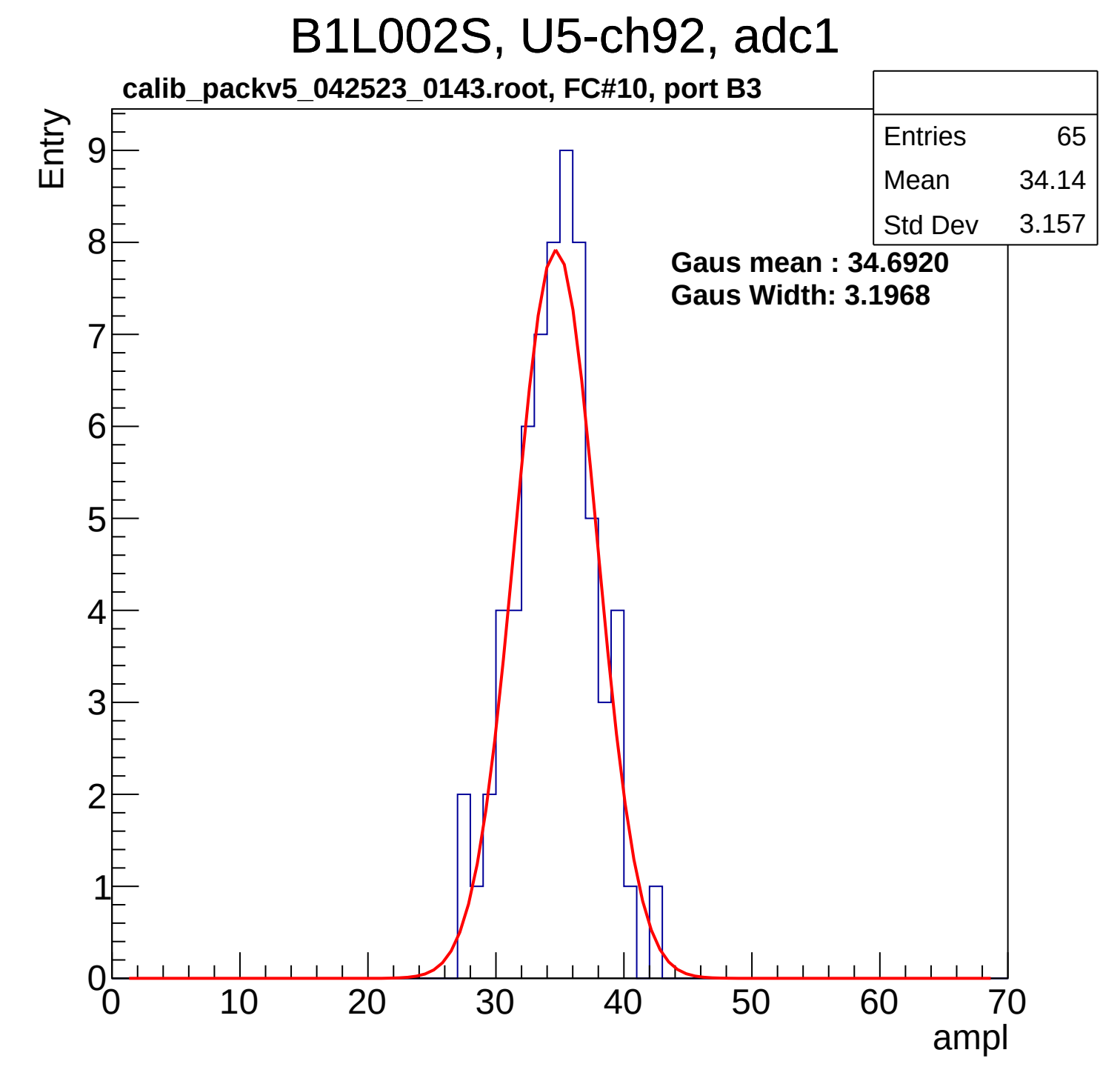
40
50
60
70


B1L002S, U5-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.14
Std Dev 3.157
Gaus mean : 34.6920
Gaus Width: 3.1968
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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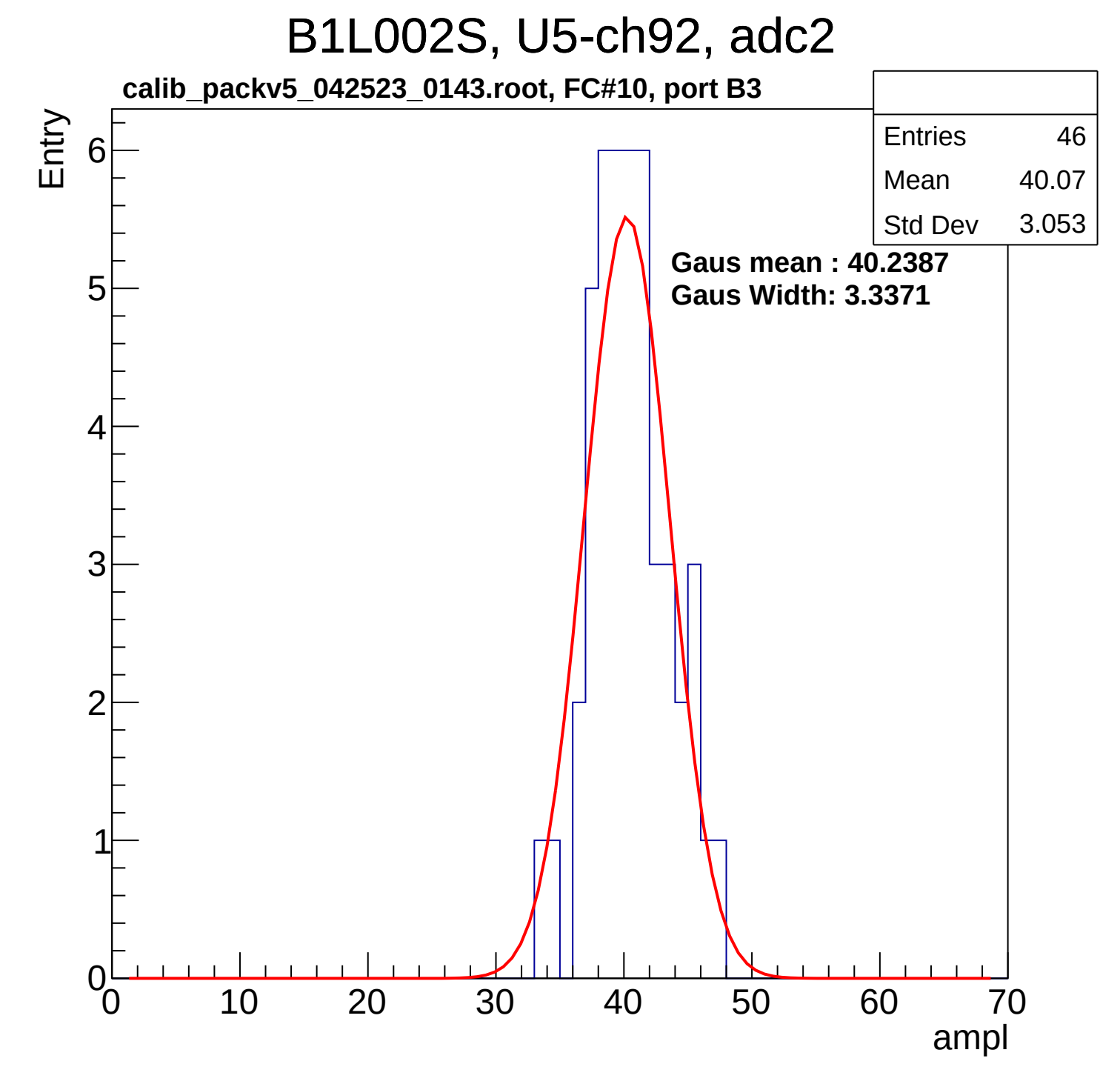
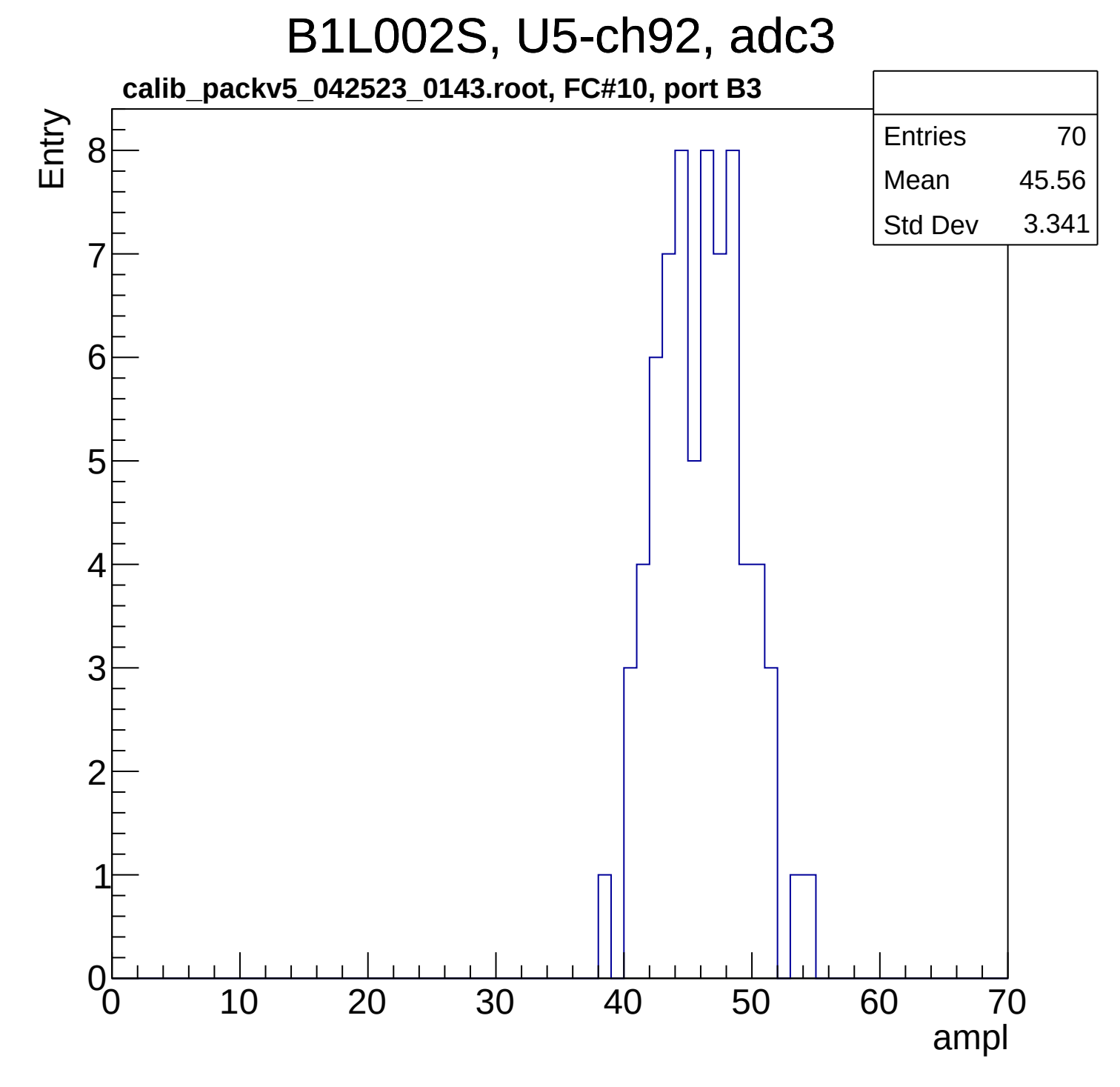

B1L002S, U5-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 40.07
Std Dev 3.053
Gaus mean : 40.2387
Gaus Width: 3.3371
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U5-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 45.56
Std Dev 3.341
Entry
ampl
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5
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60
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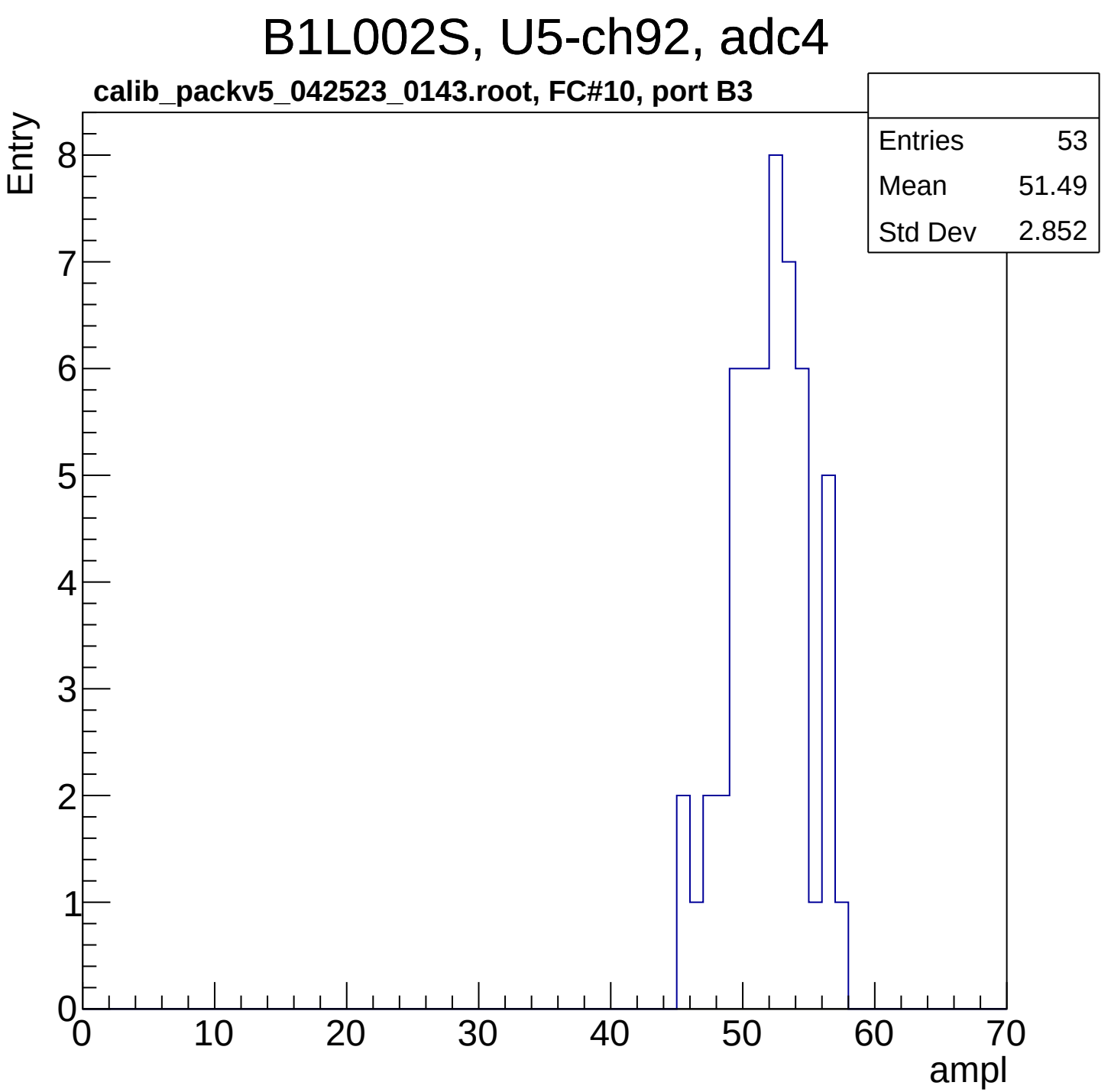
B1L002S, U5-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.49
Std Dev 2.852
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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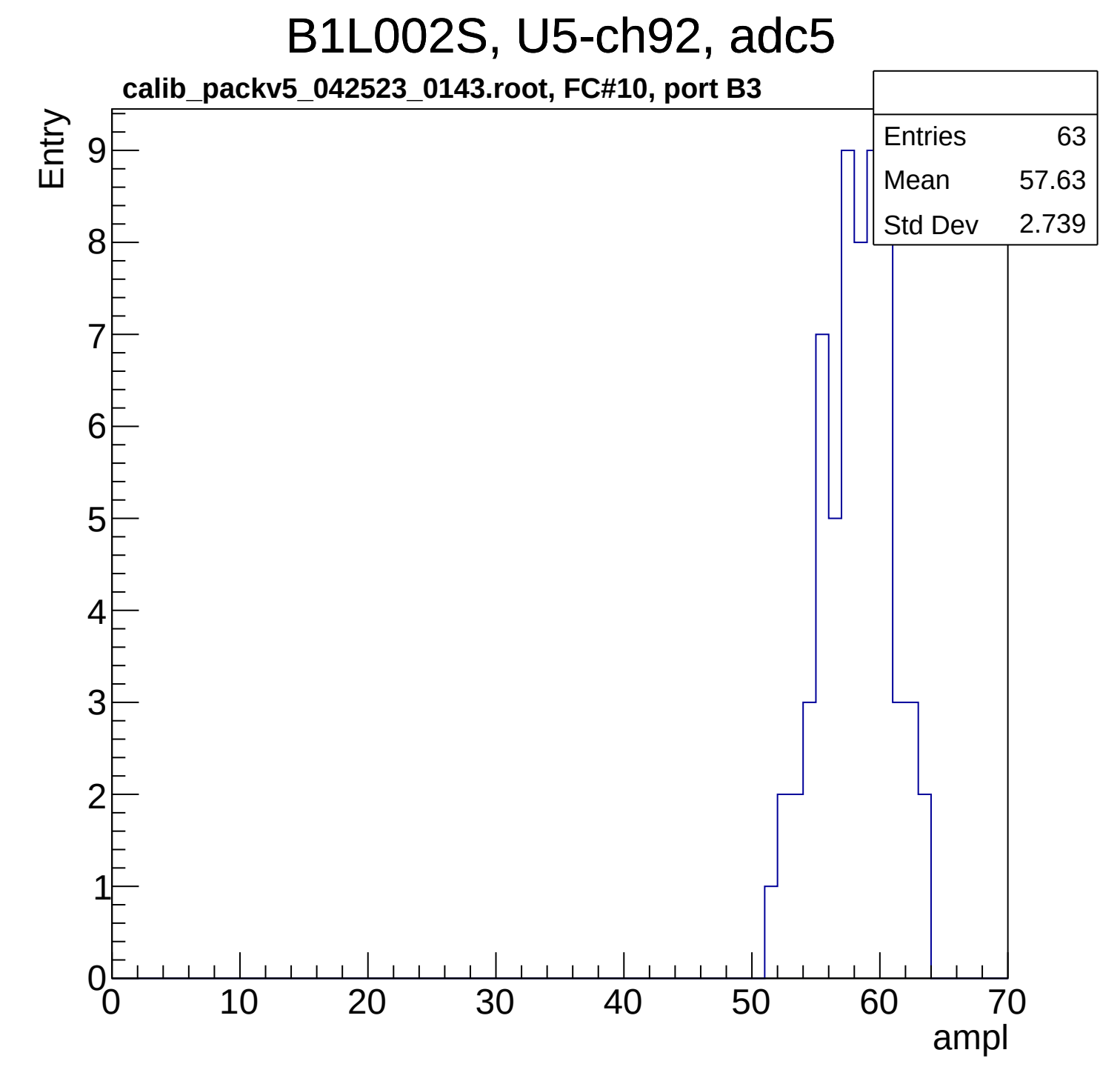
B1L002S, U5-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.63
Std Dev 2.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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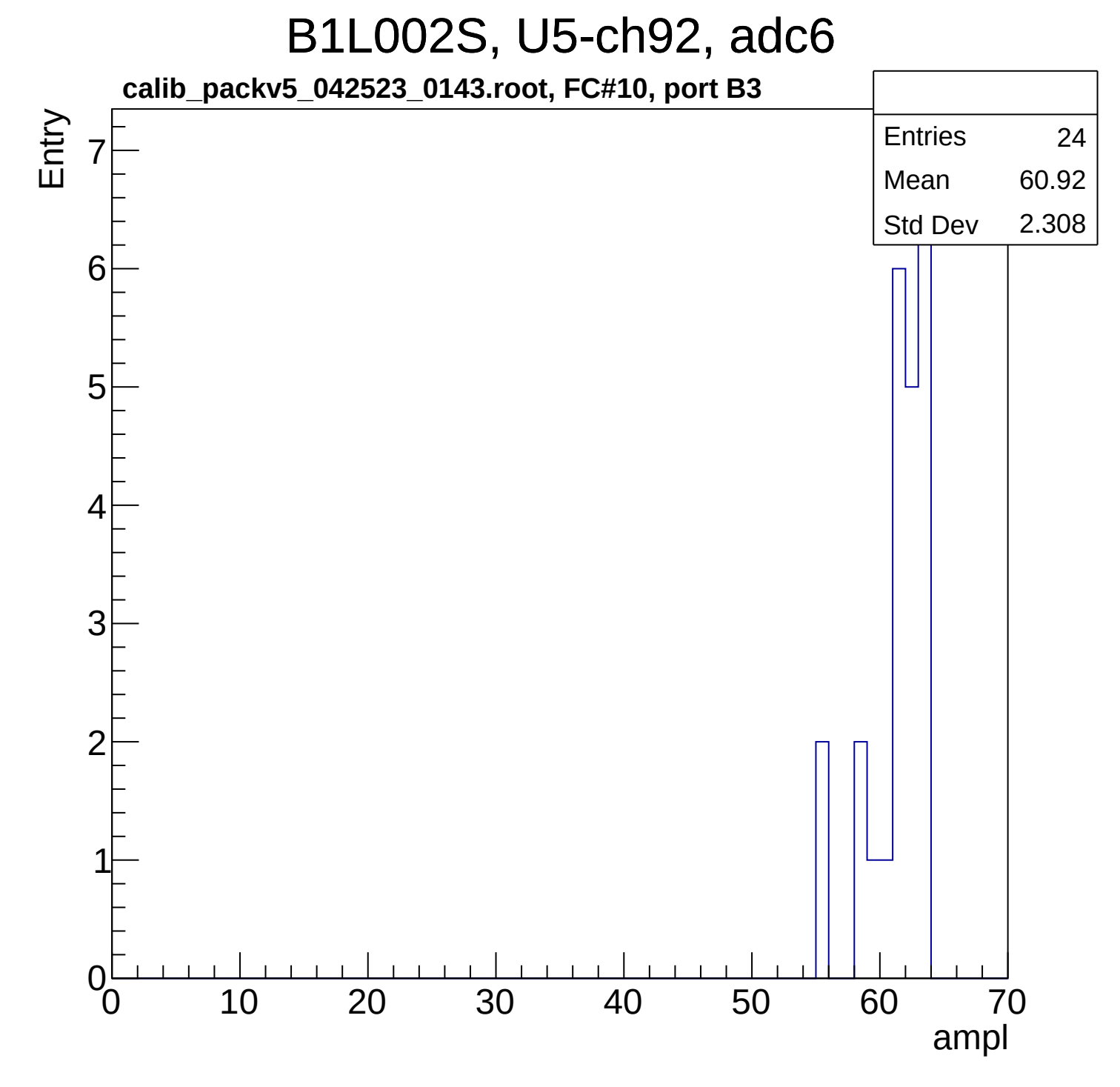
B1L002S, U5-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 60.92
Std Dev 2.308
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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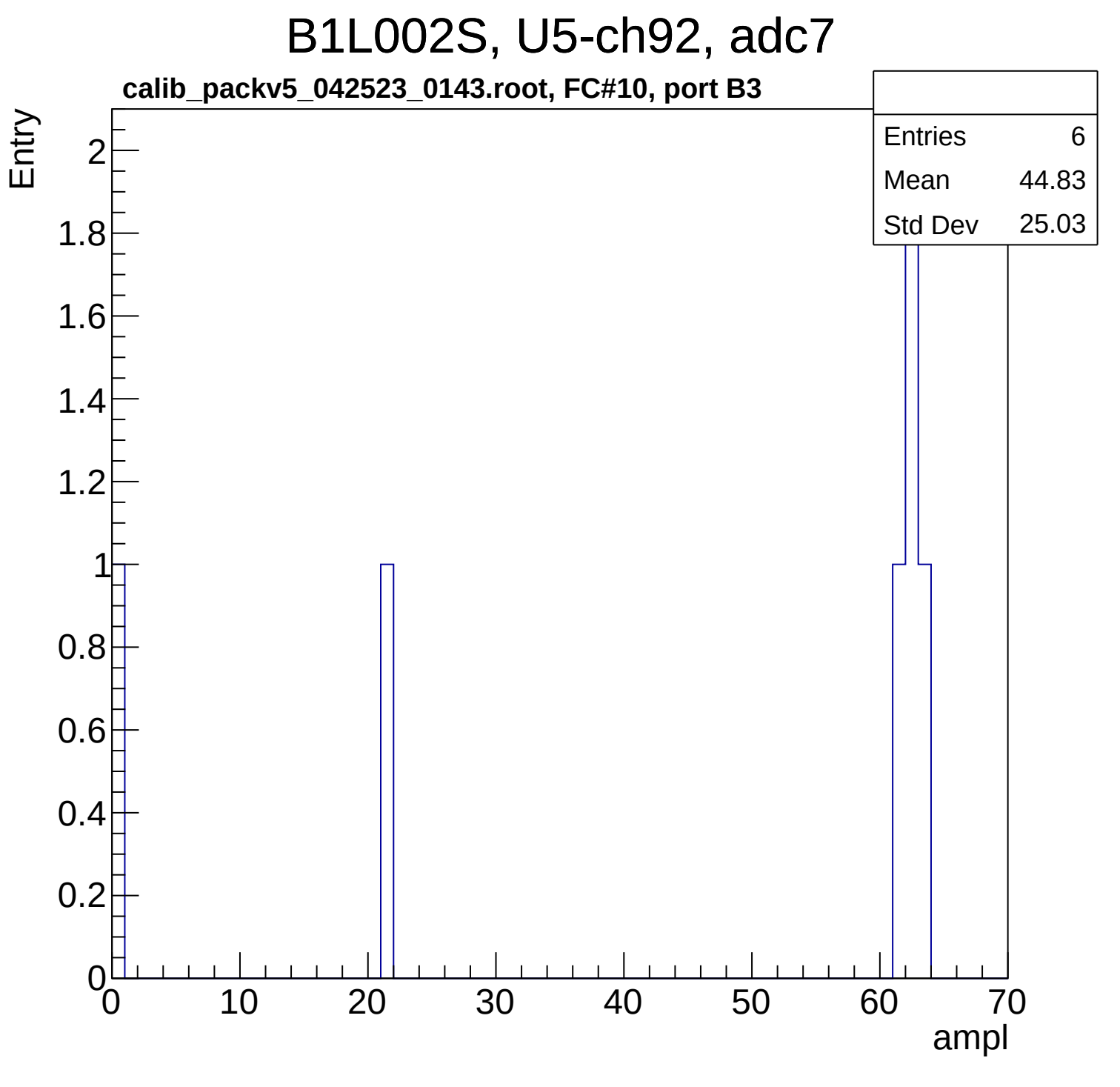
B1L002S, U5-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.83
Std Dev 25.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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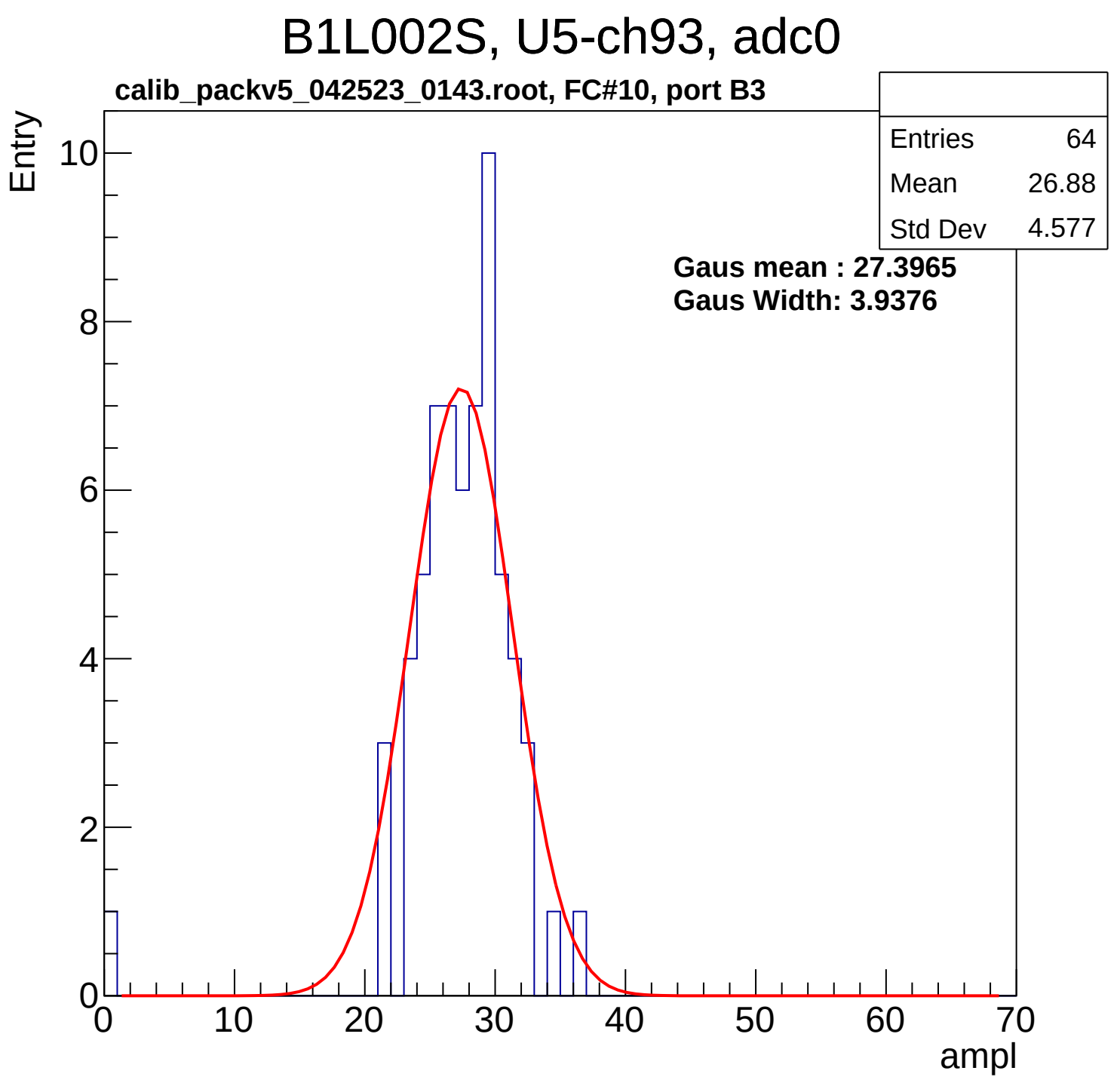
B1L002S, U5-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.88
Std Dev 4.577
Gaus mean : 27.3965
Gaus Width: 3.9376
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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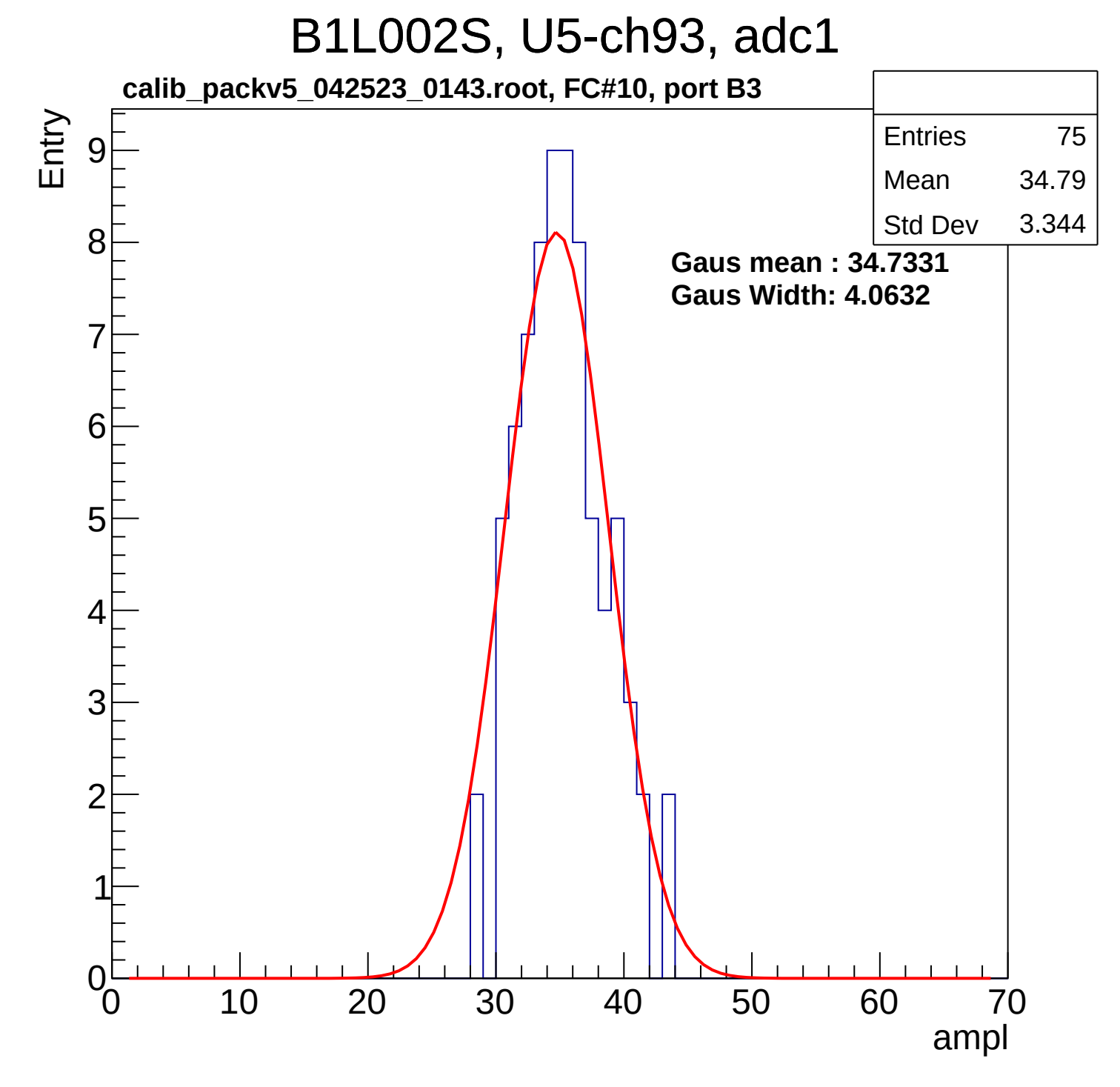
B1L002S, U5-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.79
Std Dev 3.344
Gaus mean : 34.7331
Gaus Width: 4.0632
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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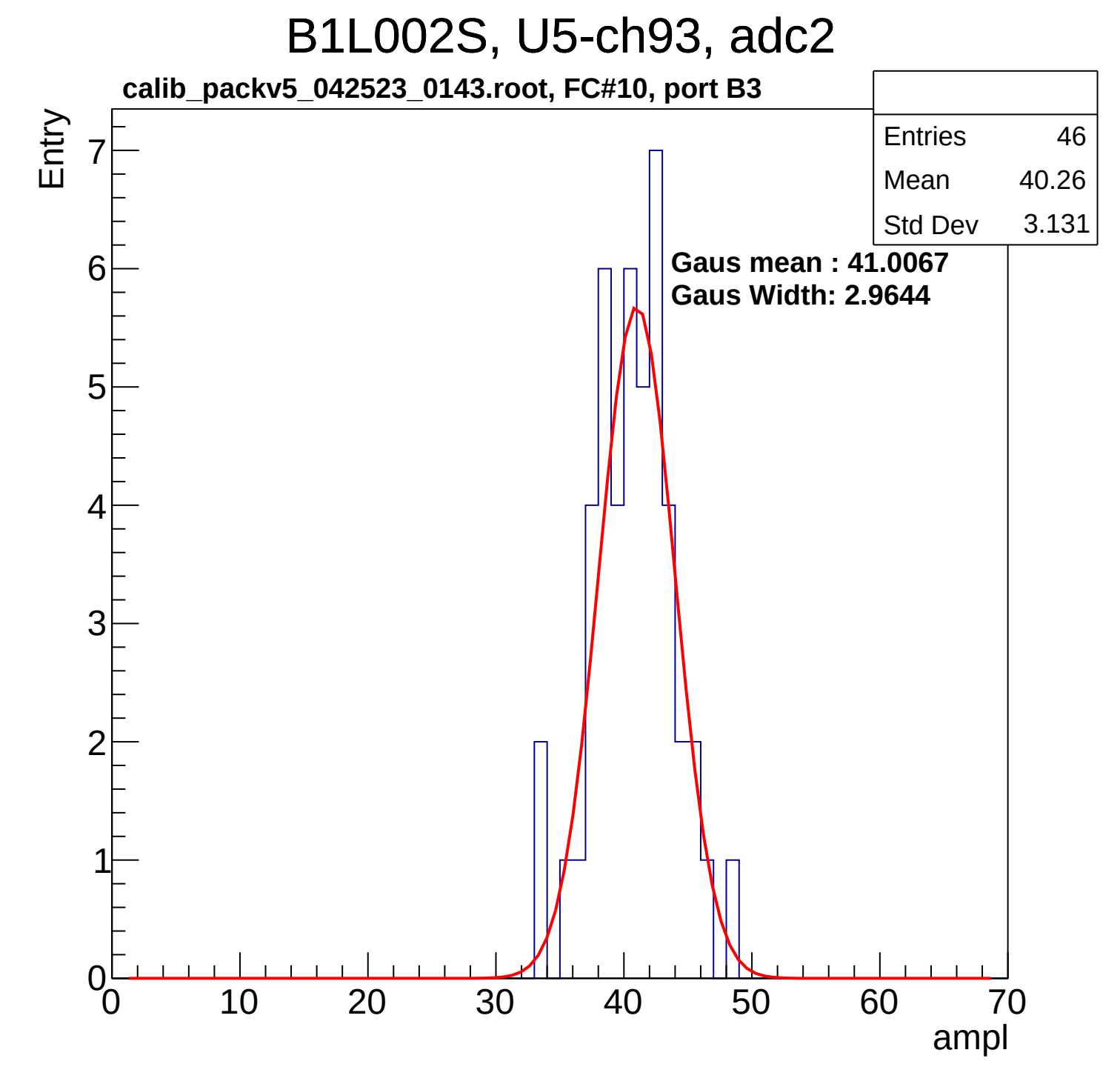
B1L002S, U5-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 40.26
Std Dev 3.131
Gaus mean : 41.0067
Gaus Width: 2.9644
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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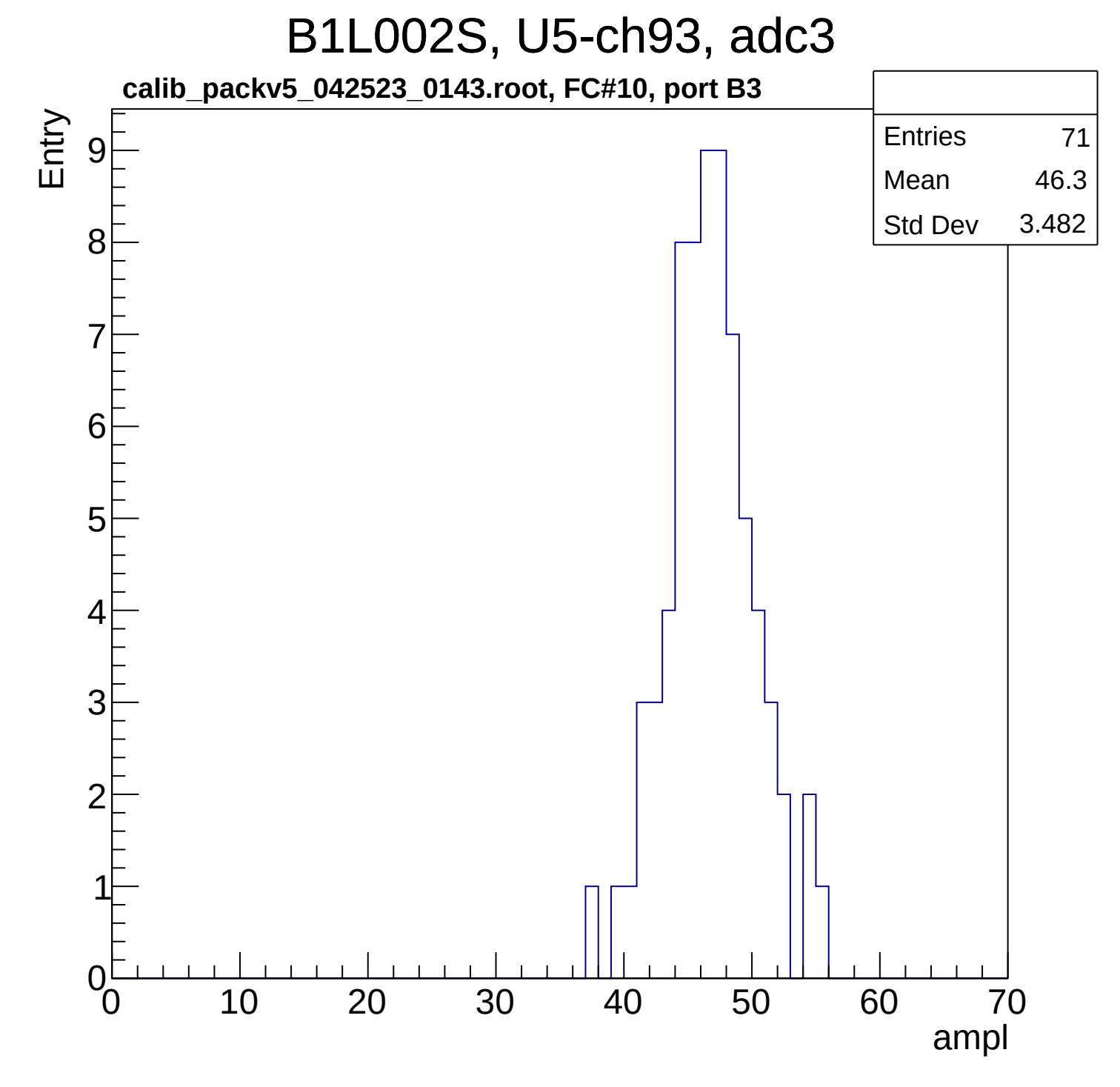
B1L002S, U5-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 46.3
Std Dev 3.482
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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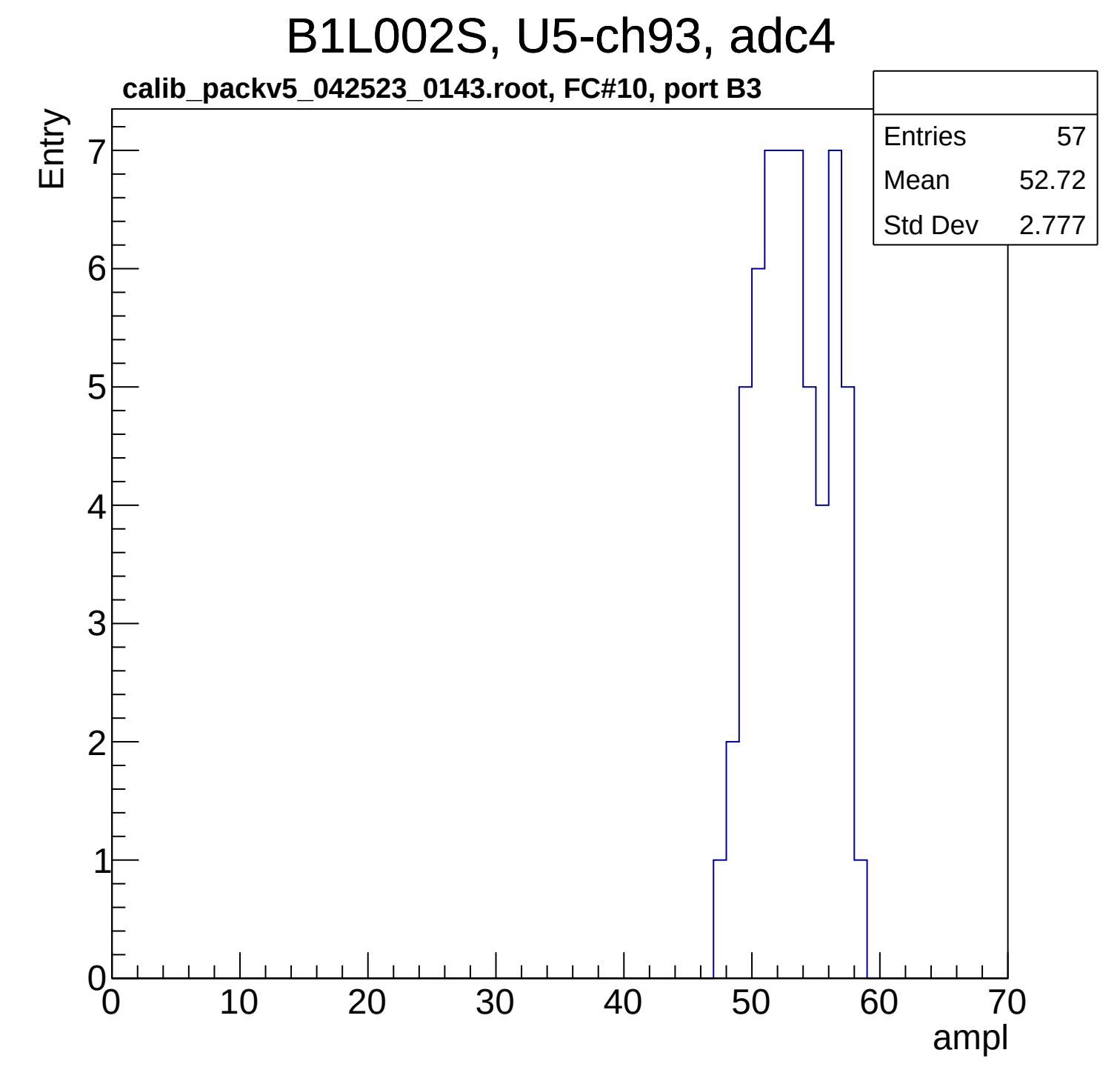
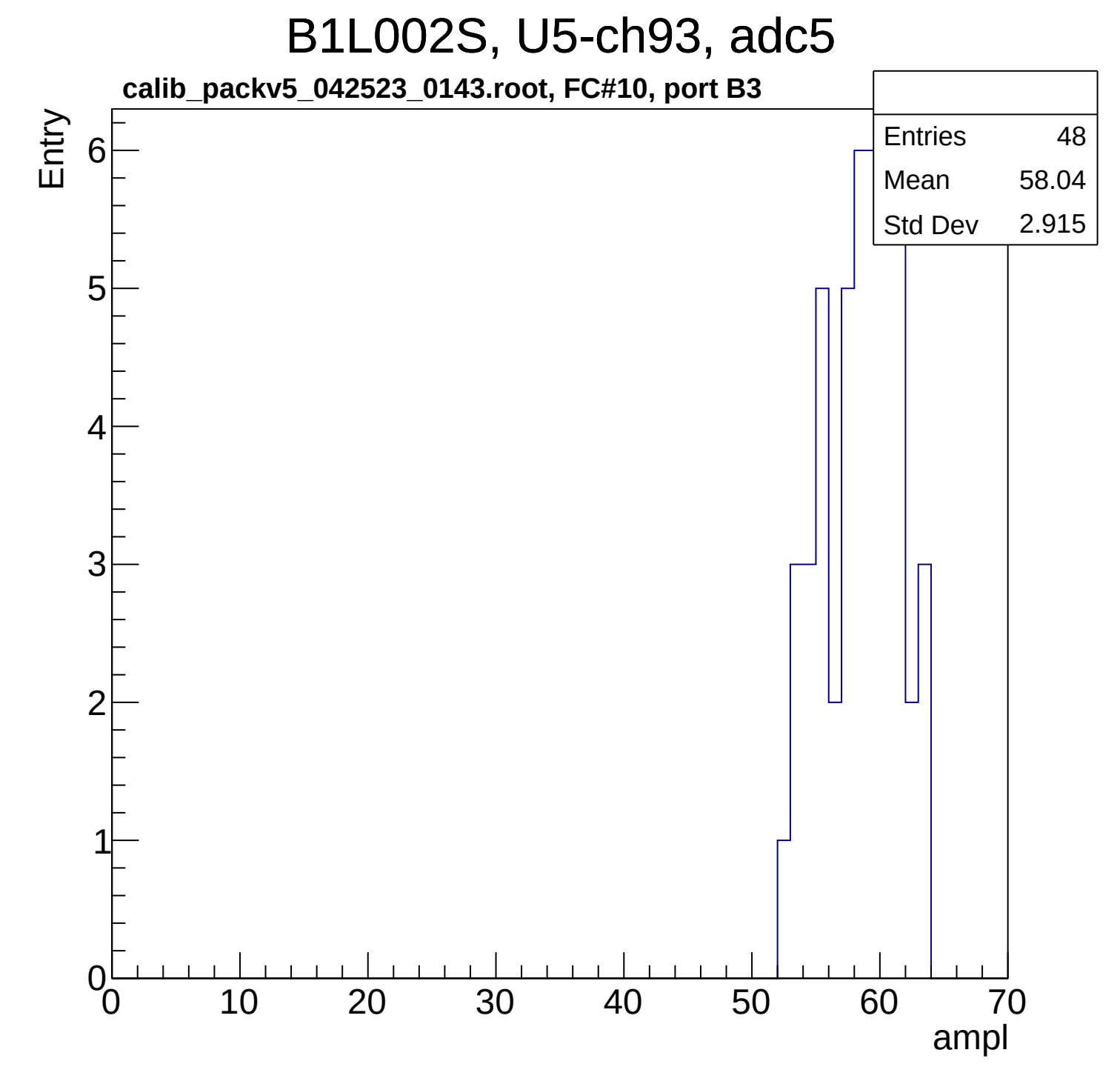
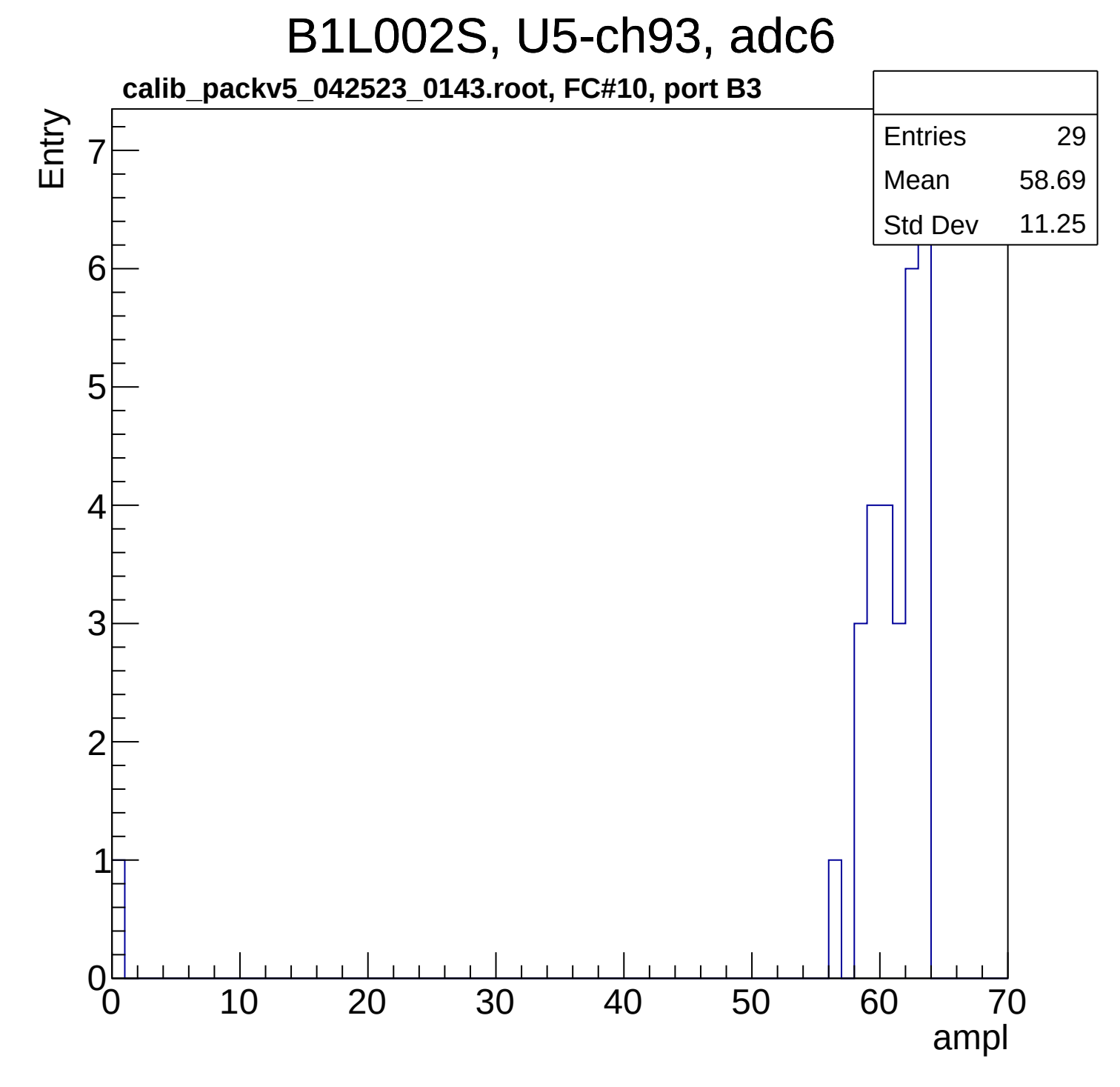
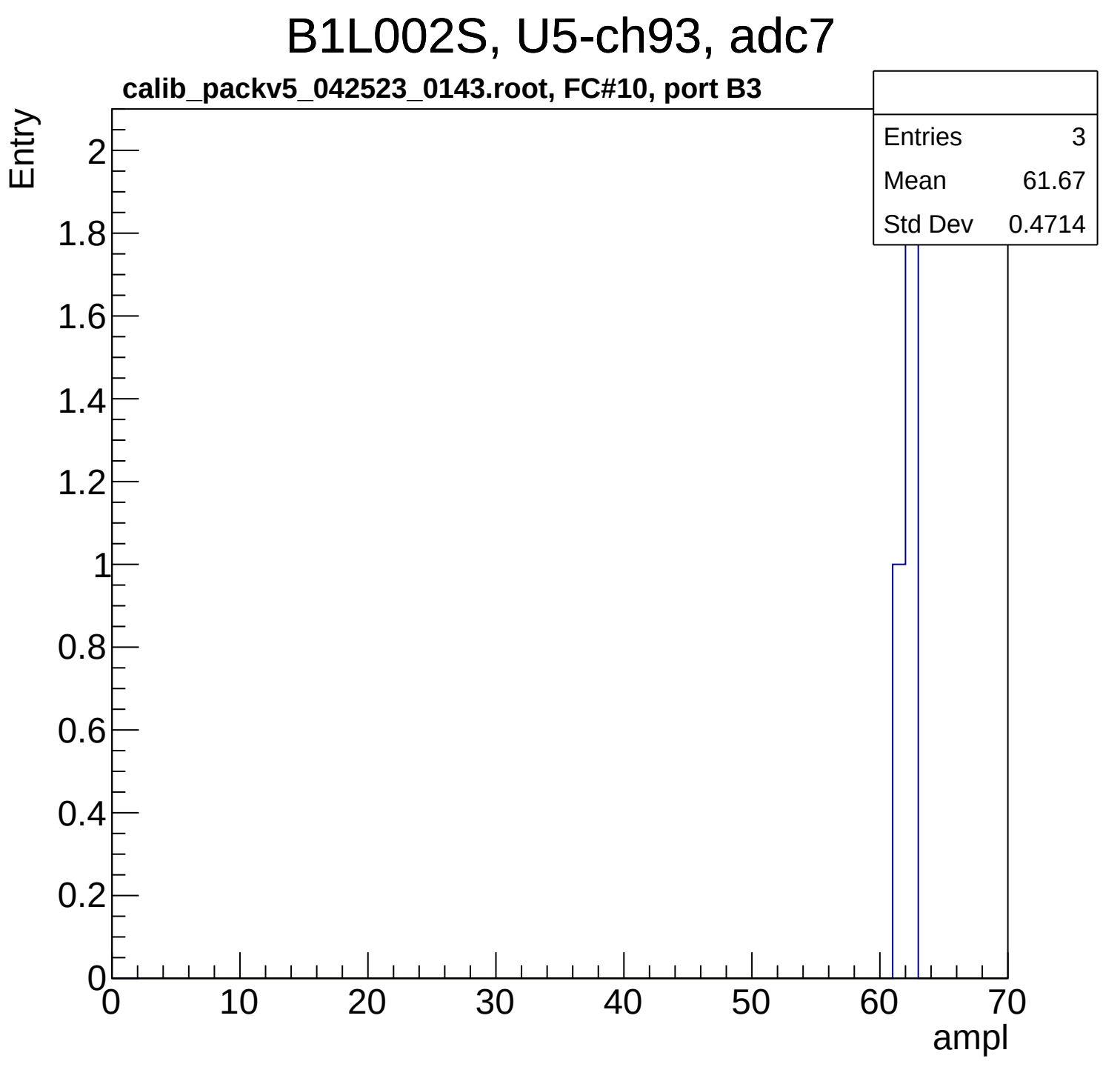

B1L002S, U5-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.72
Std Dev 2.777
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.04
Std Dev 2.915
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.69
Std Dev 11.25
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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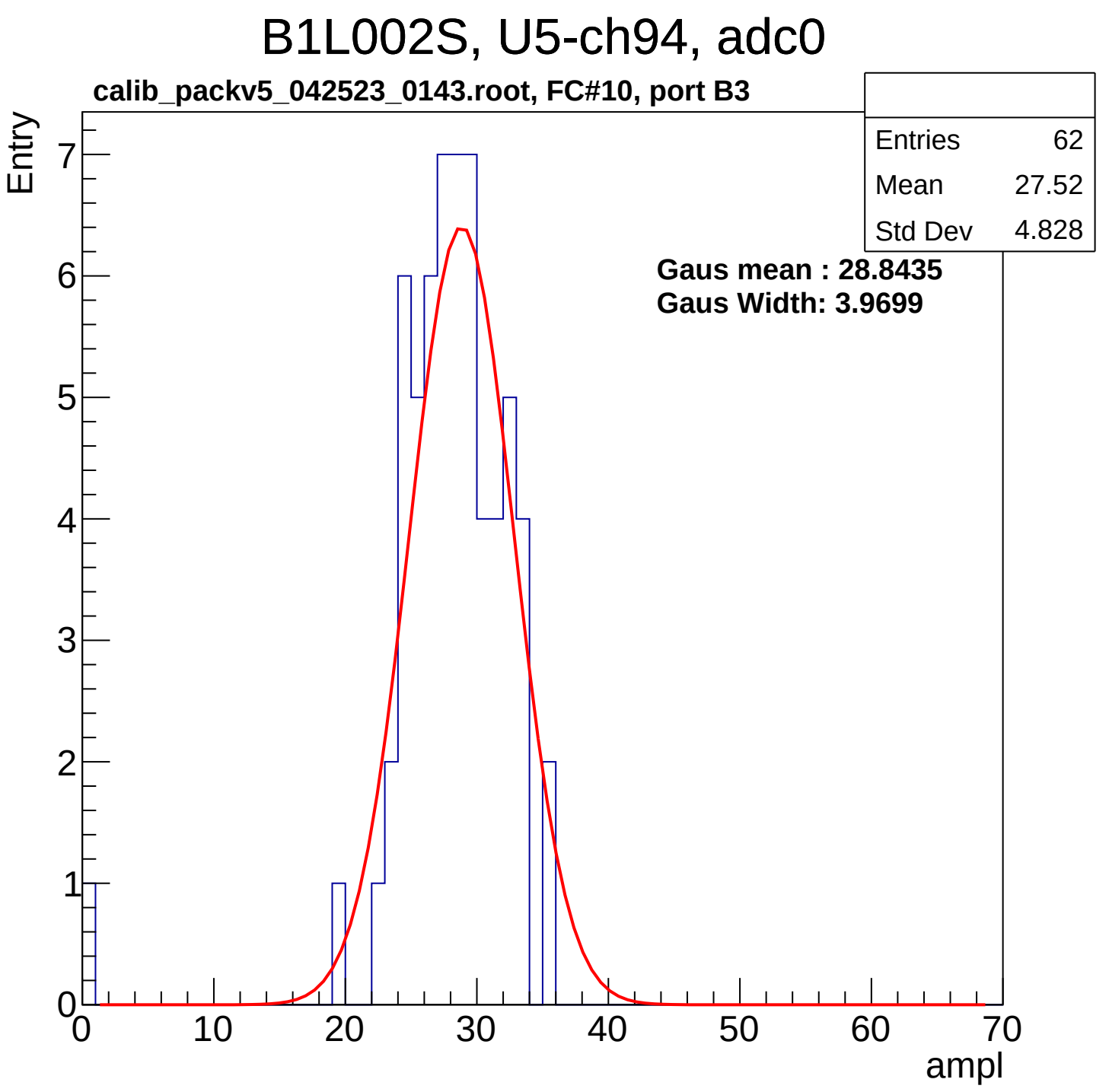
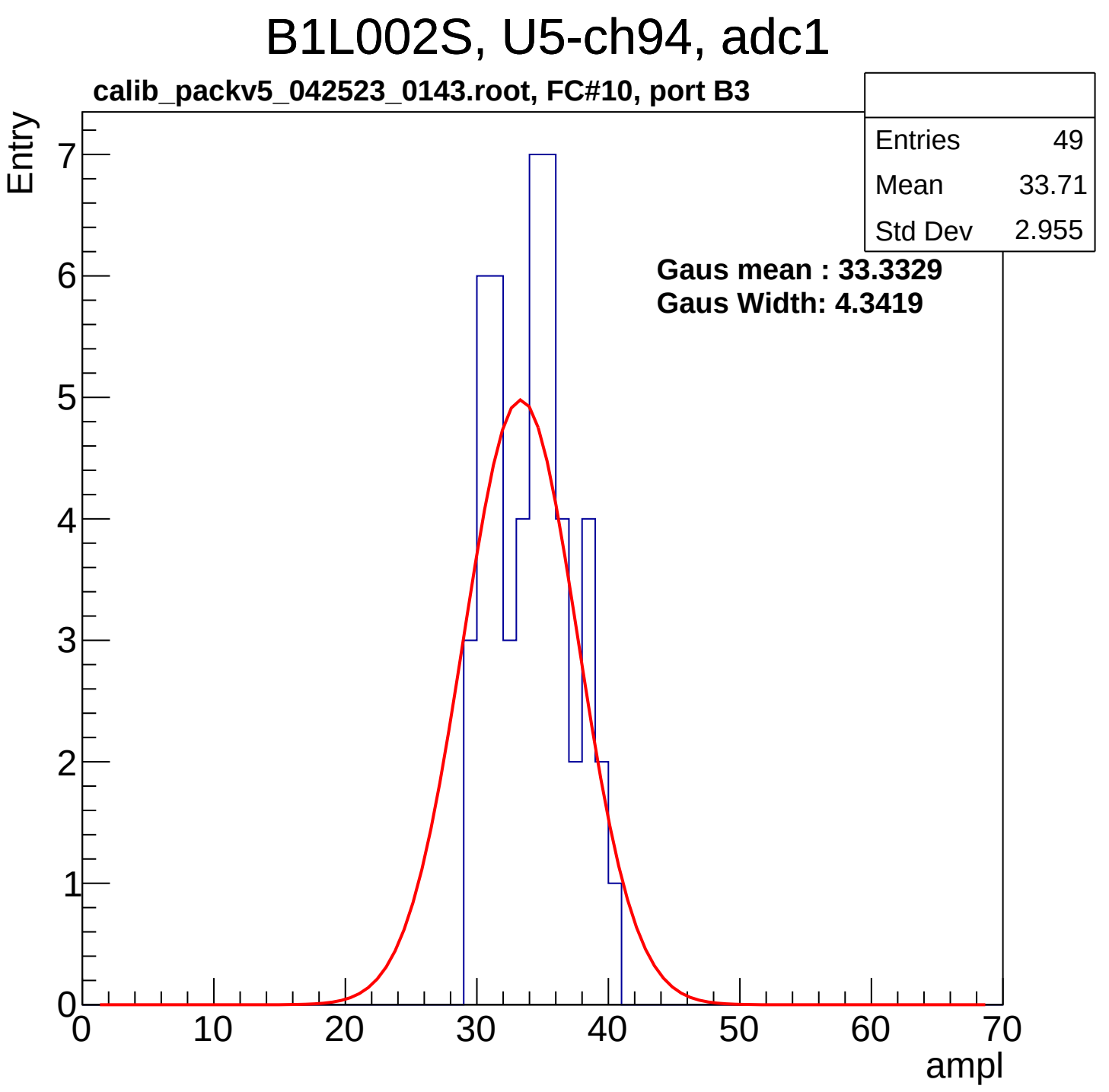

B1L002S, U5-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.52
Std Dev 4.828
Gaus mean : 28.8435
Gaus Width: 3.9699
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 33.71
Std Dev 2.955
Gaus mean : 33.3329
Gaus Width: 4.3419
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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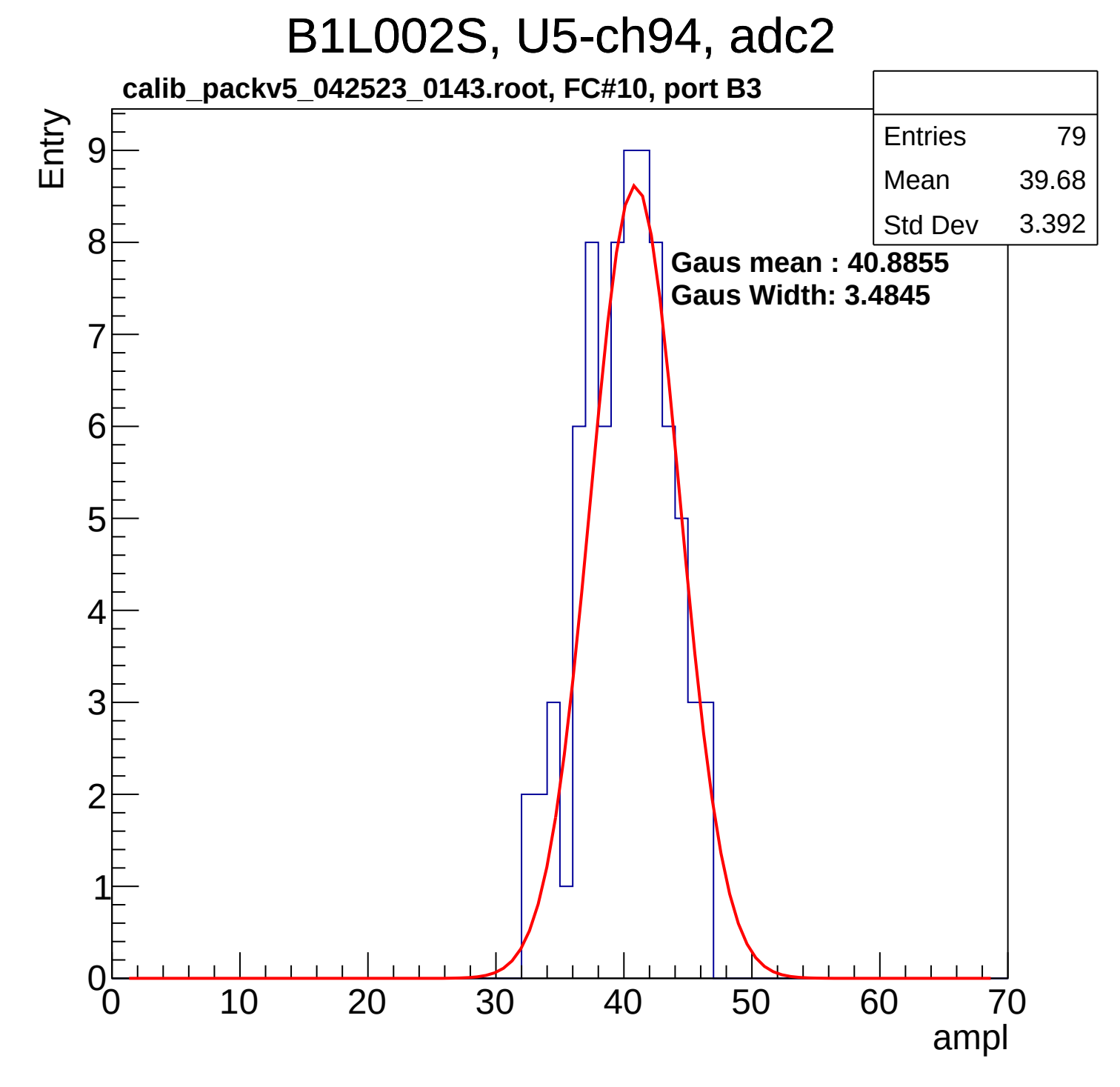
B1L002S, U5-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.68
Std Dev 3.392
Gaus mean : 40.8855
Gaus Width: 3.4845
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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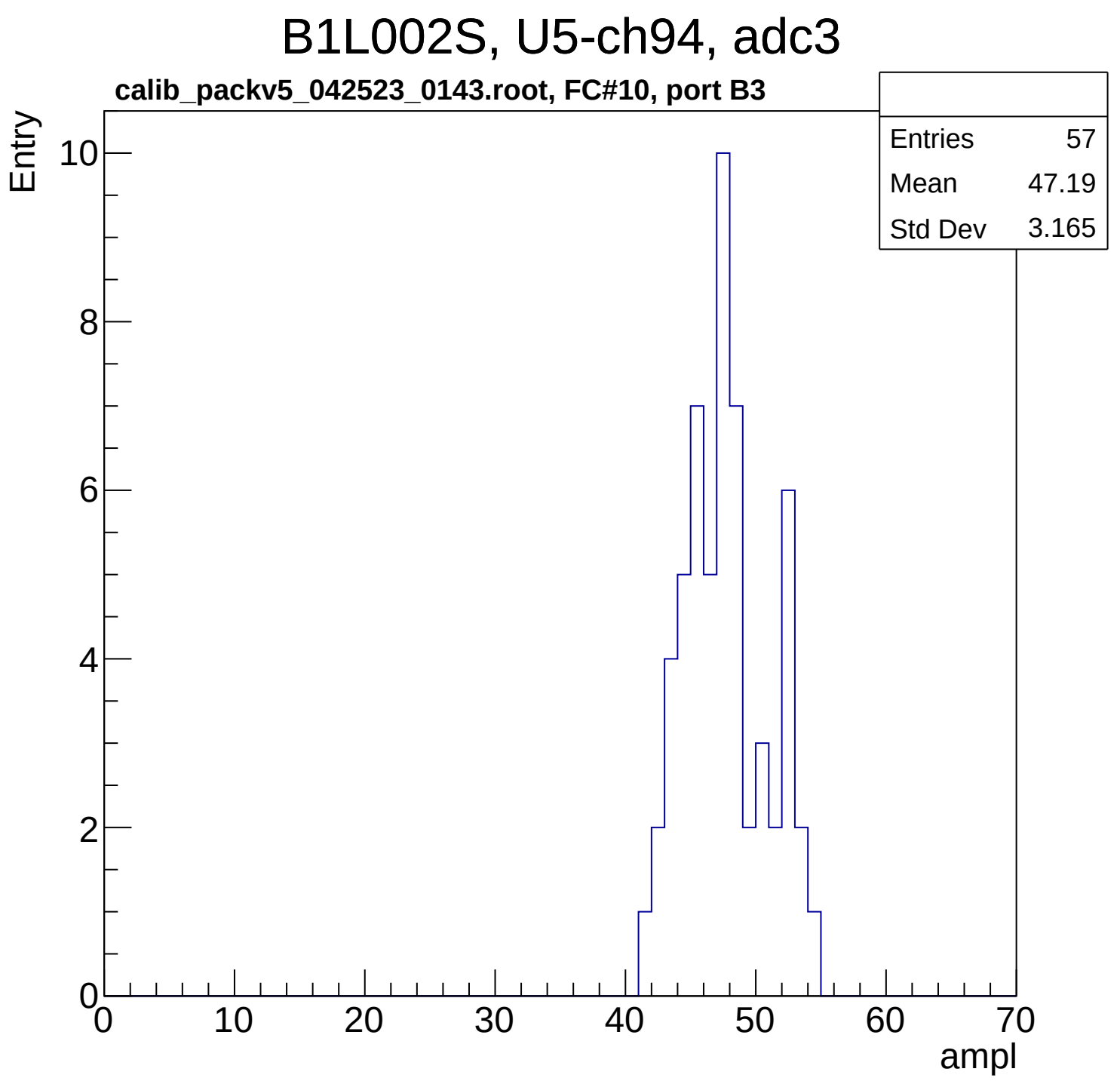
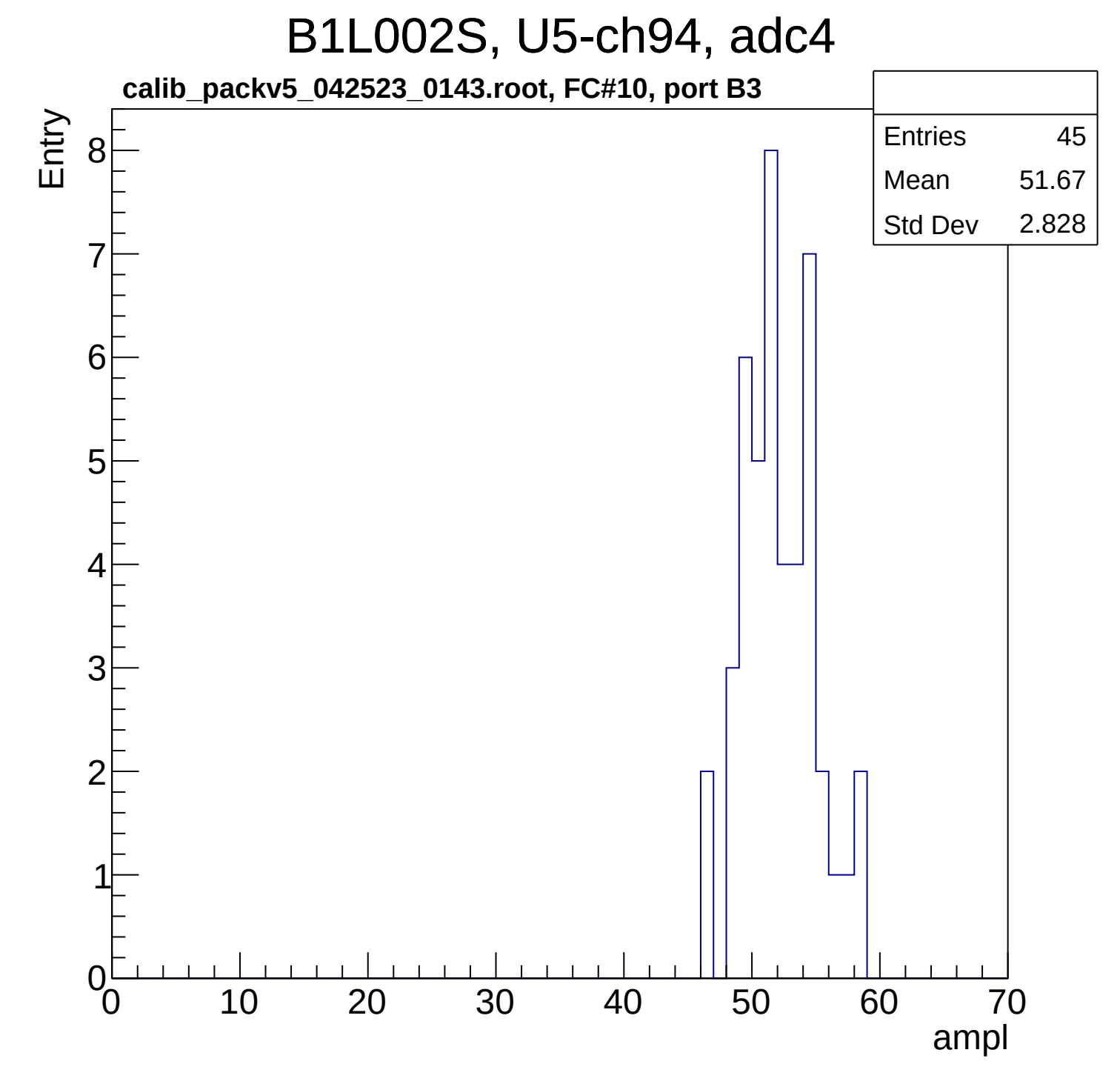

B1L002S, U5-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.19
Std Dev 3.165
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 51.67
Std Dev 2.828
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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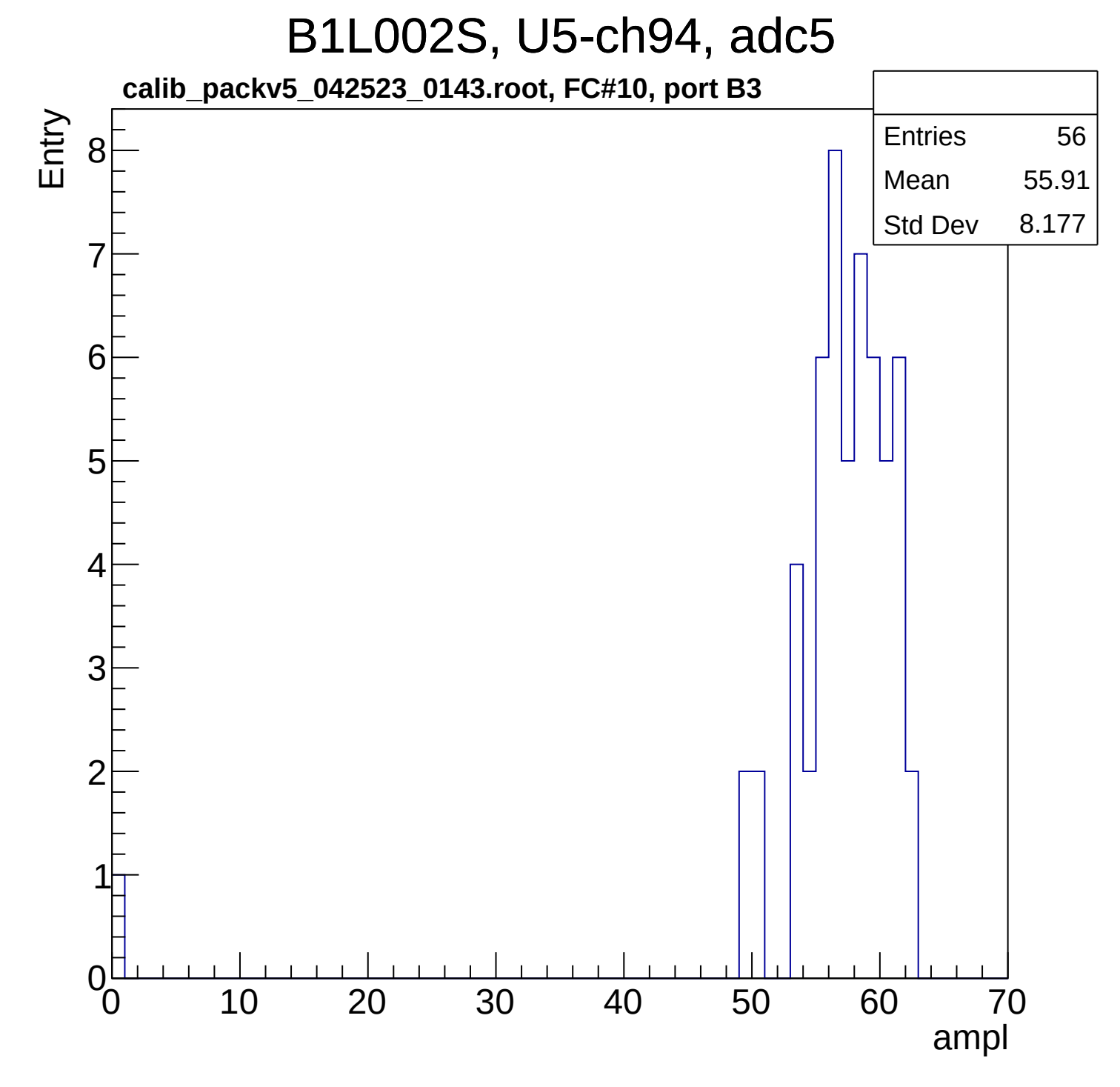
B1L002S, U5-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.91
Std Dev 8.177
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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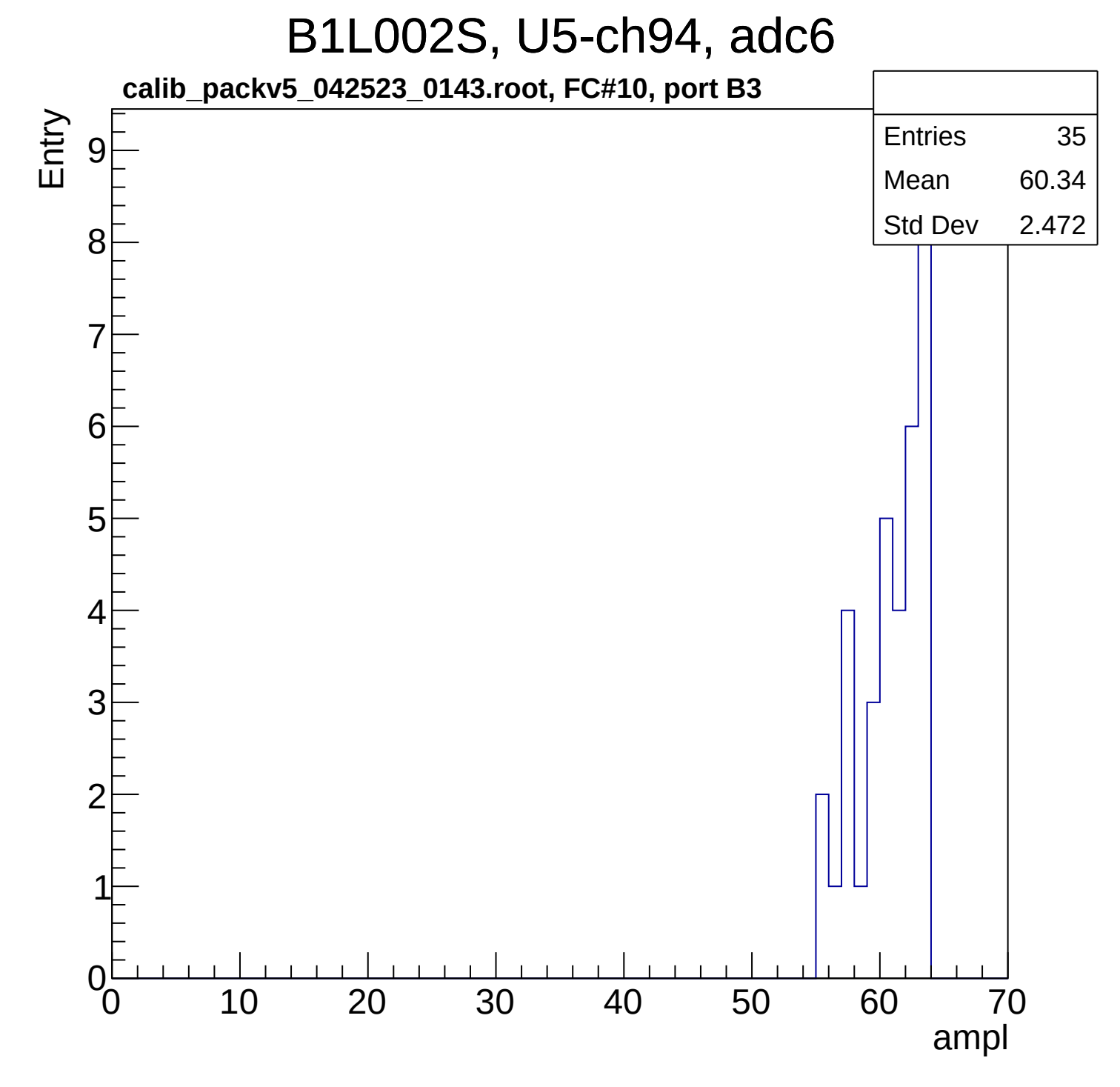
B1L002S, U5-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.34
Std Dev 2.472
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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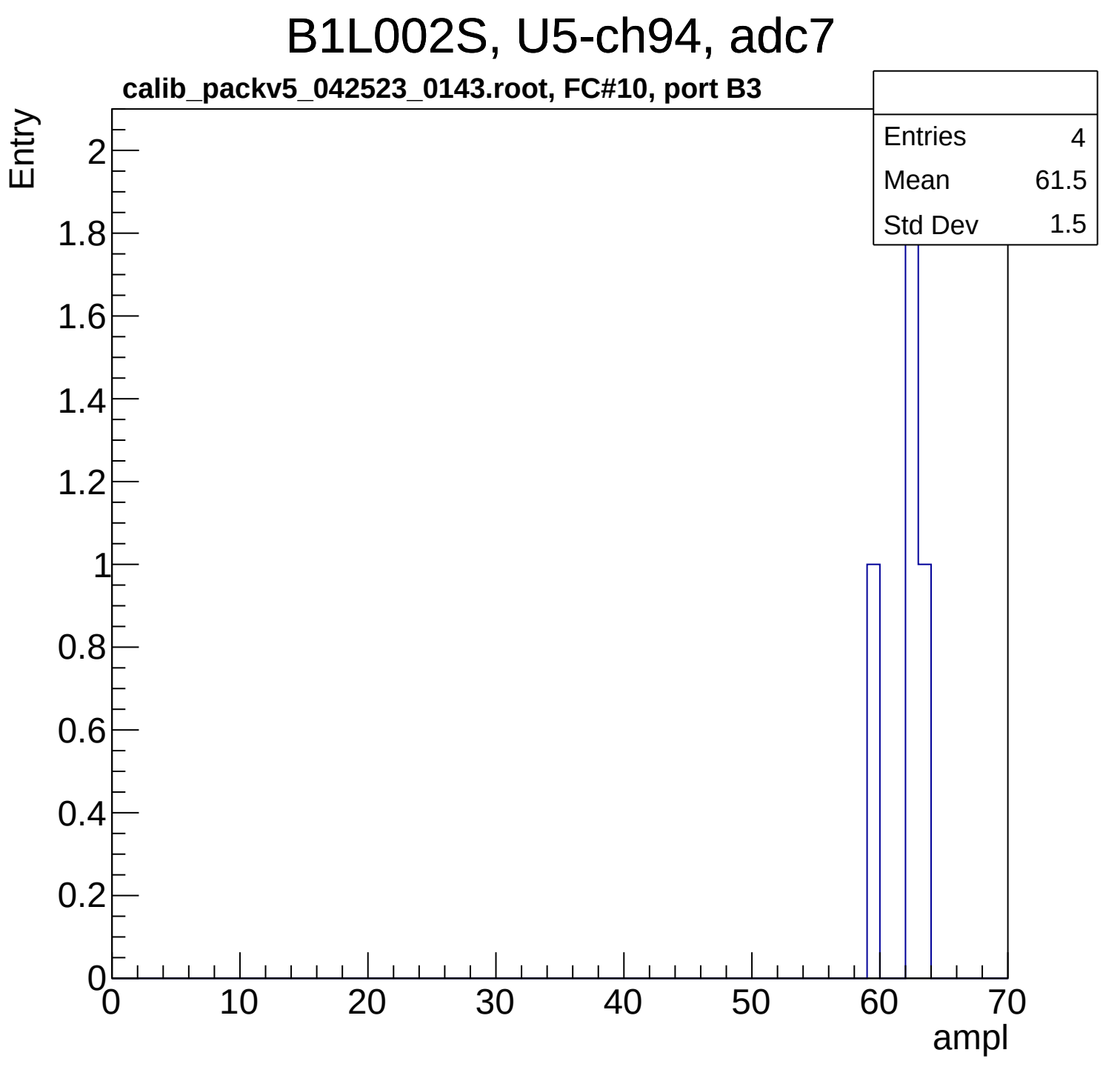
20
30
40
50
60
70


B1L002S, U5-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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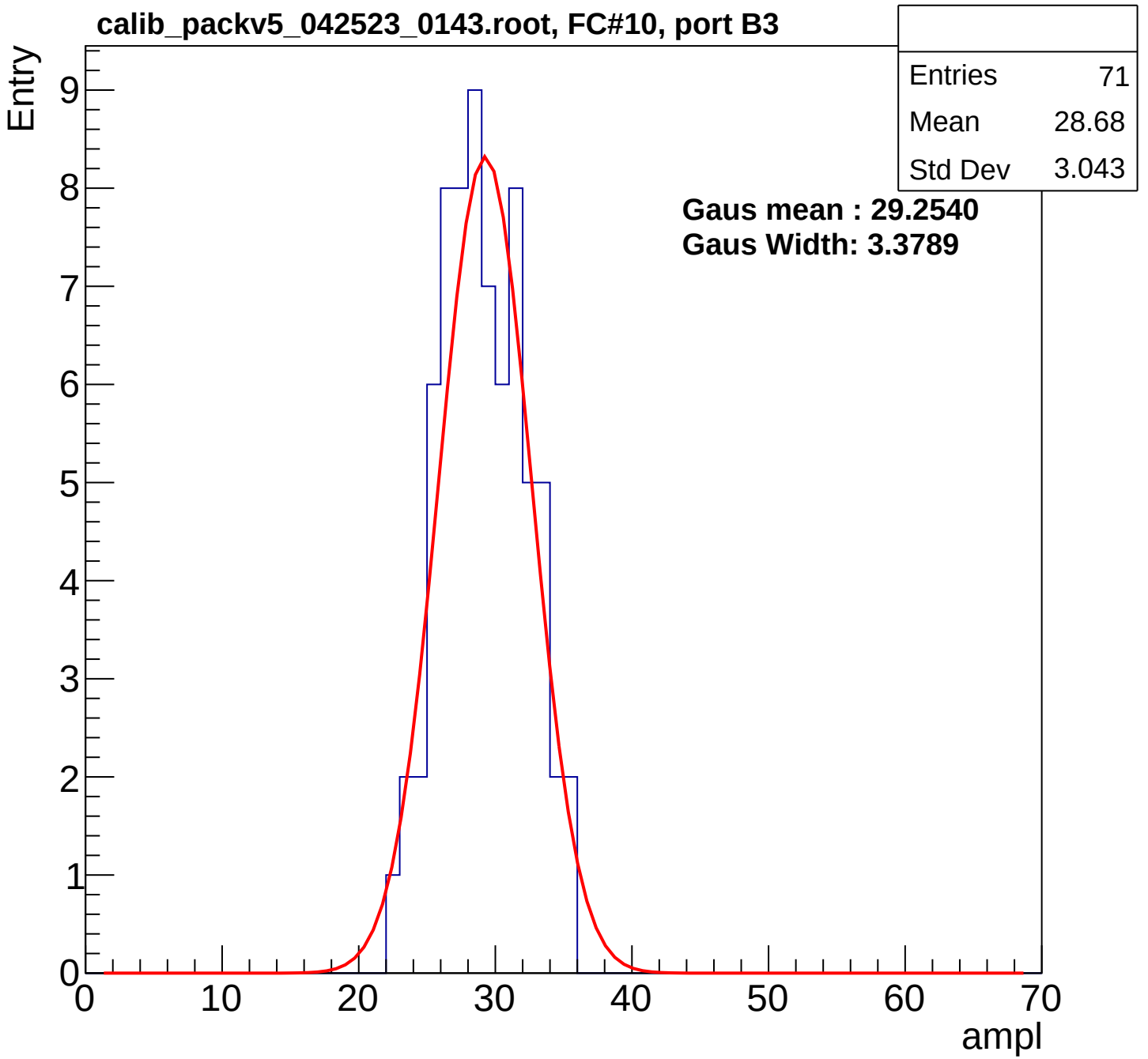
B1L002S, U5-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.68
Std Dev 3.043
Gaus mean : 29.2540
Gaus Width: 3.3789
Entry
ampl
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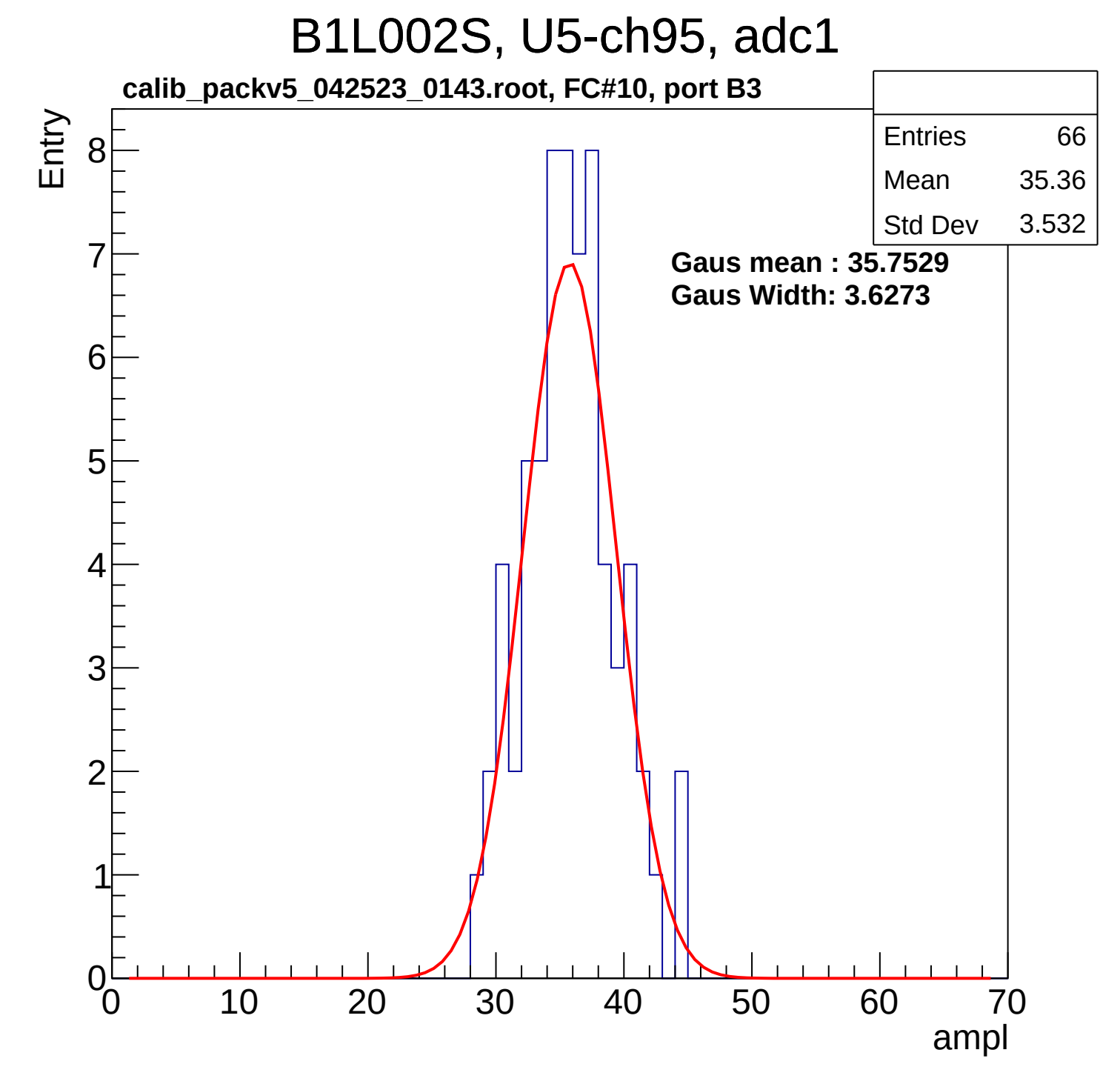
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.36
Std Dev 3.532
Gaus mean : 35.7529
Gaus Width: 3.6273
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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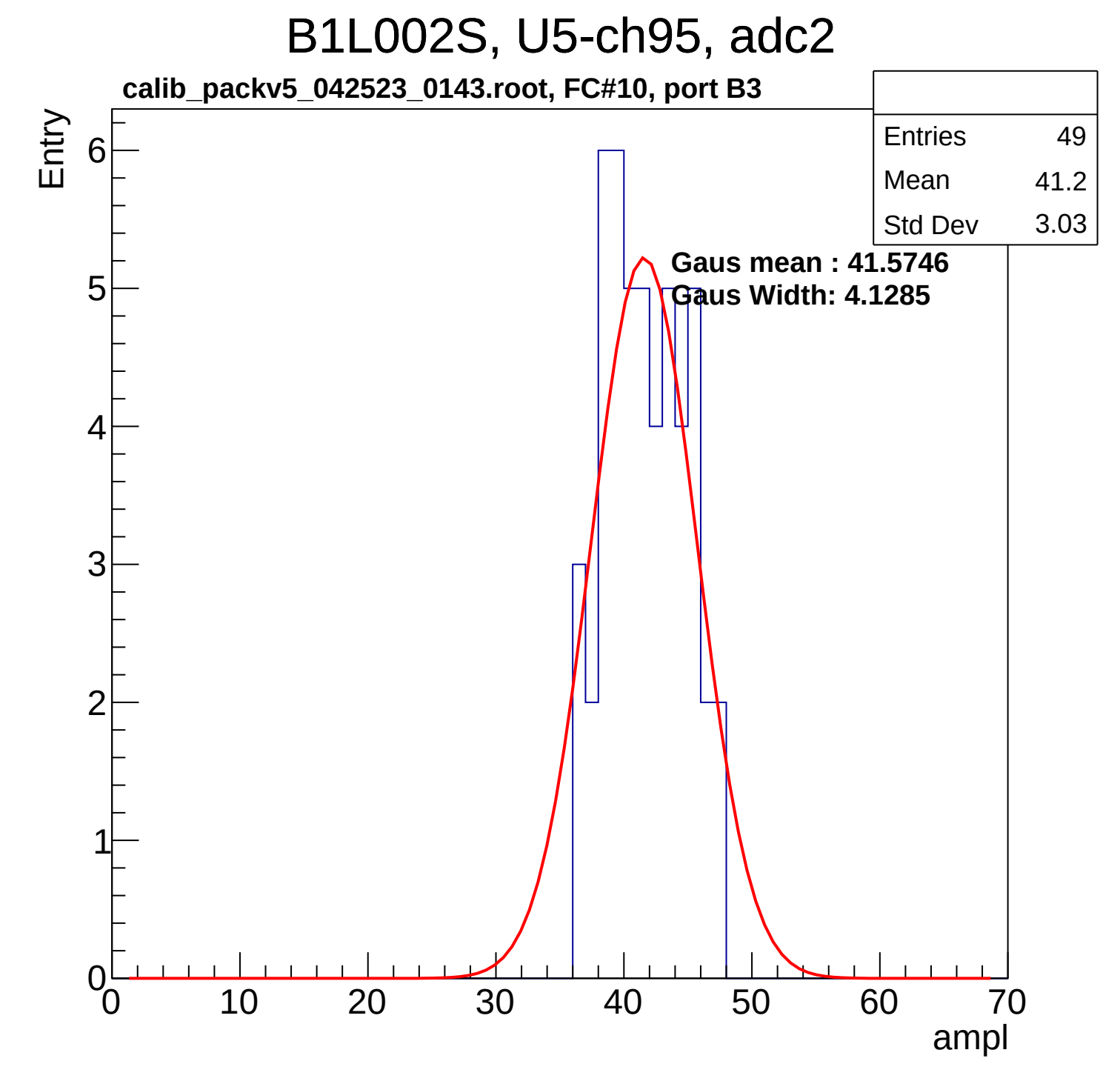
B1L002S, U5-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.2
Std Dev 3.03
Gaus mean : 41.5746
Gaus Width: 4.1285
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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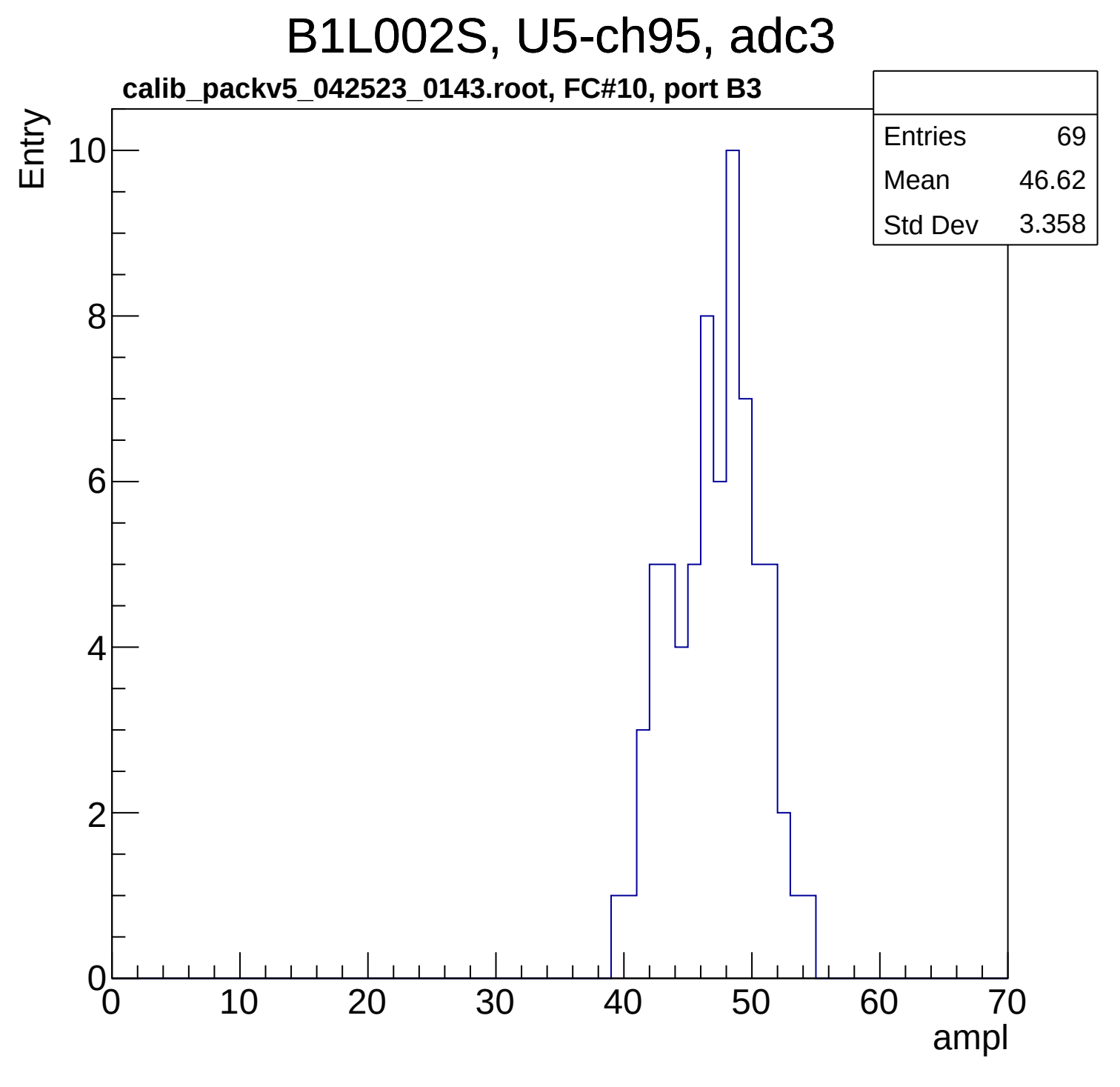
B1L002S, U5-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.62
Std Dev 3.358
Entry
ampl
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4
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10
0
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40
50
60
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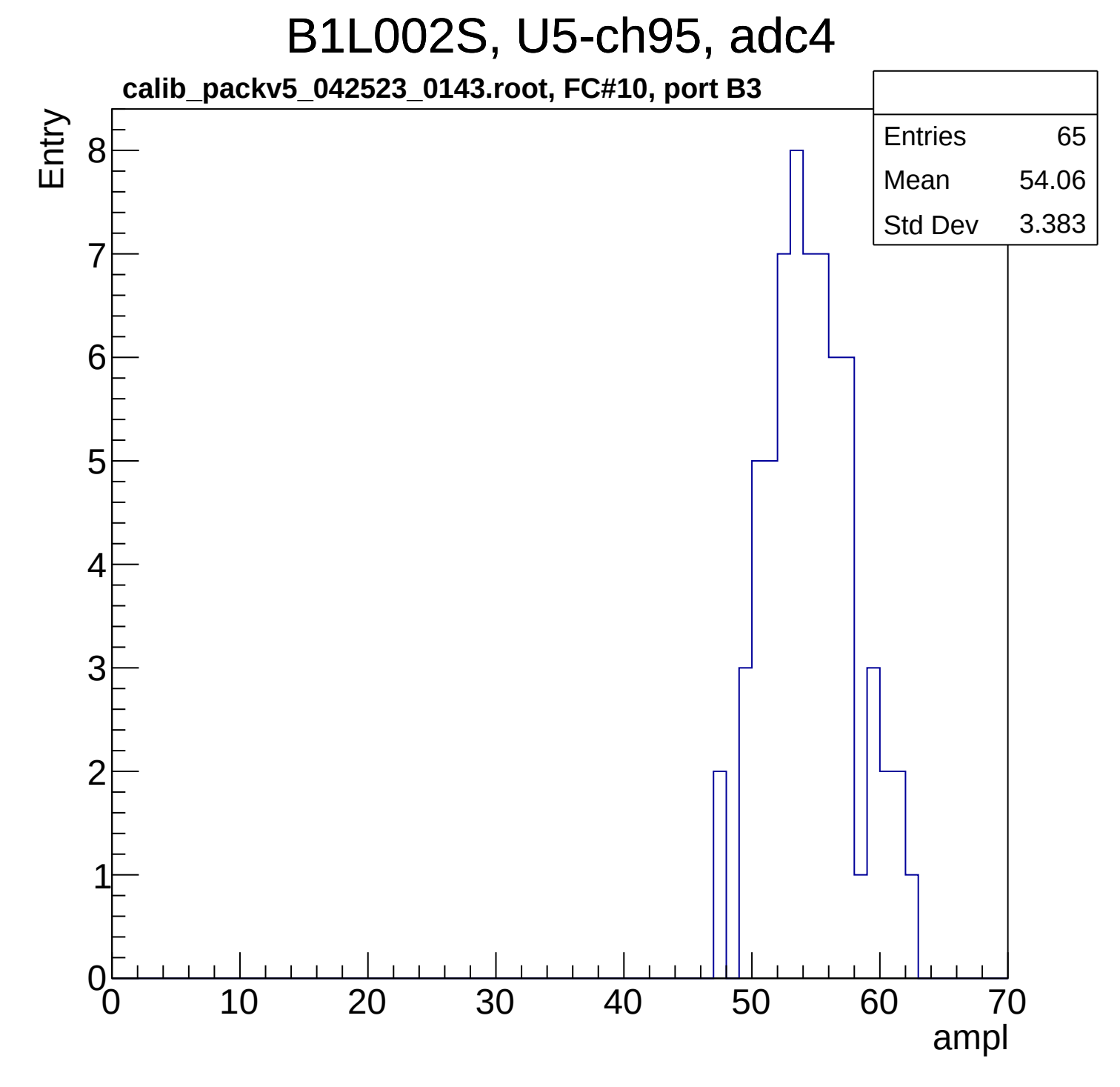
B1L002S, U5-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 54.06
Std Dev 3.383
Entry
ampl
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3
4
5
6
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0
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40
50
60
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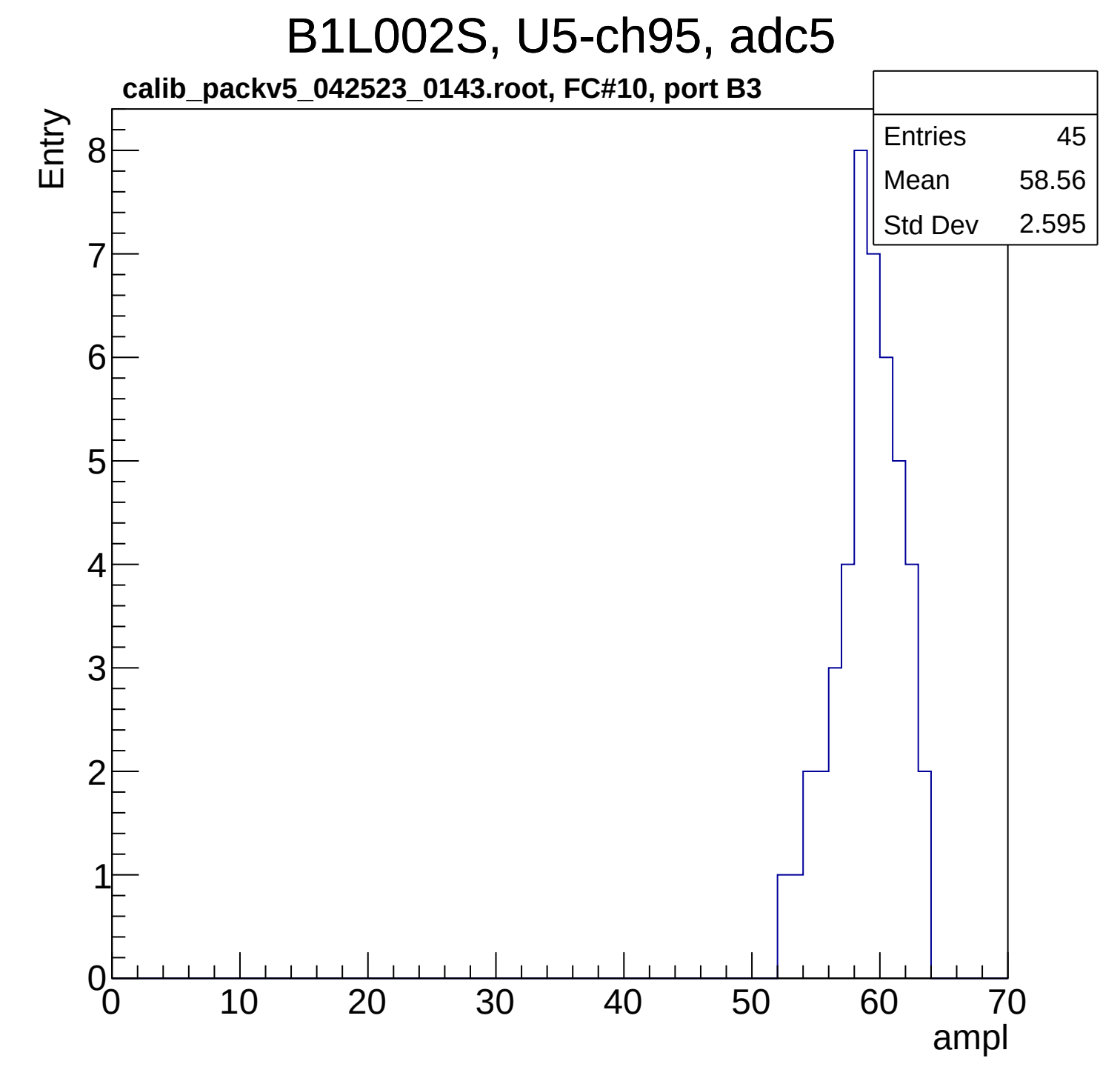
B1L002S, U5-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.56
Std Dev 2.595
Entry
ampl
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1
2
3
4
5
6
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60
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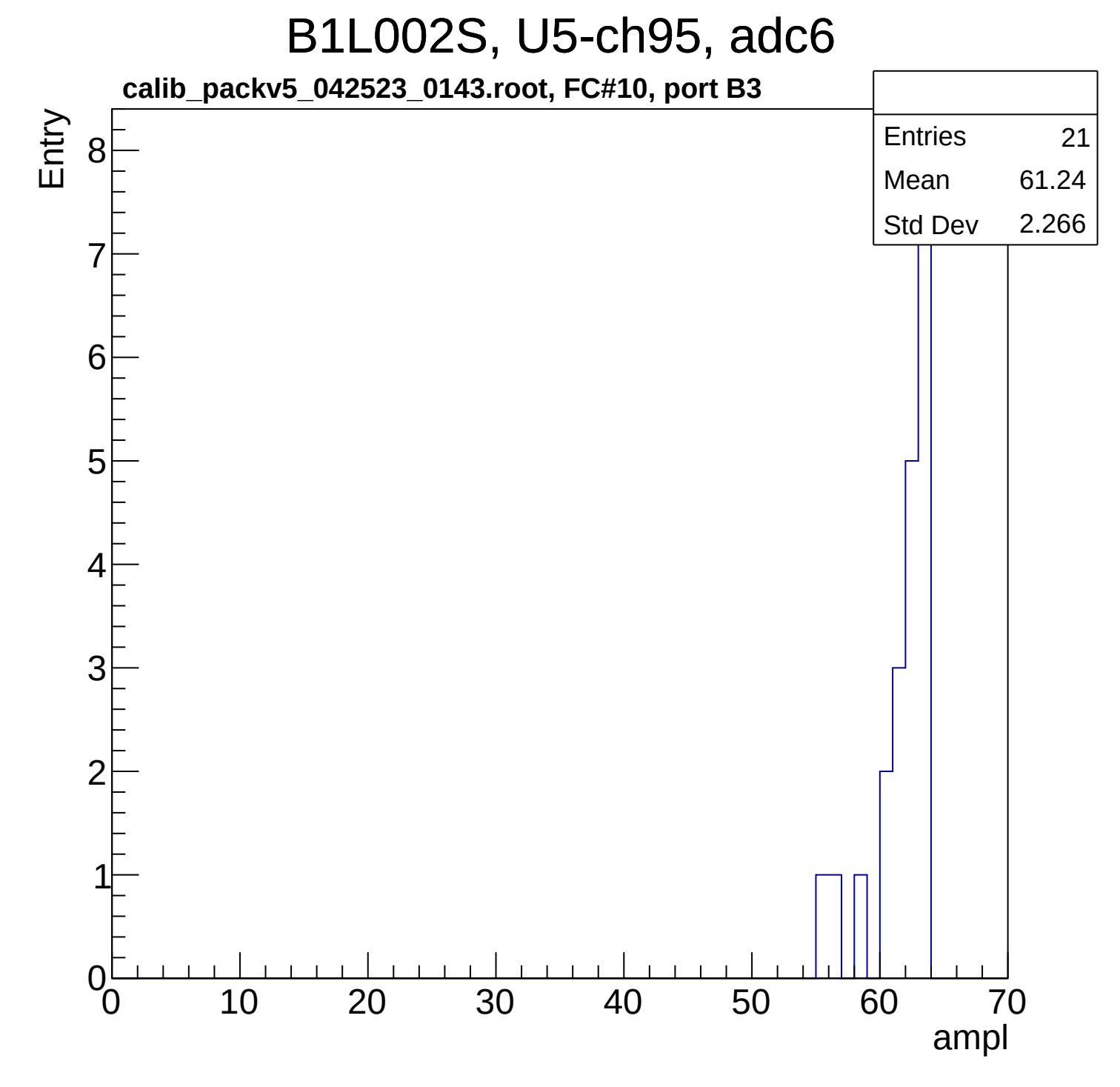
B1L002S, U5-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.24
Std Dev 2.266
Entry
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2
3
4
5
6
7
8
0
10
20
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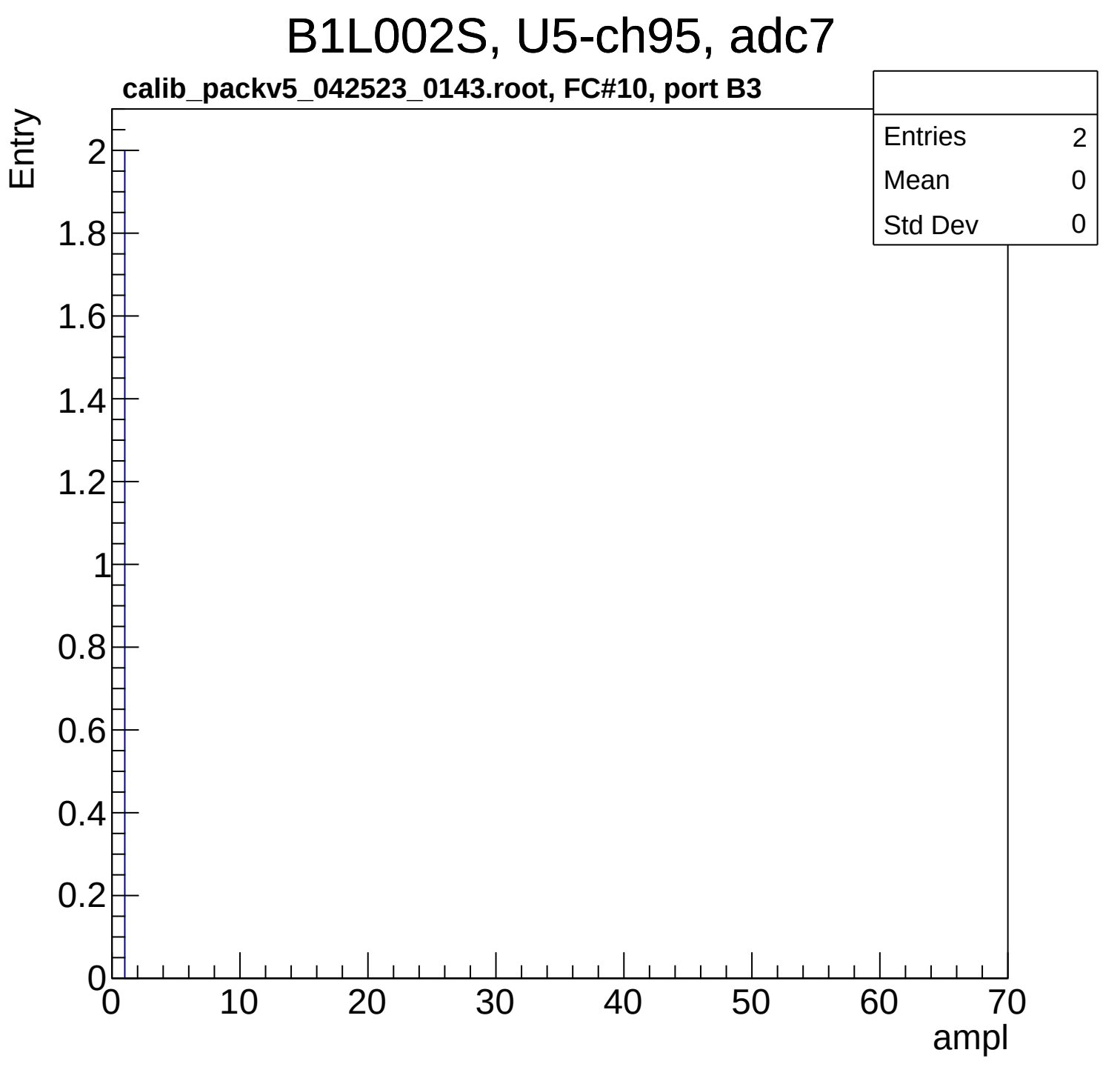
50
60
70
ampl


B1L002S, U5-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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0.6
0.4
0.2
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10
20
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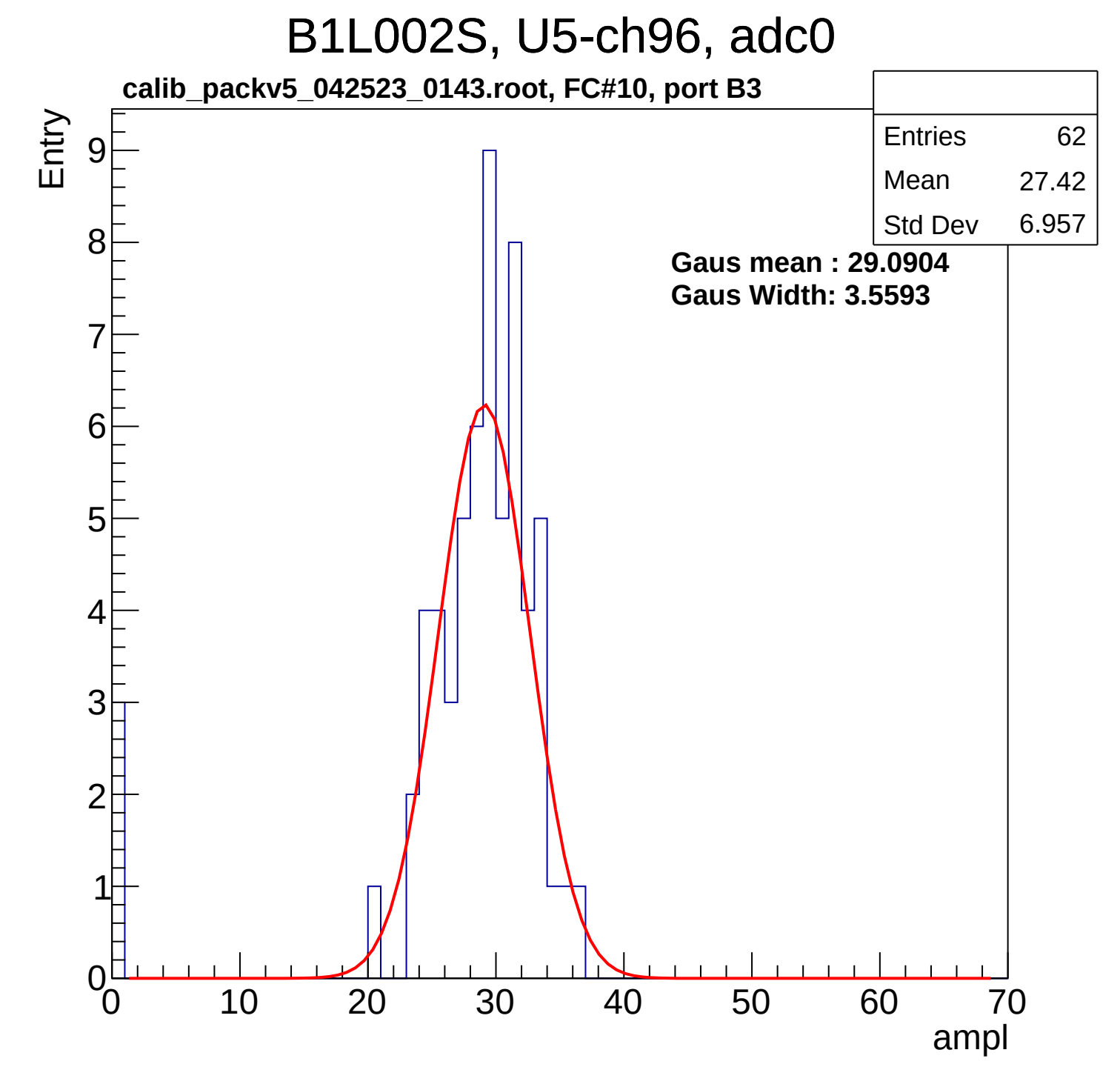
50
60
70
ampl


B1L002S, U5-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.42
Std Dev 6.957
Gaus mean : 29.0904
Gaus Width: 3.5593
Entry
ampl
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2
3
4
5
6
7
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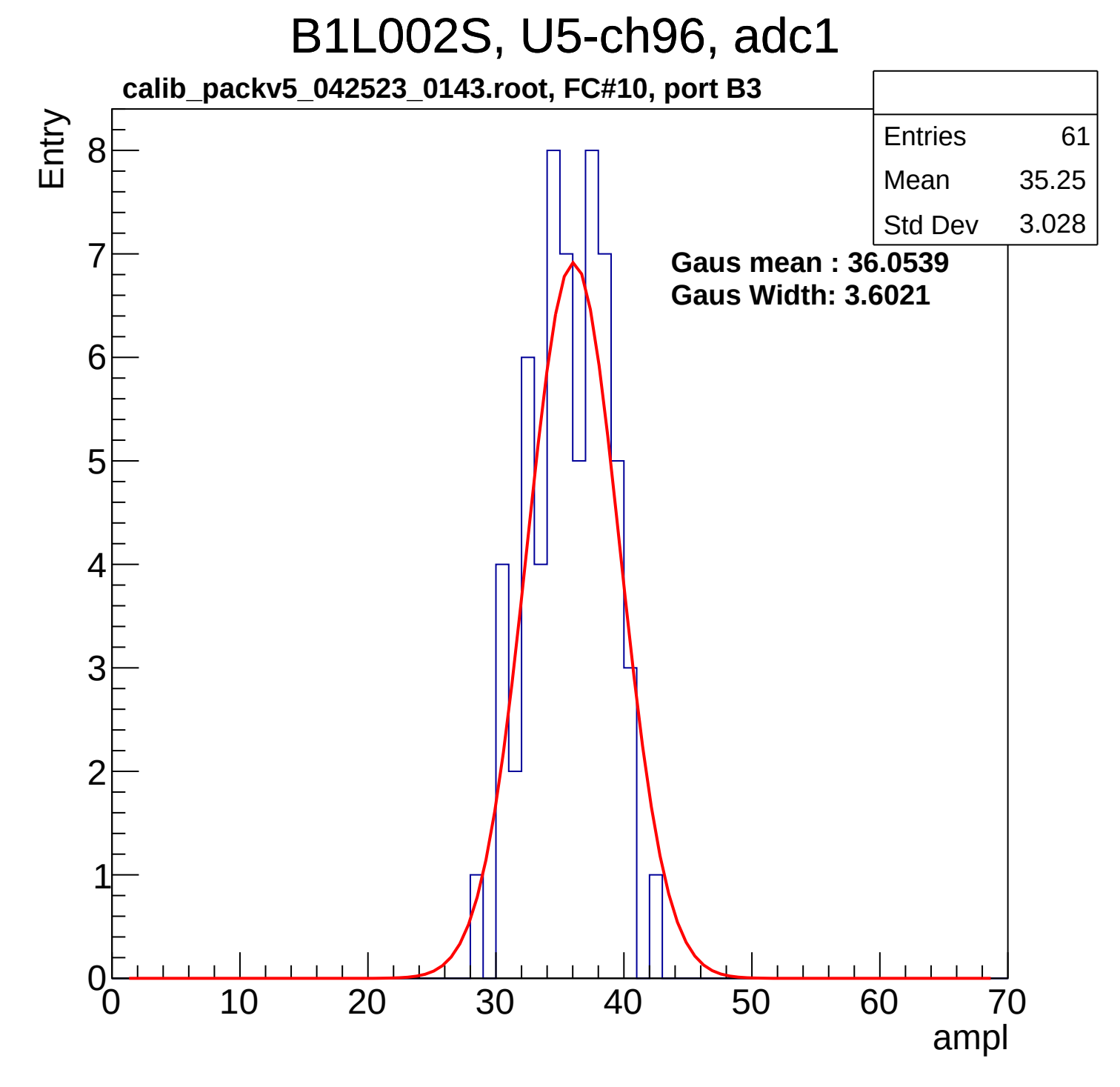
B1L002S, U5-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.25
Std Dev 3.028
Gaus mean : 36.0539
Gaus Width: 3.6021
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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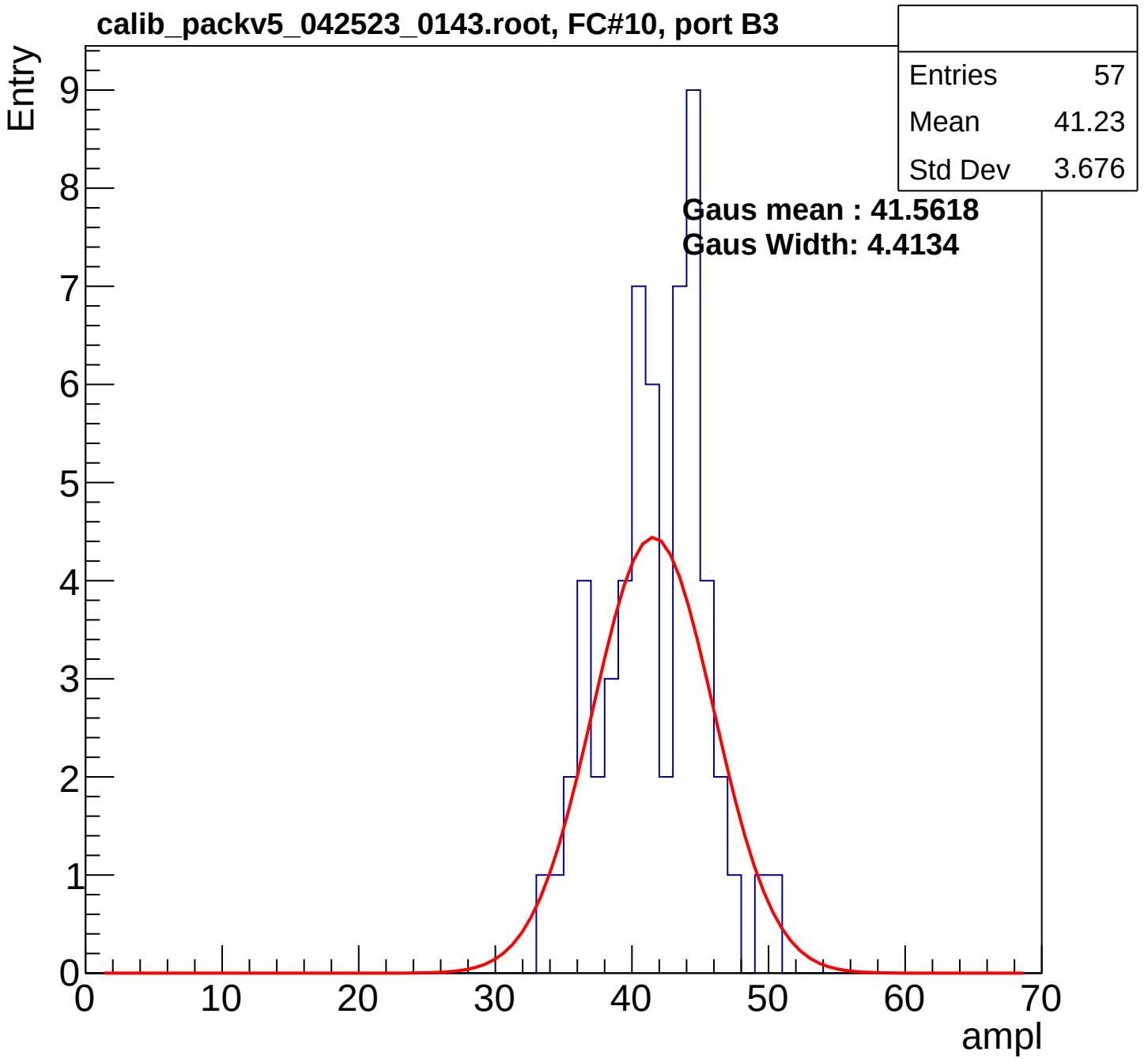
40
50
60
70


B1L002S, U5-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.23
Std Dev 3.676
Gaus mean : 41.5618
Gaus Width: 4.4134
Entry
ampl
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2
3
4
5
6
7
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0
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60
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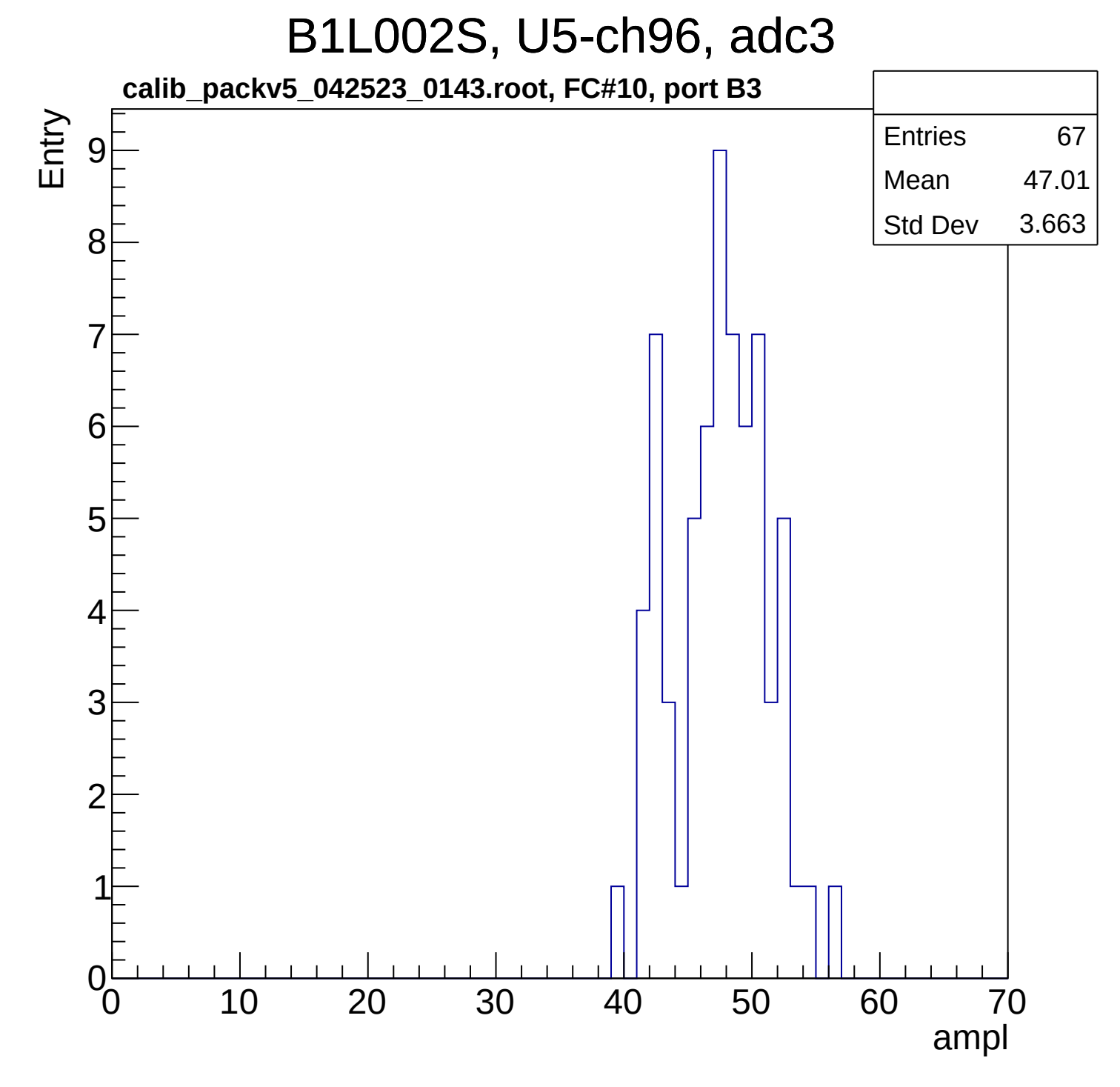
B1L002S, U5-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.01
Std Dev 3.663
Entry
ampl
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2
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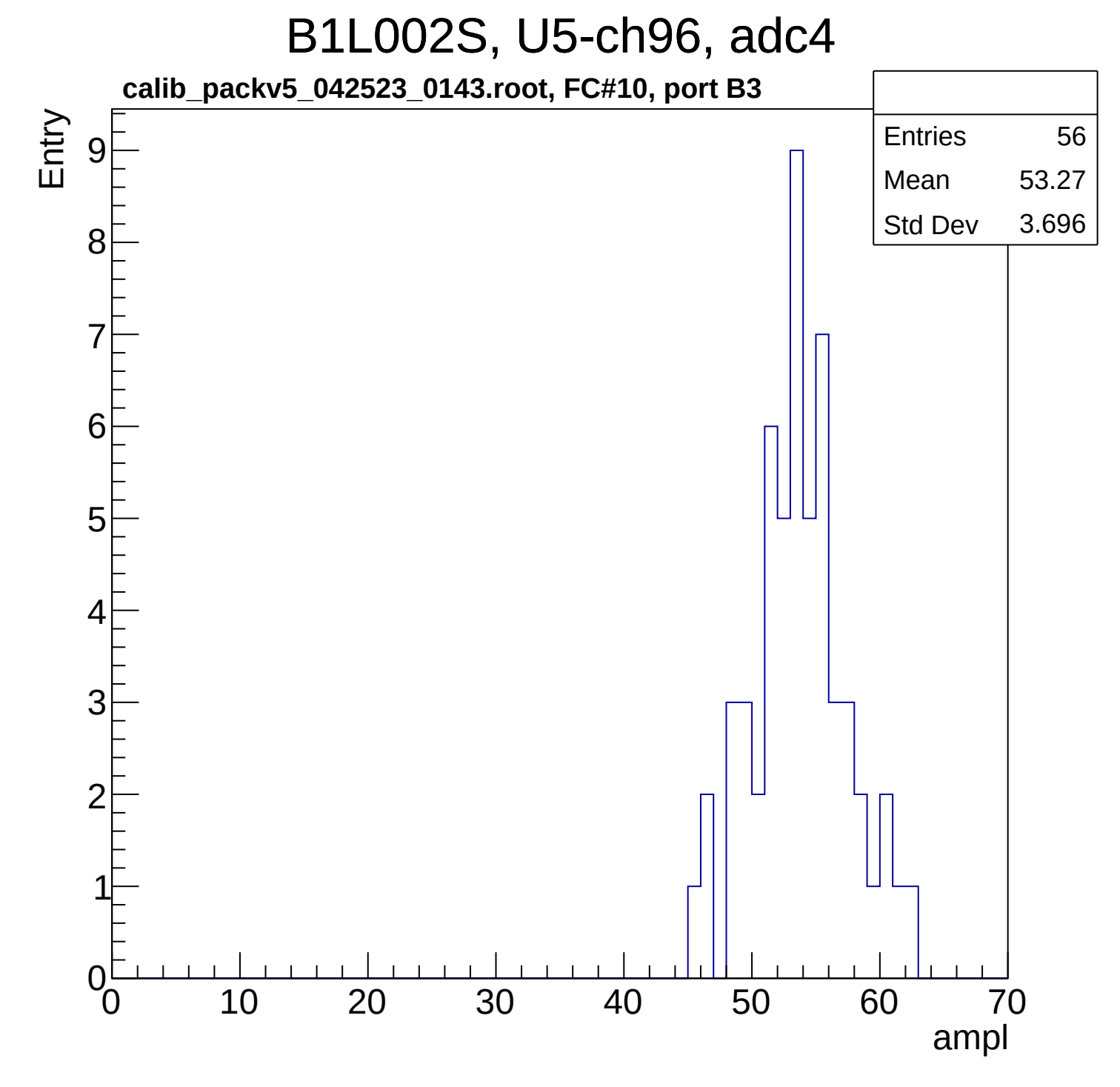
B1L002S, U5-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.27
Std Dev 3.696
Entry
ampl
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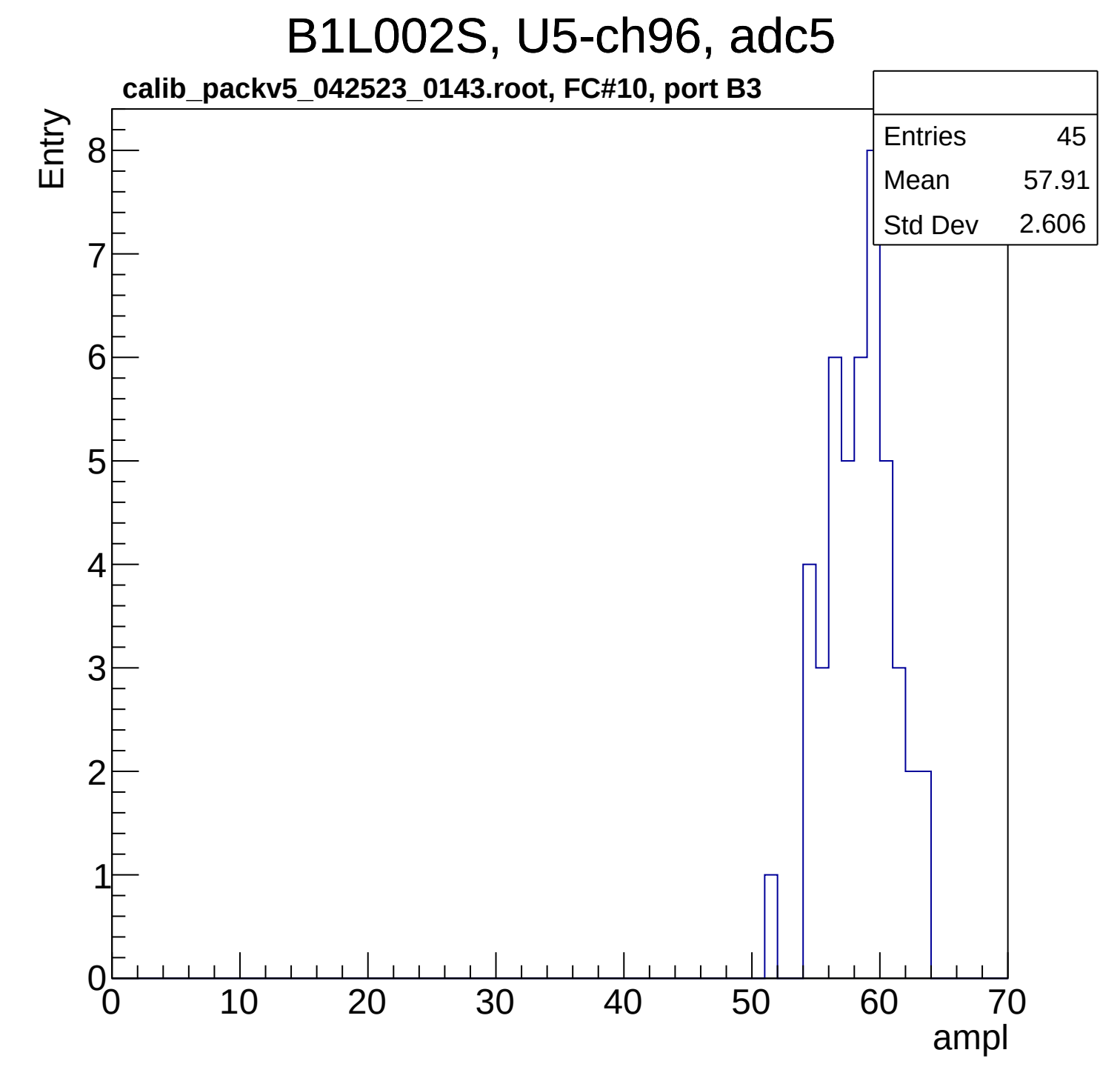
B1L002S, U5-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.91
Std Dev 2.606
Entry
ampl
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7
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0
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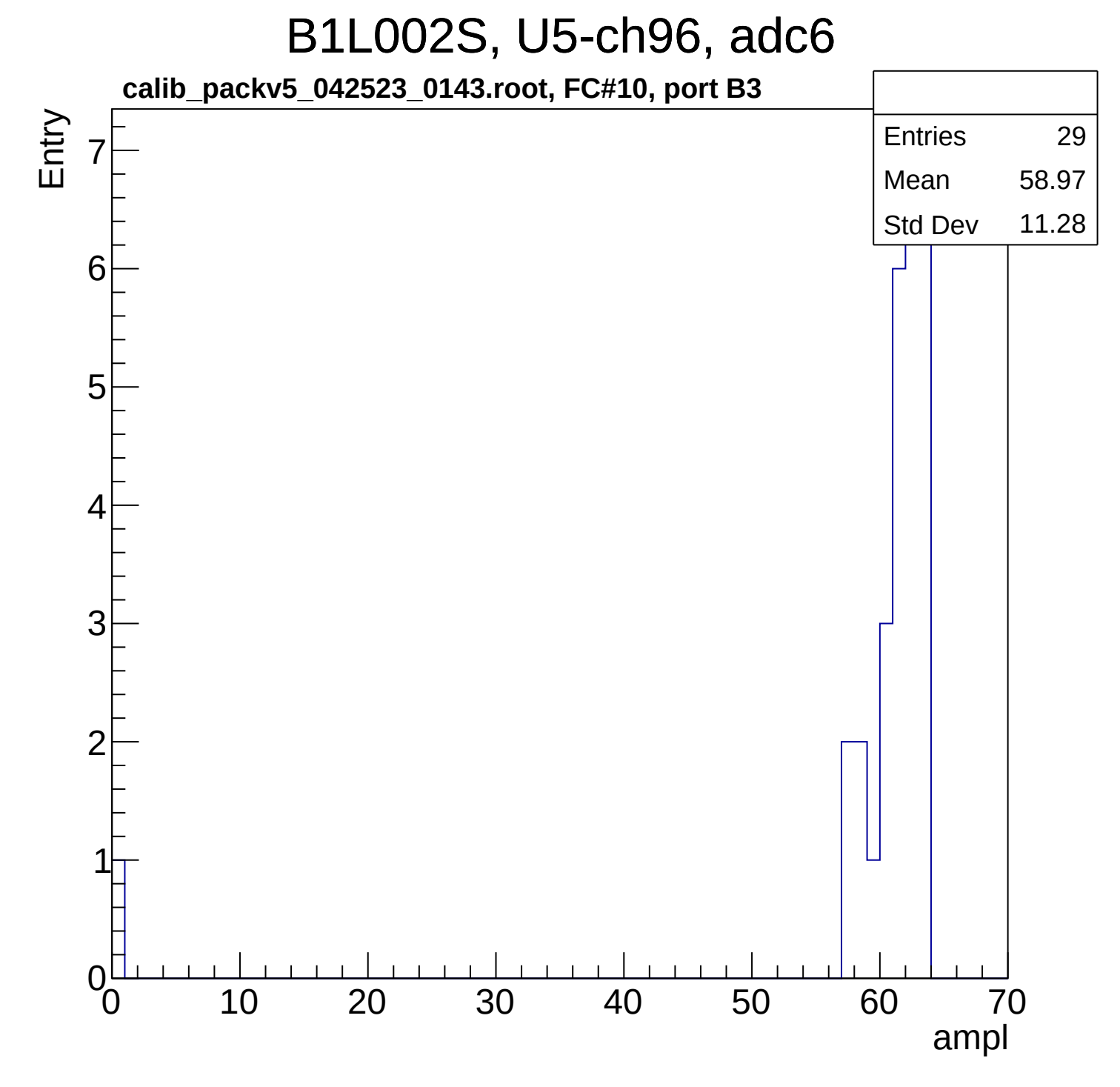
B1L002S, U5-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.97
Std Dev 11.28
Entry
0
1
2
3
4
5
6
7
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10
20
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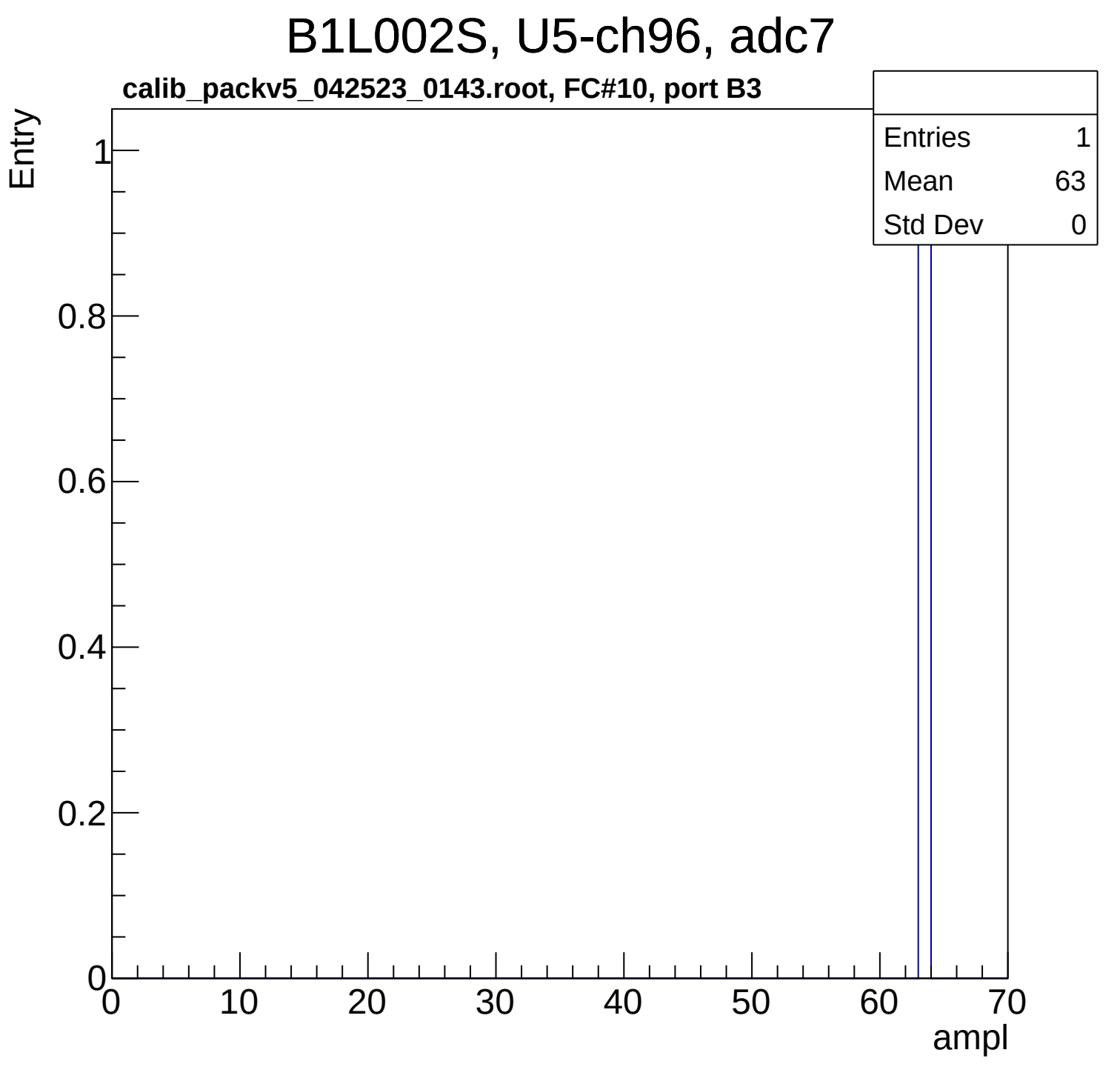
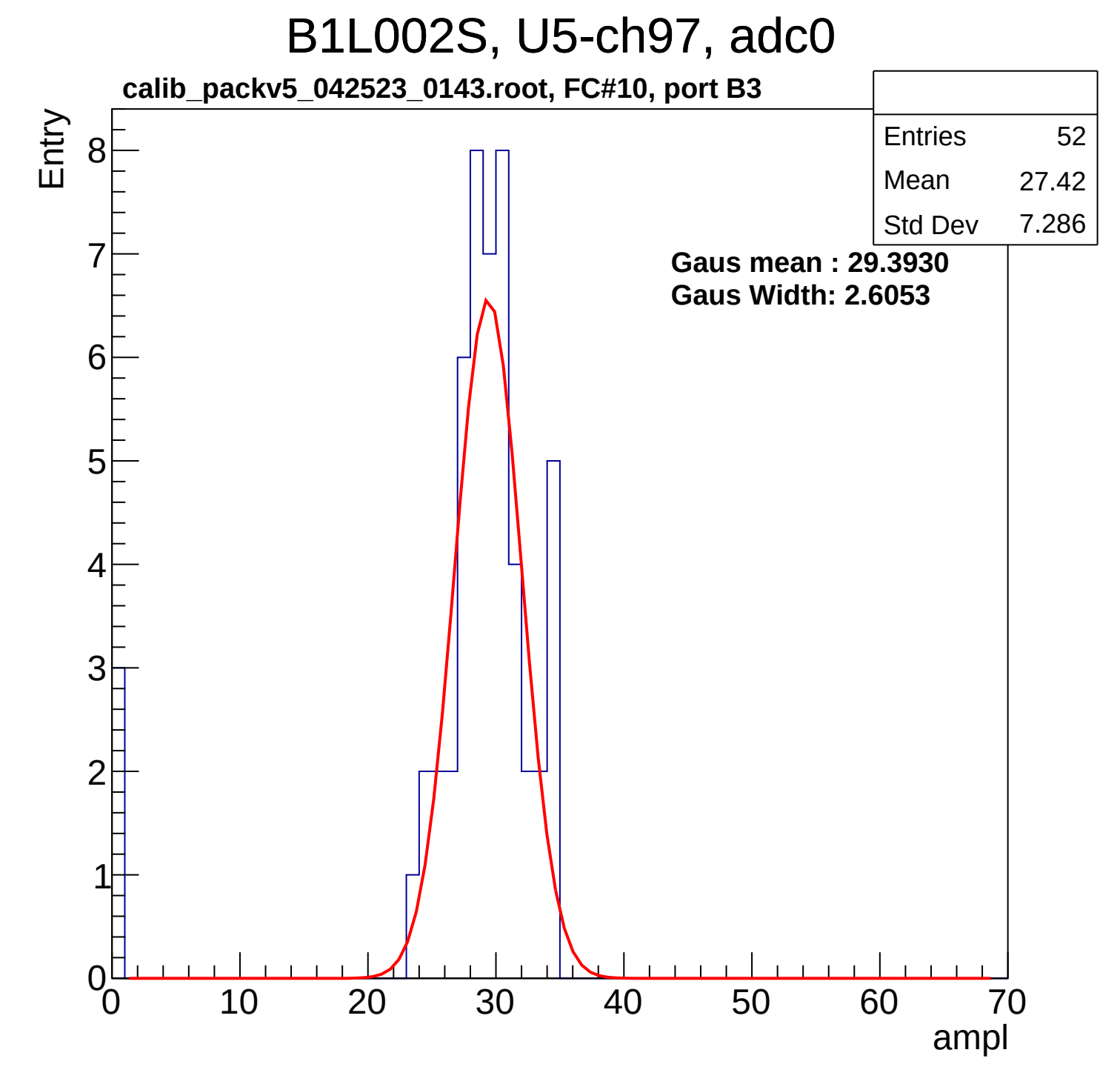
50
60
70
ampl


B1L002S, U5-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.42
Std Dev 7.286
Gaus mean : 29.3930
Gaus Width: 2.6053
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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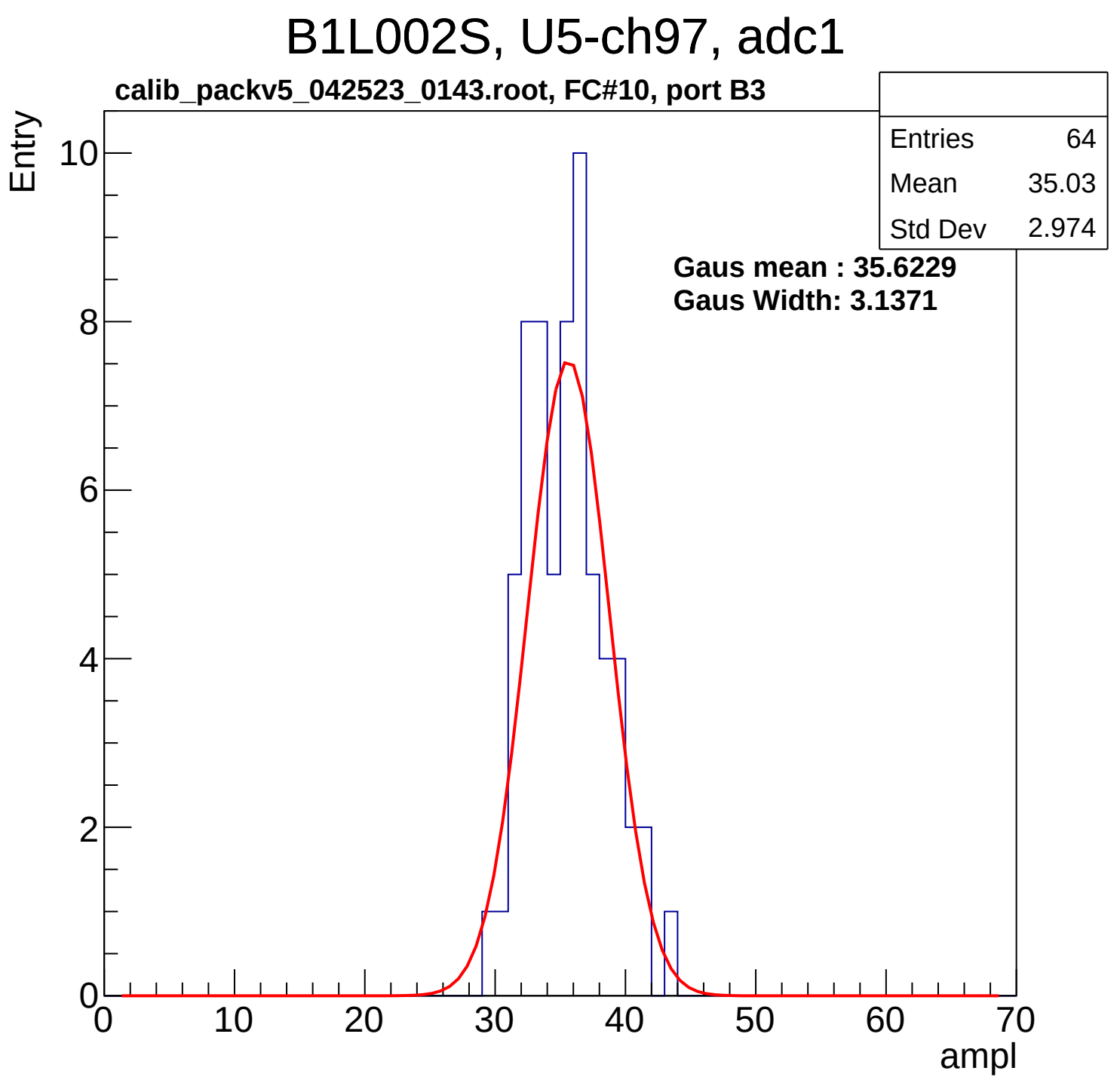
B1L002S, U5-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.03
Std Dev 2.974
Gaus mean : 35.6229
Gaus Width: 3.1371
Entry
ampl
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2
4
6
8
10
0
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20
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40
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60
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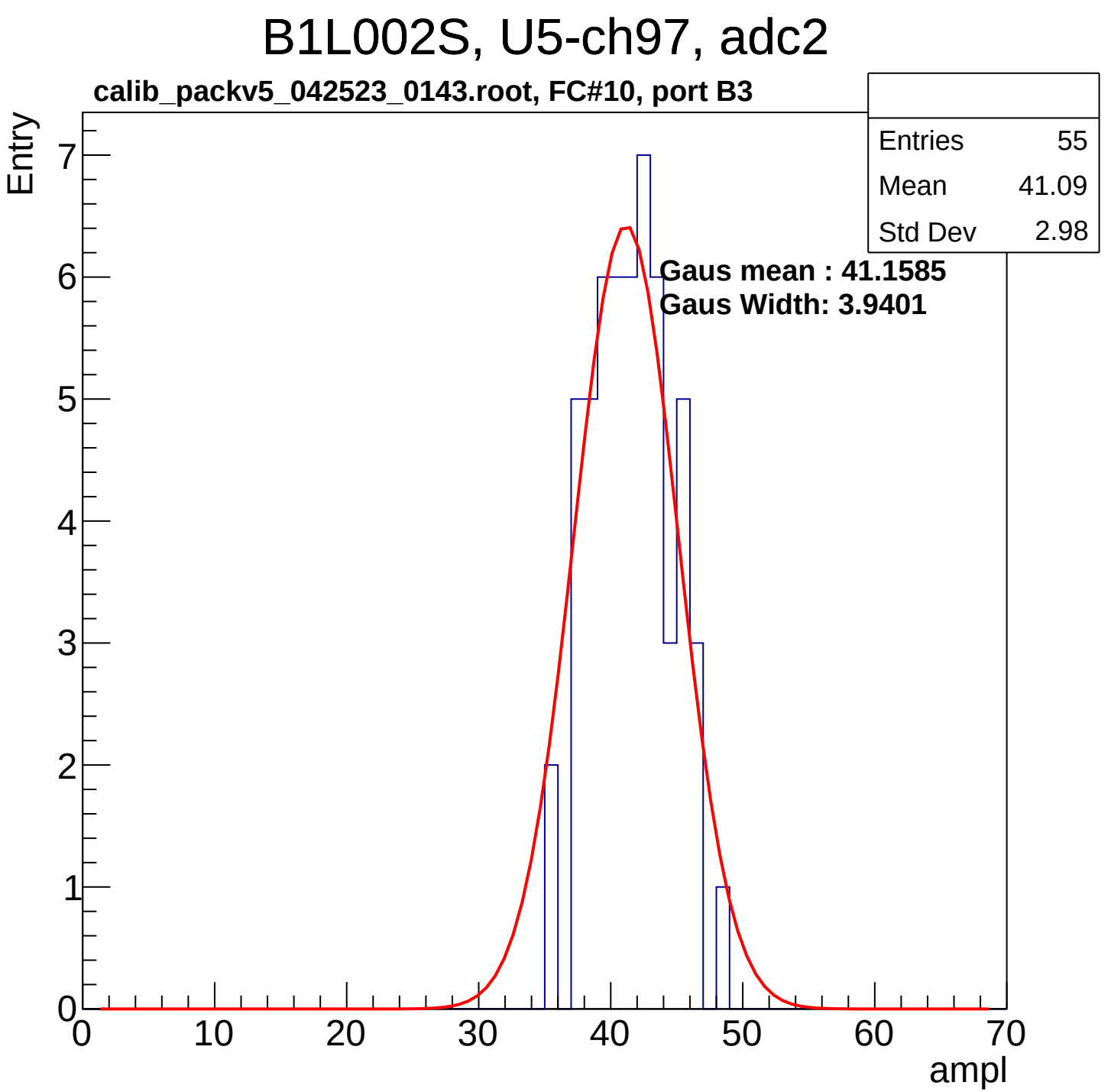
B1L002S, U5-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.09
Std Dev 2.98
Gaus mean : 41.1585
Gaus Width: 3.9401
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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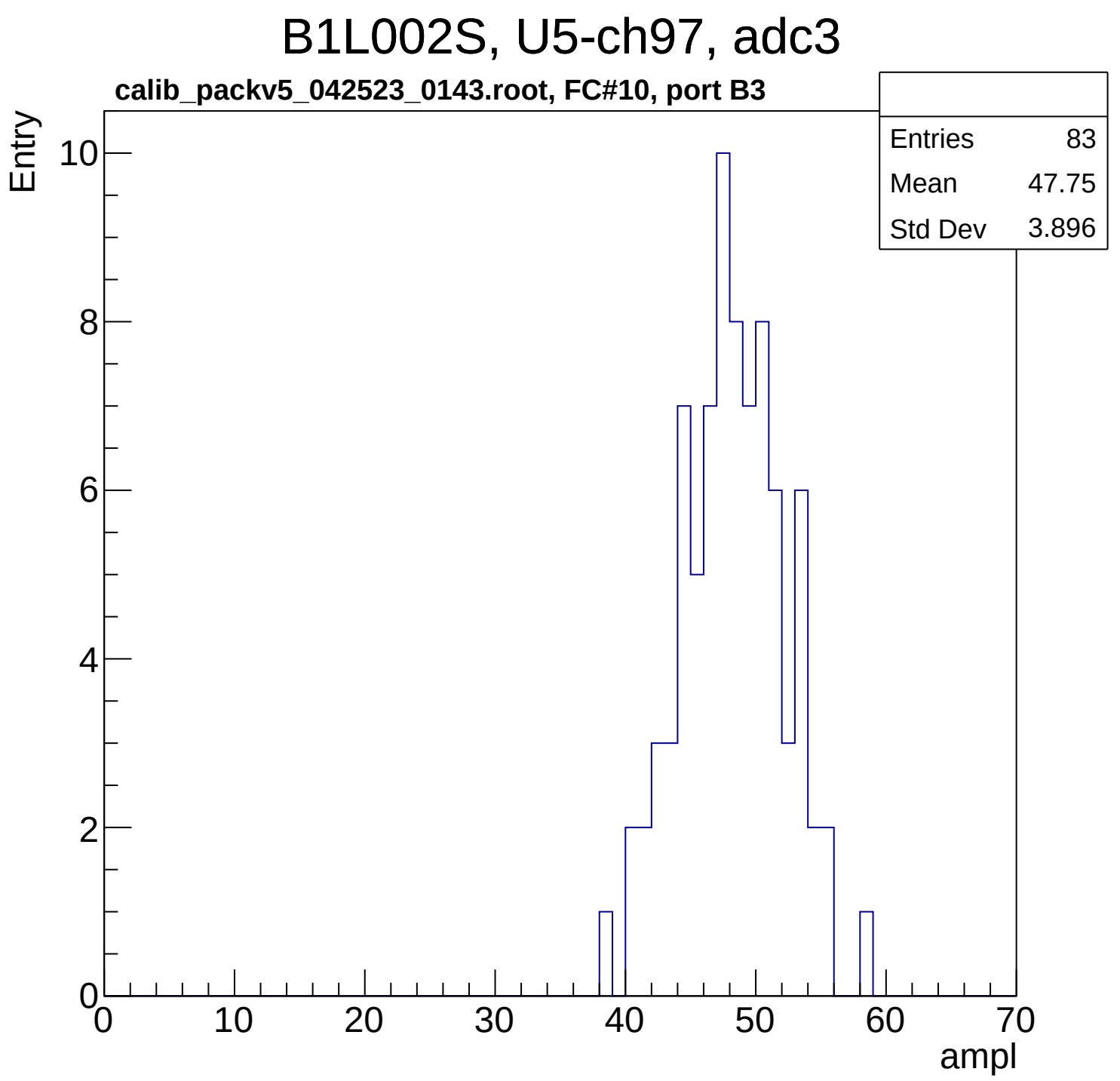
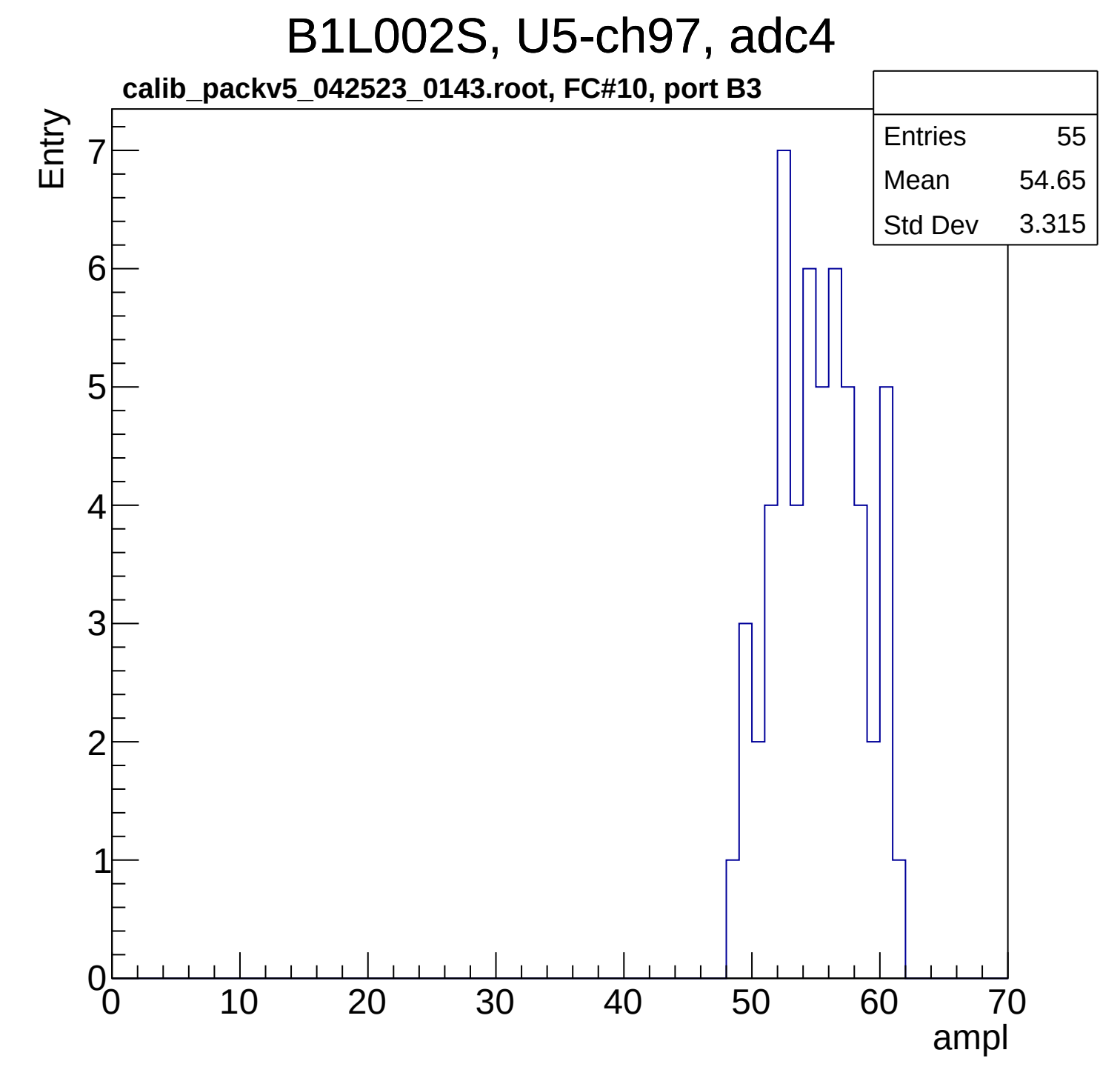
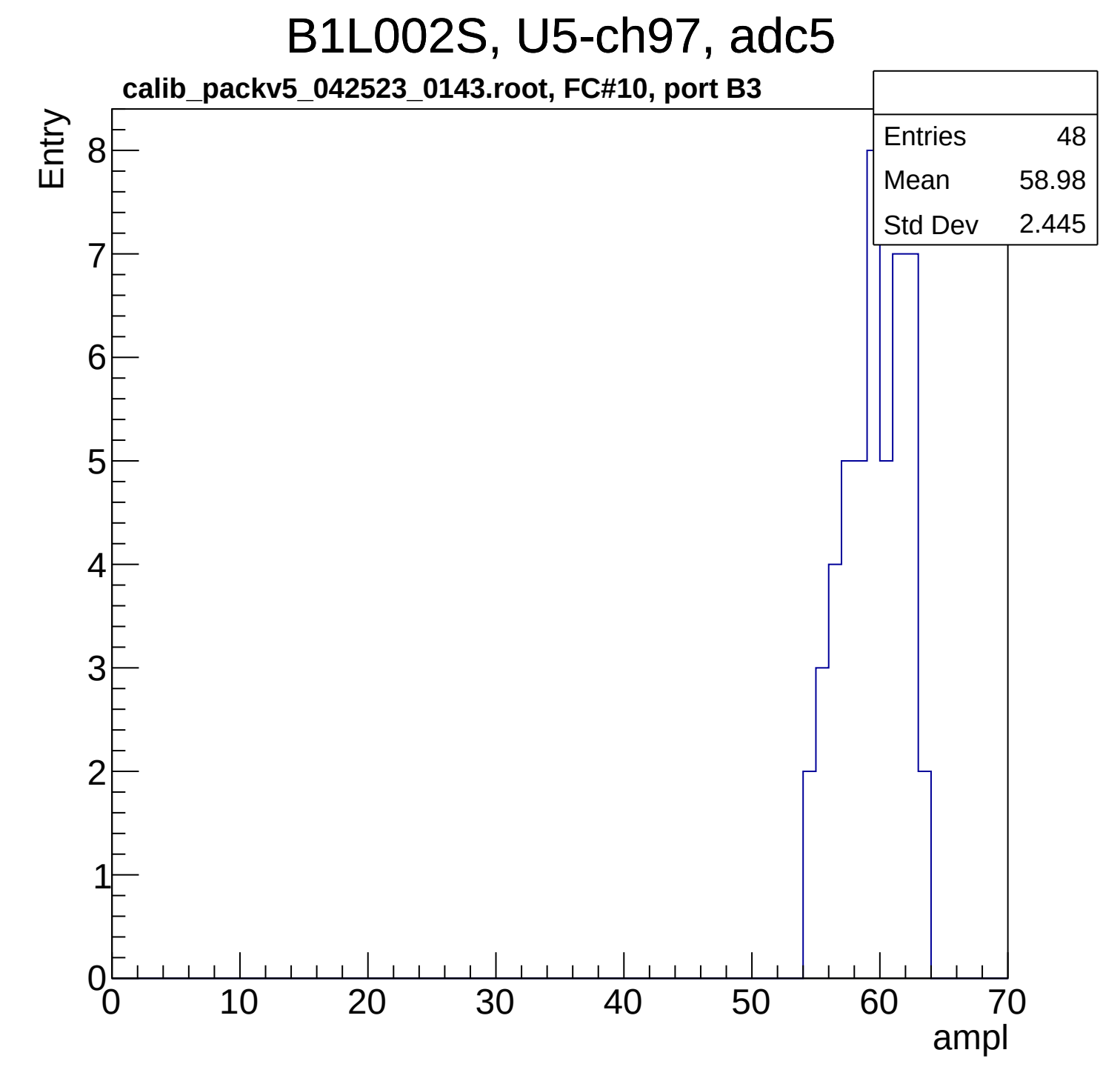
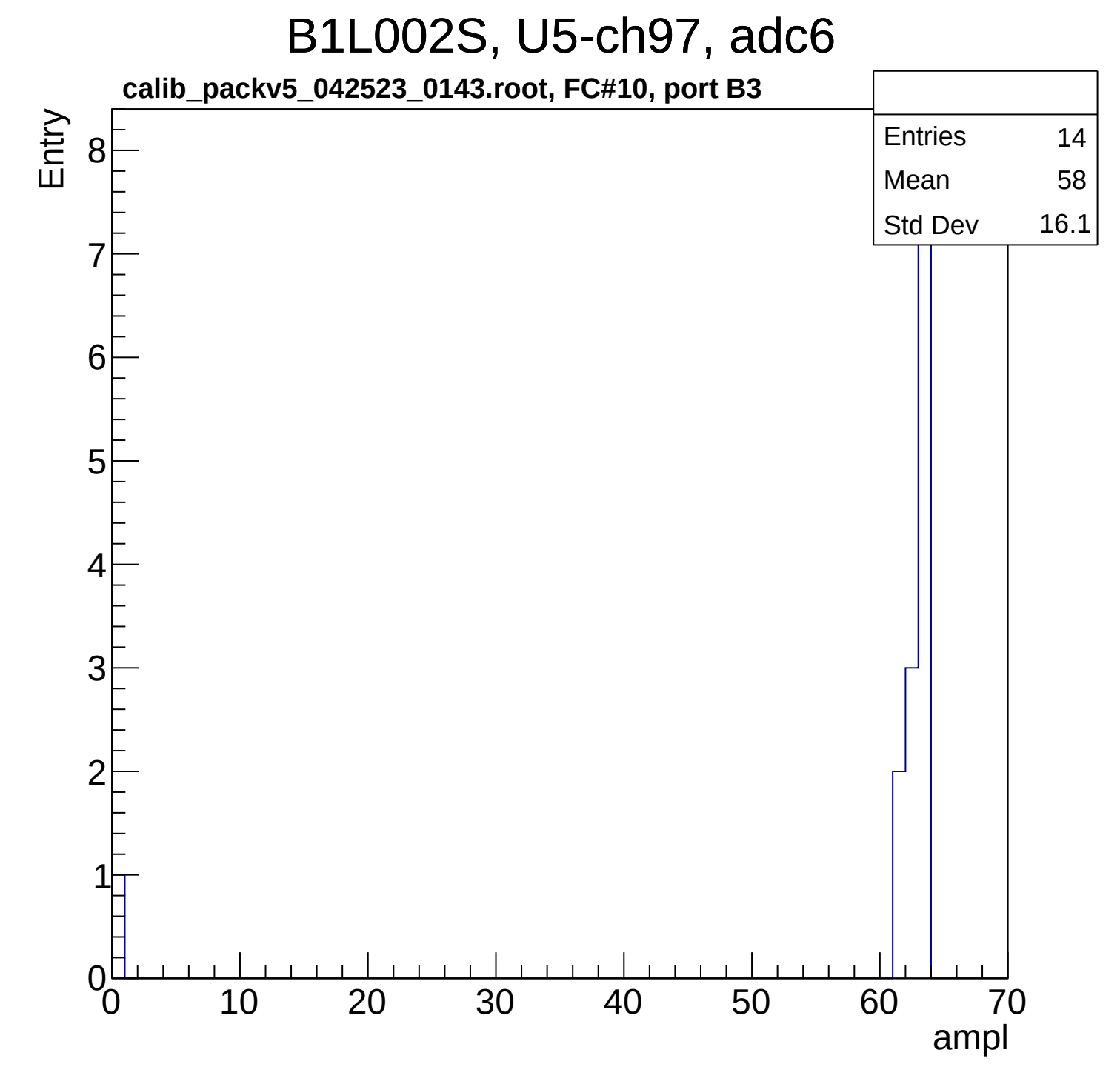

B1L002S, U5-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 47.75
Std Dev 3.896
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.65
Std Dev 3.315
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.98
Std Dev 2.445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 58
Std Dev 16.1
Entry
0
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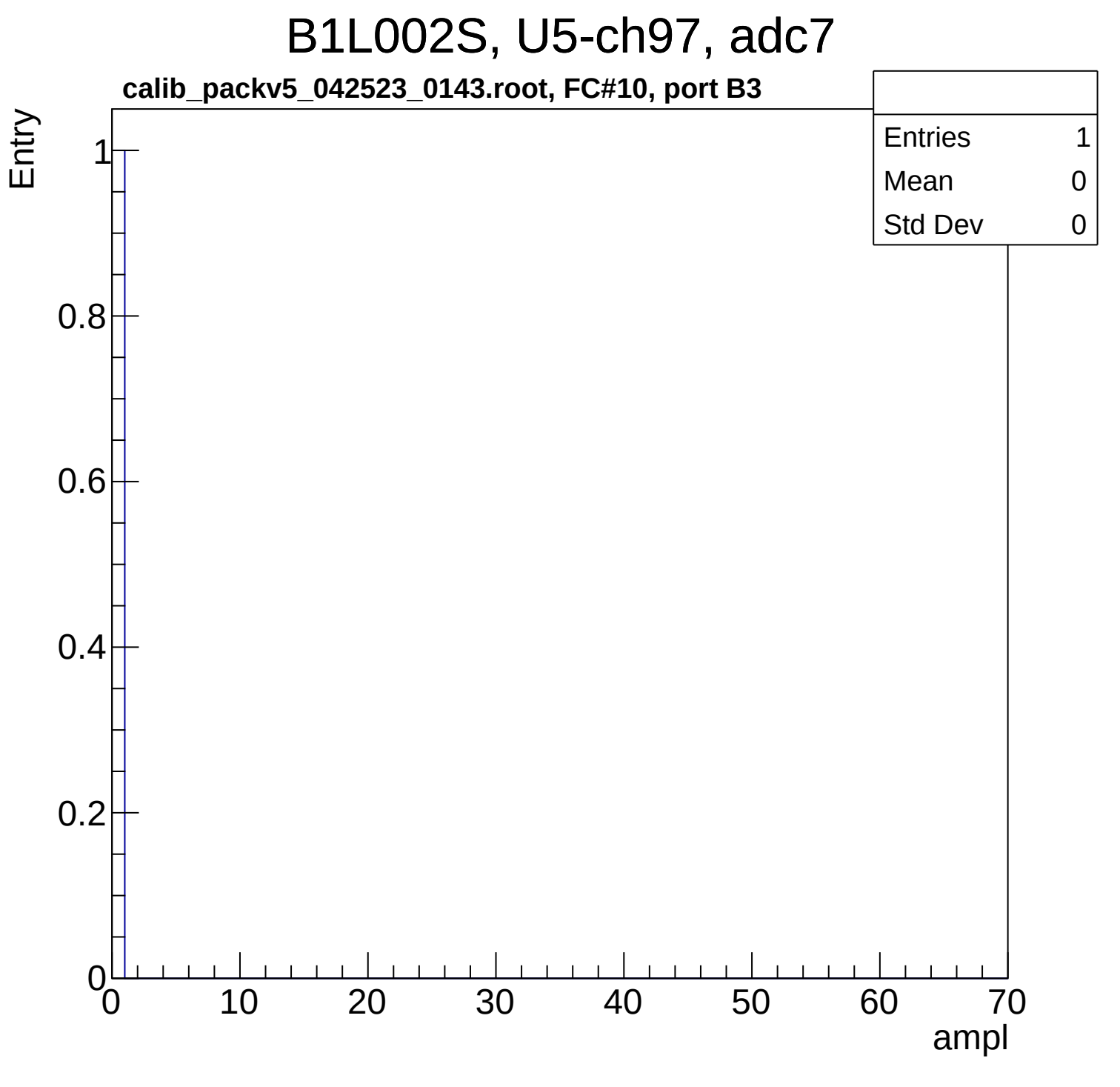
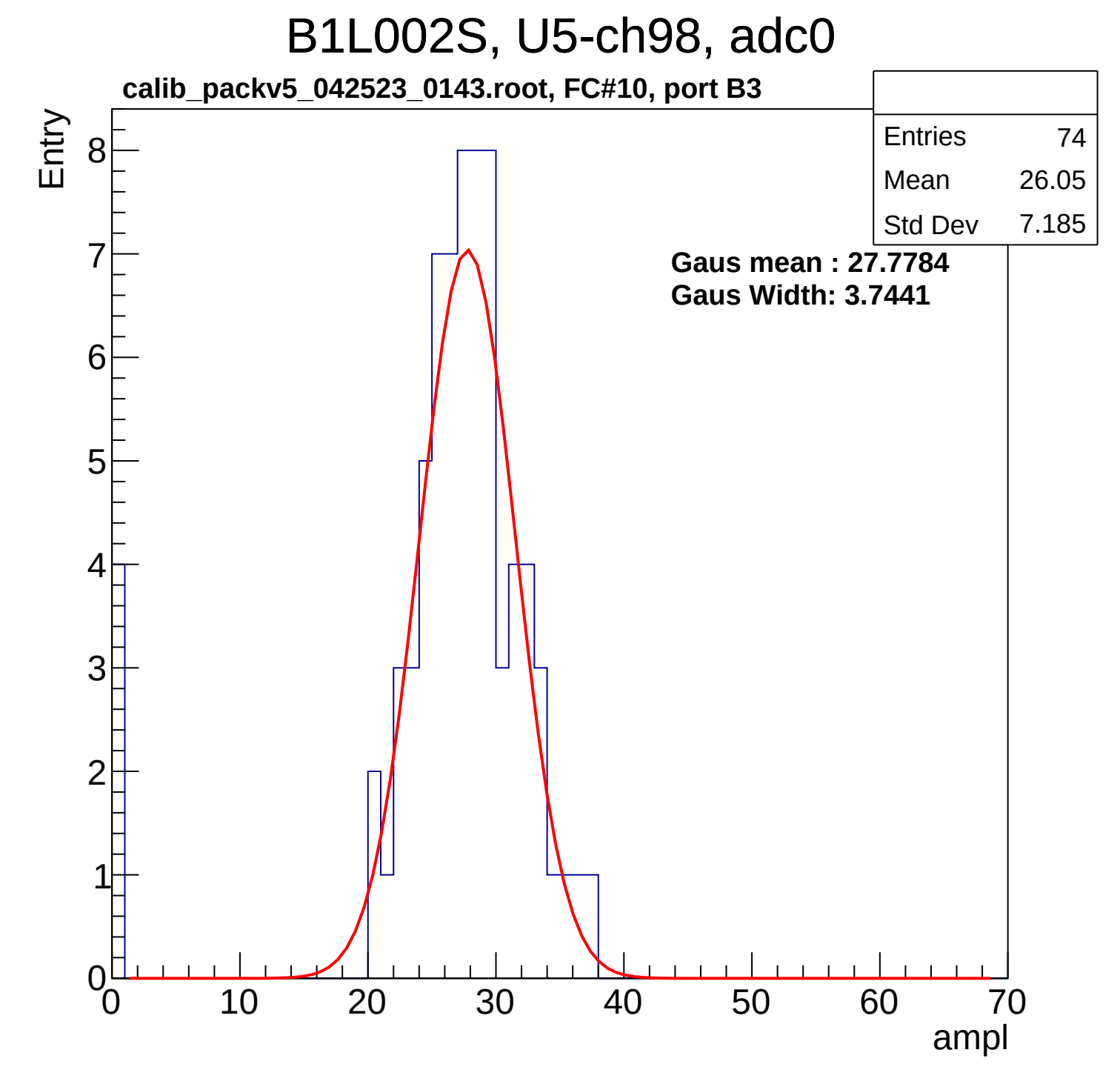
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 26.05
Std Dev 7.185
Gaus mean : 27.7784
Gaus Width: 3.7441
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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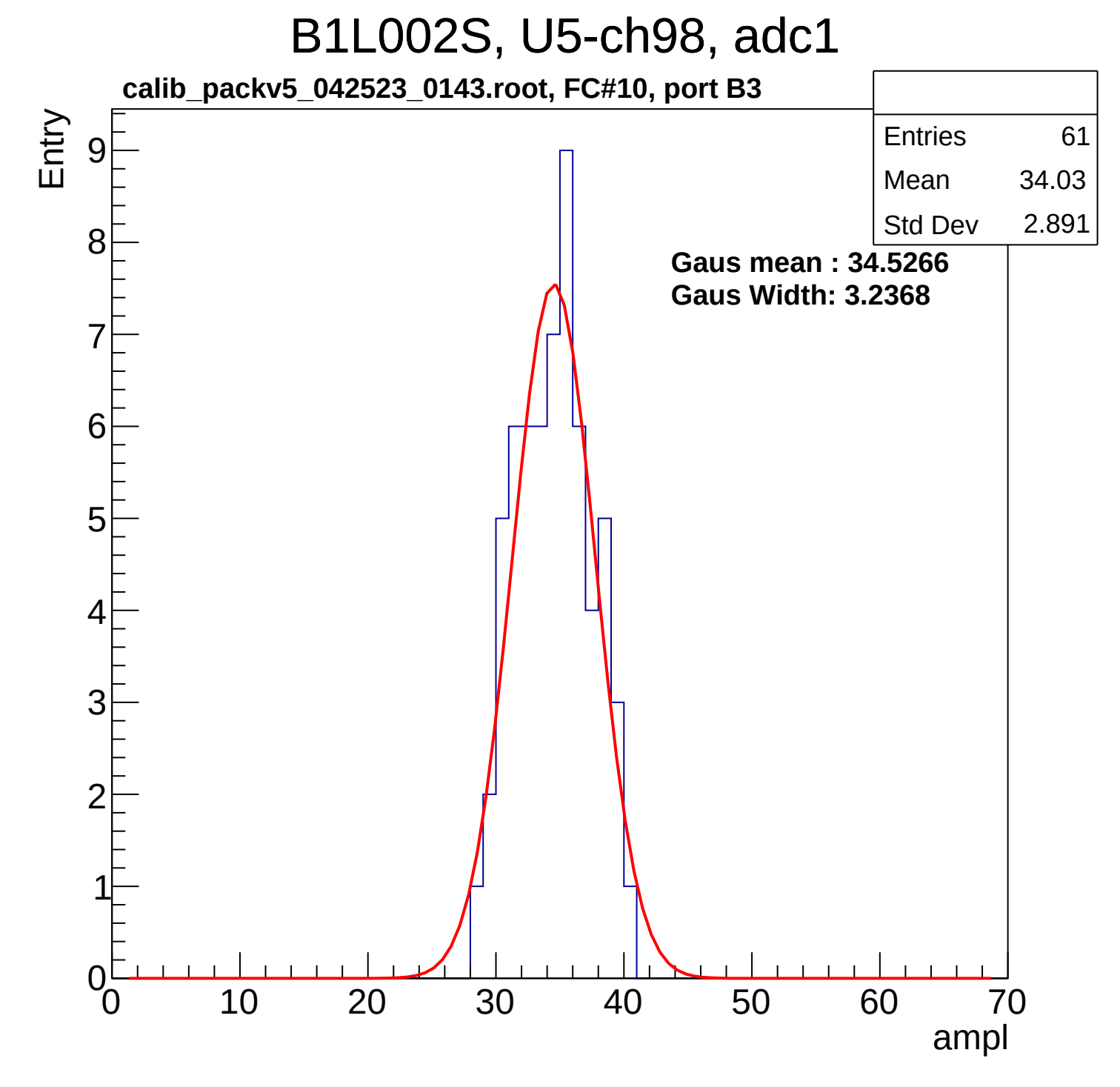
B1L002S, U5-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.03
Std Dev 2.891
Gaus mean : 34.5266
Gaus Width: 3.2368
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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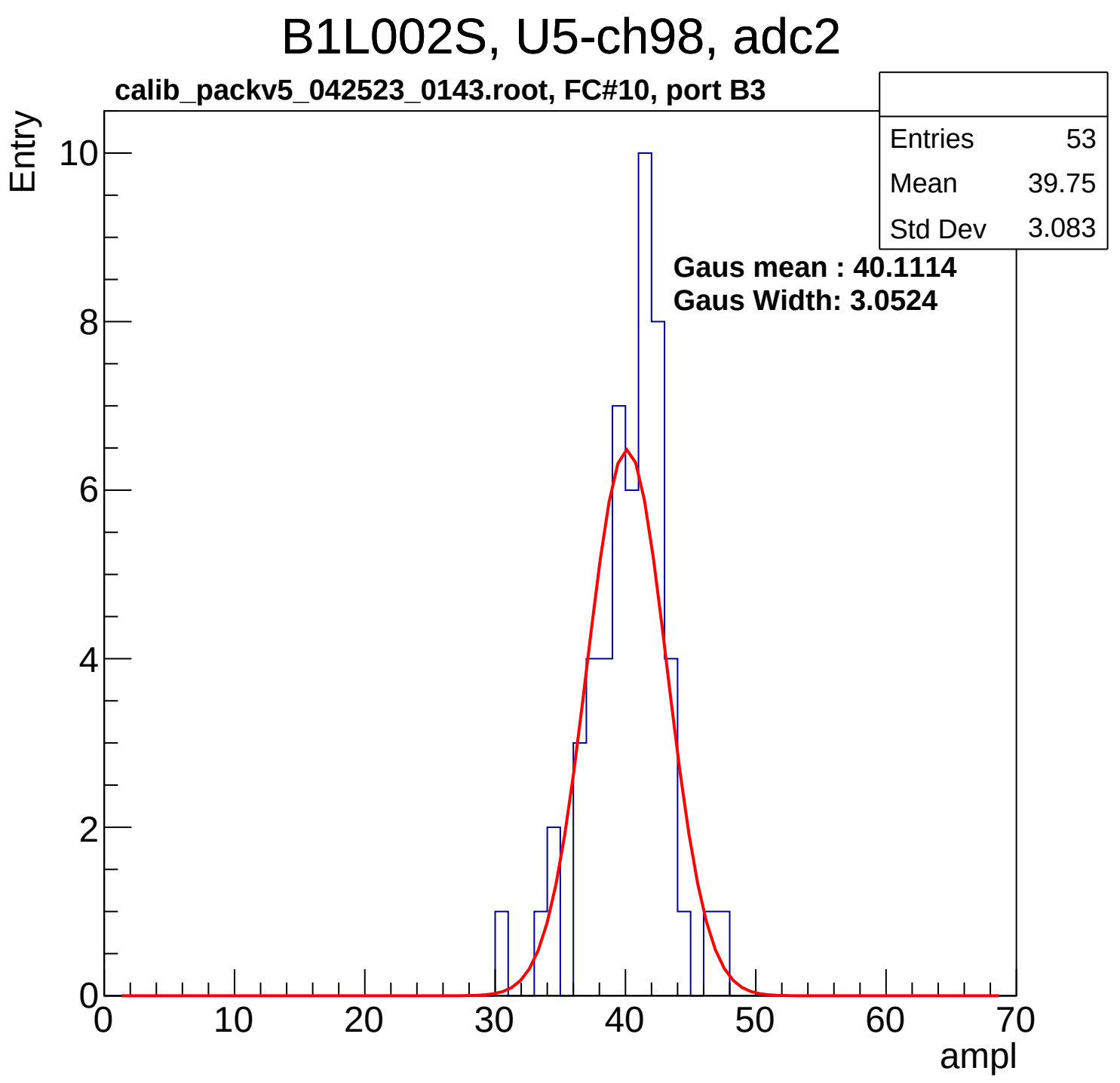
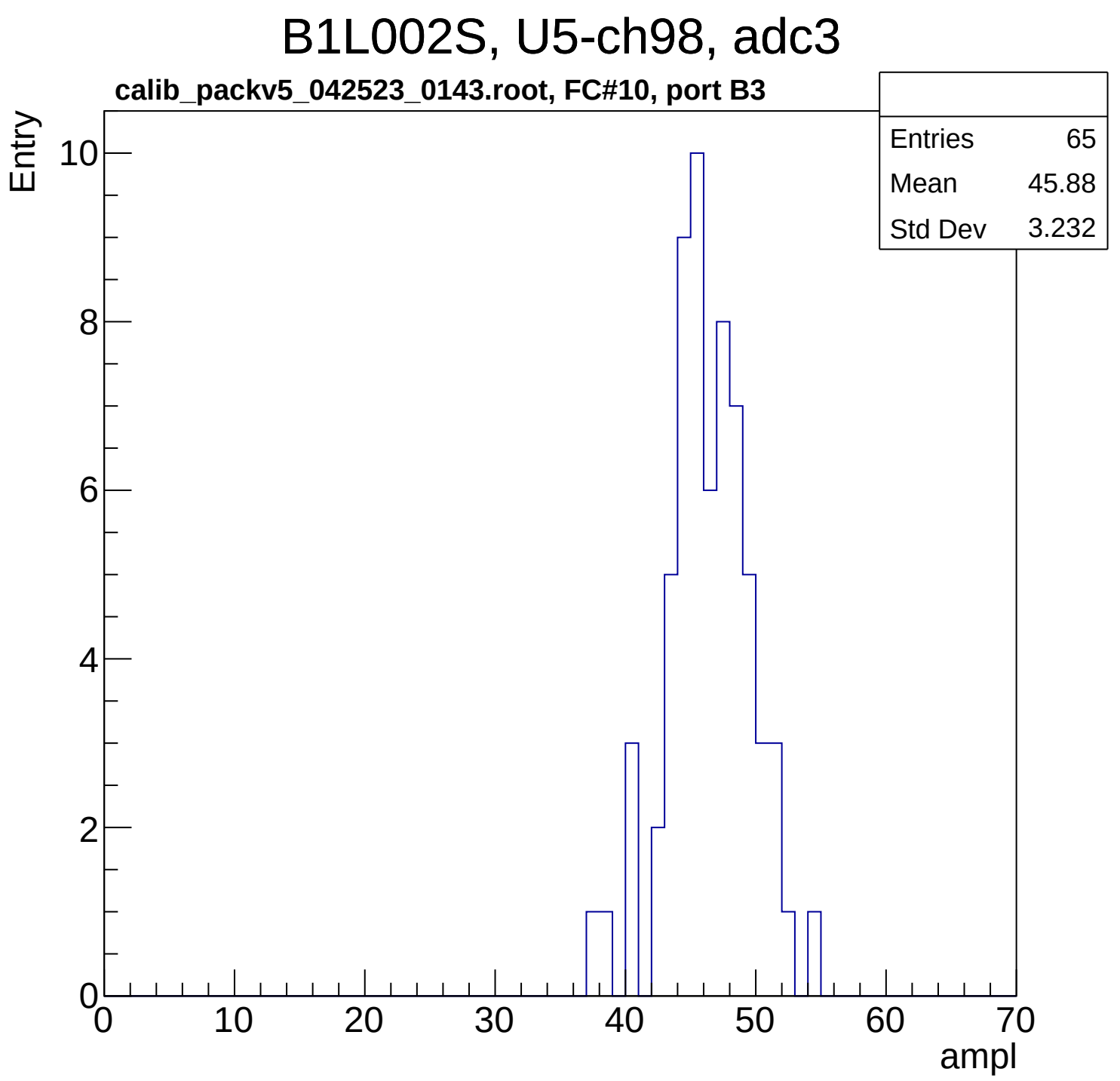
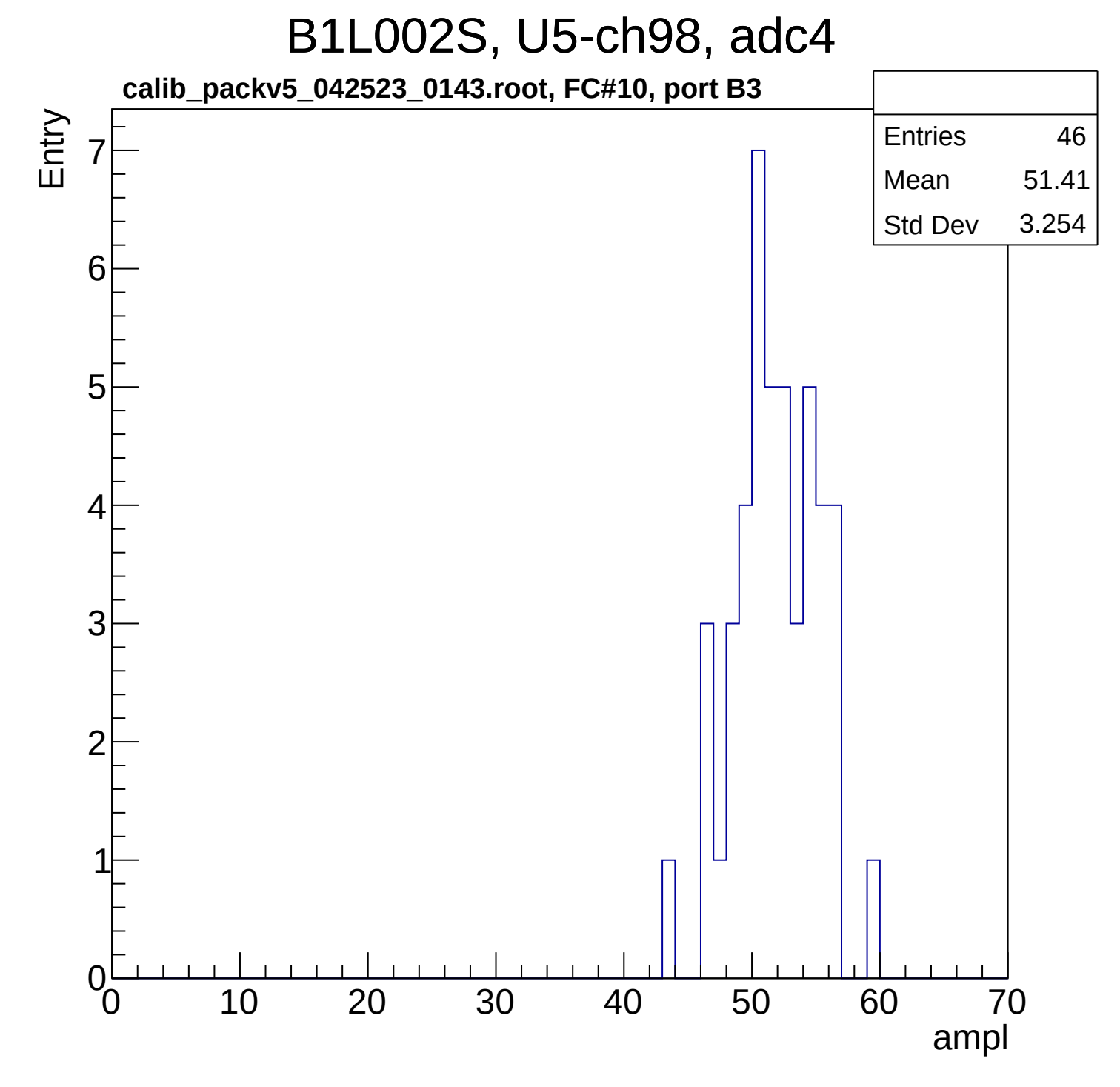

B1L002S, U5-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 39.75
Std Dev 3.083
Gaus mean : 40.1114
Gaus Width: 3.0524
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.88
Std Dev 3.232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 51.41
Std Dev 3.254
Entry
ampl
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2
3
4
5
6
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0
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20
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40
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60
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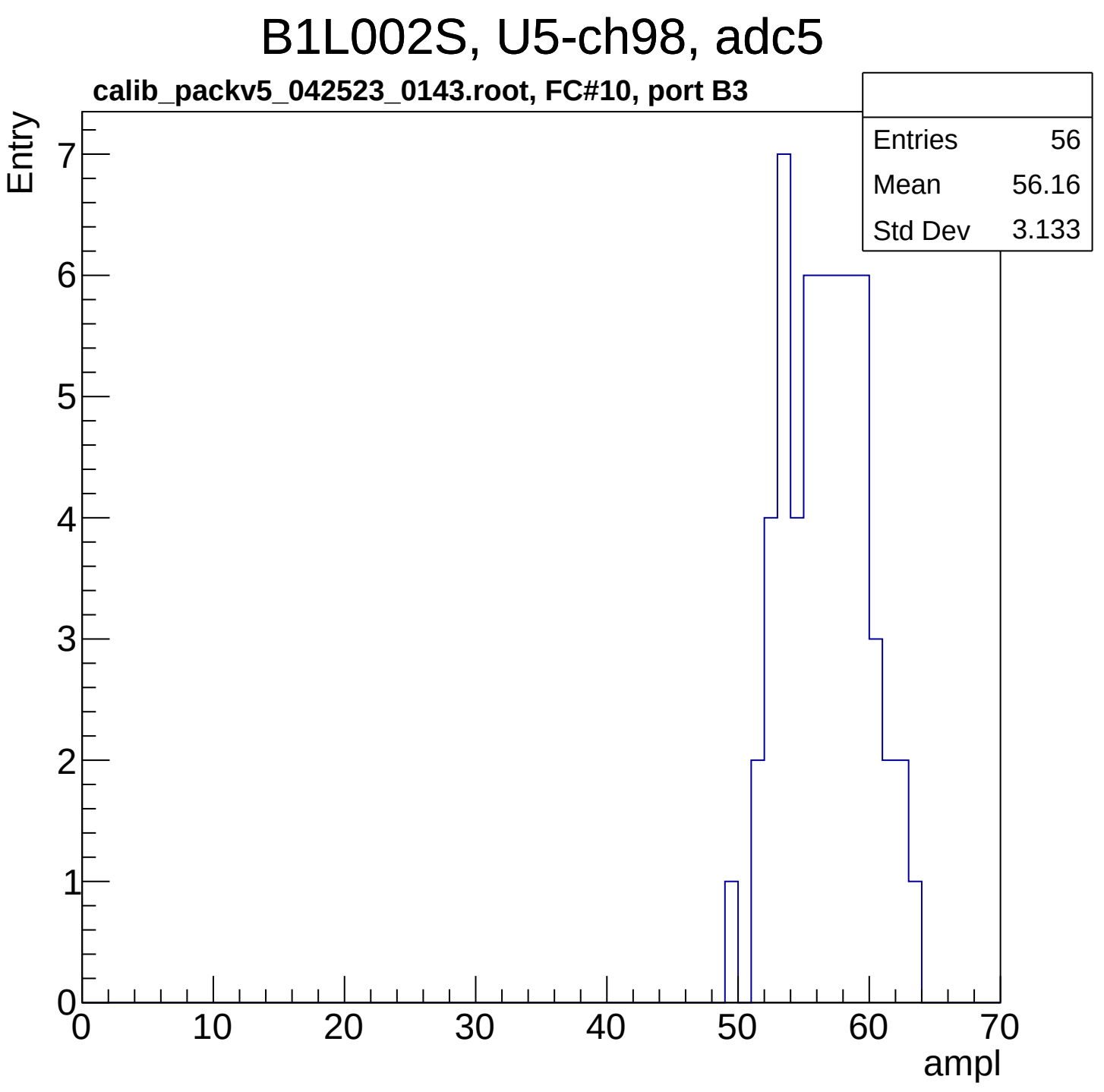
B1L002S, U5-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 56.16
Std Dev 3.133
Entry
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2
3
4
5
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10
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30
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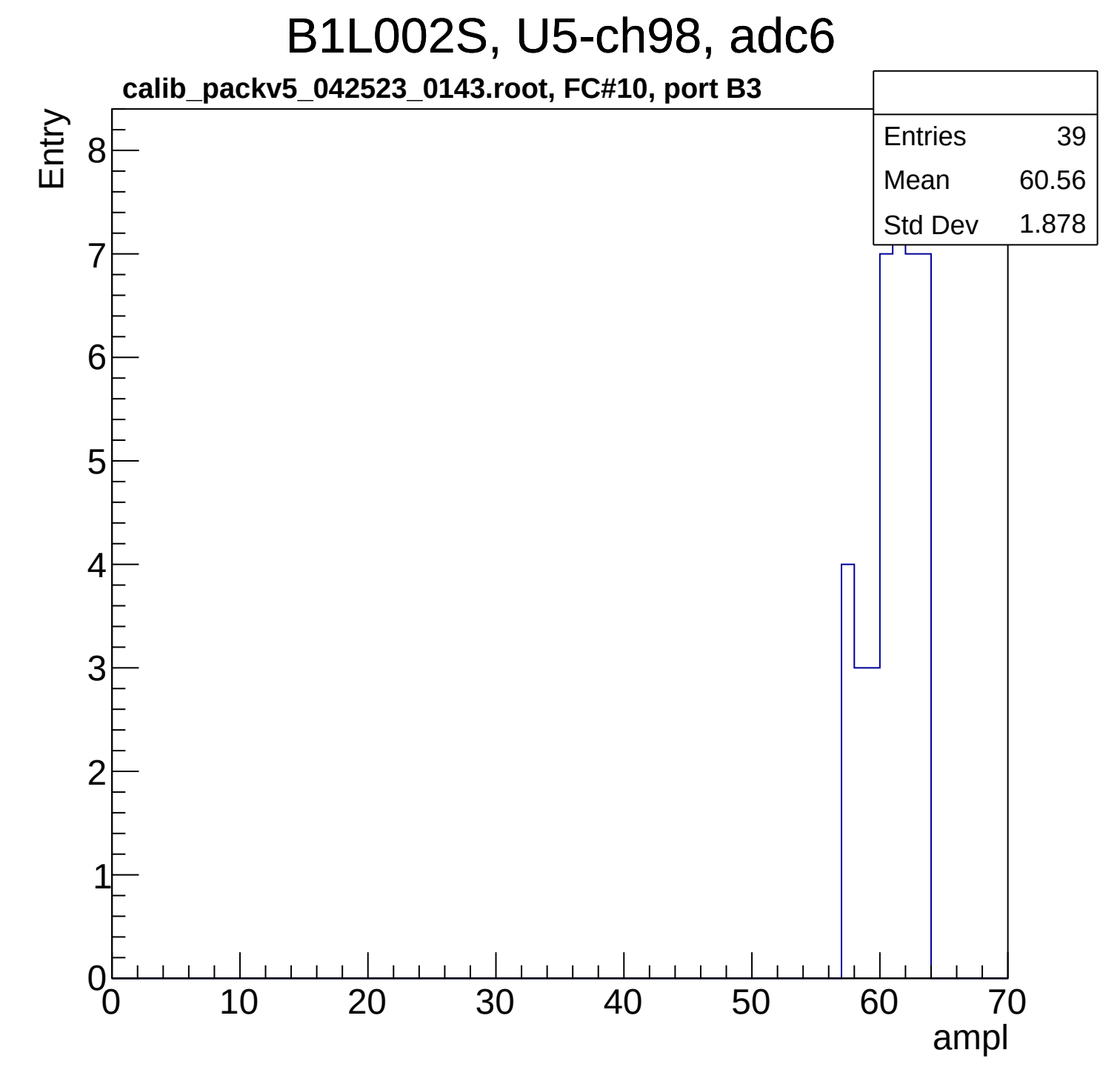
50
60
70
ampl


B1L002S, U5-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 60.56
Std Dev 1.878
Entry
ampl
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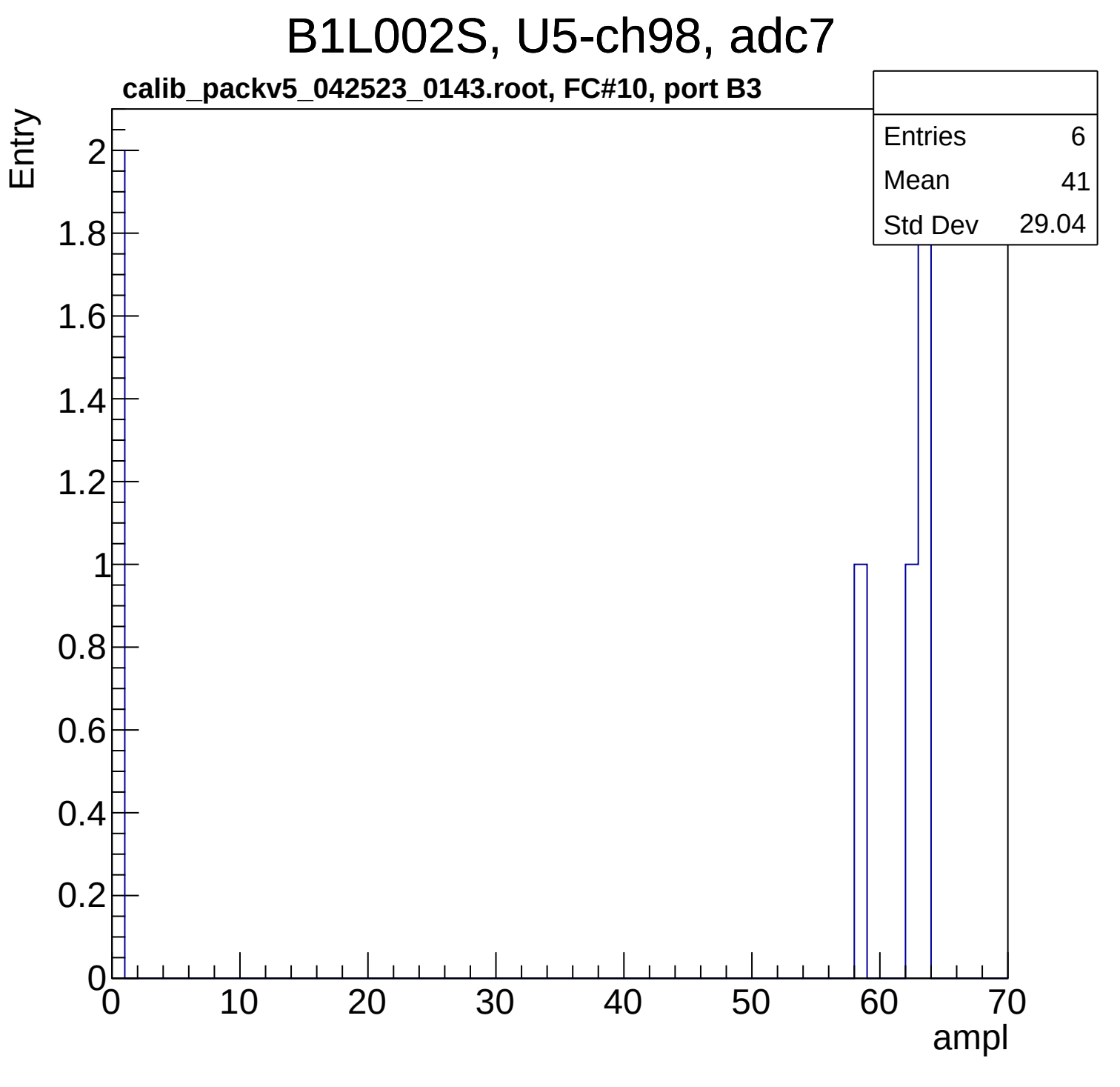
B1L002S, U5-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41
Std Dev 29.04
Entry
ampl
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0.2
0.4
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1.2
1.4
1.6
1.8
2
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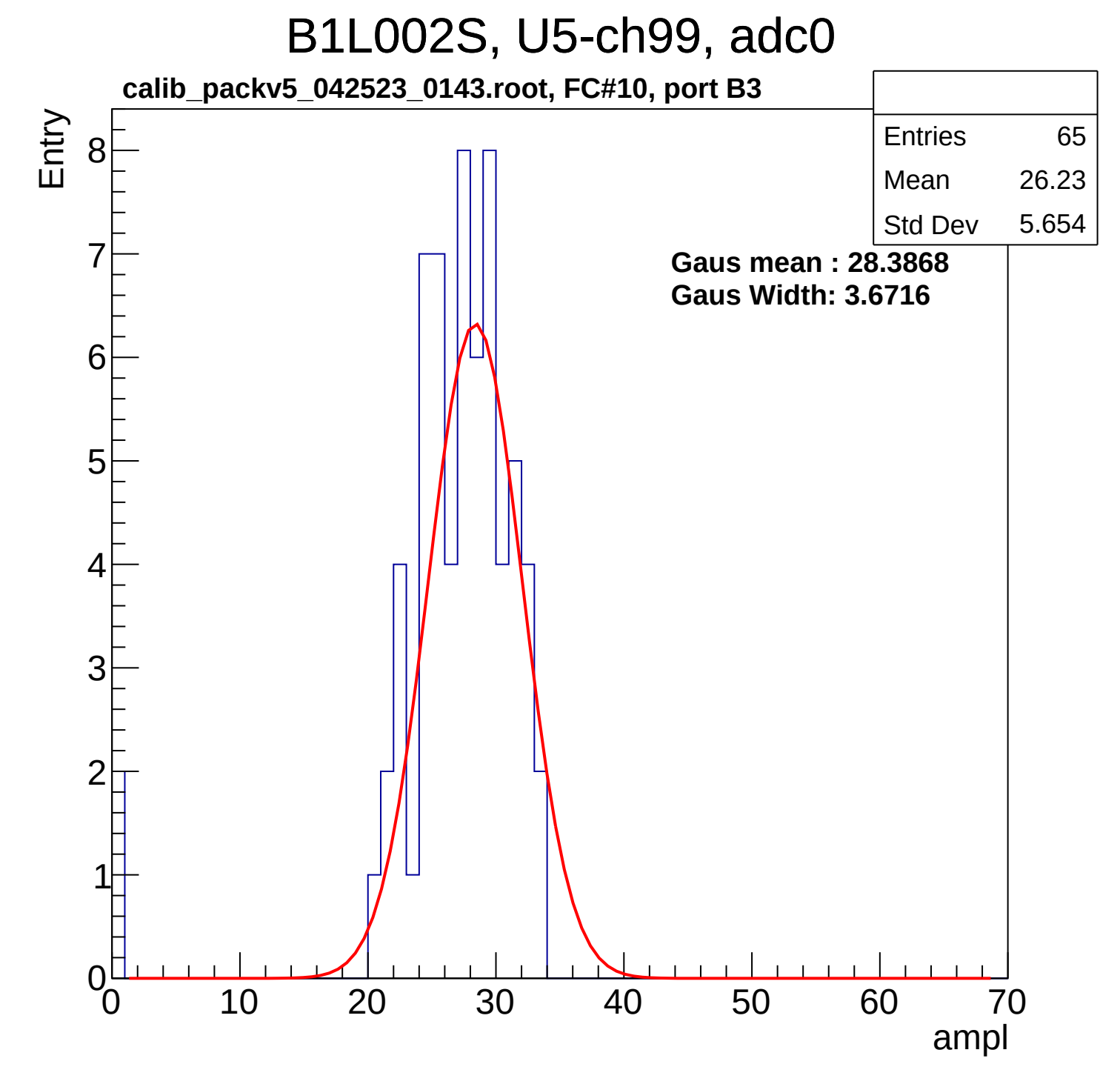
B1L002S, U5-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.23
Std Dev 5.654
Gaus mean : 28.3868
Gaus Width: 3.6716
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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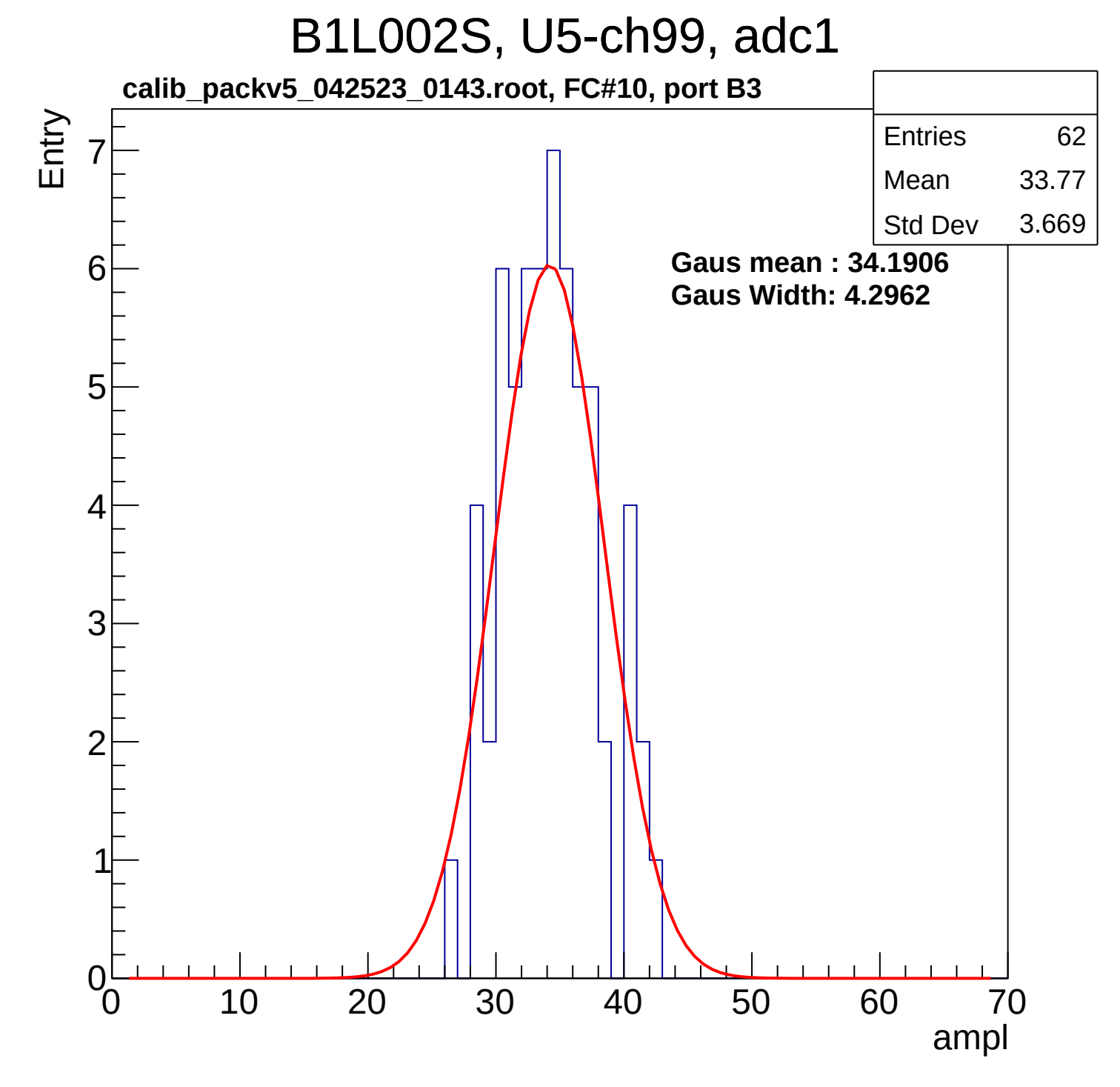
B1L002S, U5-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.77
Std Dev 3.669
Gaus mean : 34.1906
Gaus Width: 4.2962
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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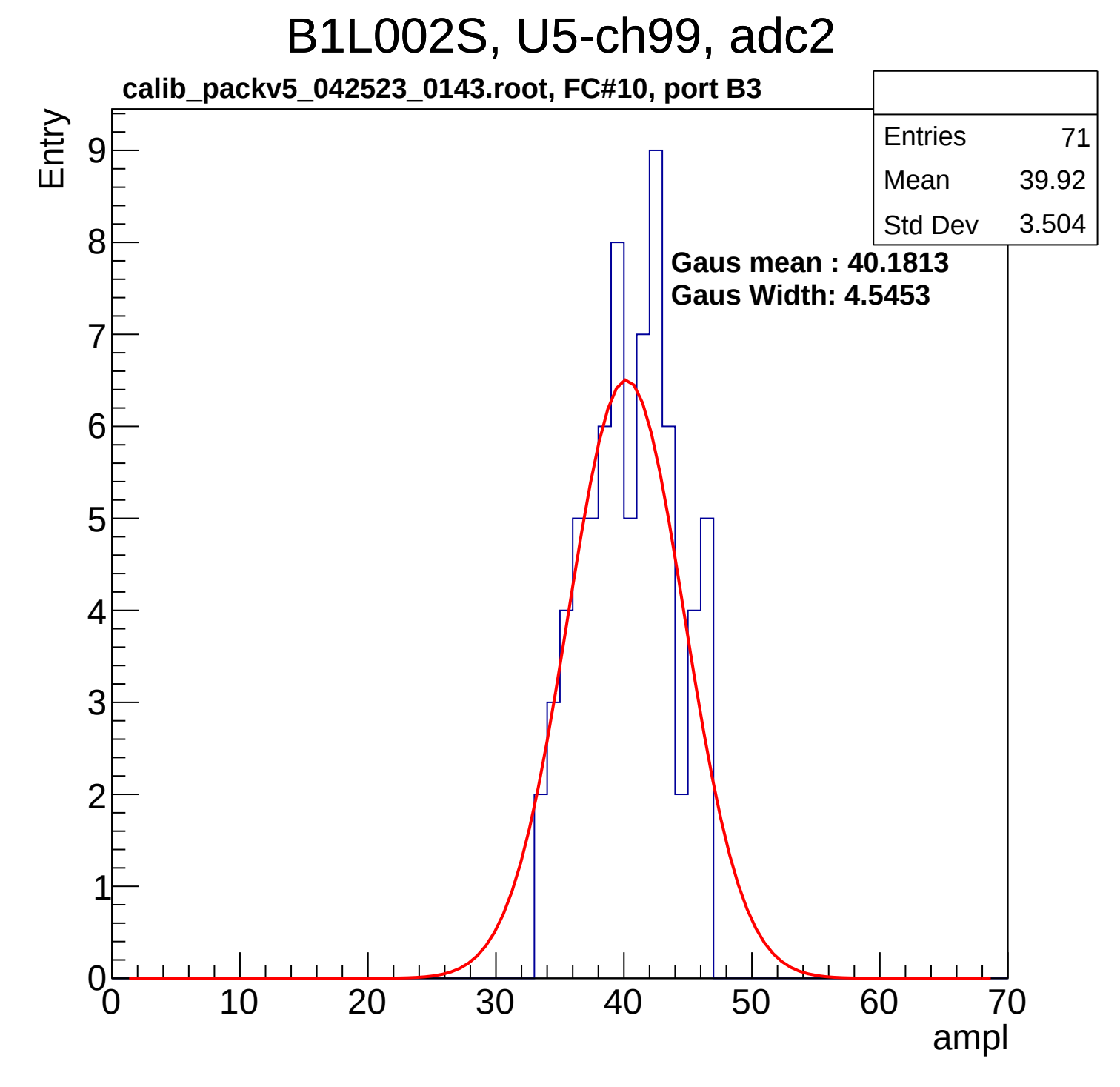
B1L002S, U5-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 39.92
Std Dev 3.504
Gaus mean : 40.1813
Gaus Width: 4.5453
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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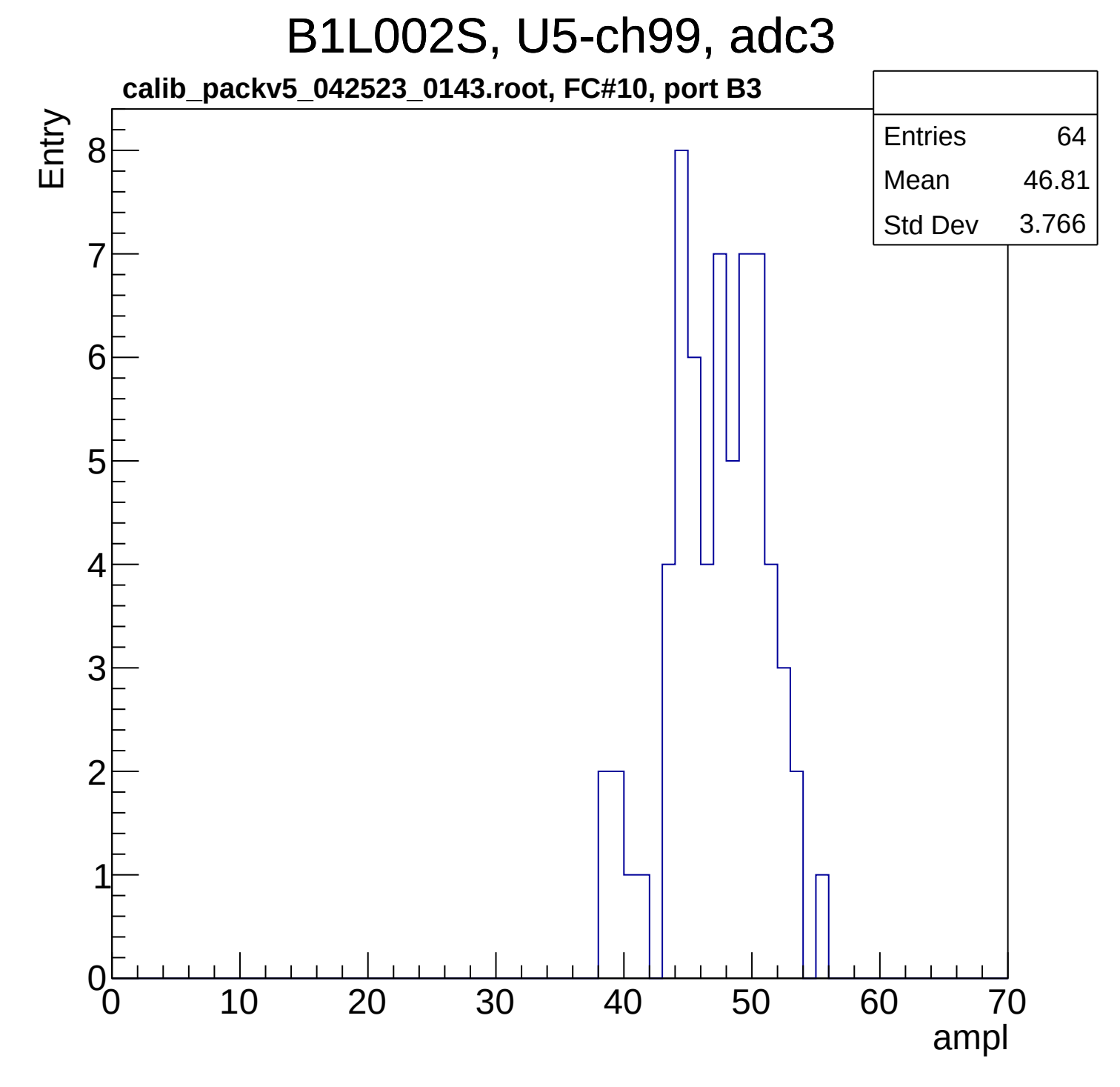
B1L002S, U5-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.81
Std Dev 3.766
Entry
ampl
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4
5
6
7
8
0
10
20
30
40
50
60
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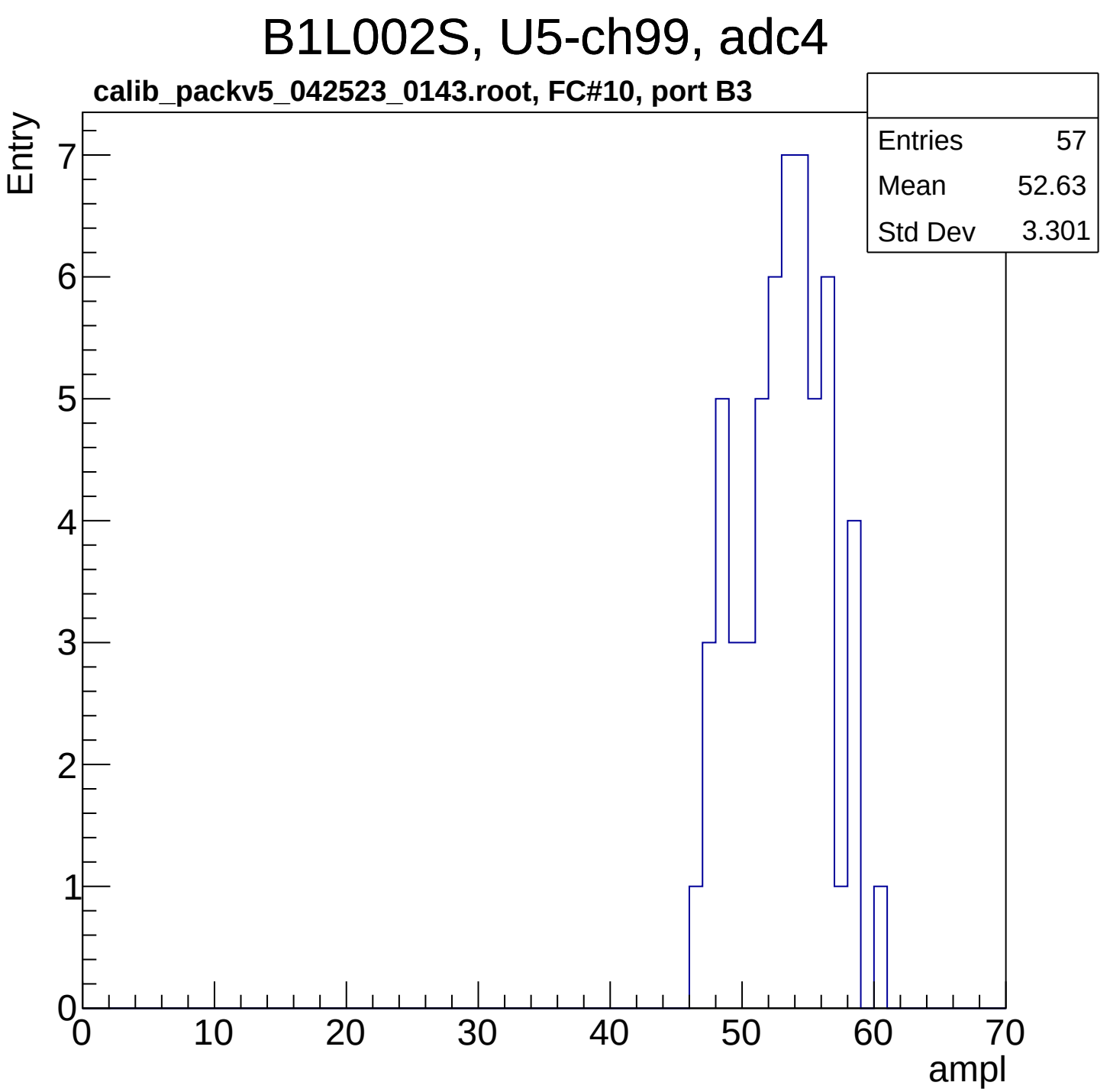
B1L002S, U5-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.63
Std Dev 3.301
Entry
ampl
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4
5
6
7
0
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40
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60
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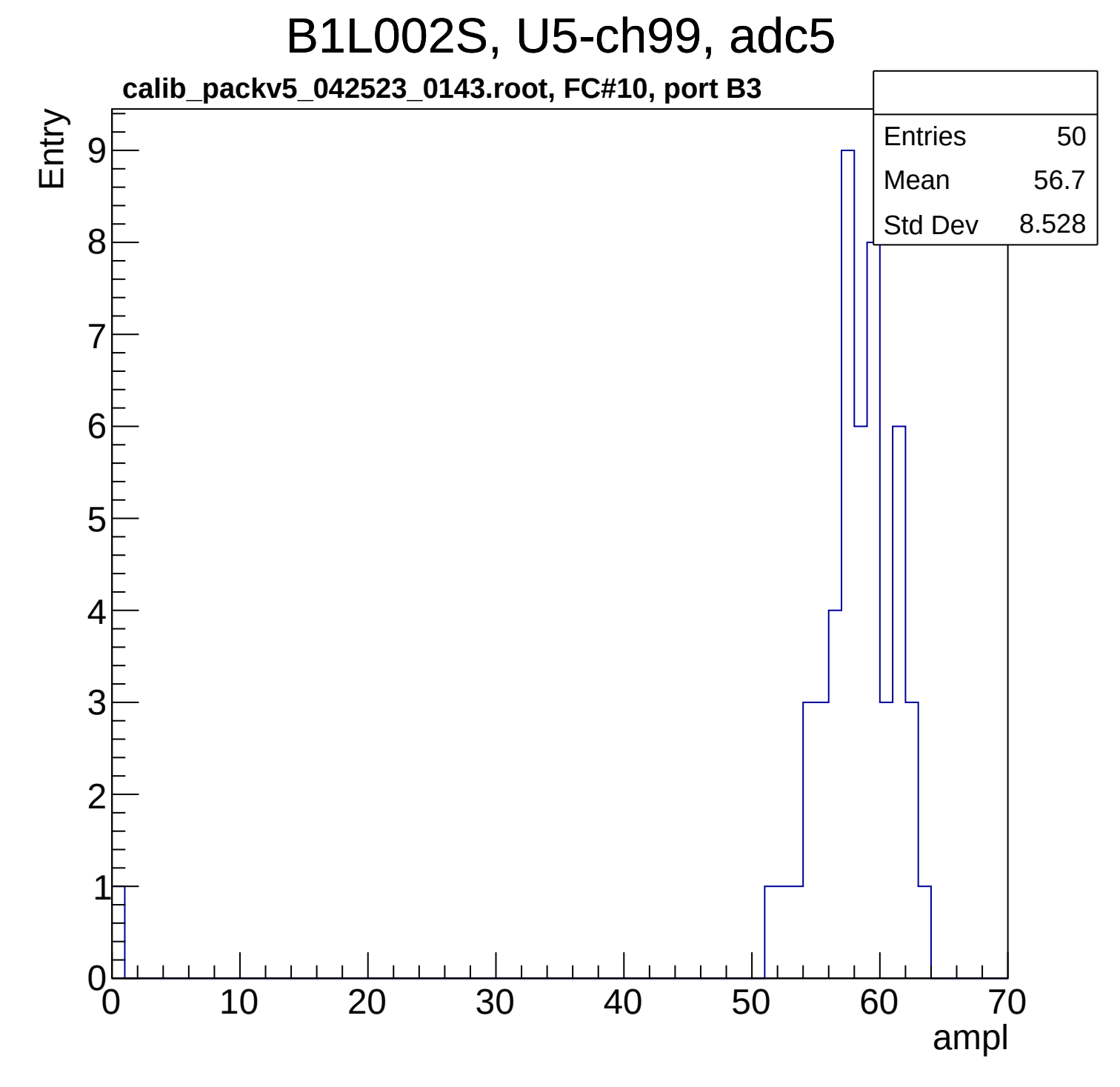
B1L002S, U5-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 56.7
Std Dev 8.528
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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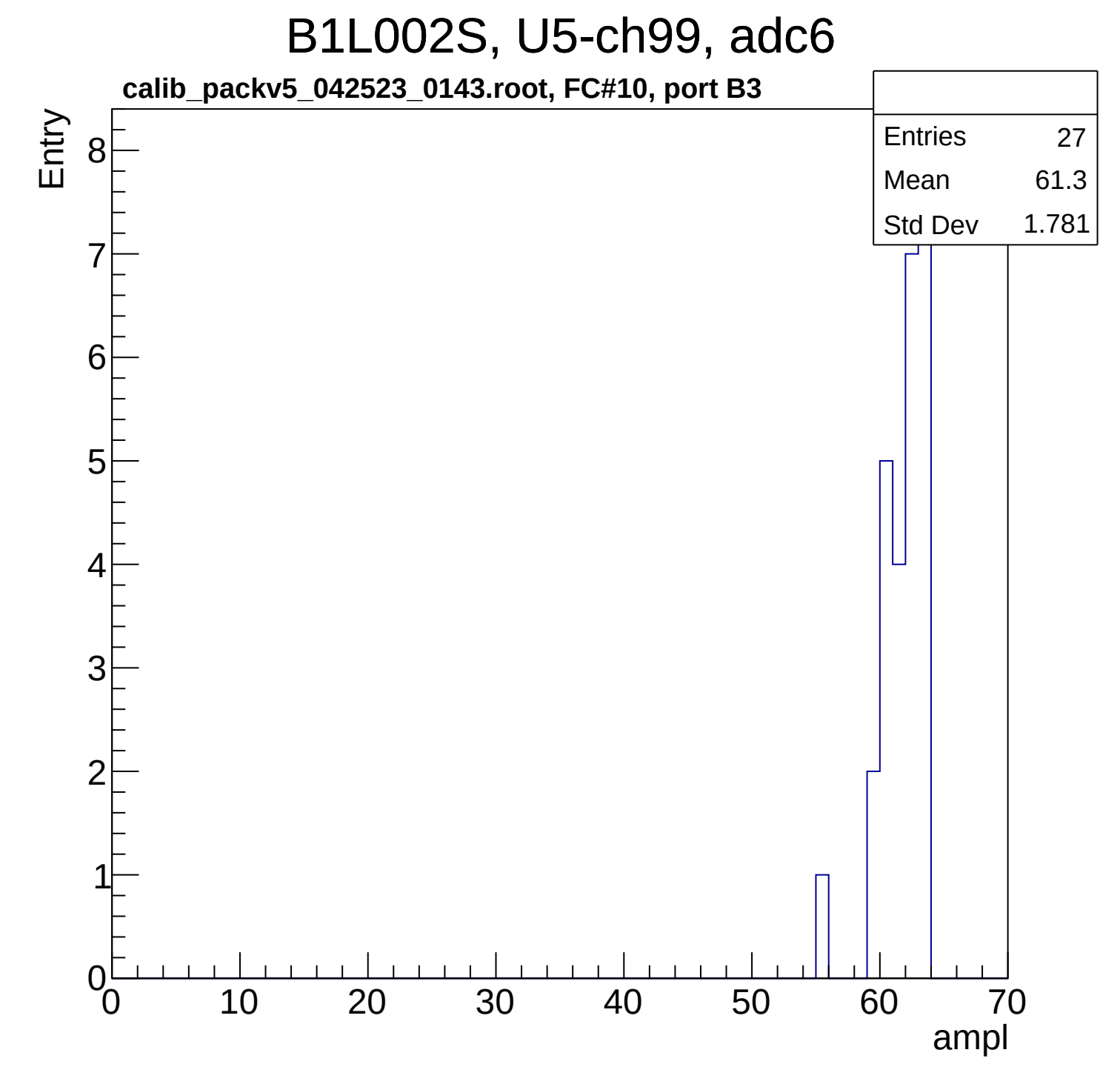
B1L002S, U5-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 61.3
Std Dev 1.781
Entry
ampl
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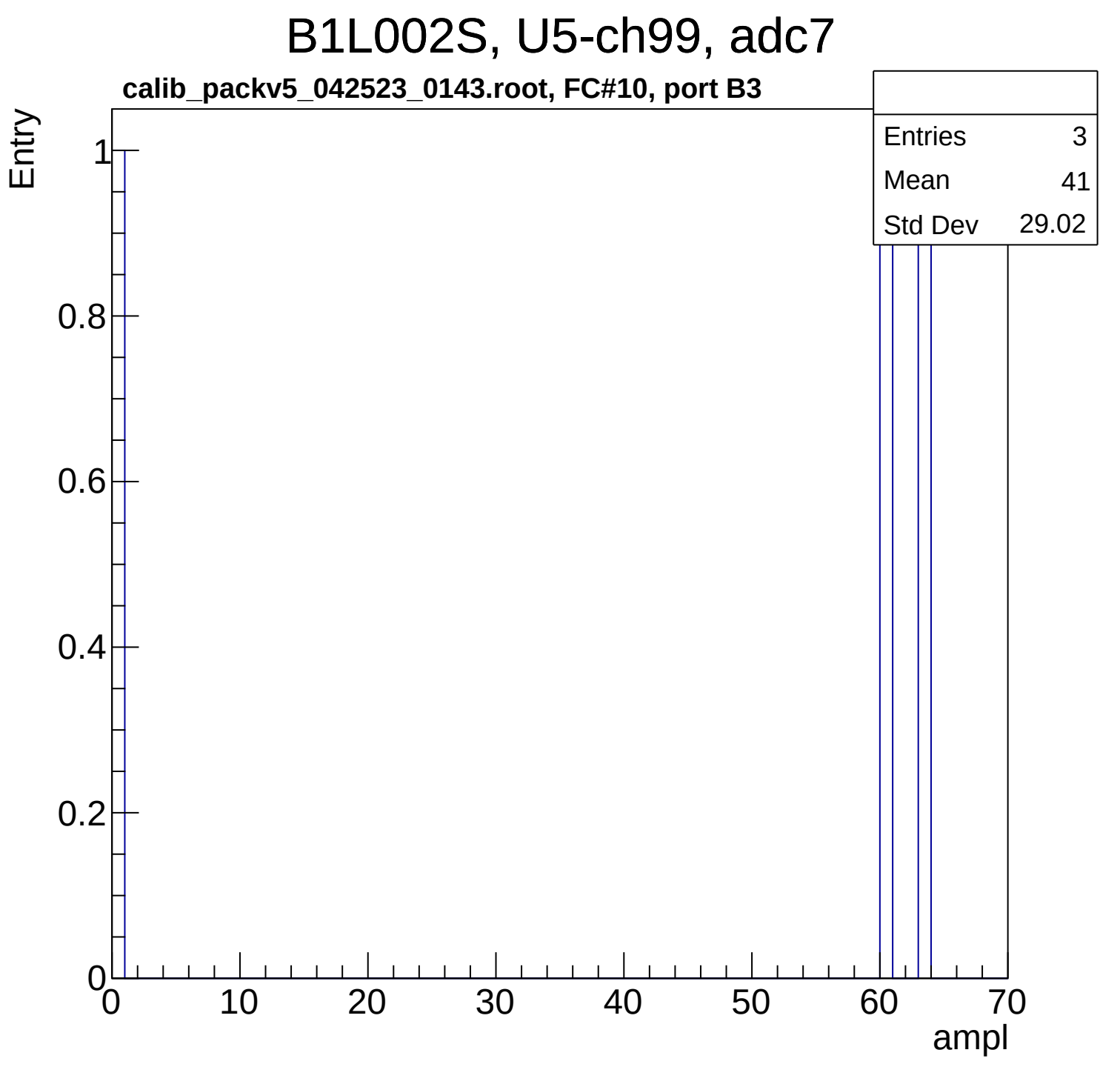
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
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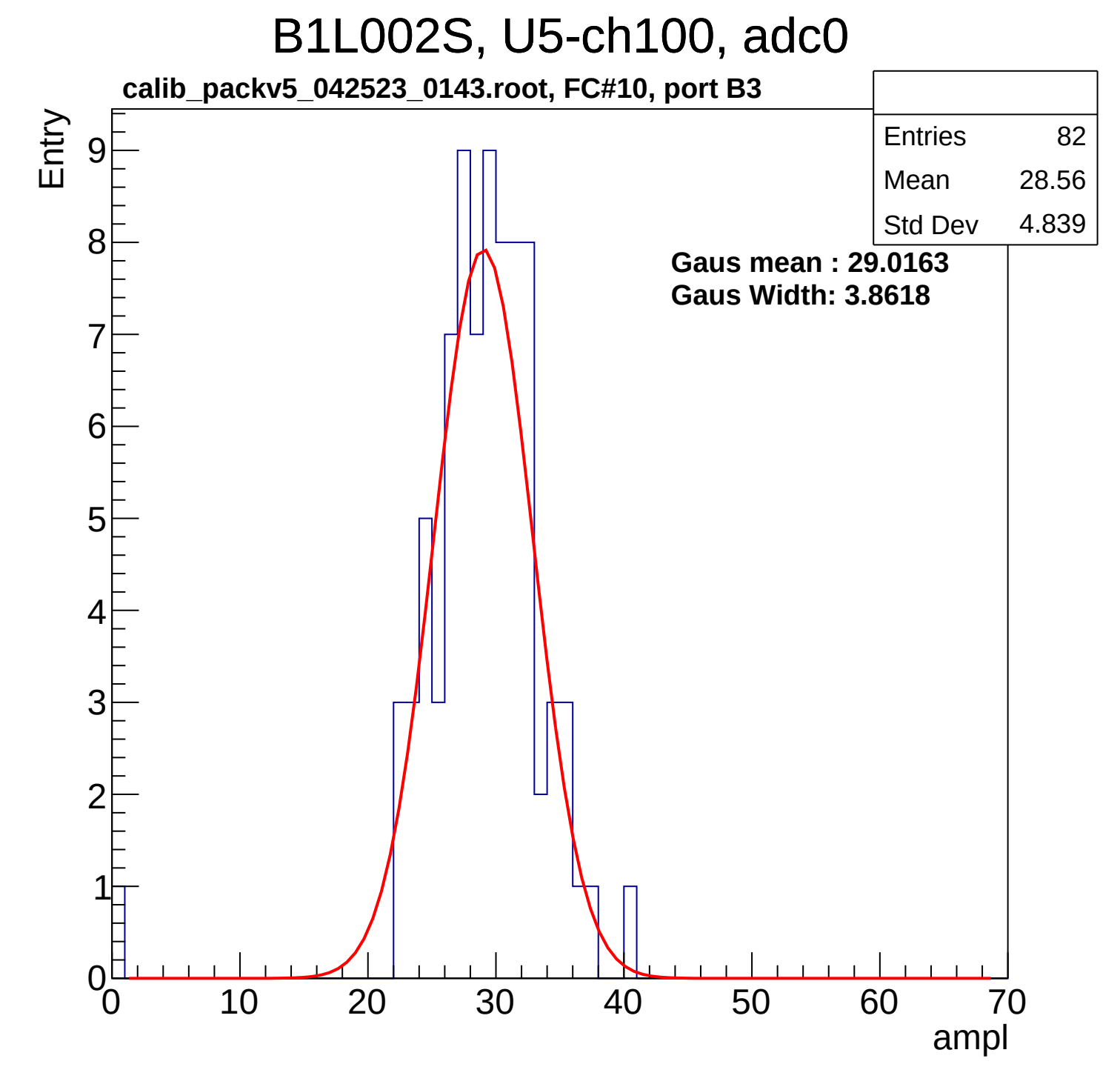
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.56
Std Dev 4.839
Gaus mean : 29.0163
Gaus Width: 3.8618
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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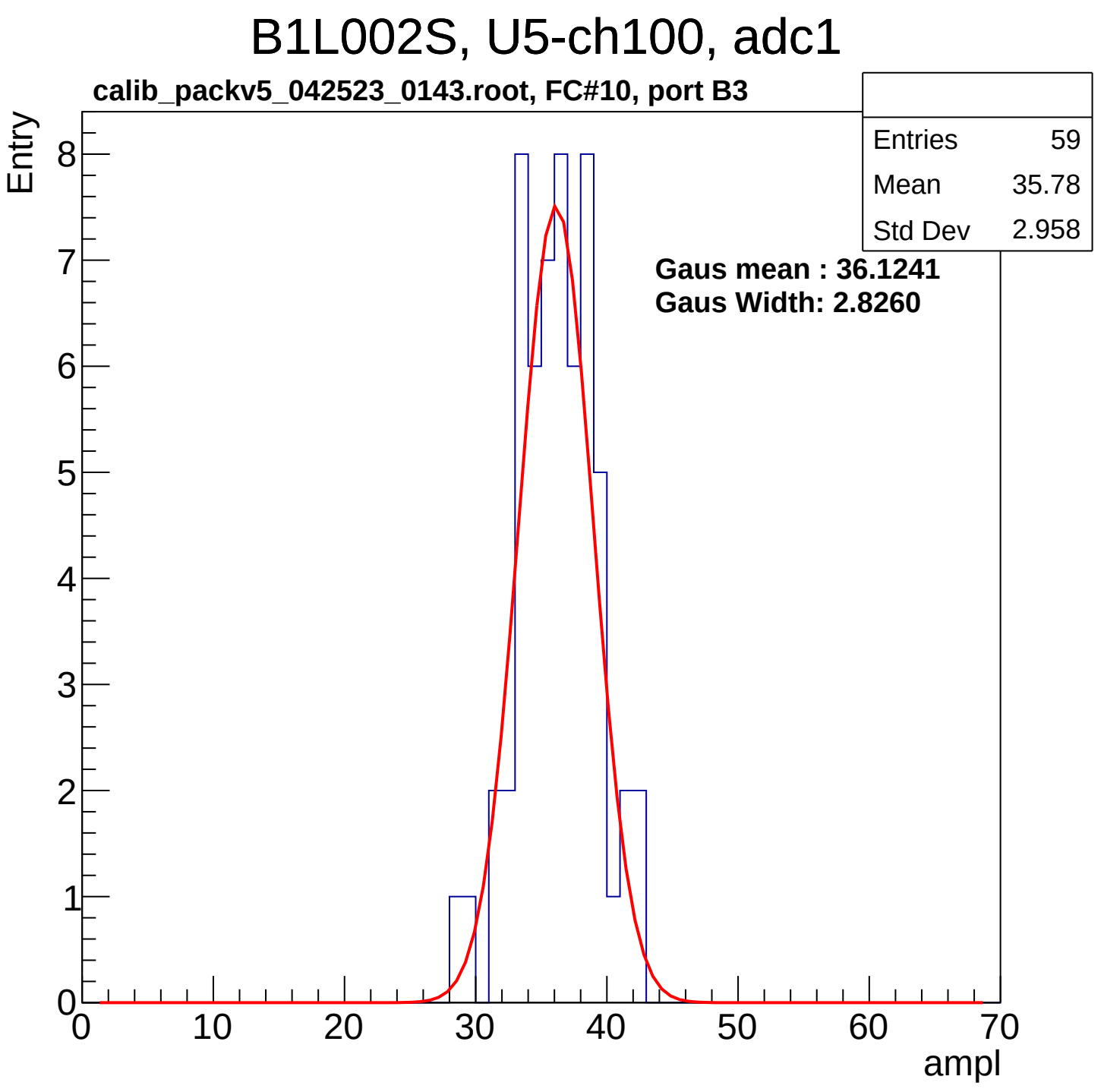
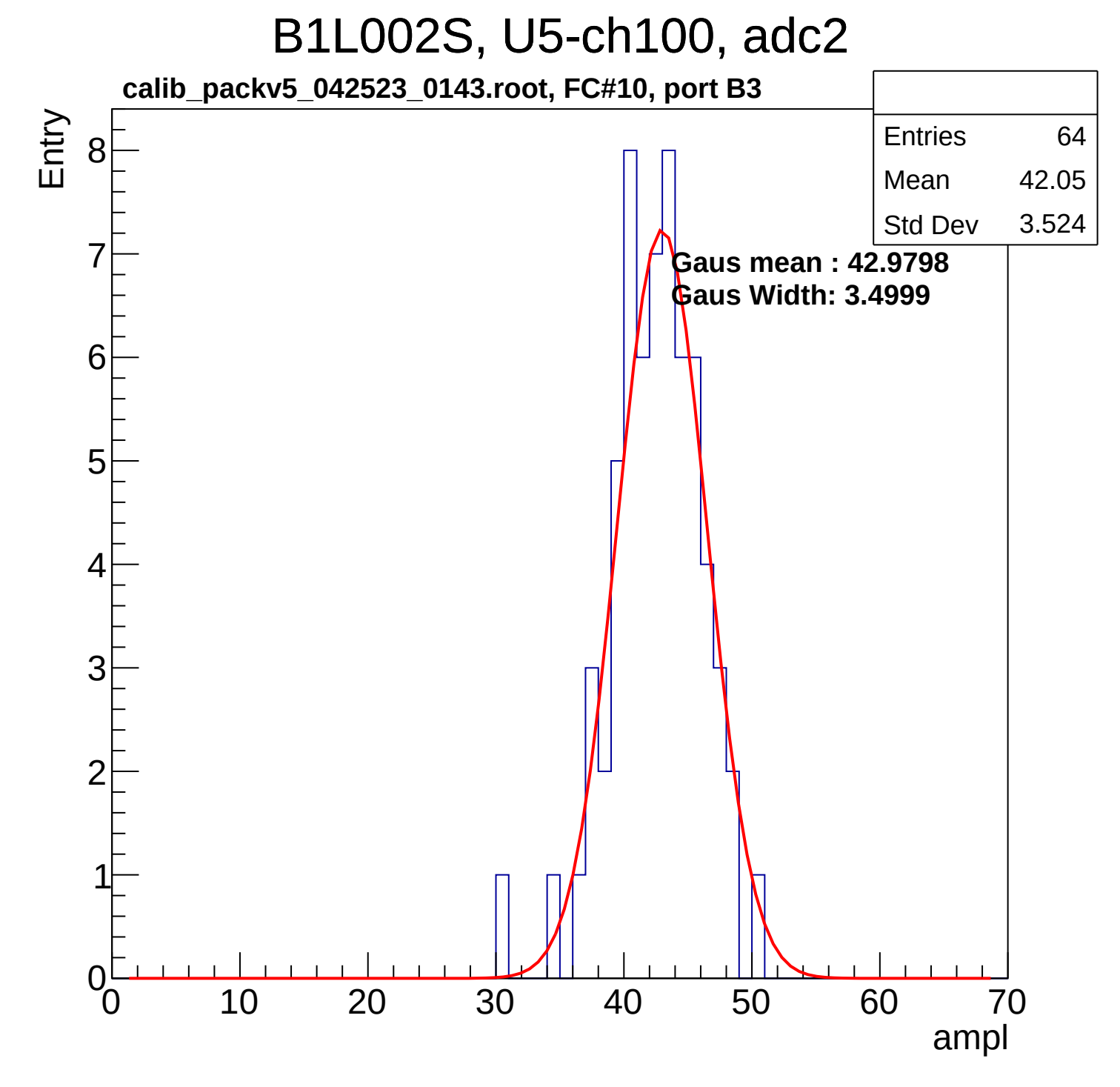

B1L002S, U5-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.78
Std Dev 2.958
Gaus mean : 36.1241
Gaus Width: 2.8260
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.05
Std Dev 3.524
Gaus mean : 42.9798
Gaus Width: 3.4999
Entry
ampl
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1
2
3
4
5
6
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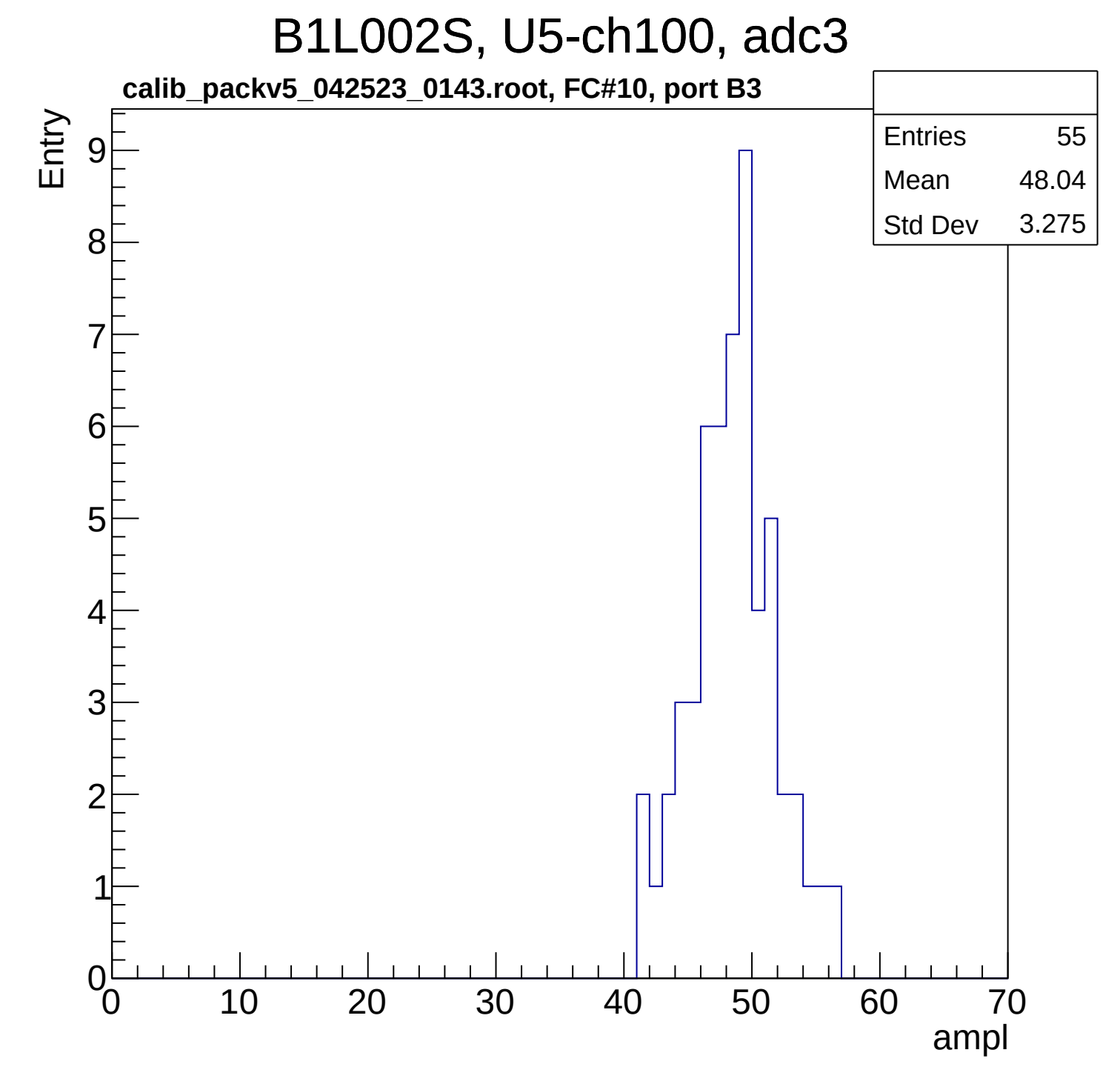
B1L002S, U5-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.04
Std Dev 3.275
Entry
ampl
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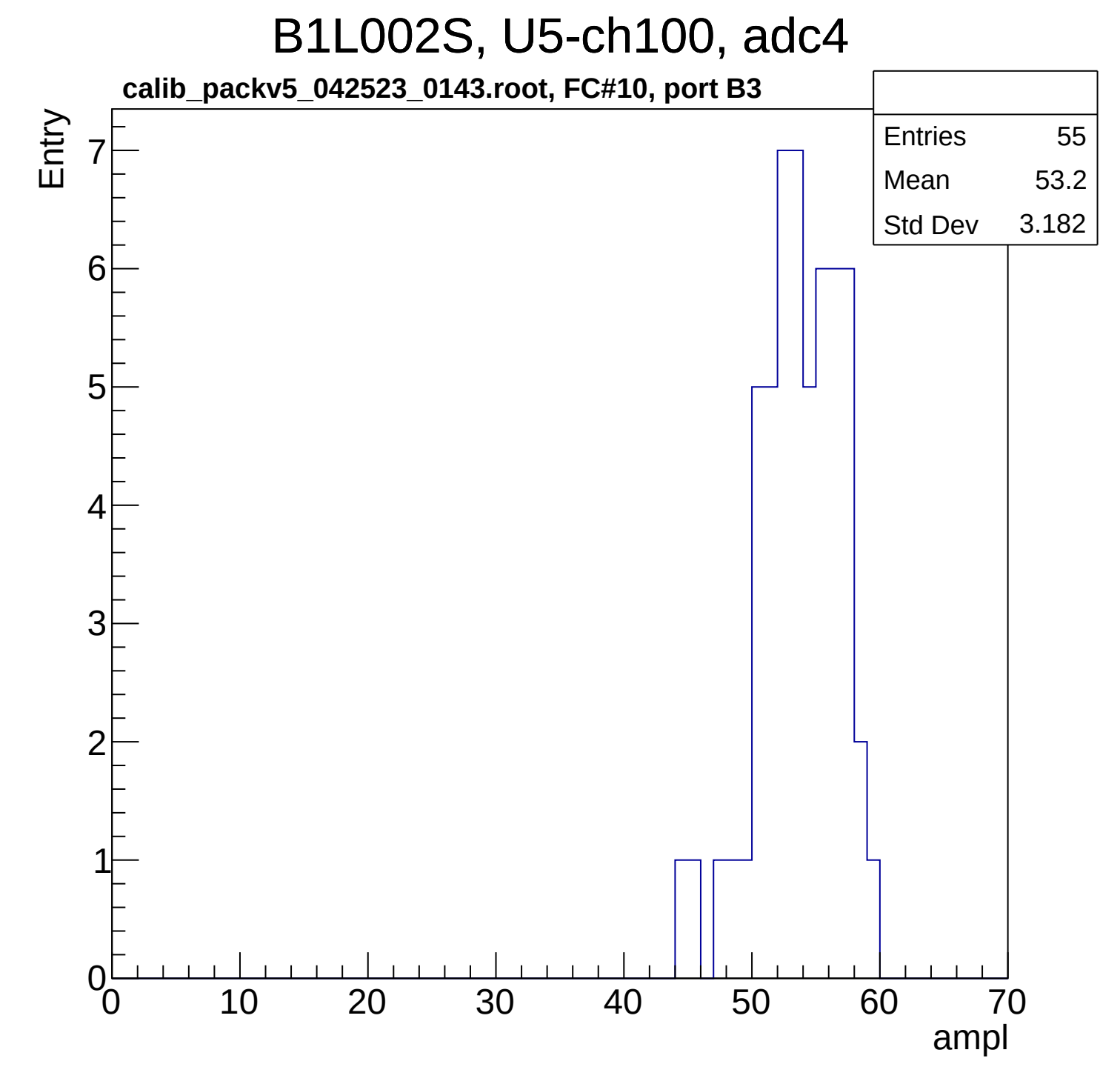
B1L002S, U5-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.2
Std Dev 3.182
Entry
ampl
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1
2
3
4
5
6
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0
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60
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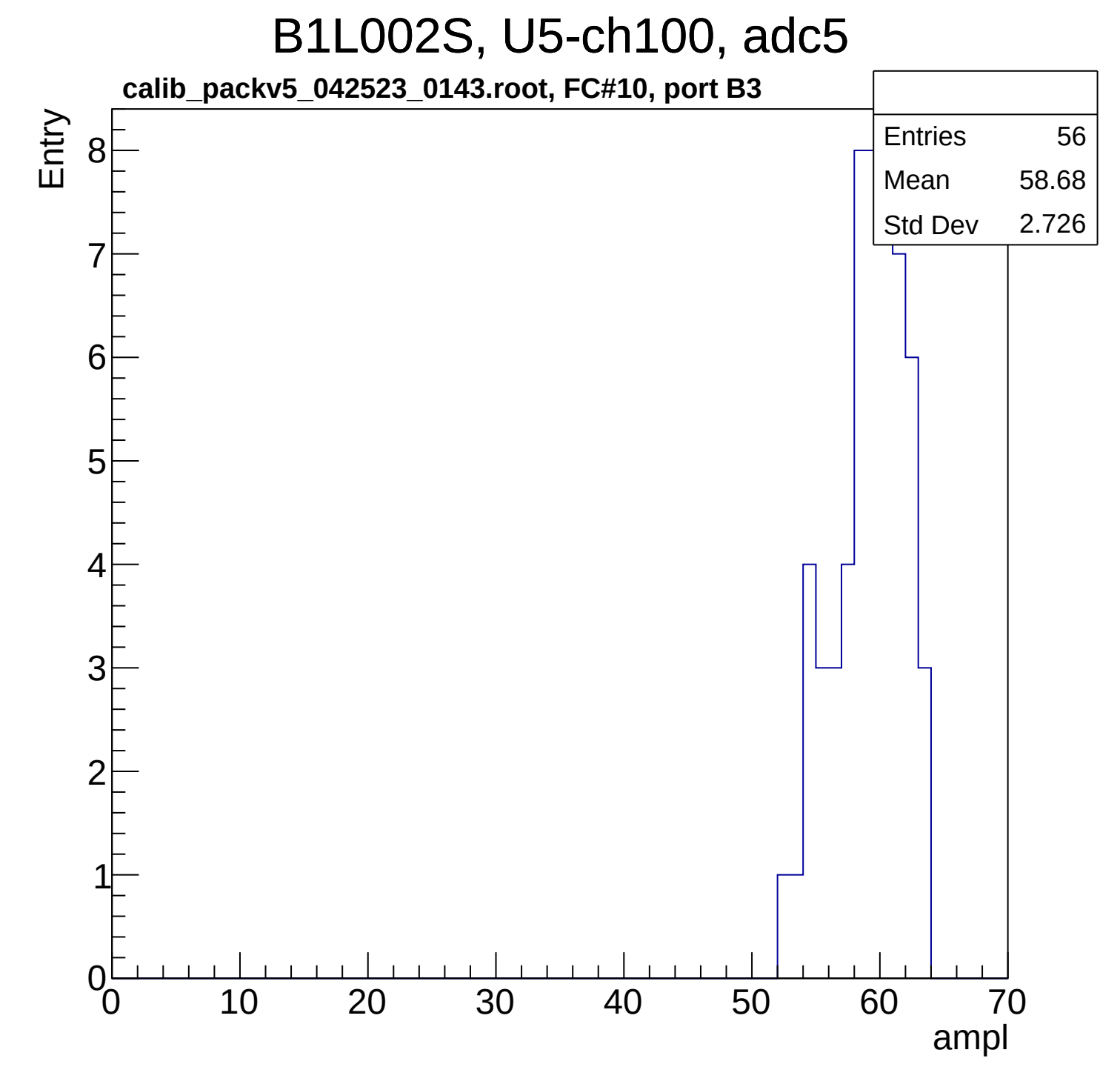
B1L002S, U5-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.68
Std Dev 2.726
Entry
ampl
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1
2
3
4
5
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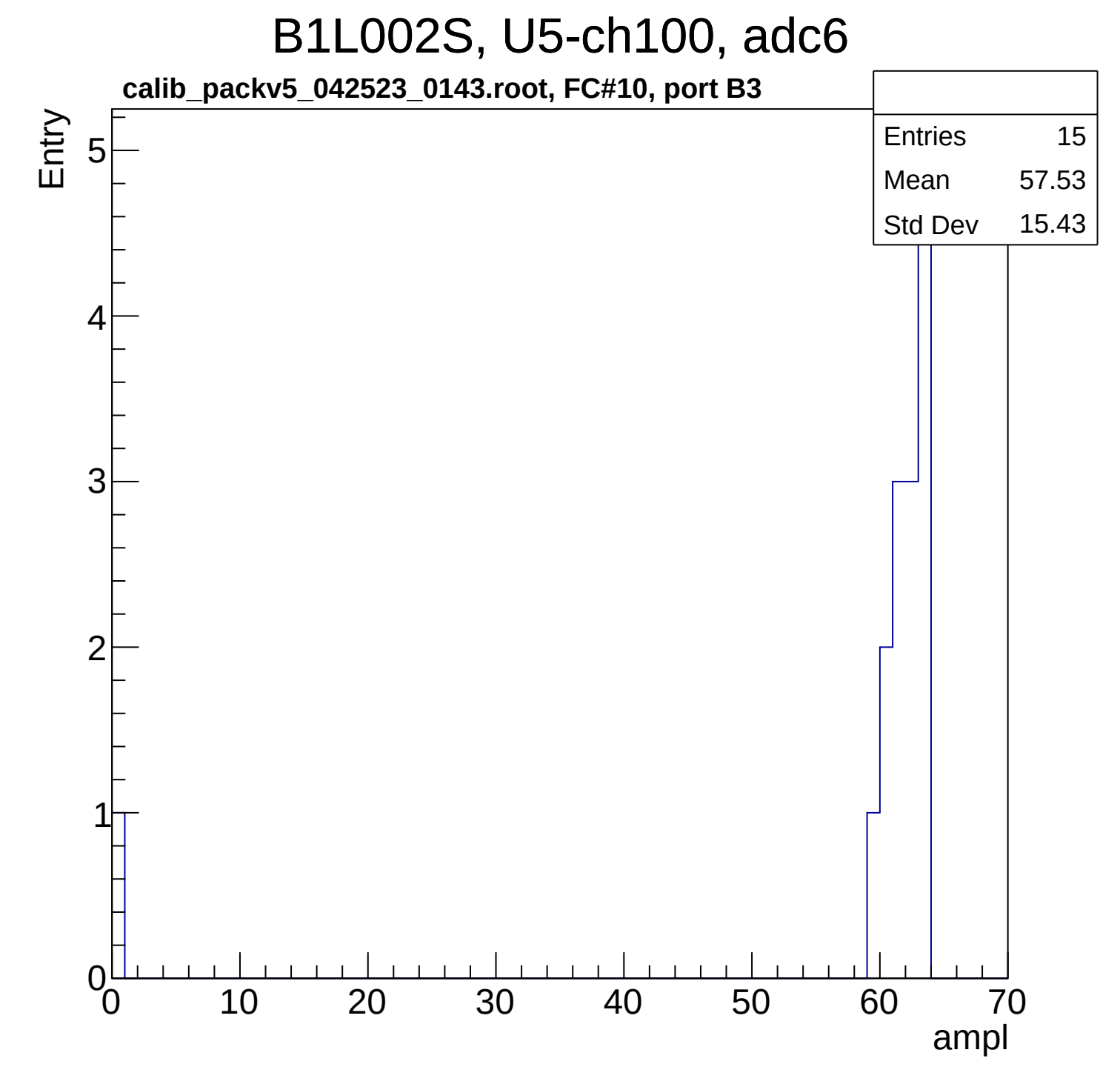
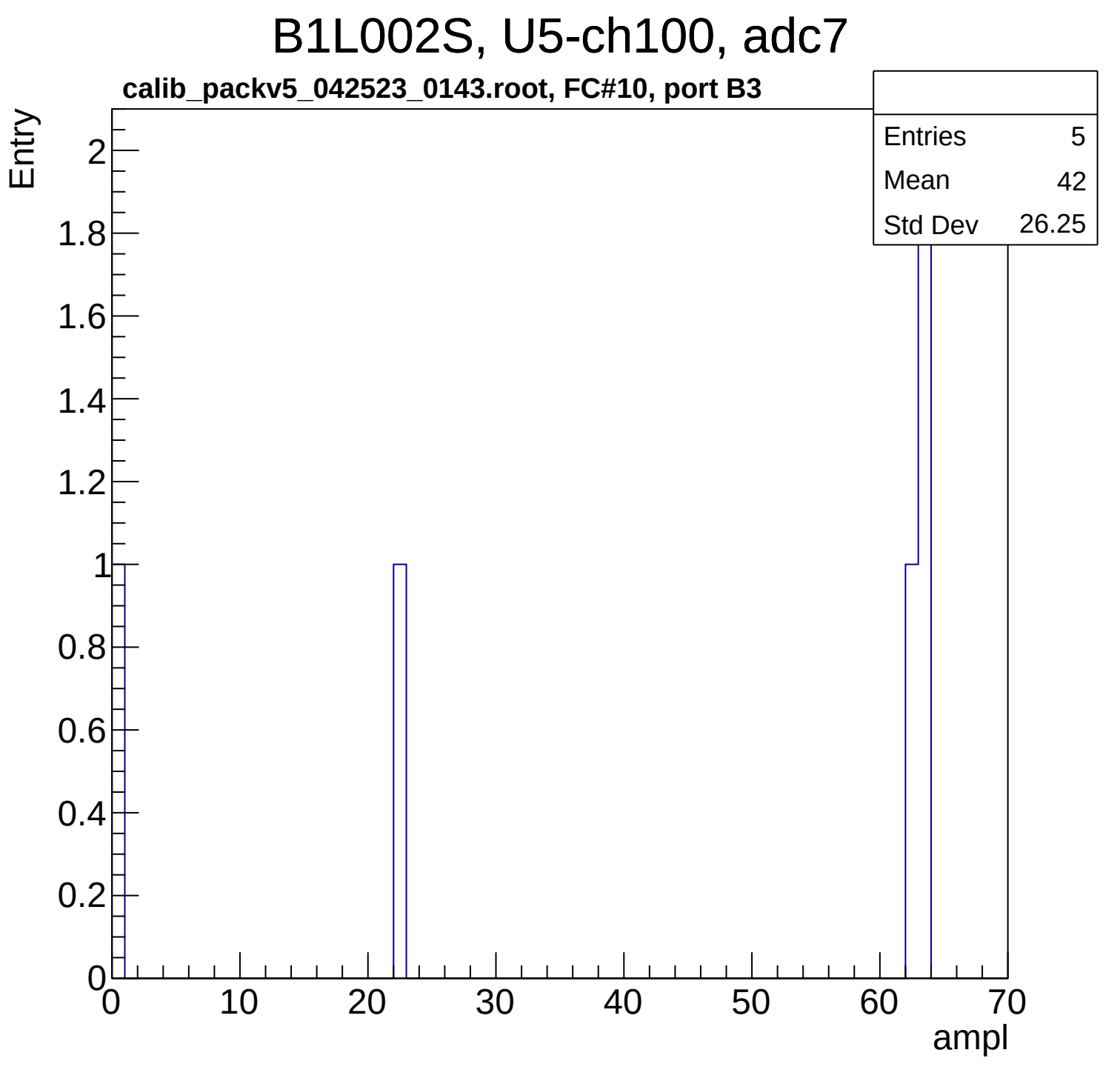

B1L002S, U5-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.53
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 42
Std Dev 26.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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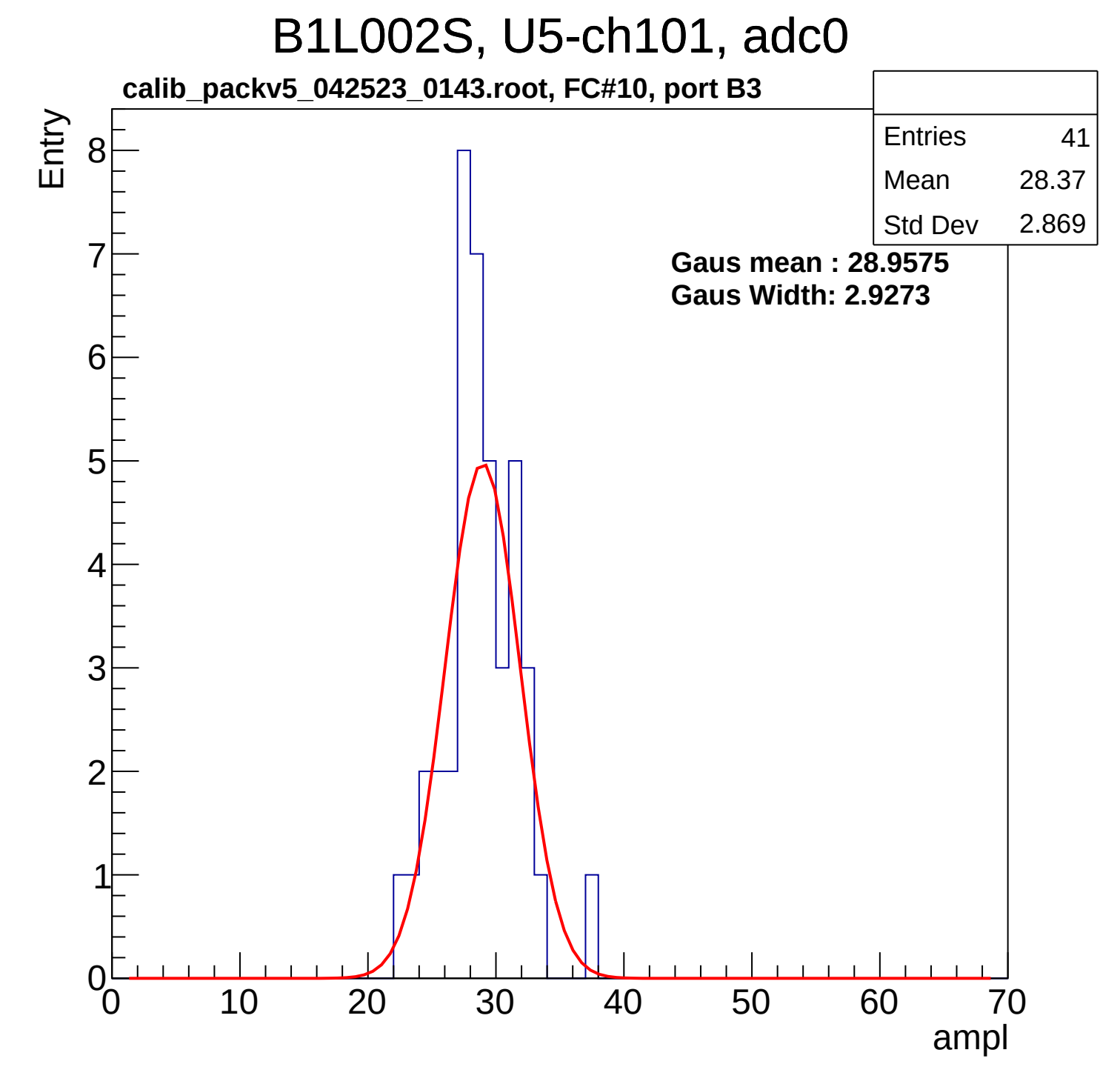
B1L002S, U5-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 28.37
Std Dev 2.869
Gaus mean : 28.9575
Gaus Width: 2.9273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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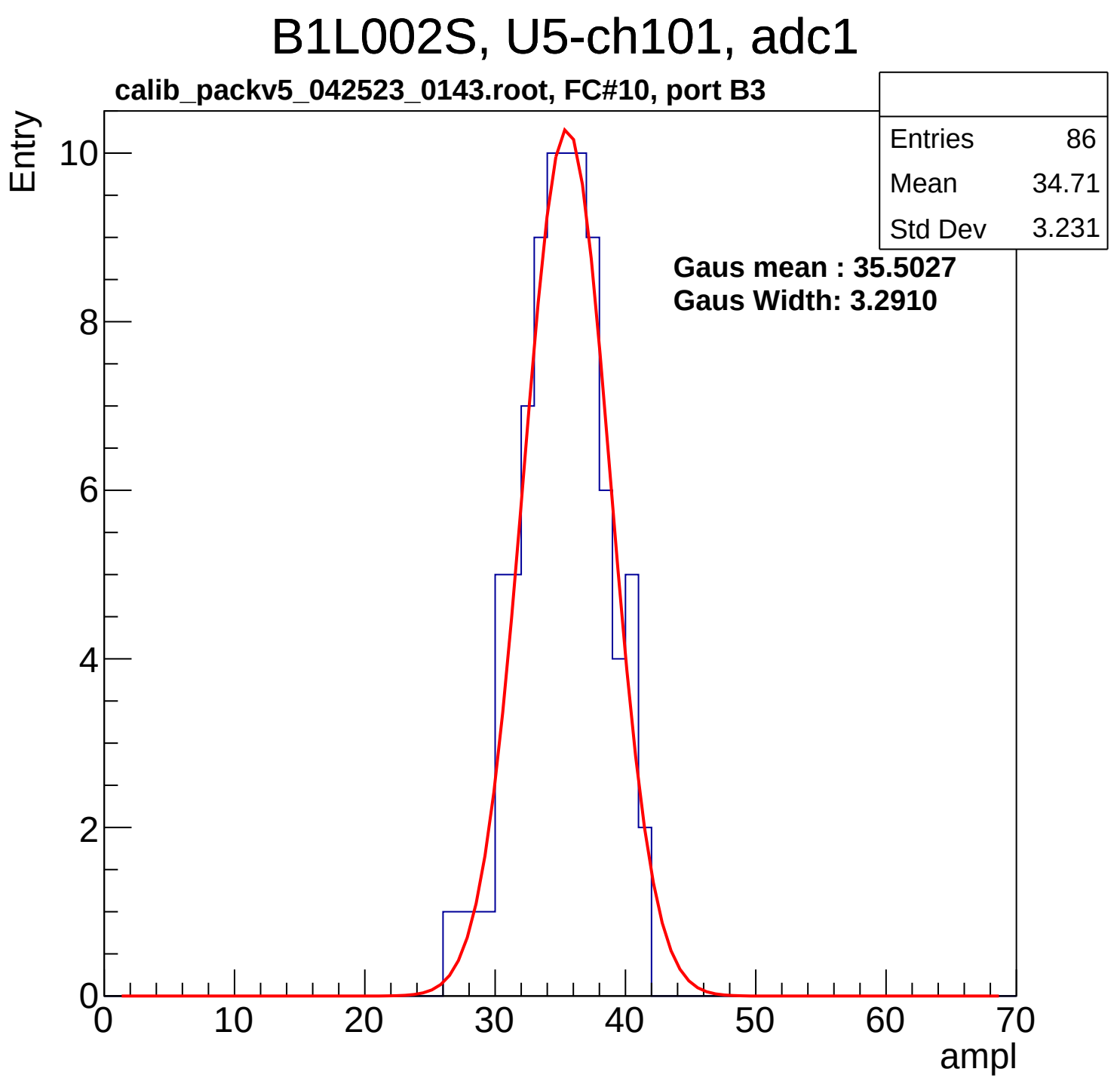
B1L002S, U5-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 34.71
Std Dev 3.231
Gaus mean : 35.5027
Gaus Width: 3.2910
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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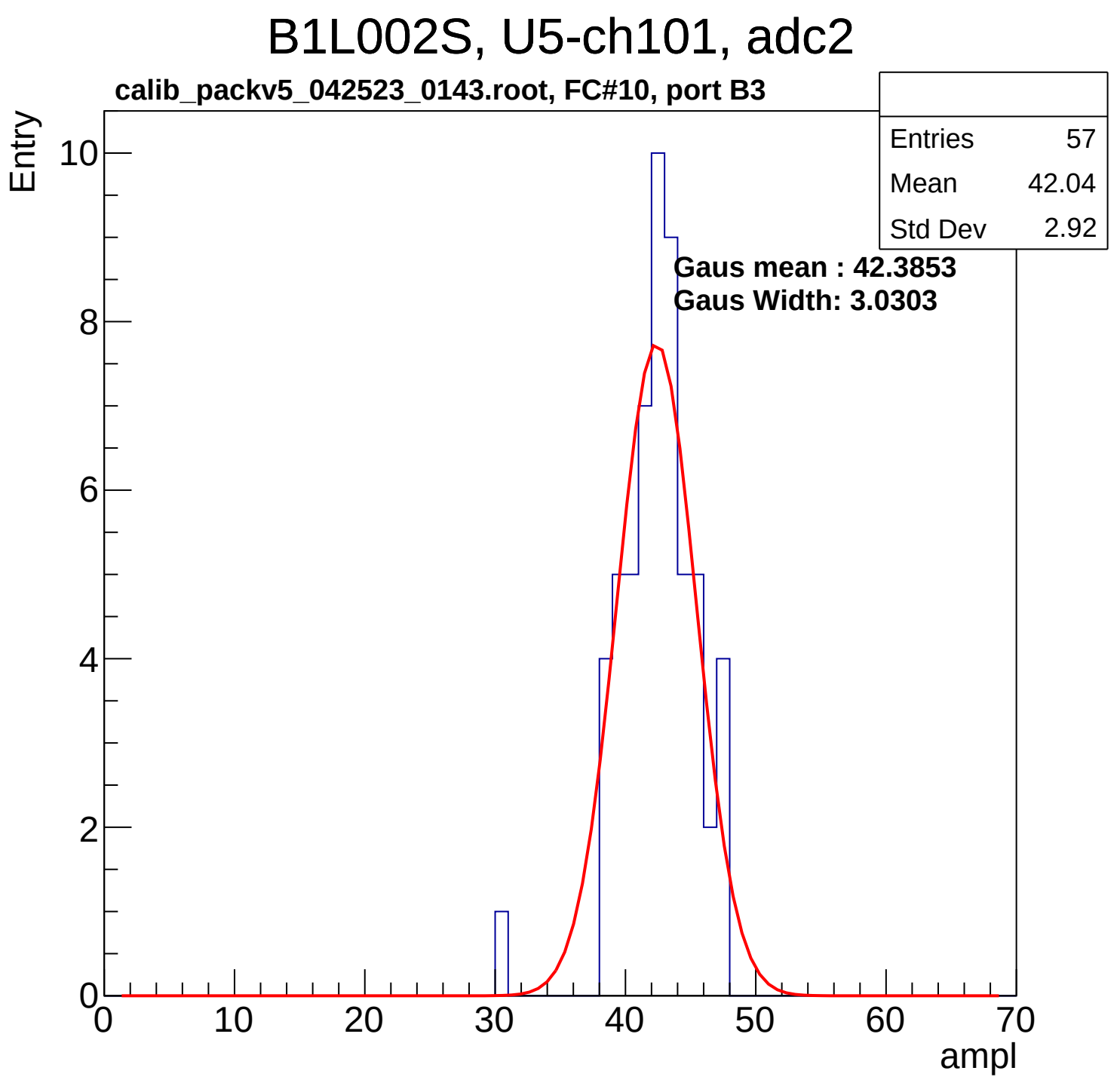
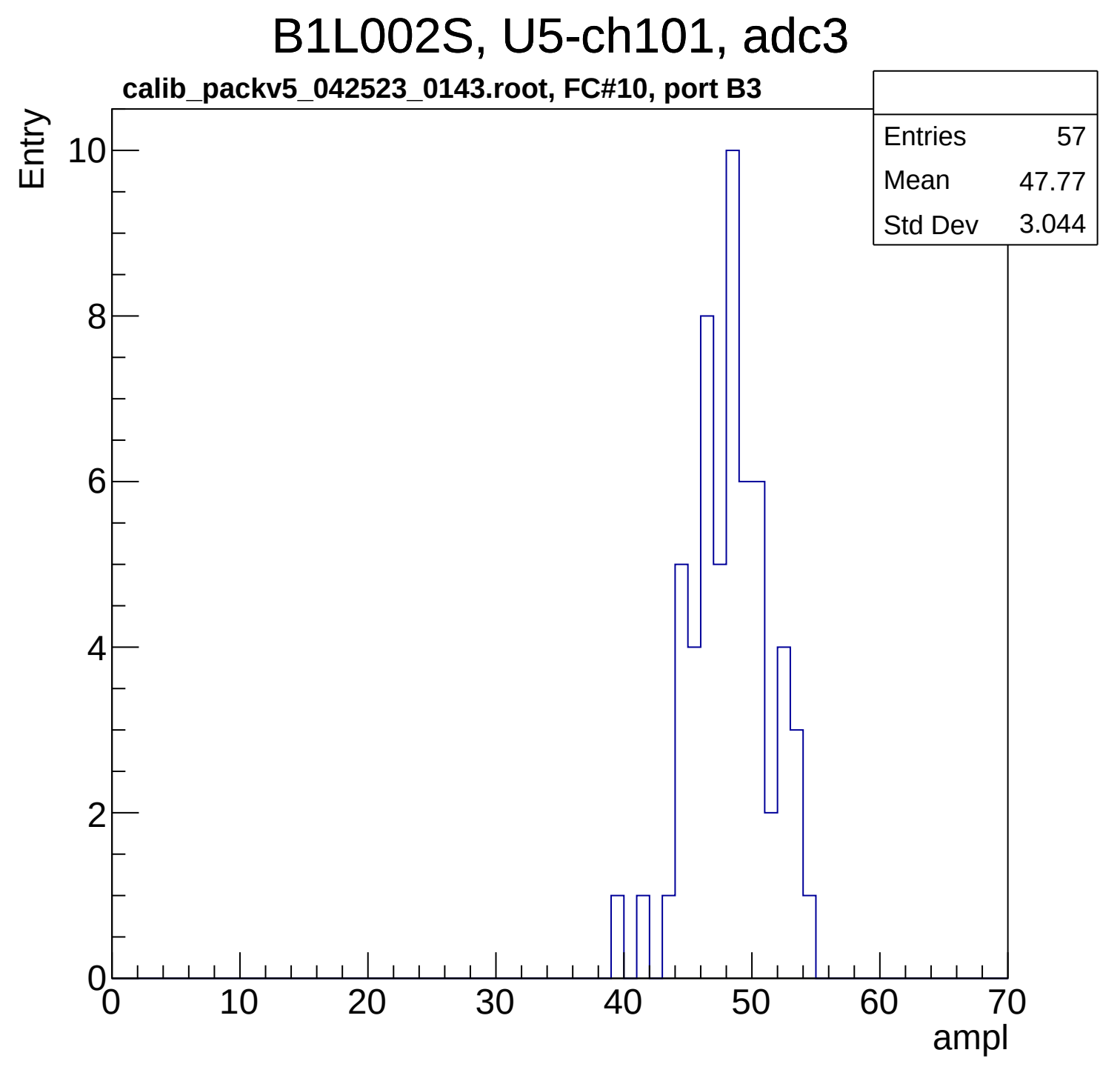
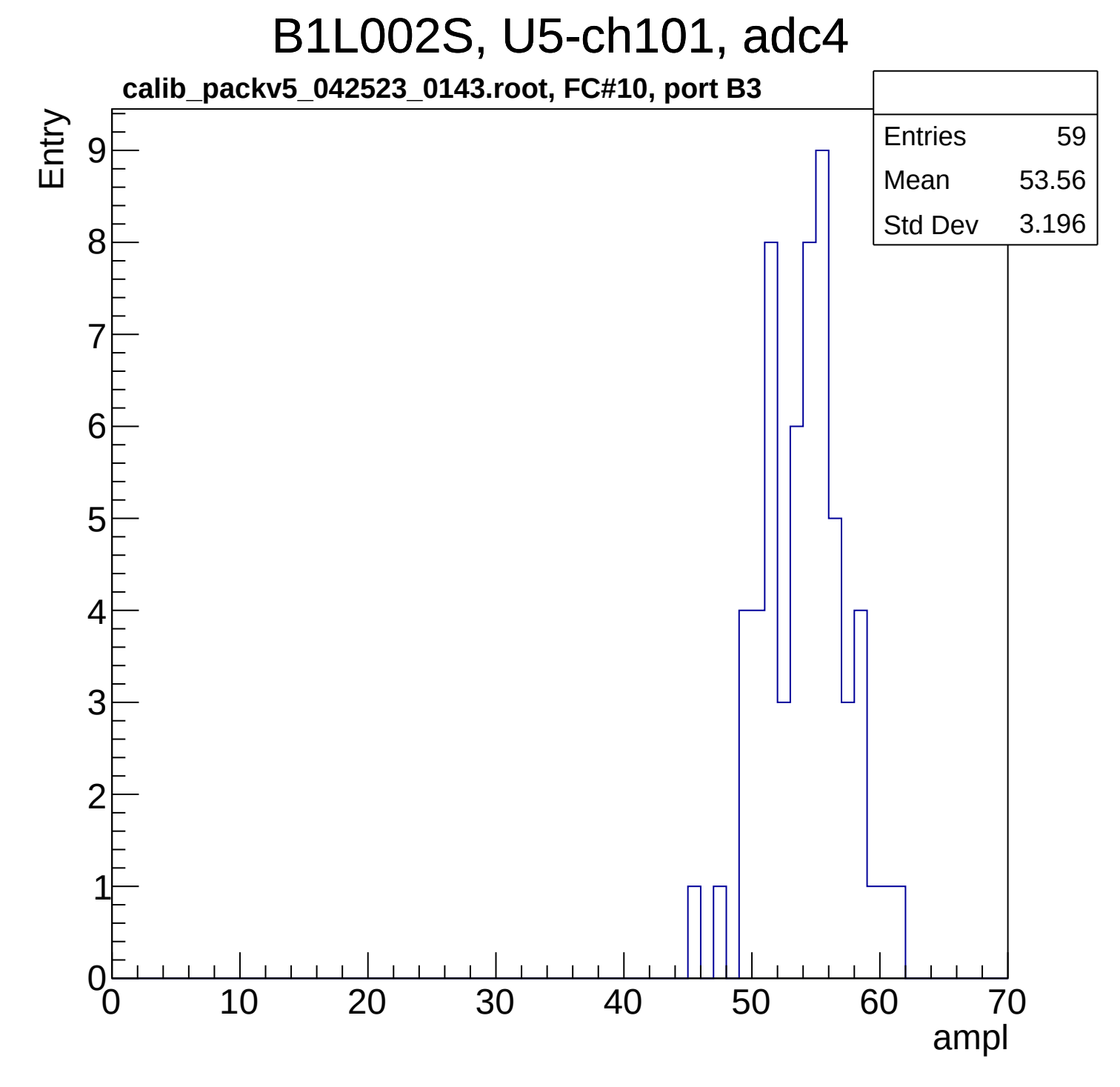

B1L002S, U5-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.04
Std Dev 2.92
Gaus mean : 42.3853
Gaus Width: 3.0303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.77
Std Dev 3.044
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.56
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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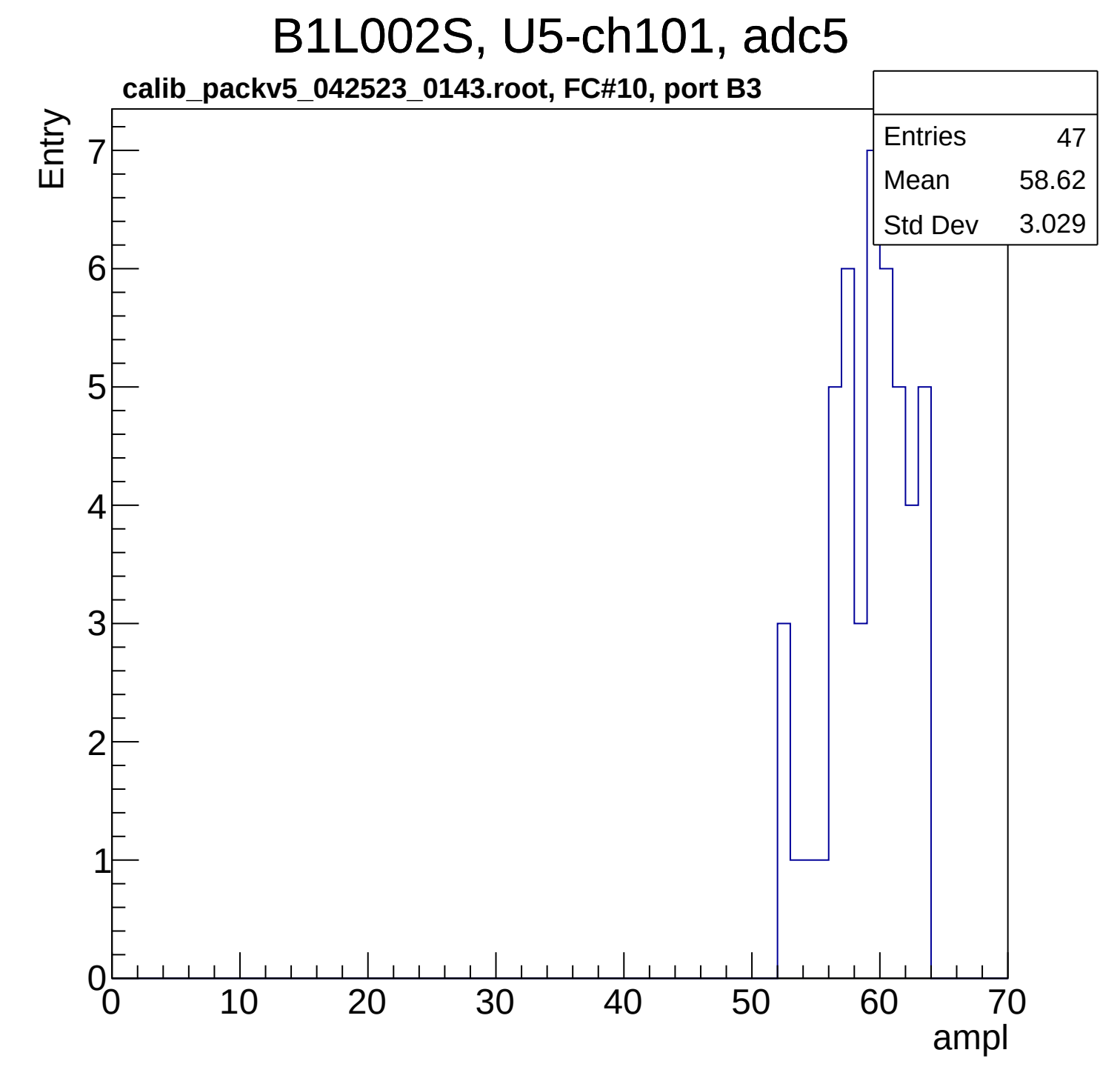
B1L002S, U5-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.62
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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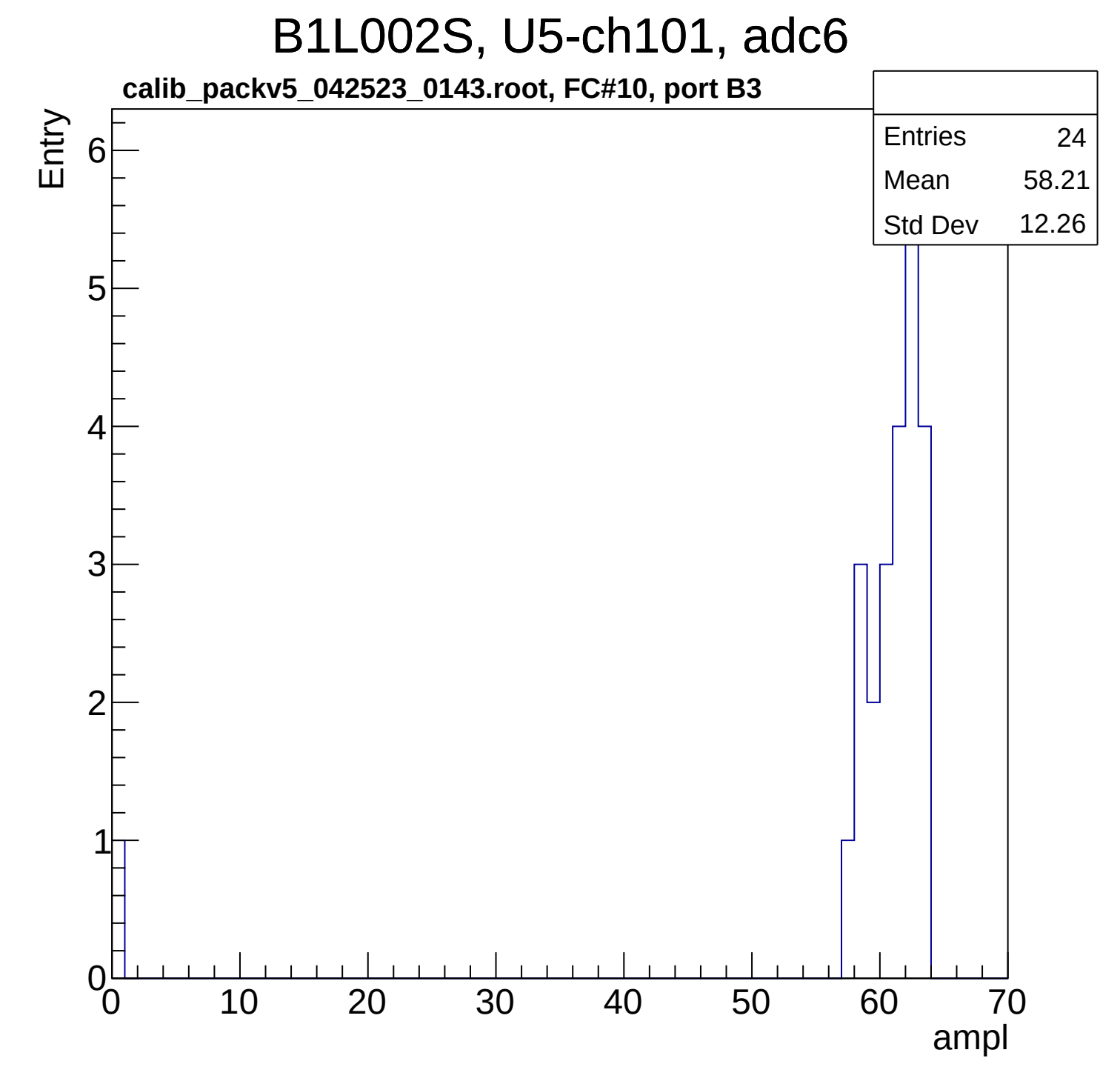
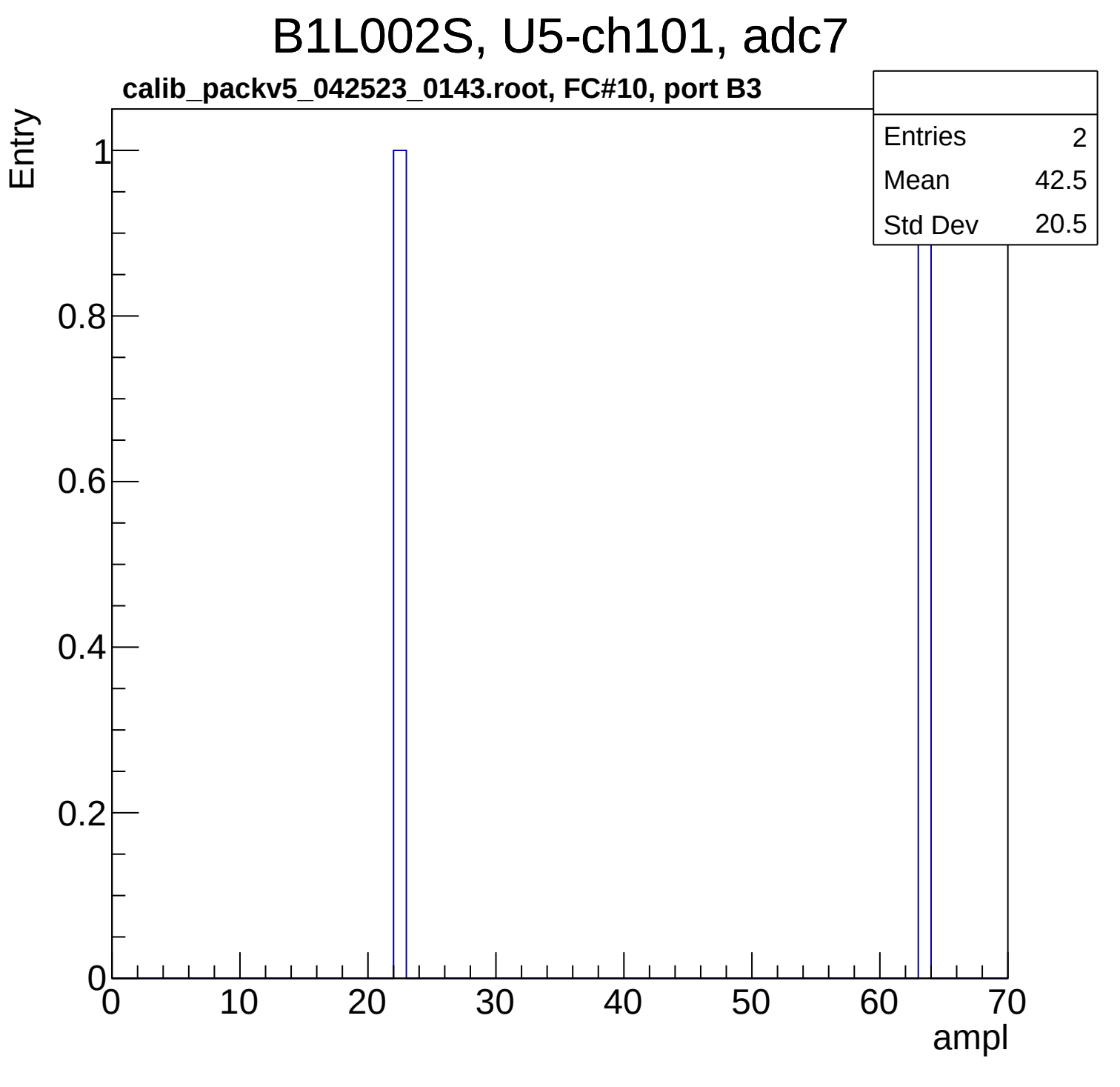
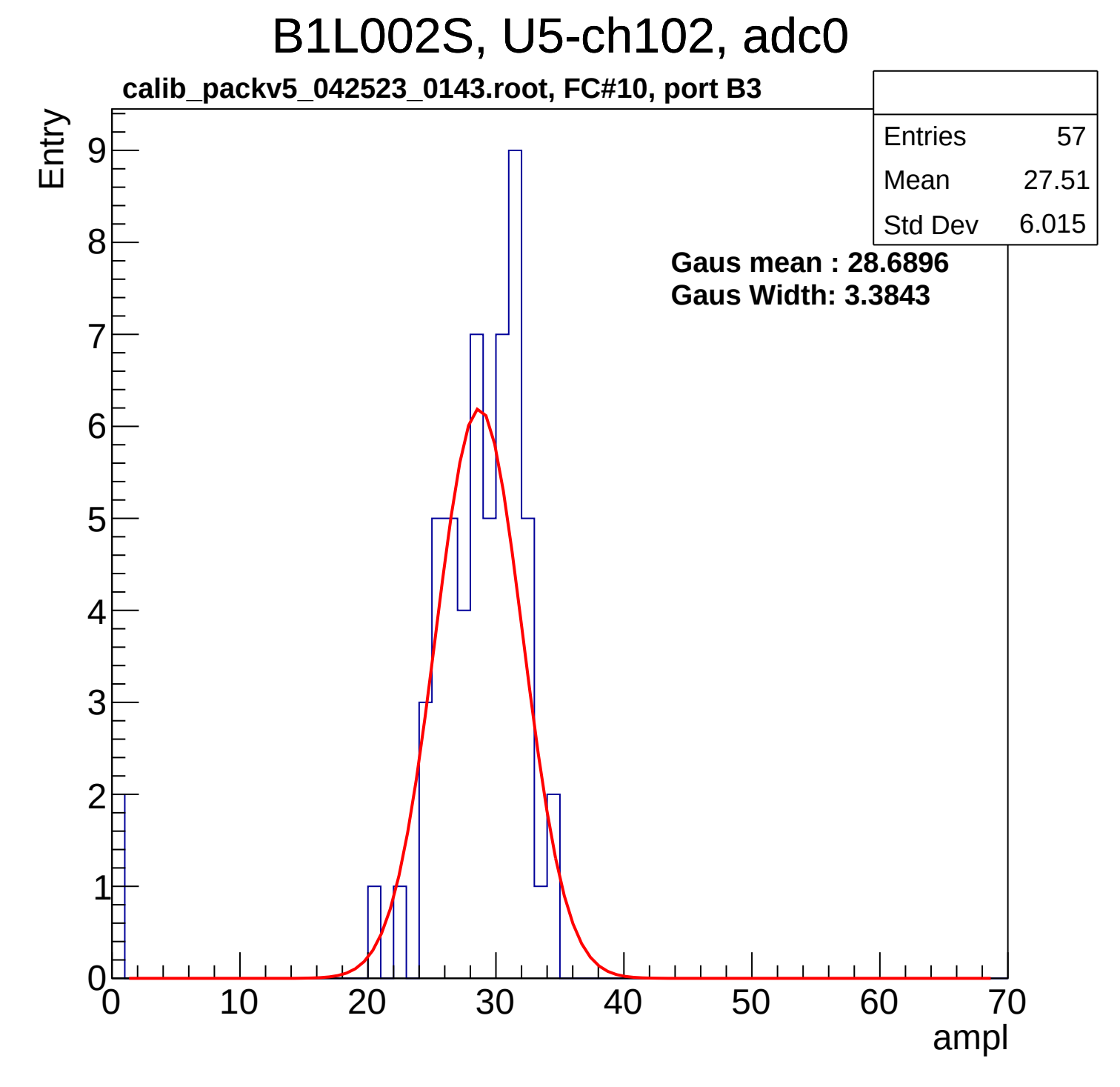
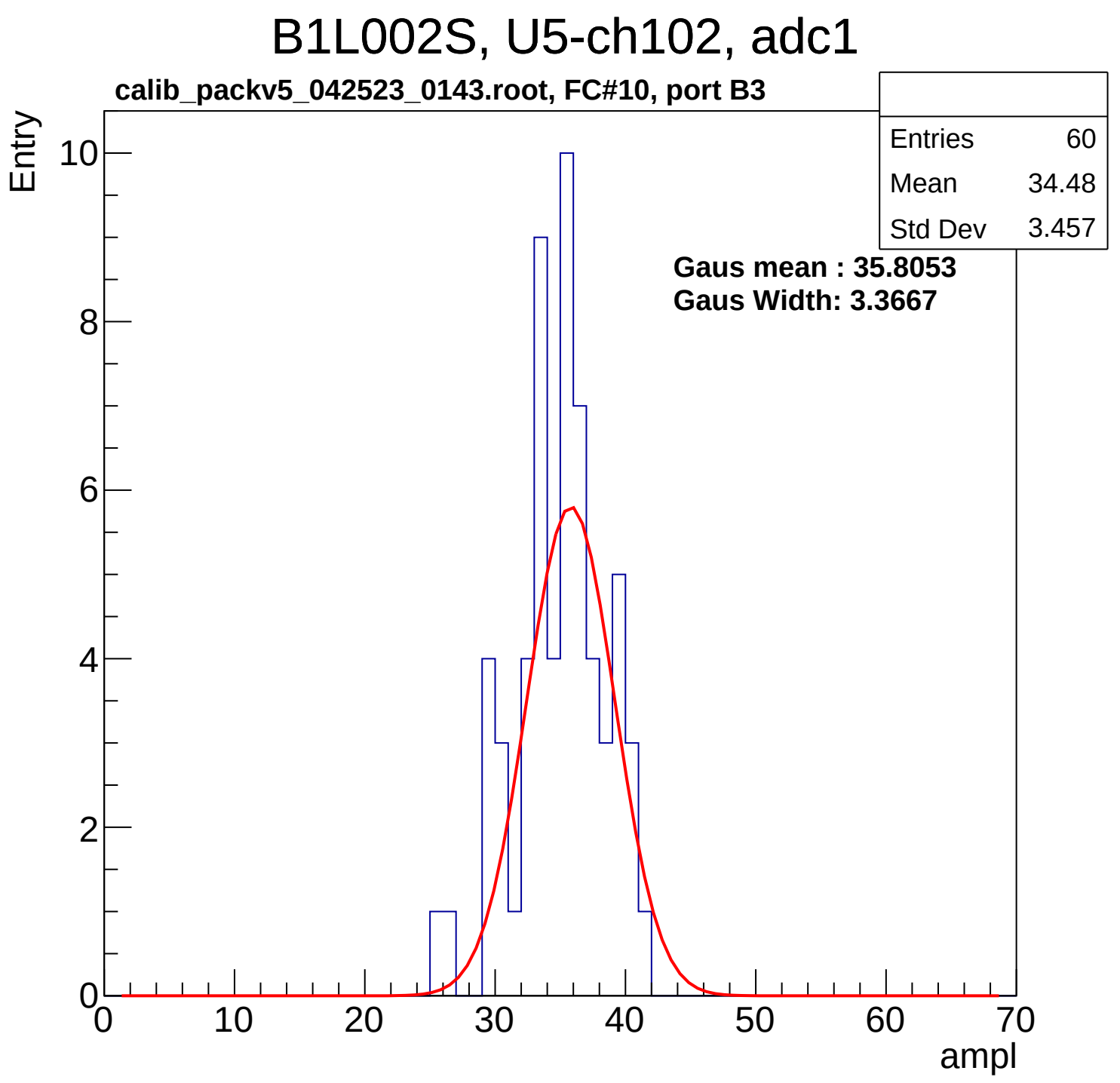

B1L002S, U5-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.21
Std Dev 12.26
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.51
Std Dev 6.015
Gaus mean : 28.6896
Gaus Width: 3.3843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.48
Std Dev 3.457
Gaus mean : 35.8053
Gaus Width: 3.3667
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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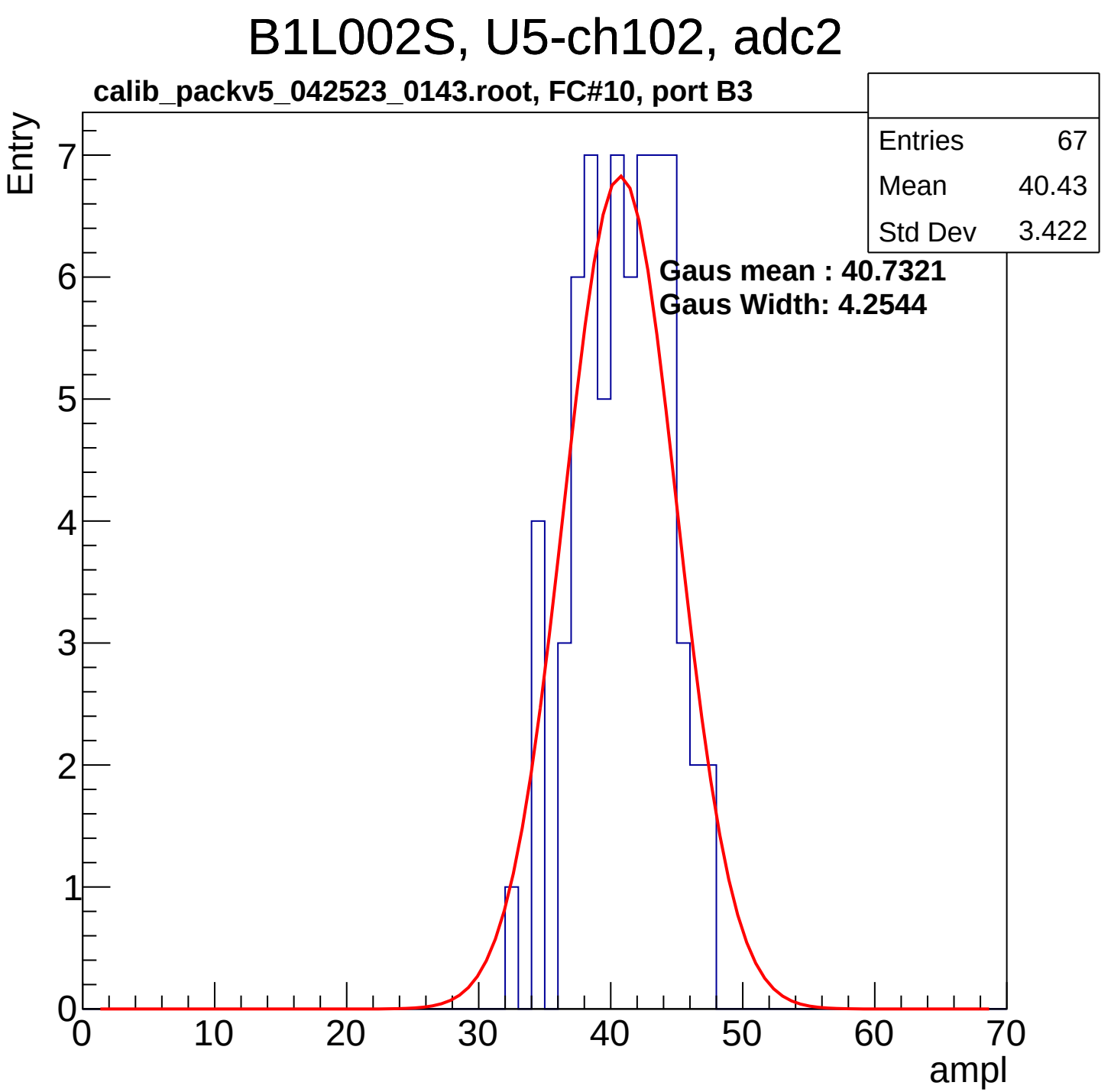
B1L002S, U5-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.43
Std Dev 3.422
Gaus mean : 40.7321
Gaus Width: 4.2544
Entry
ampl
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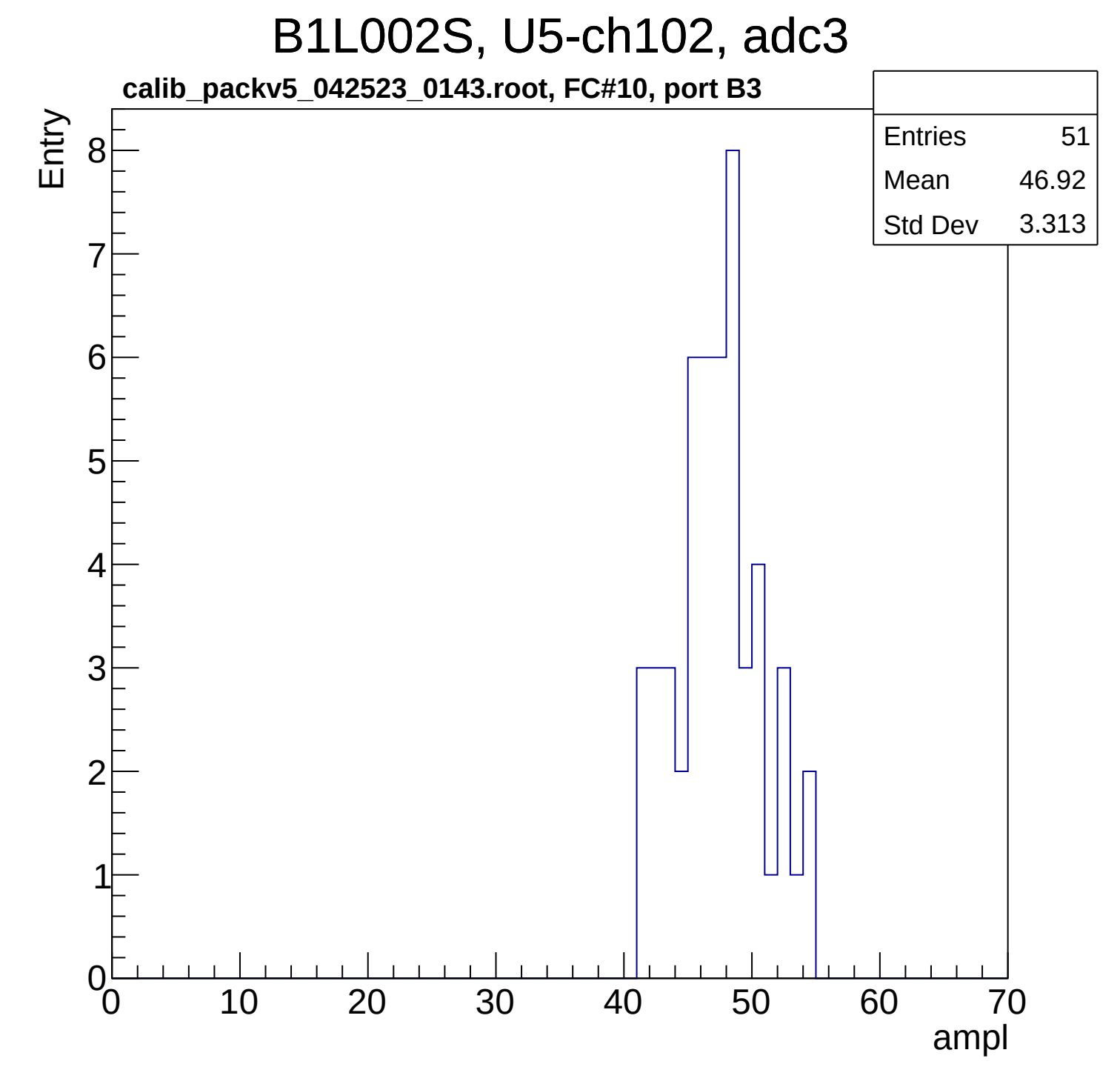
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.92
Std Dev 3.313
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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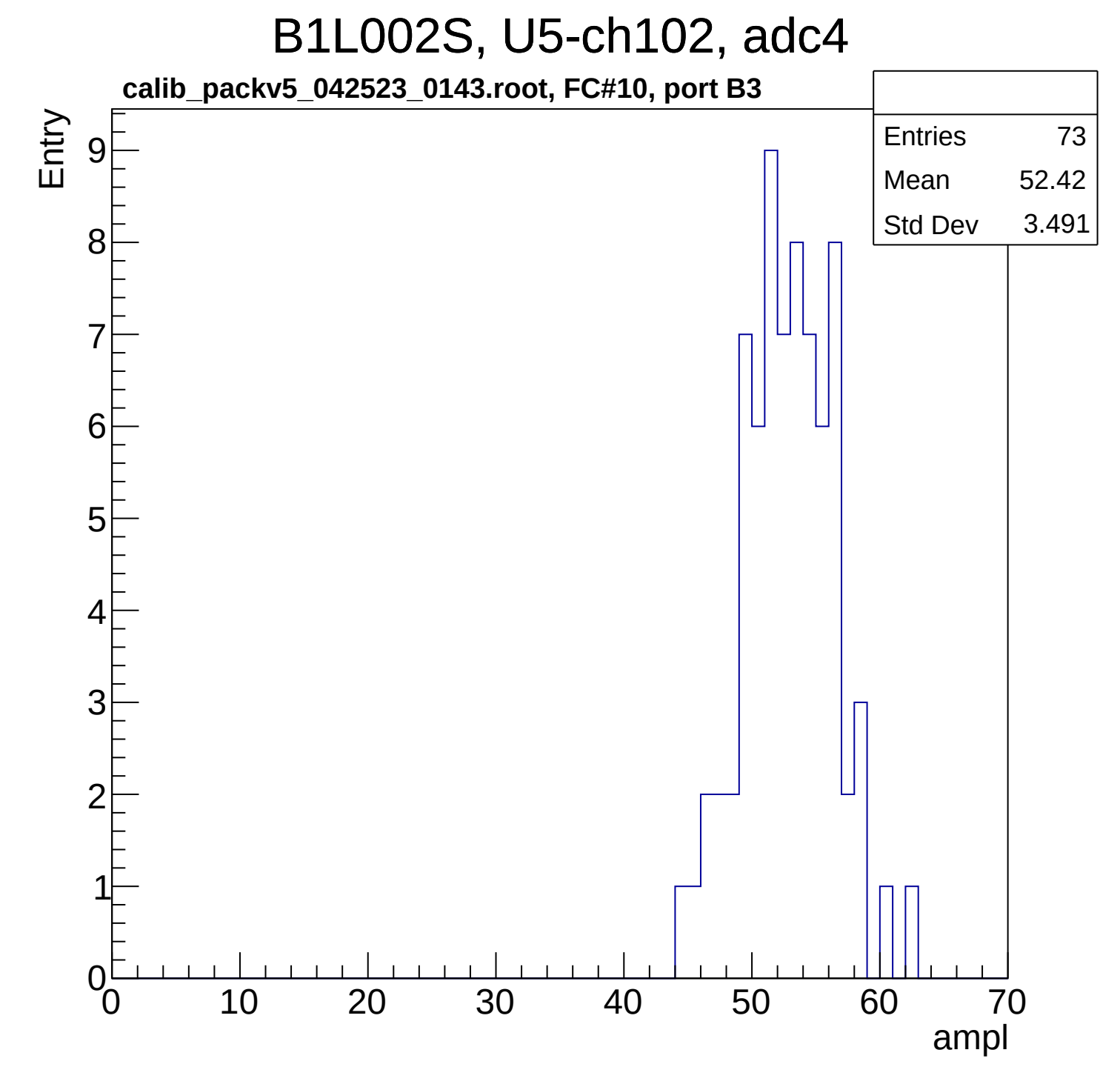
B1L002S, U5-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 52.42
Std Dev 3.491
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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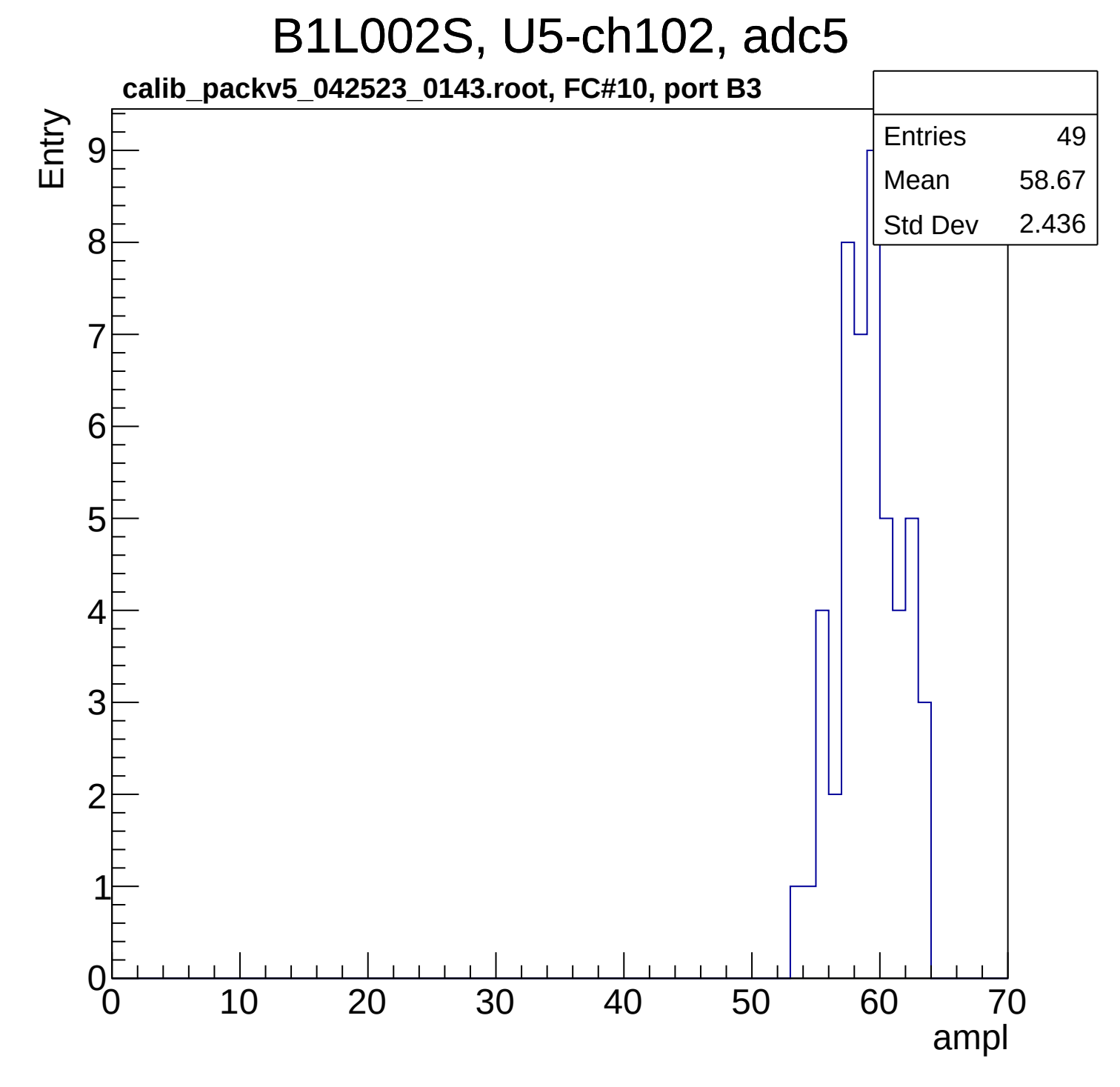
B1L002S, U5-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.67
Std Dev 2.436
Entry
ampl
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3
4
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6
7
8
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0
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60
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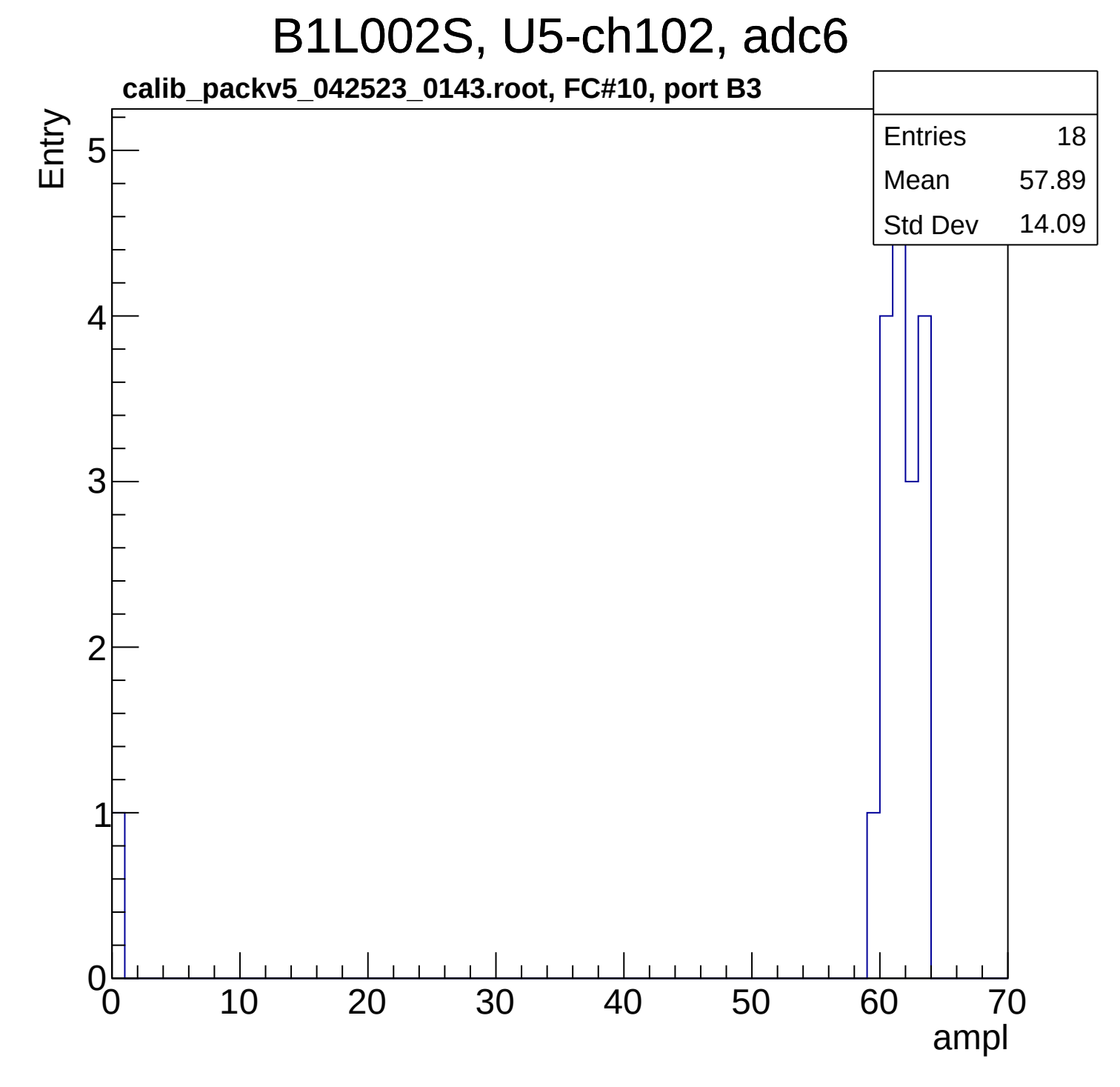
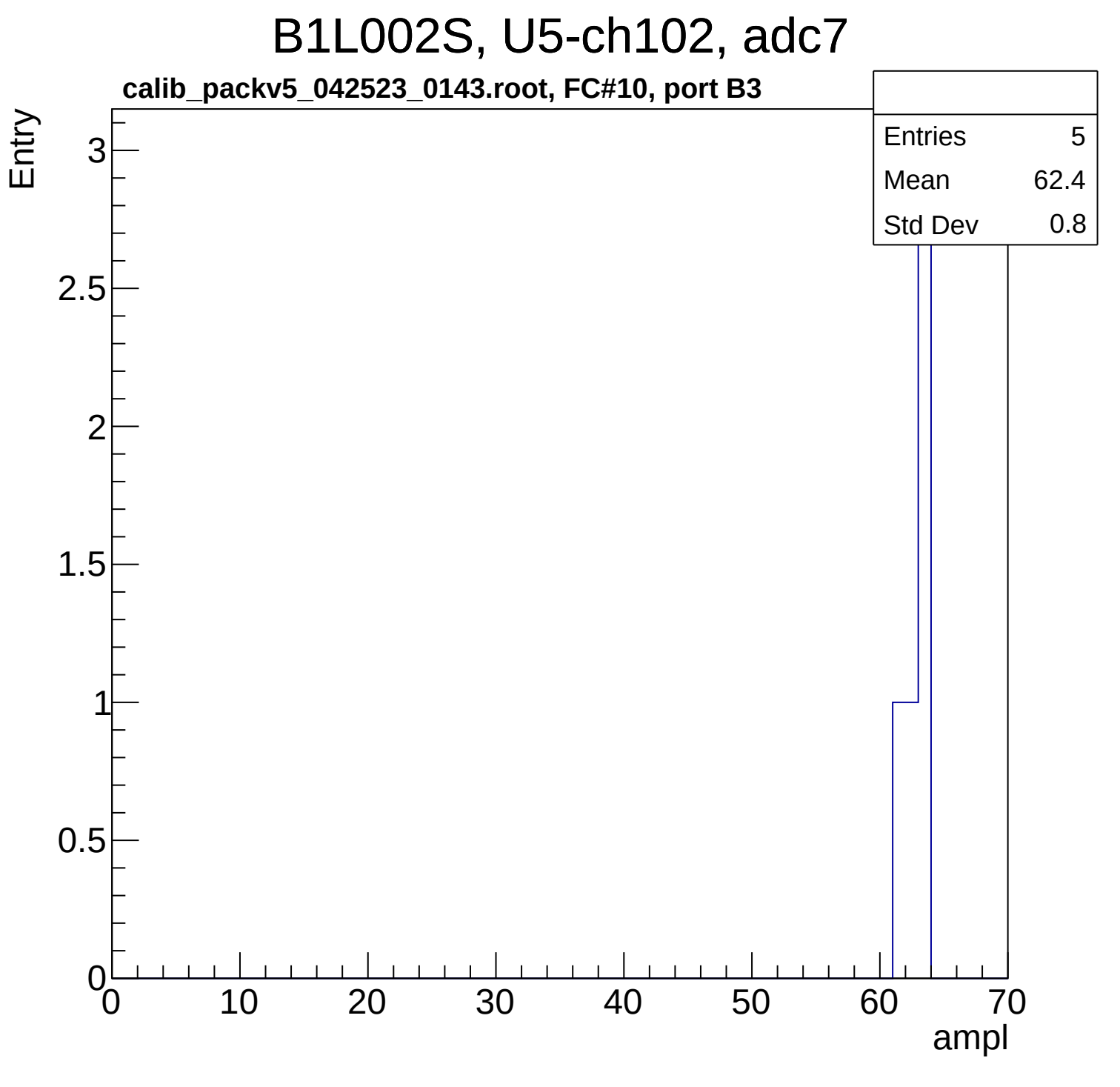

B1L002S, U5-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.89
Std Dev 14.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.4
Std Dev 0.8
Entry
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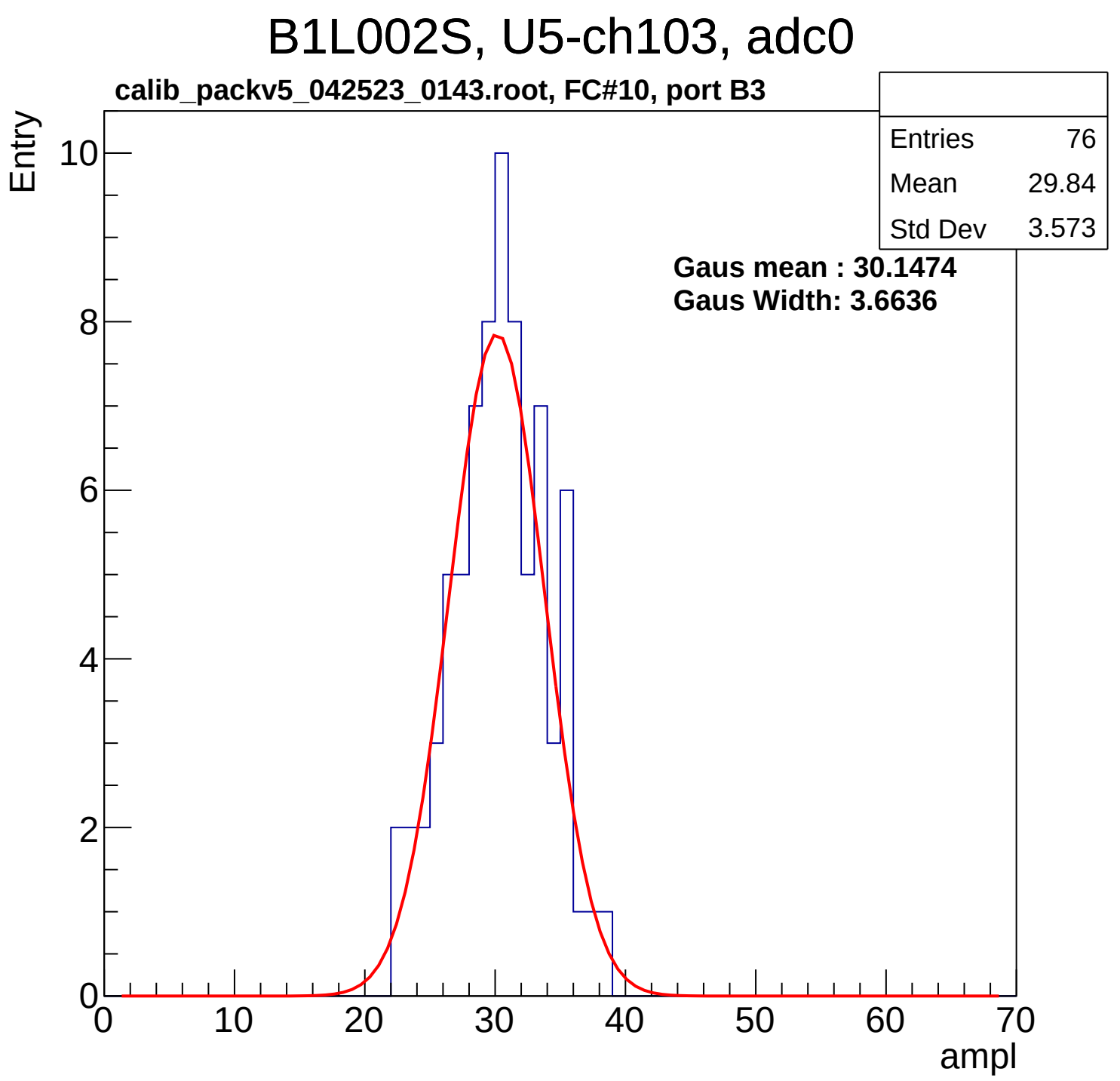
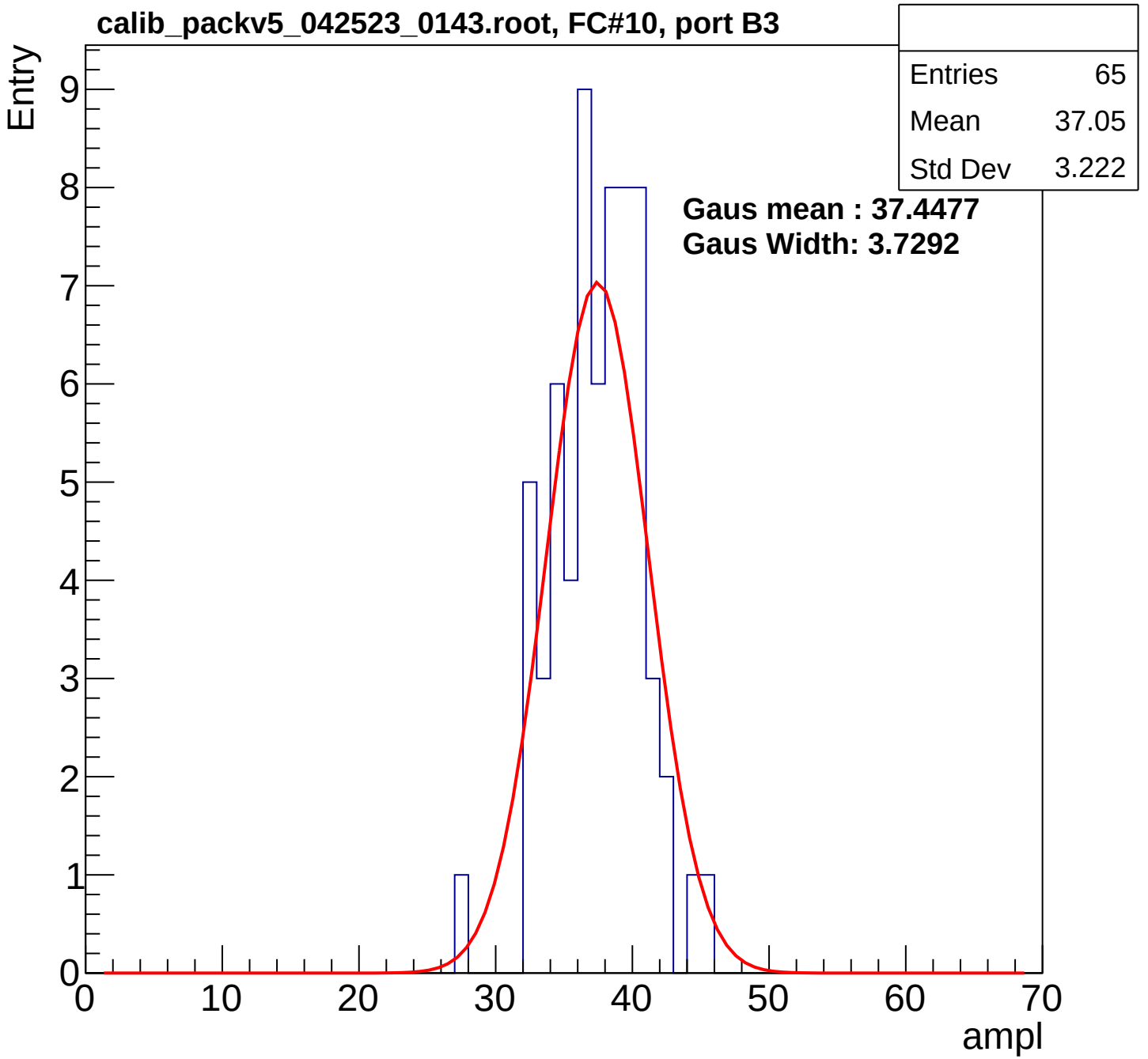
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 29.84
Std Dev 3.573
Gaus mean : 30.1474
Gaus Width: 3.6636
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 37.05
Std Dev 3.222
Gaus mean : 37.4477
Gaus Width: 3.7292
Entry
ampl
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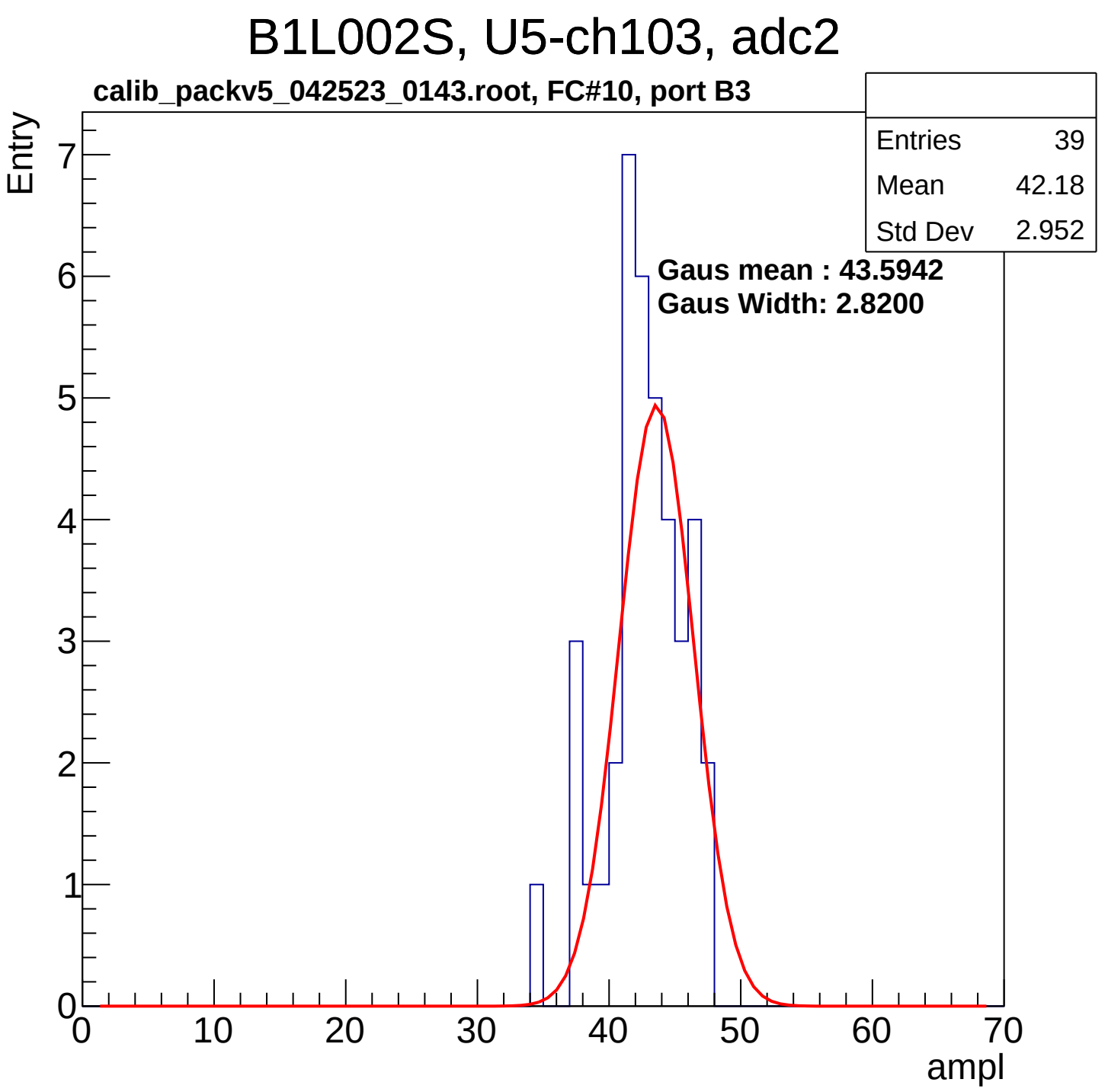
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 42.18
Std Dev 2.952
Gaus mean : 43.5942
Gaus Width: 2.8200
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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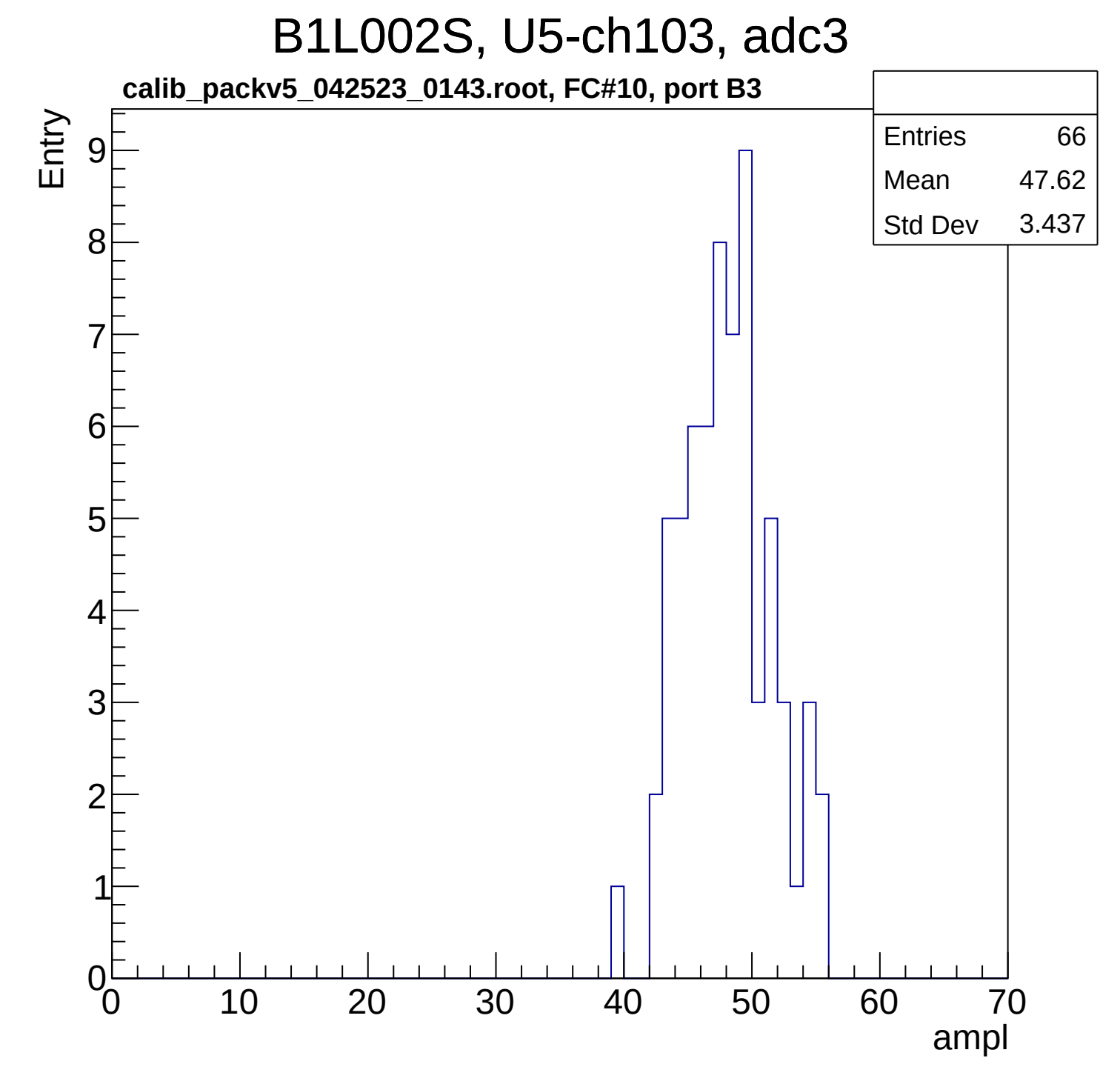
B1L002S, U5-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.62
Std Dev 3.437
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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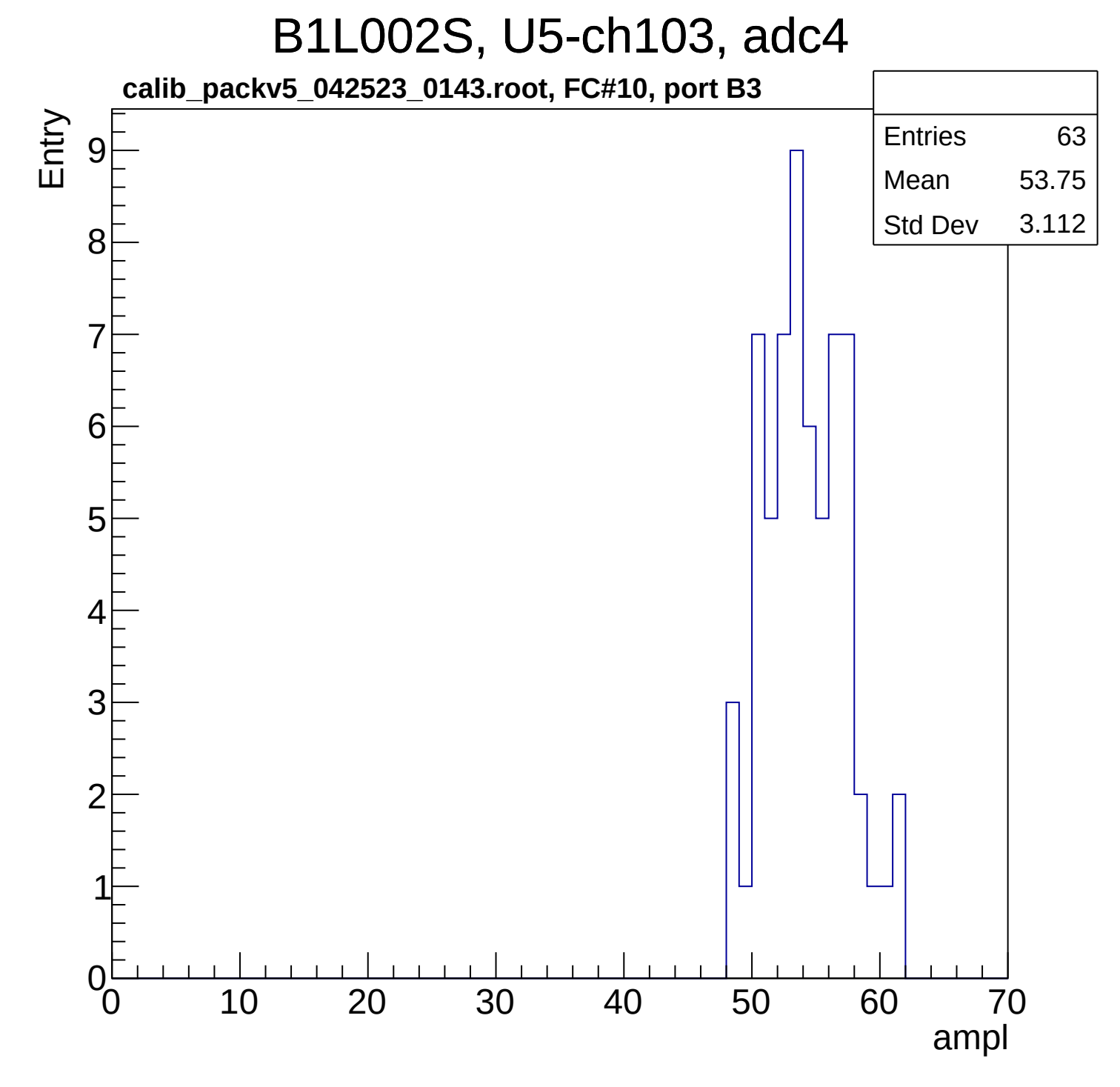
60
70


B1L002S, U5-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.75
Std Dev 3.112
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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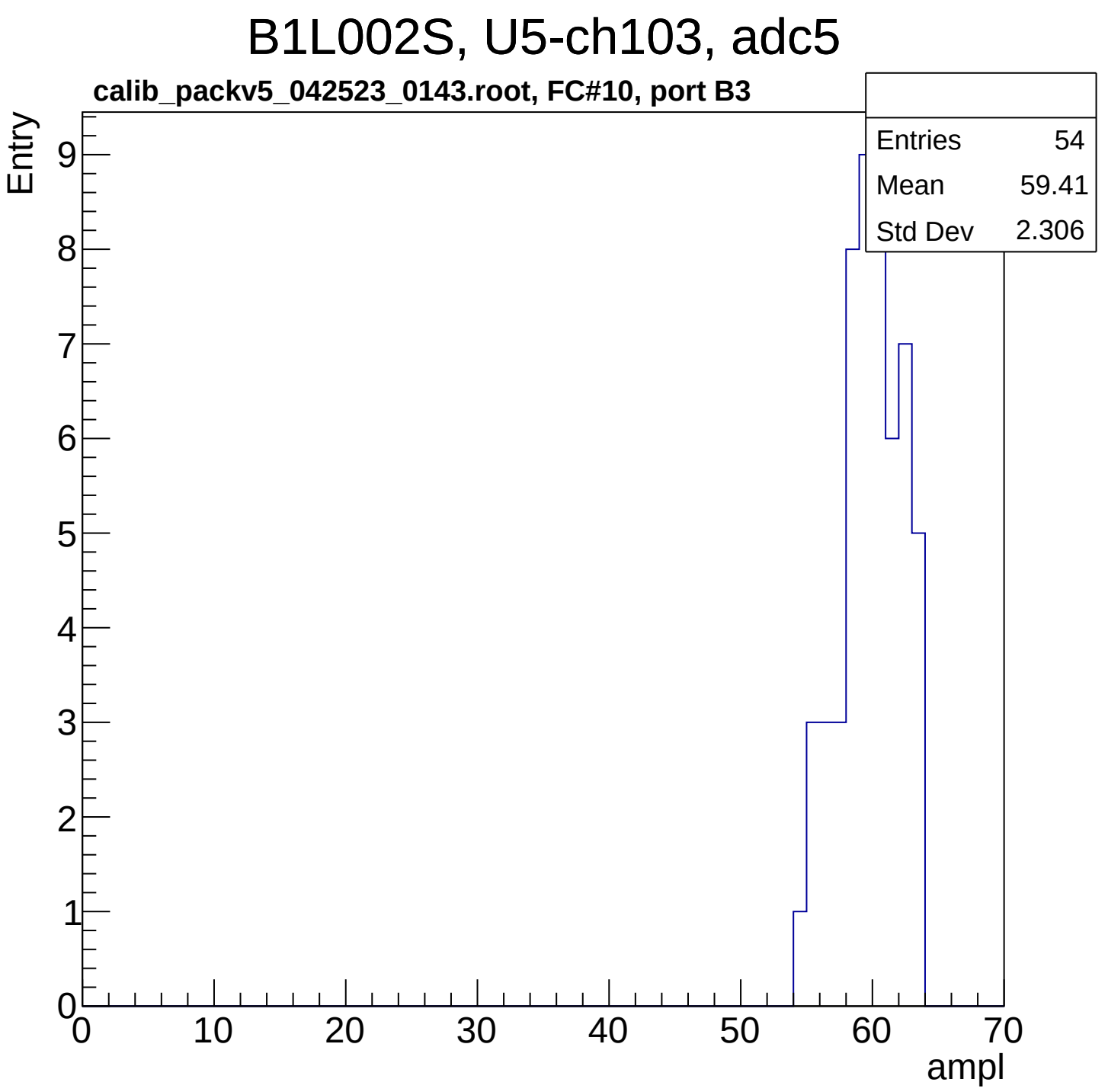
B1L002S, U5-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.41
Std Dev 2.306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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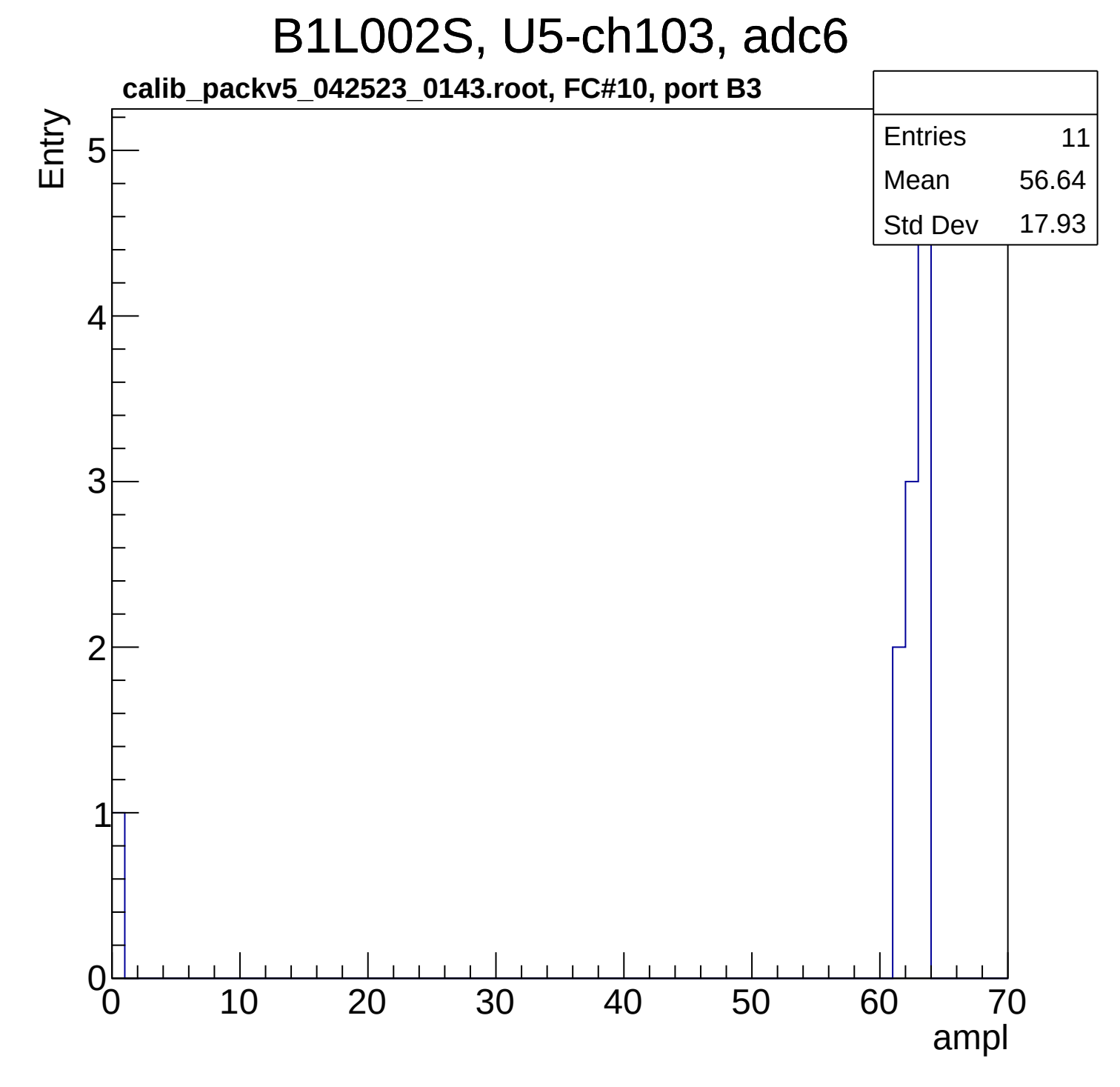
B1L002S, U5-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.64
Std Dev 17.93
Entry
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4
5
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10
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30
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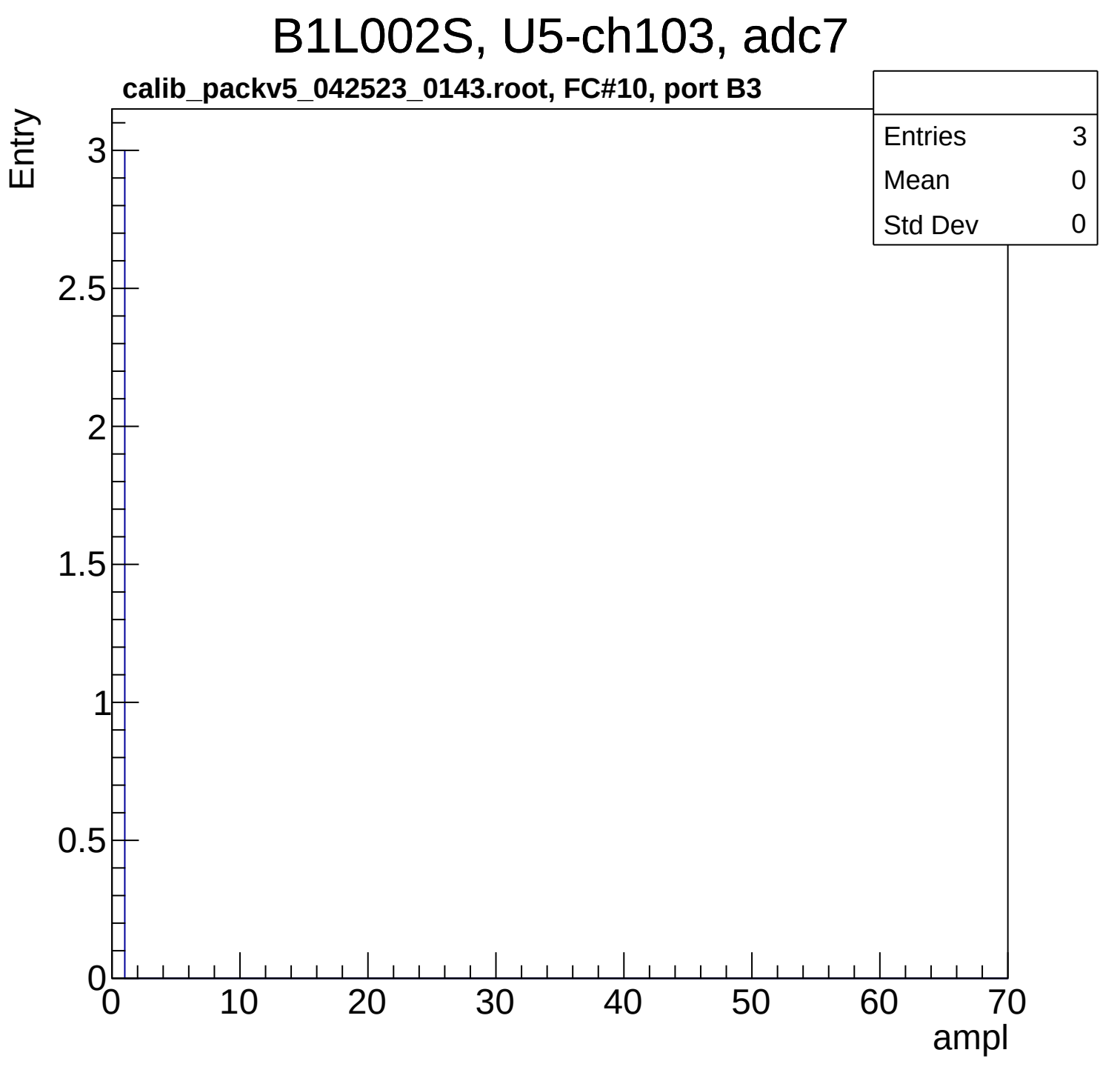
50
60
70
ampl


B1L002S, U5-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
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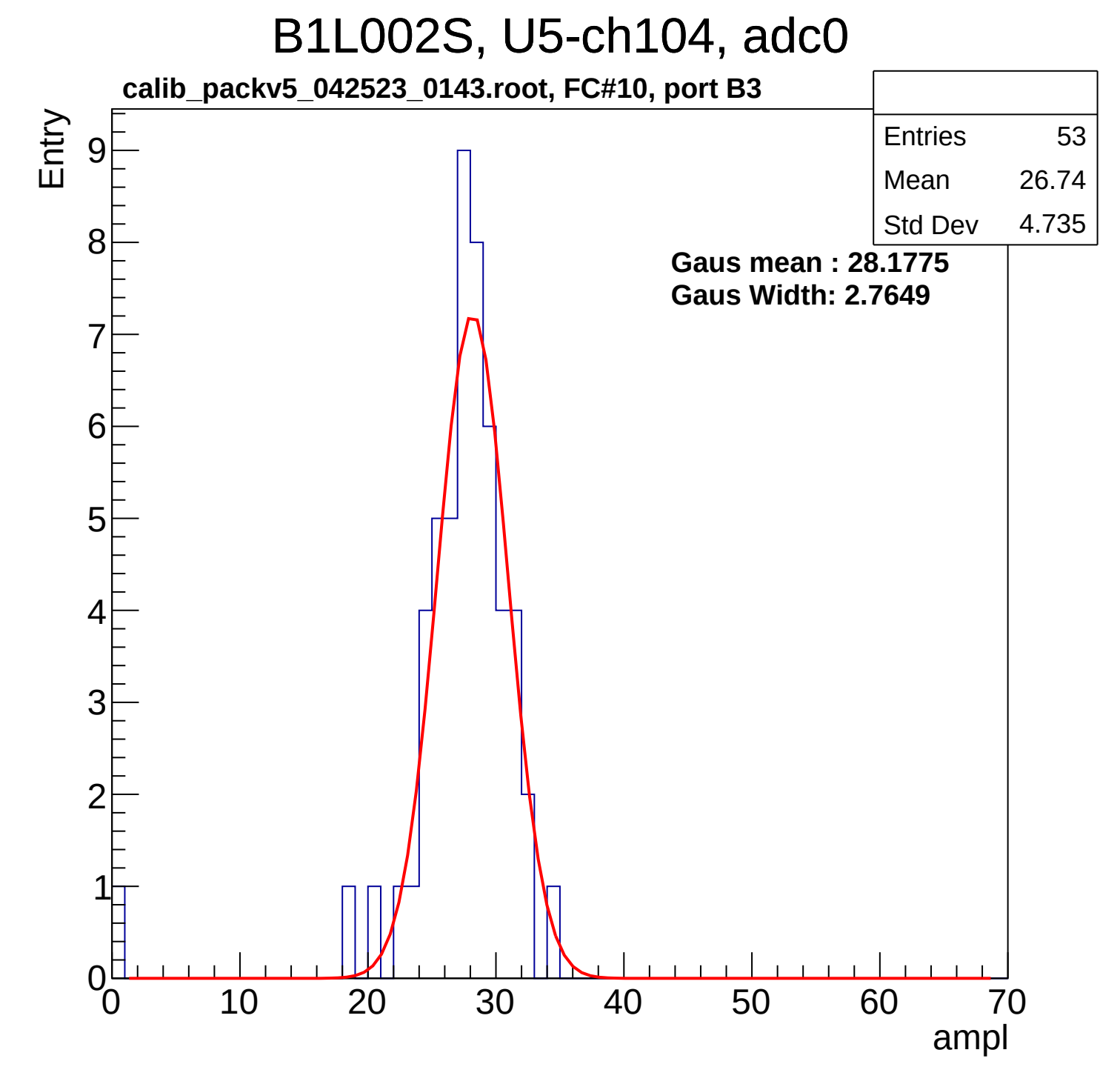
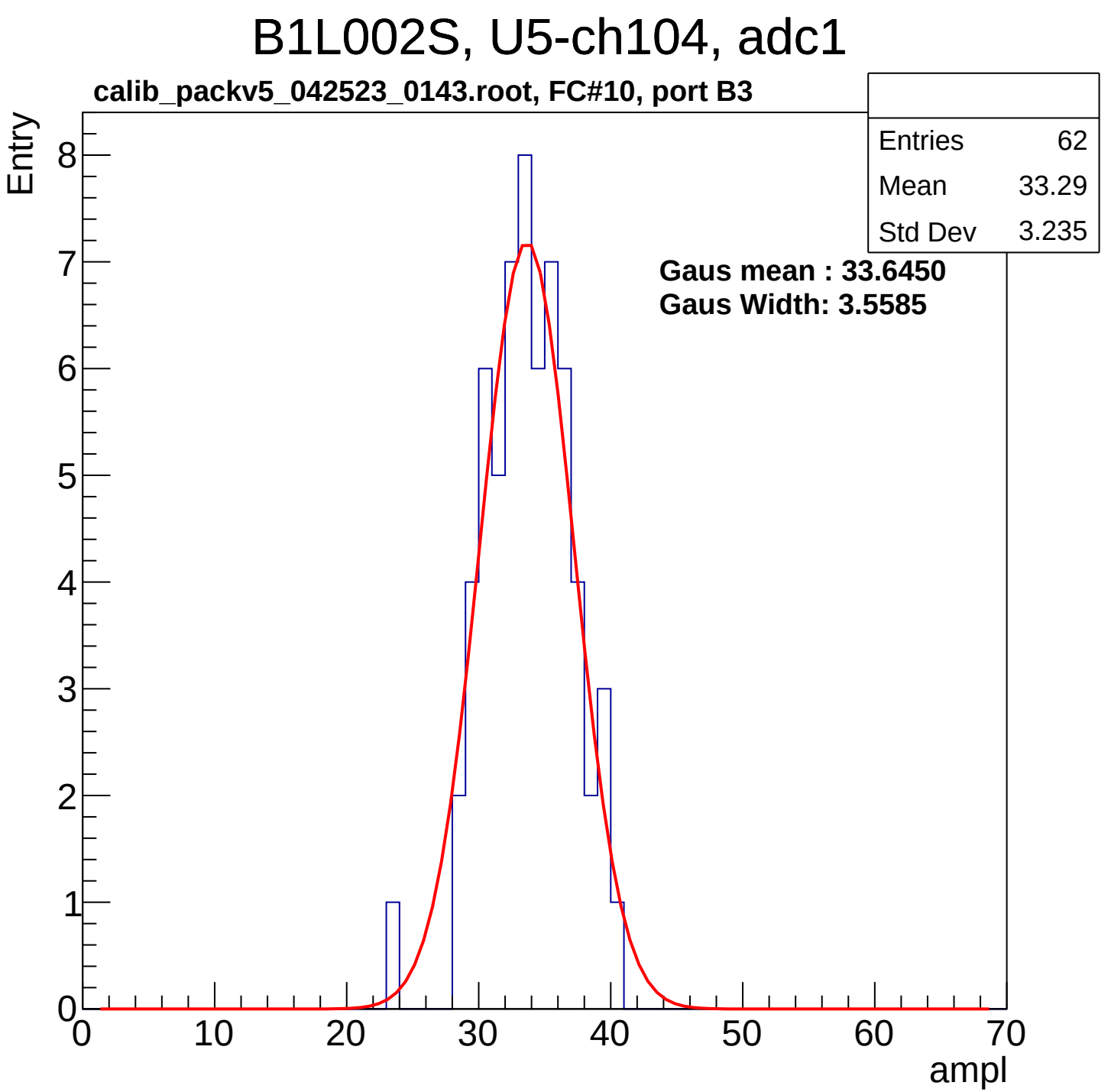
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 26.74
Std Dev 4.735
Gaus mean : 28.1775
Gaus Width: 2.7649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.29
Std Dev 3.235
Gaus mean : 33.6450
Gaus Width: 3.5585
Entry
ampl
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2
3
4
5
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7
8
0
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20
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40
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60
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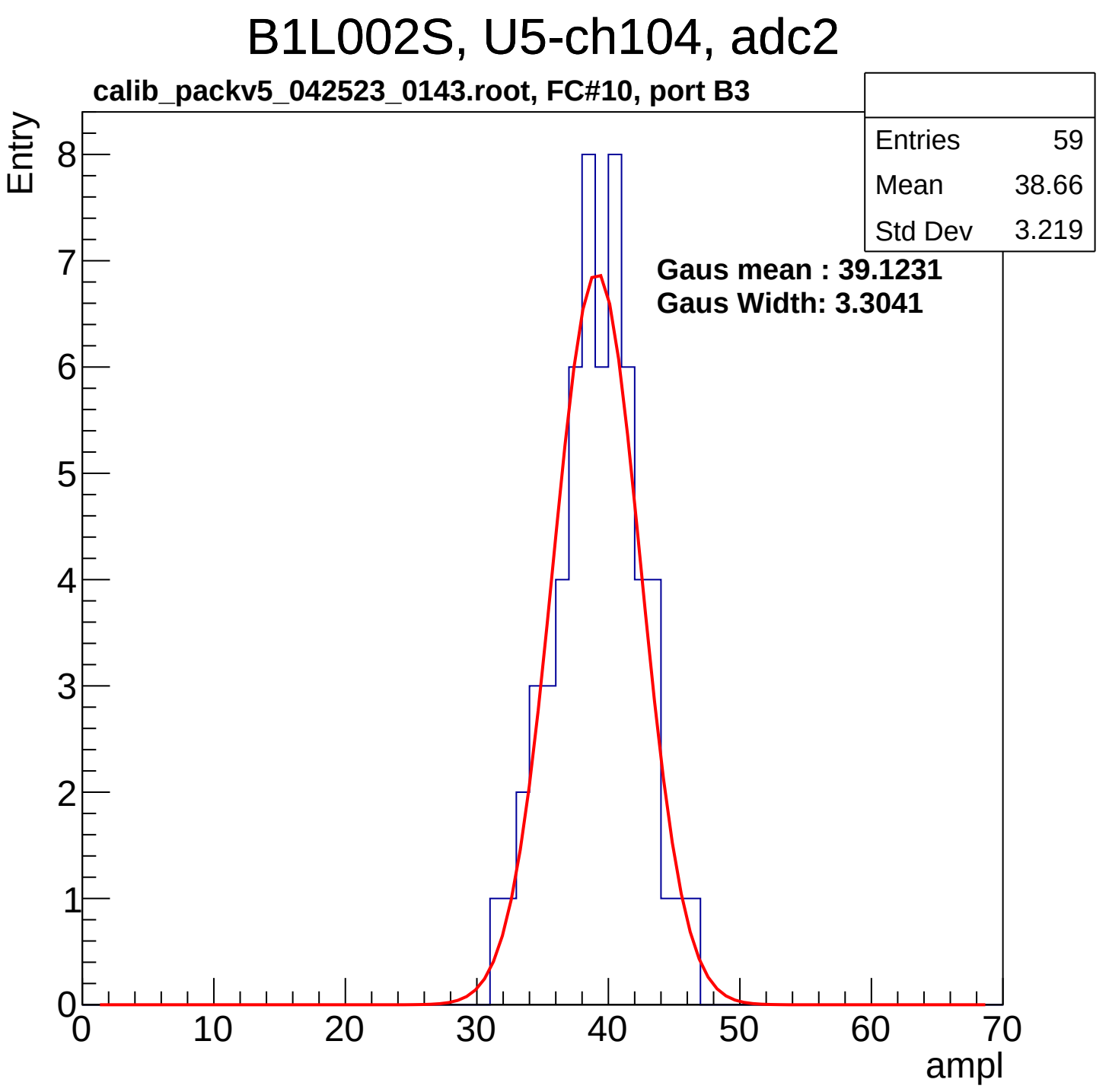
B1L002S, U5-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 38.66
Std Dev 3.219
Gaus mean : 39.1231
Gaus Width: 3.3041
Entry
ampl
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2
3
4
5
6
7
8
0
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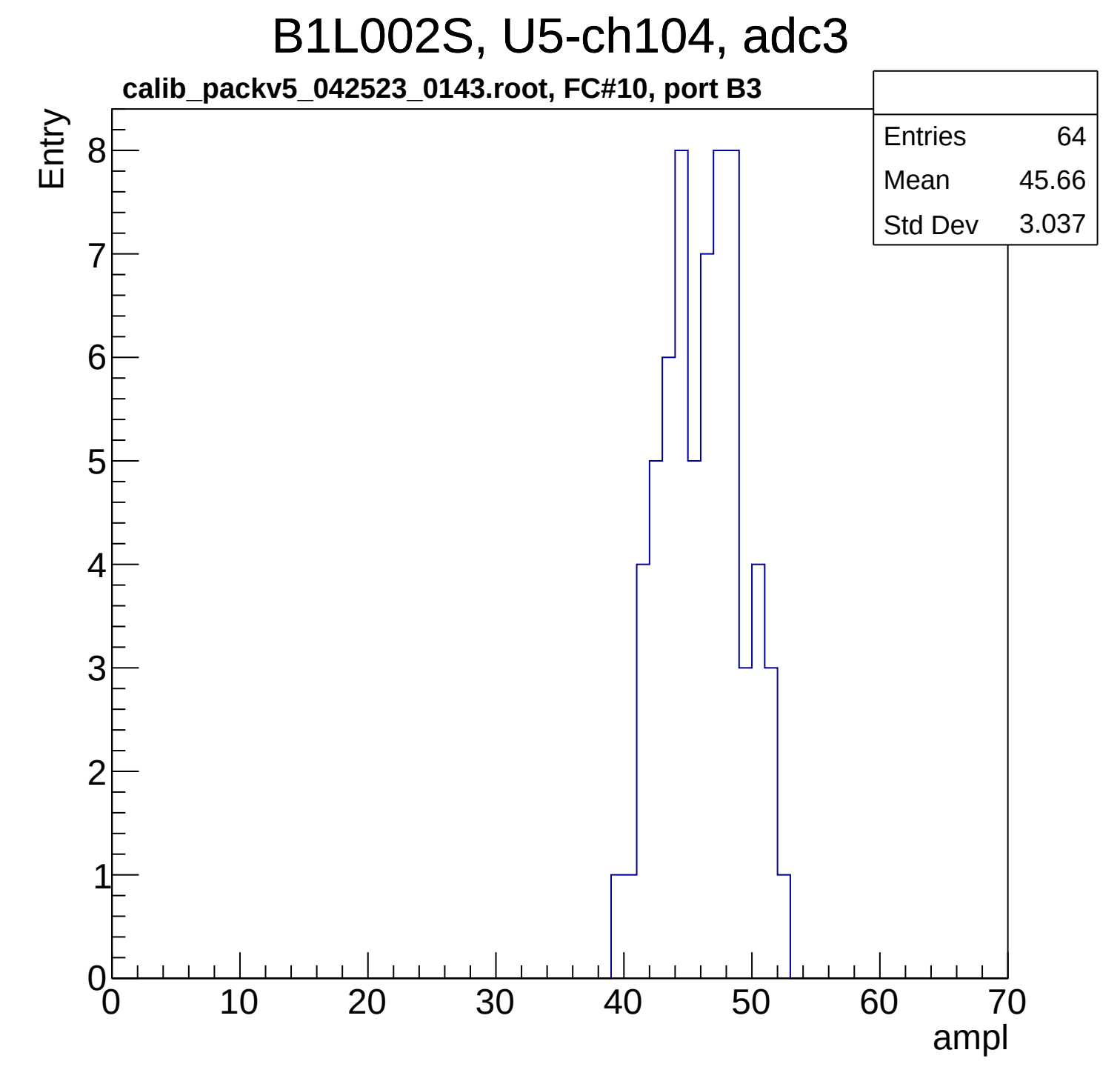
B1L002S, U5-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.66
Std Dev 3.037
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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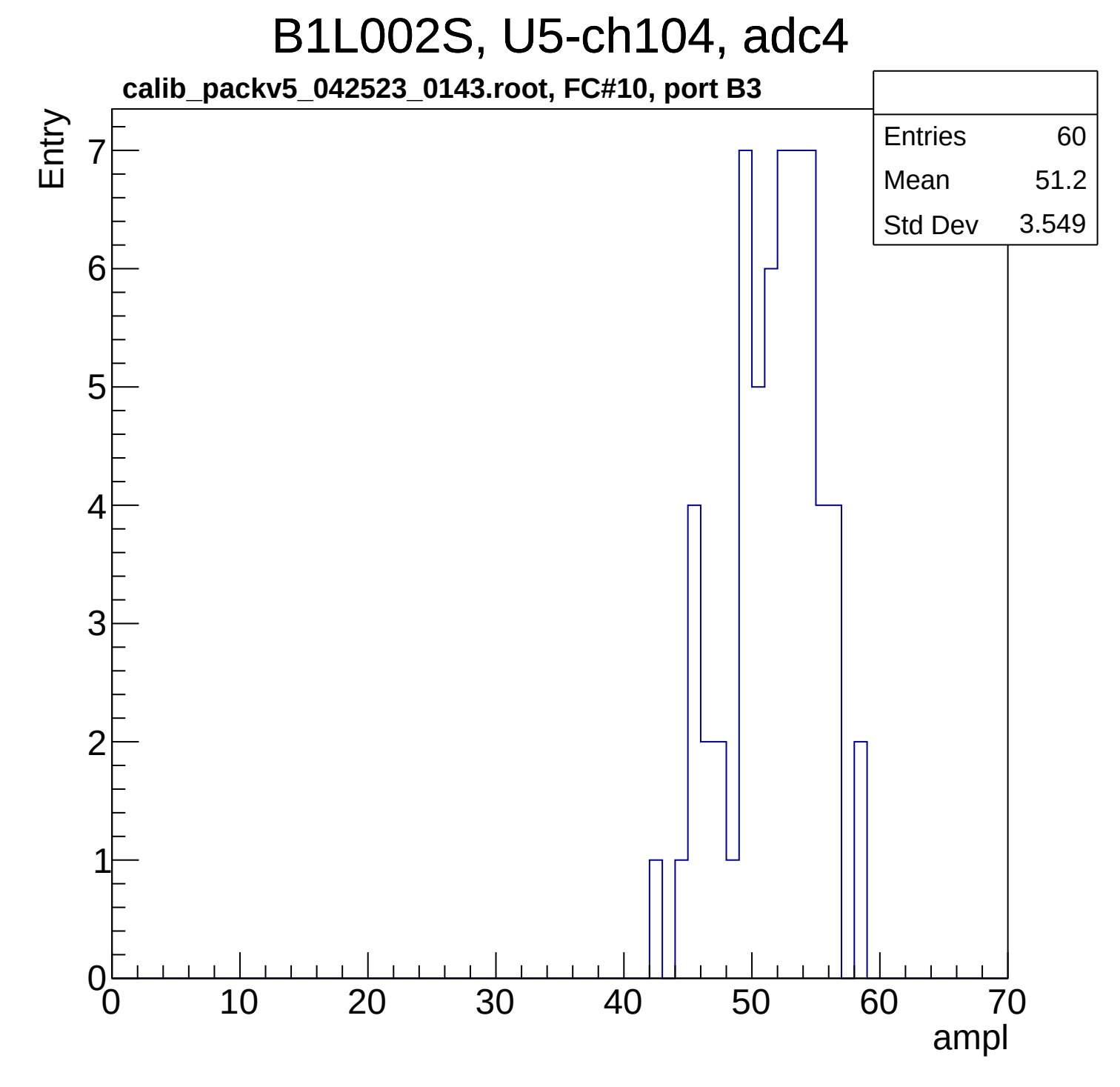
B1L002S, U5-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.2
Std Dev 3.549
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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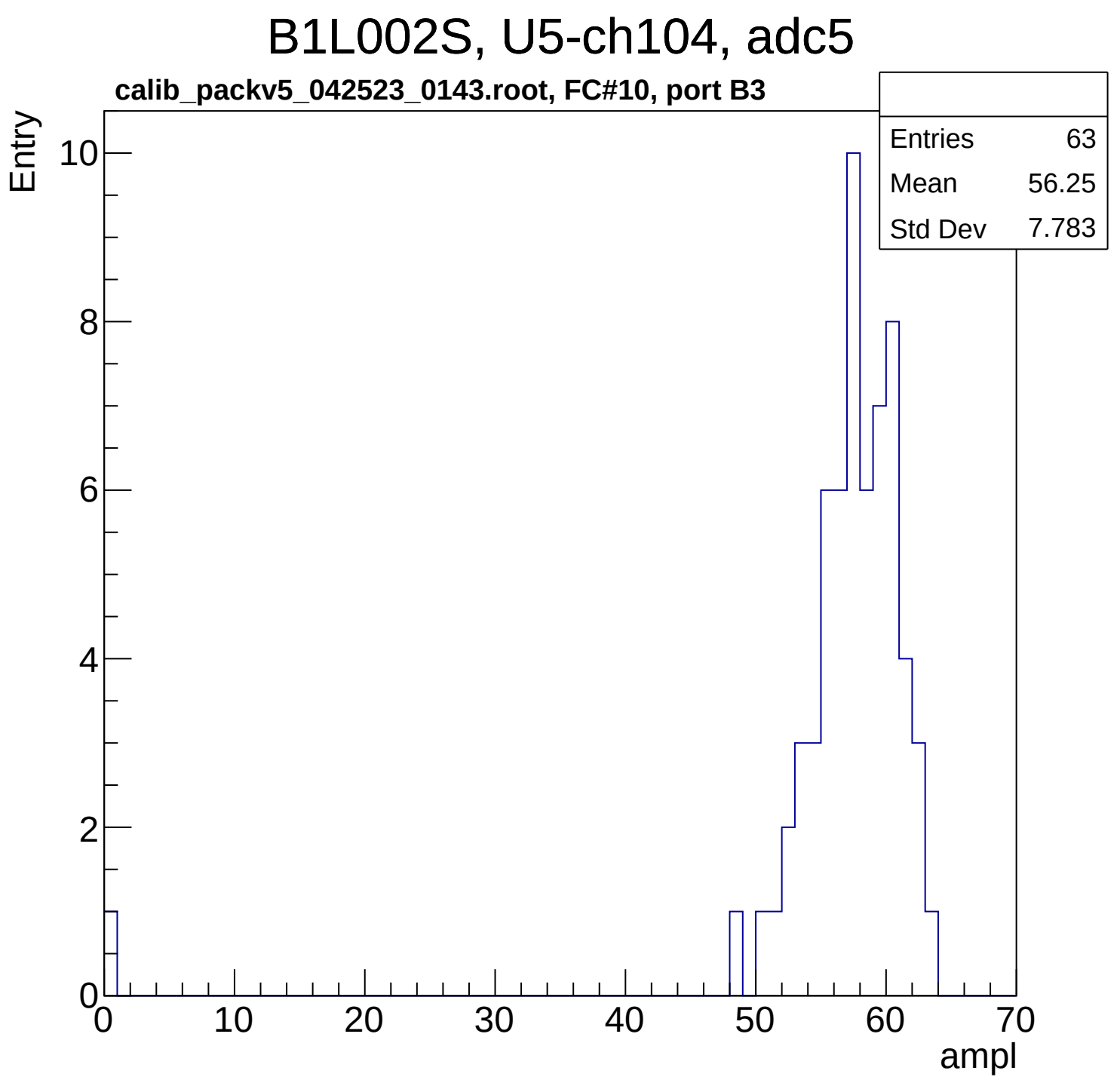
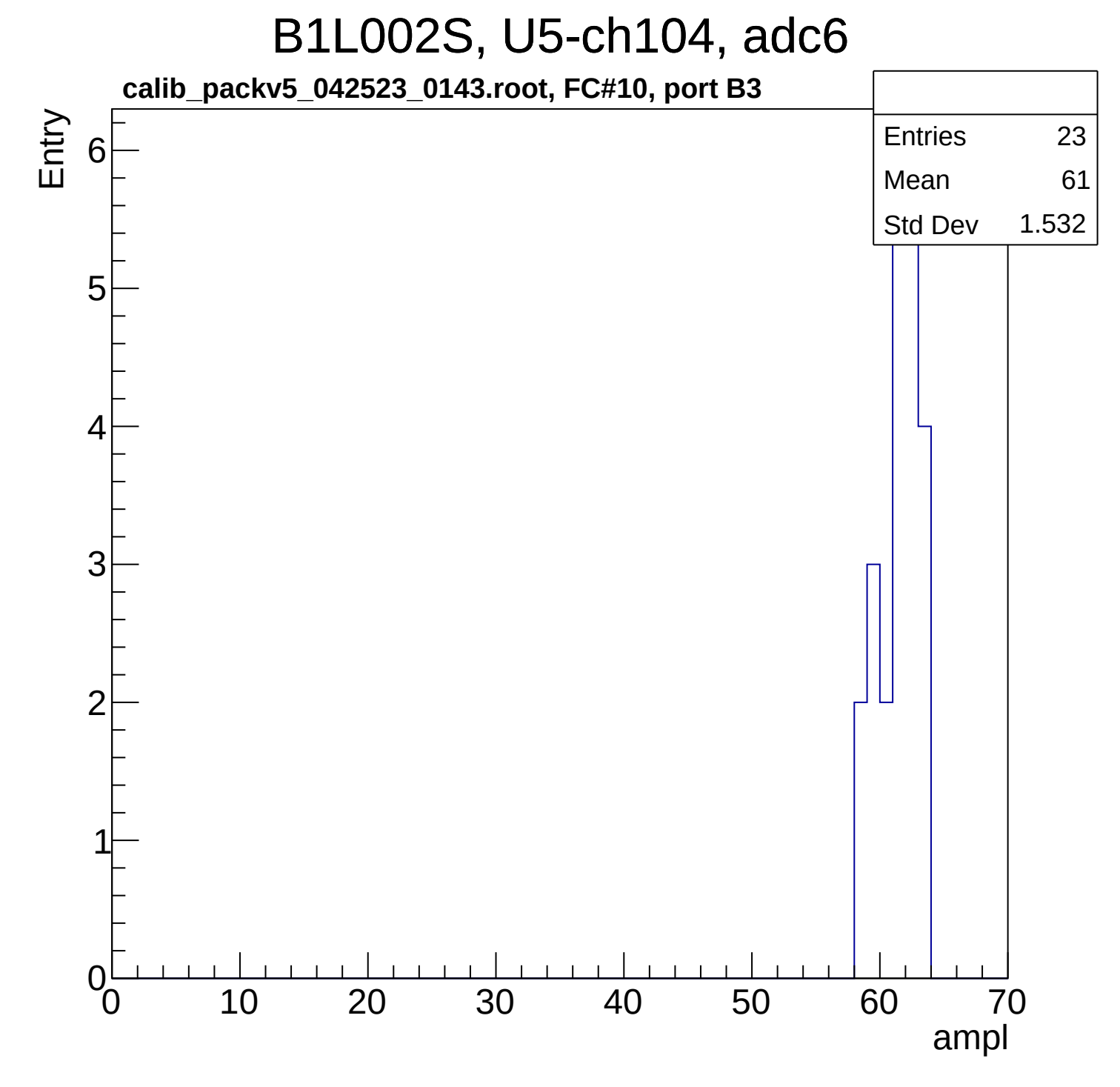
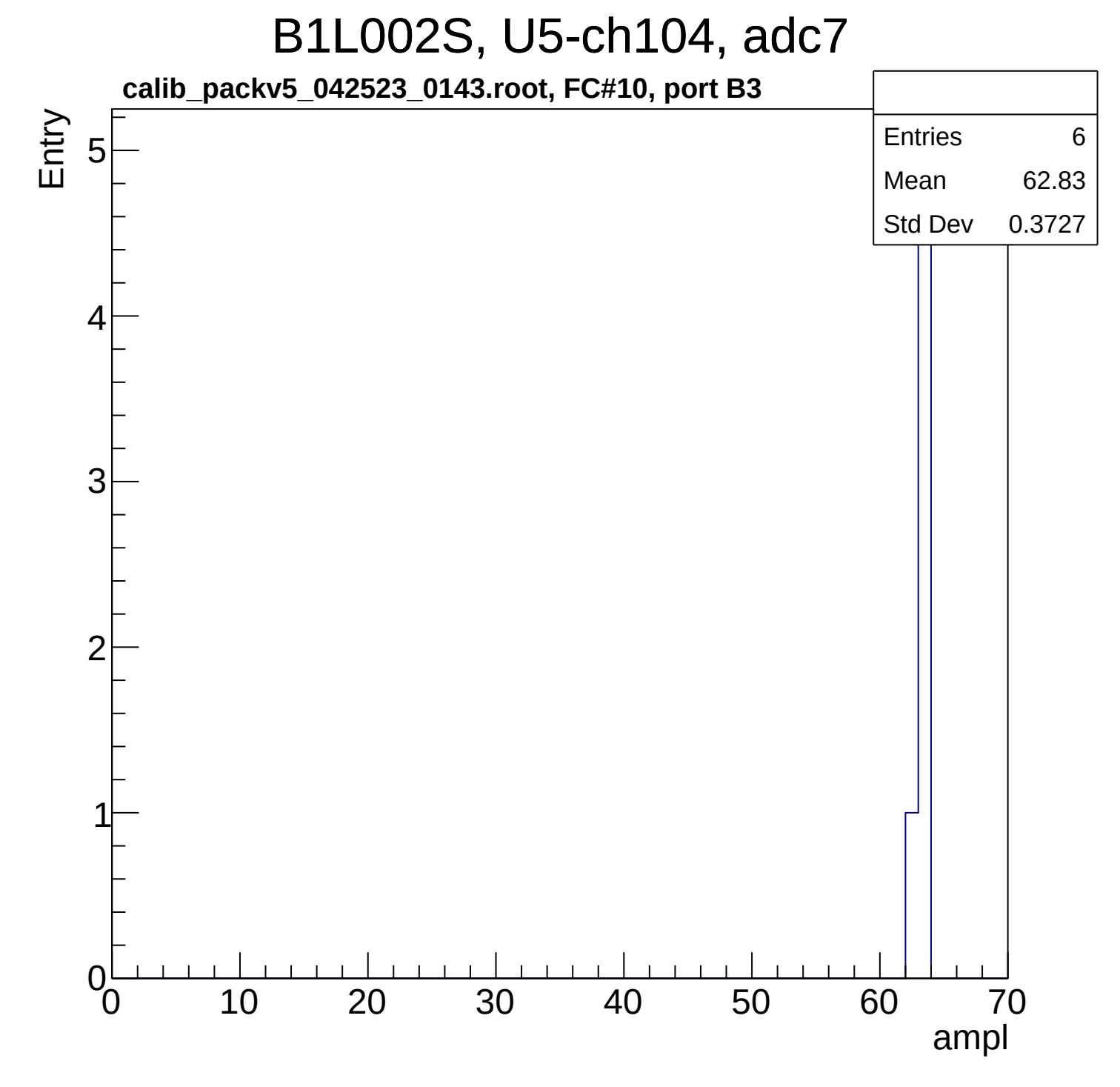

B1L002S, U5-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 56.25
Std Dev 7.783
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61
Std Dev 1.532
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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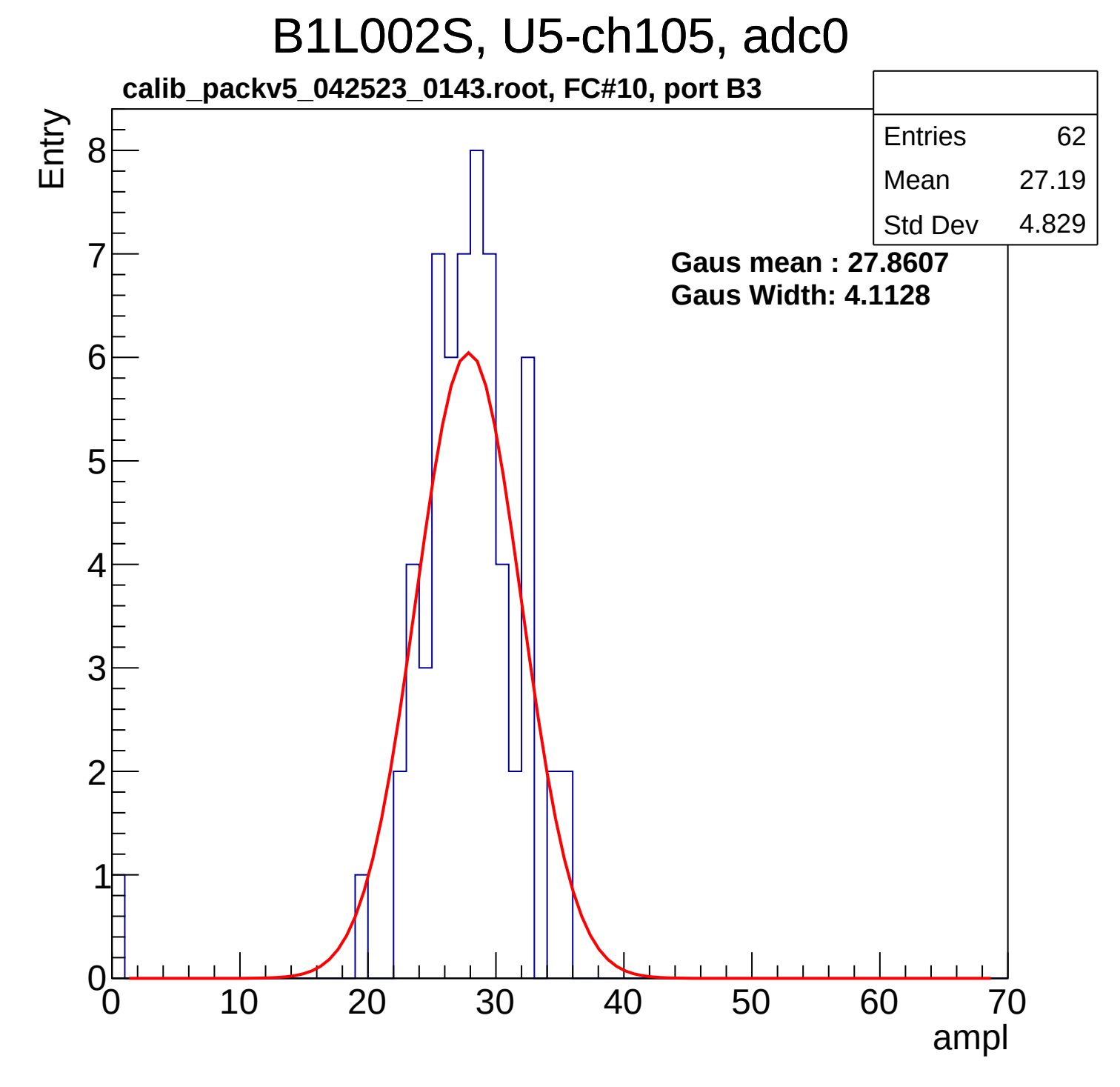
B1L002S, U5-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.19
Std Dev 4.829
Gaus mean : 27.8607
Gaus Width: 4.1128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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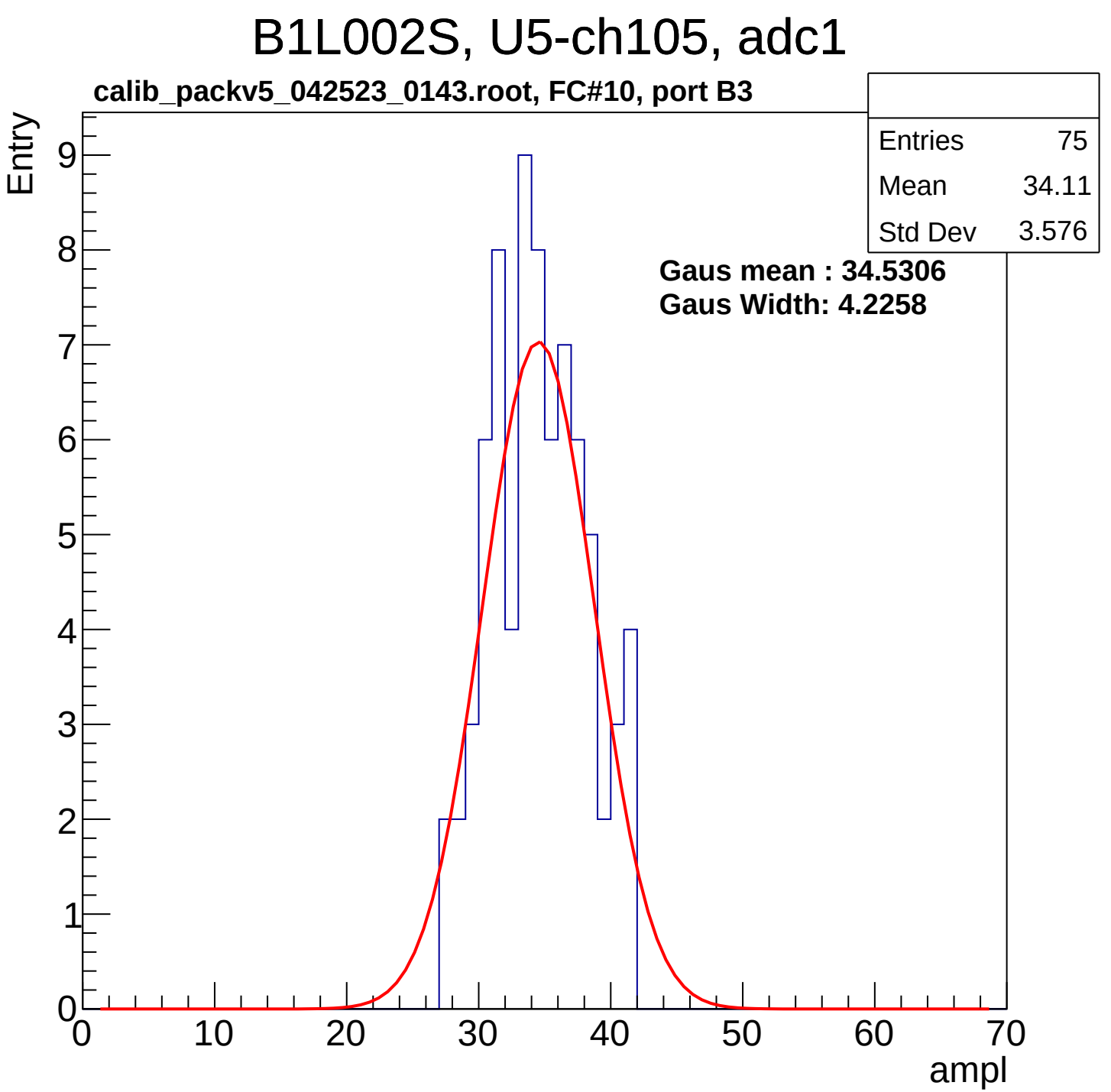
B1L002S, U5-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.11
Std Dev 3.576
Gaus mean : 34.5306
Gaus Width: 4.2258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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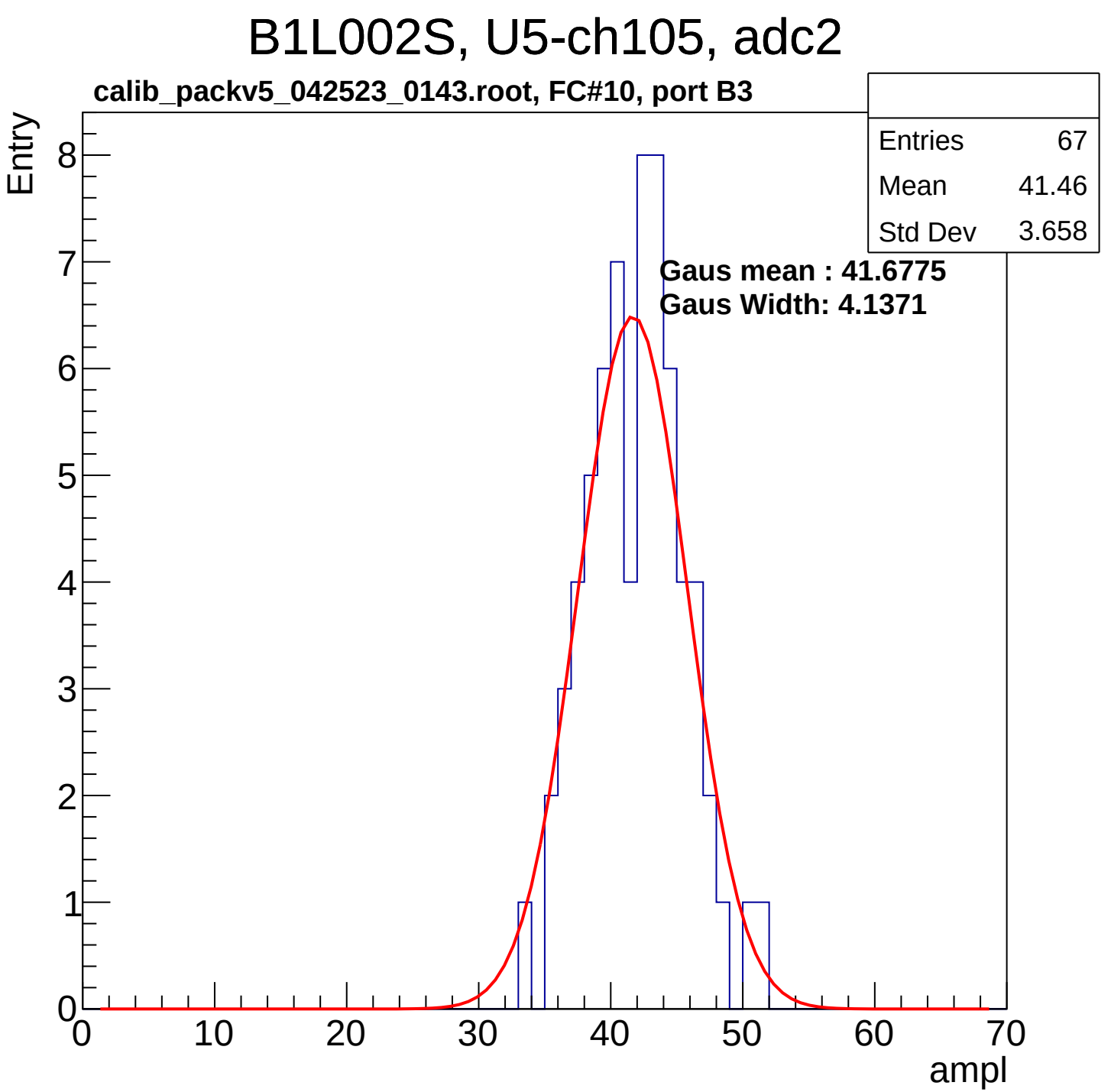
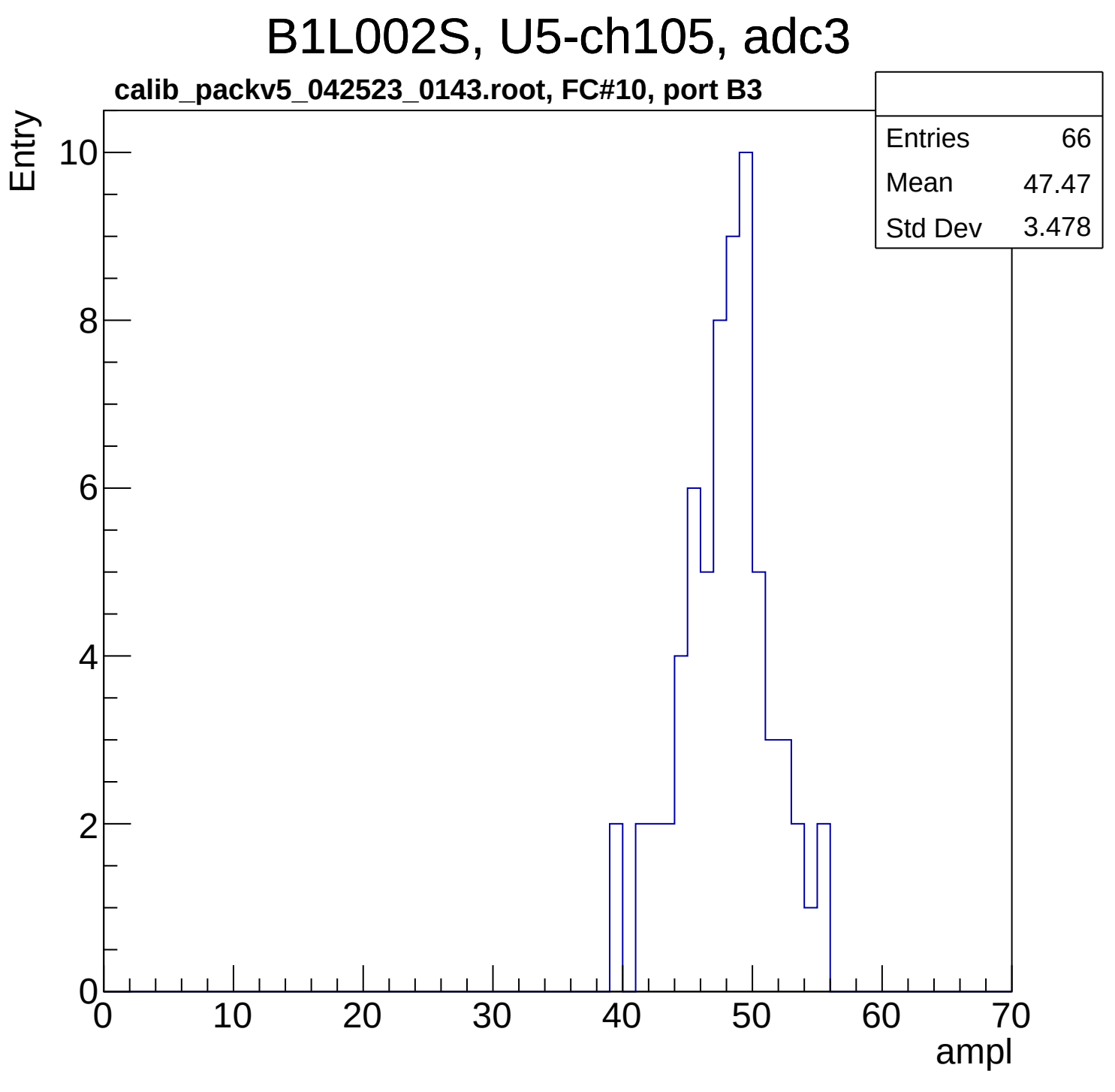
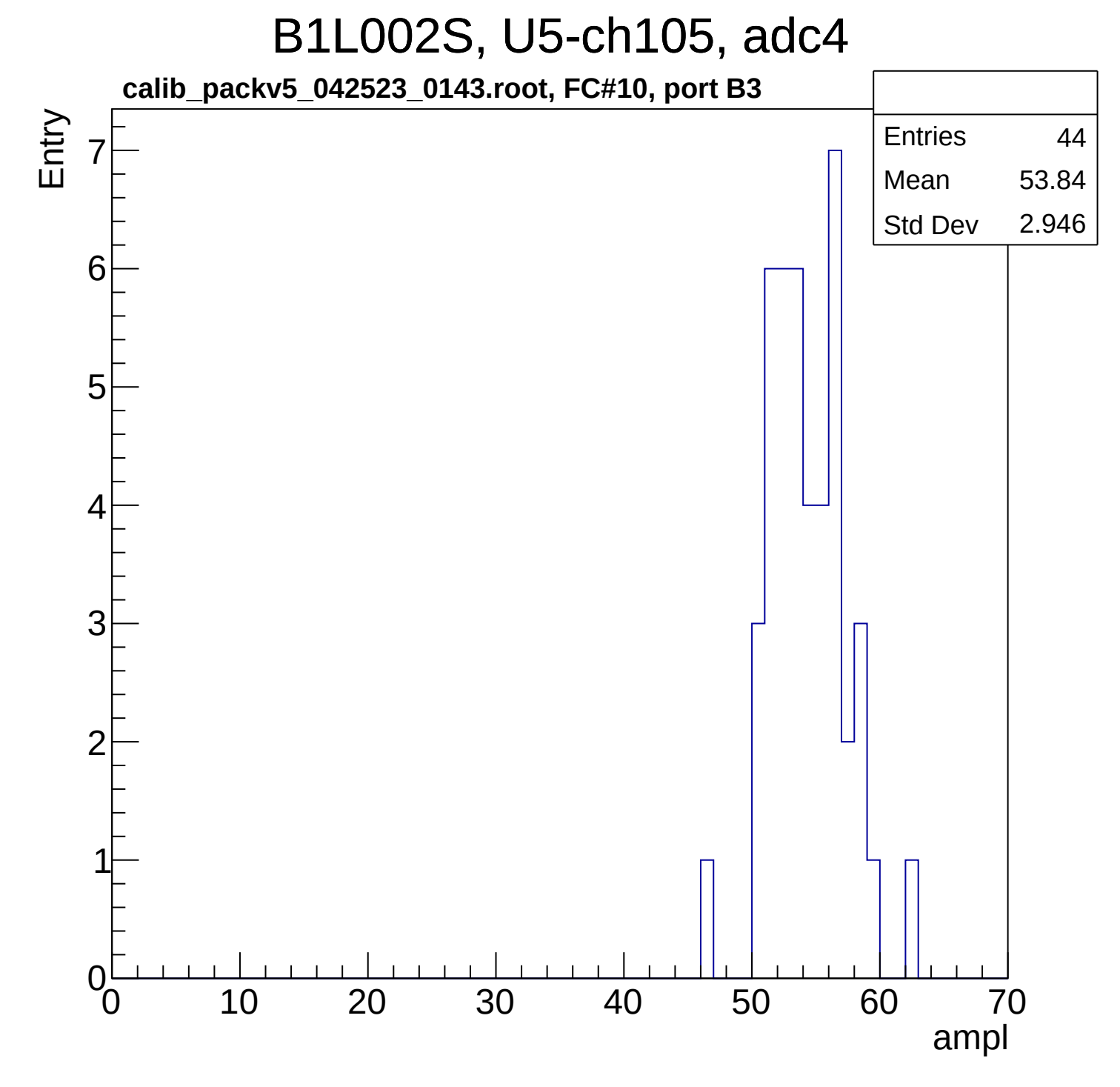
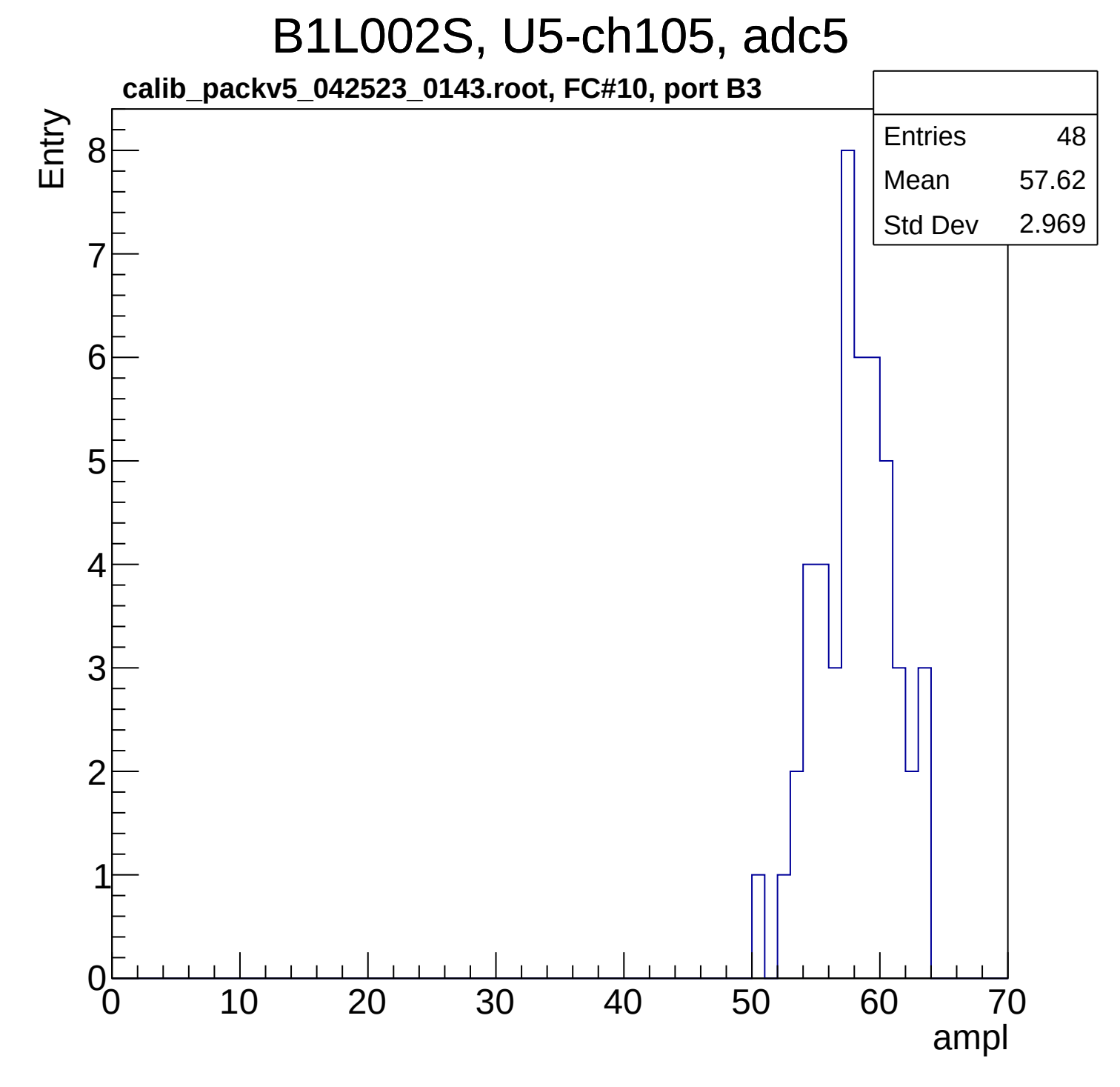
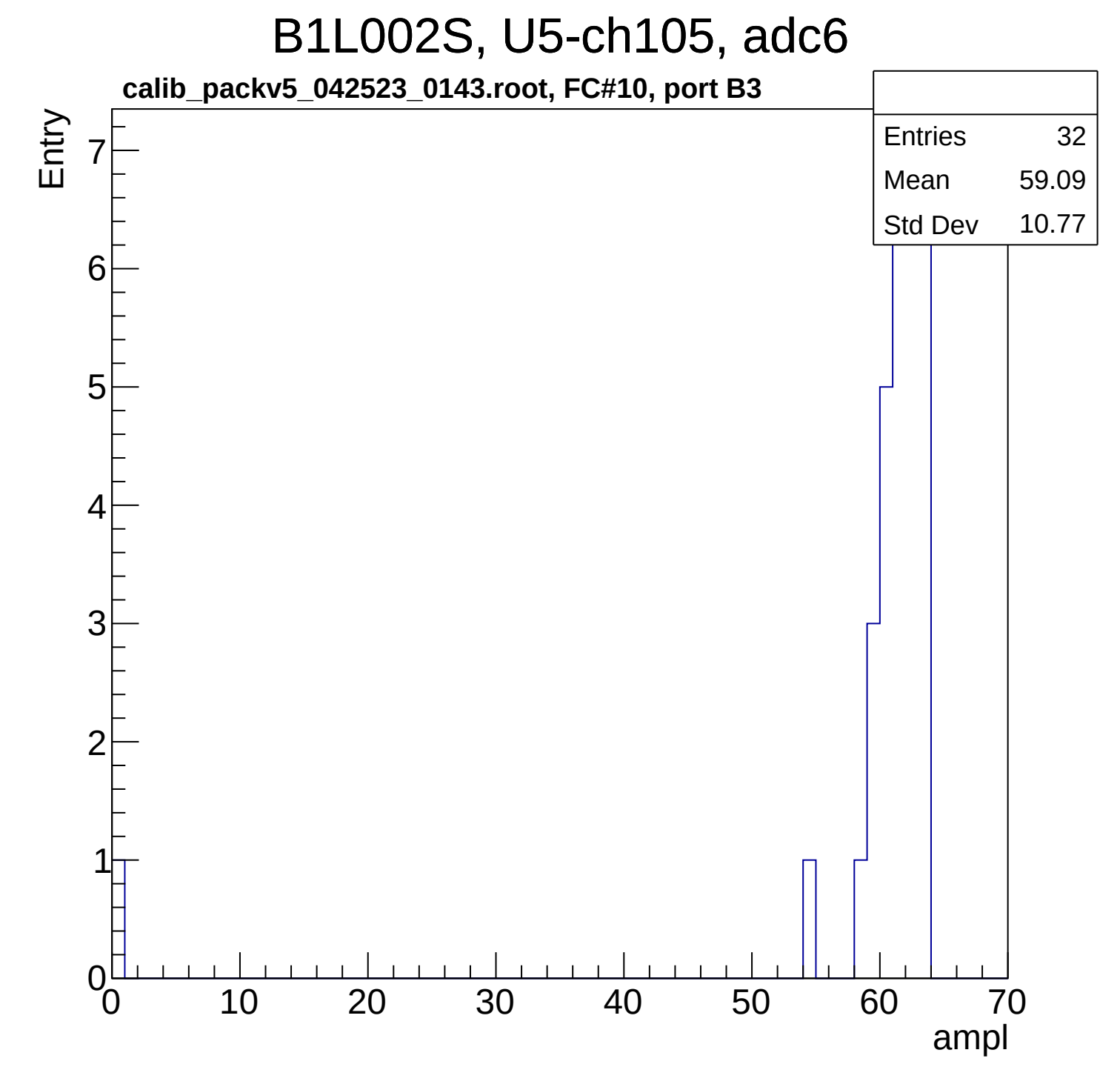
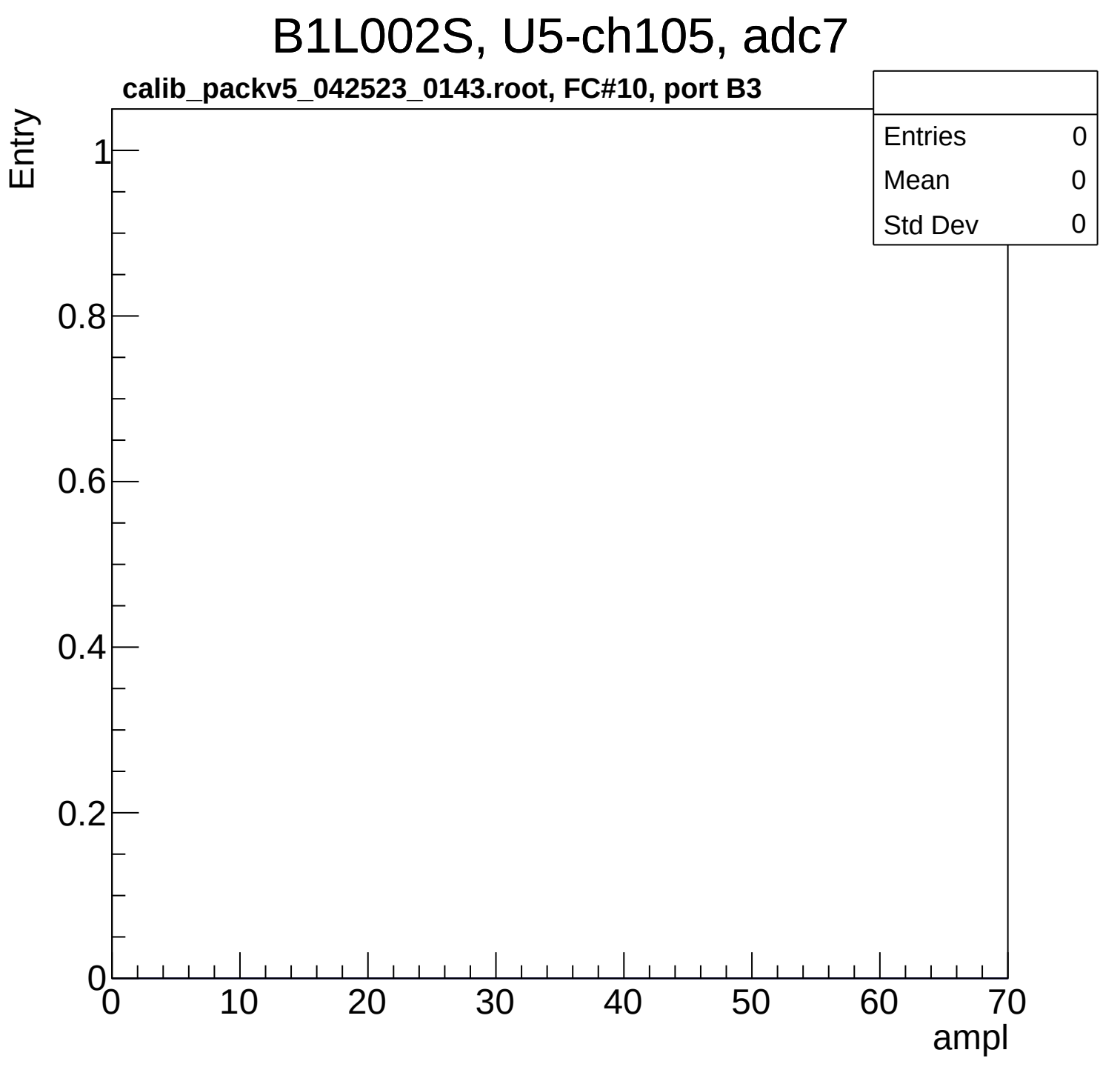

B1L002S, U5-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.46
Std Dev 3.658
Gaus mean : 41.6775
Gaus Width: 4.1371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

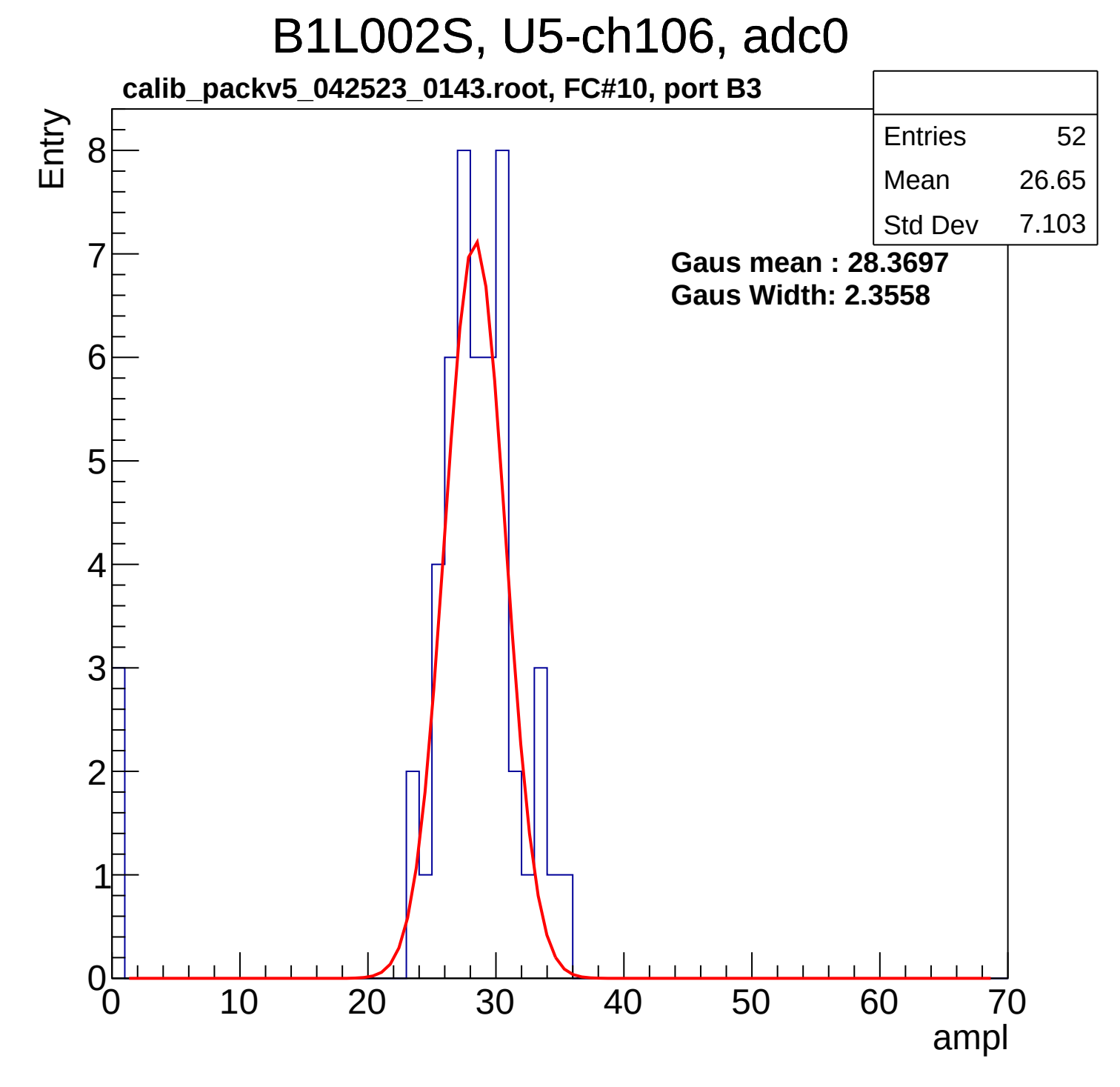

B1L002S, U5-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.47
Std Dev 3.478
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

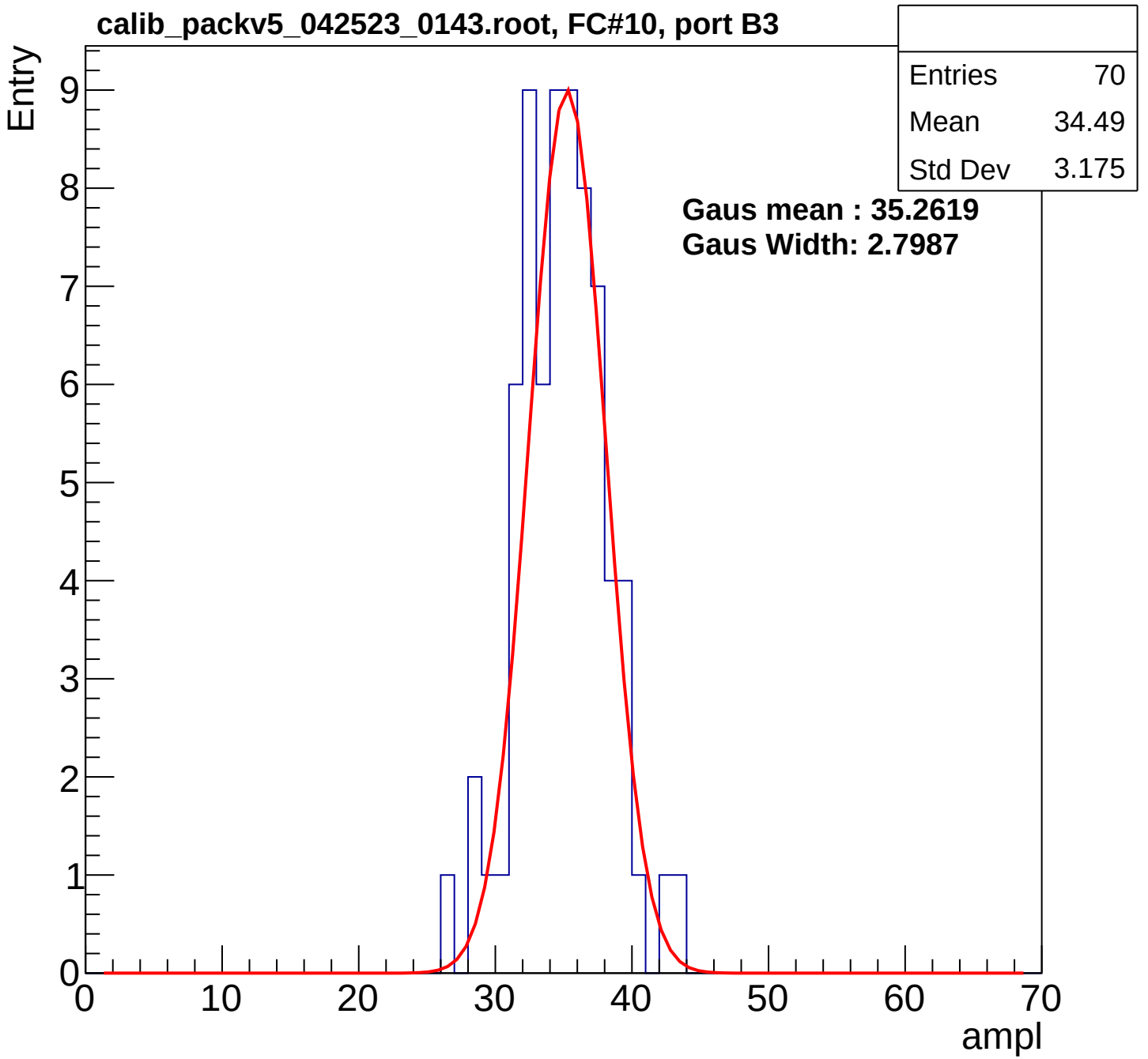

B1L002S, U5-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.84
Std Dev 2.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

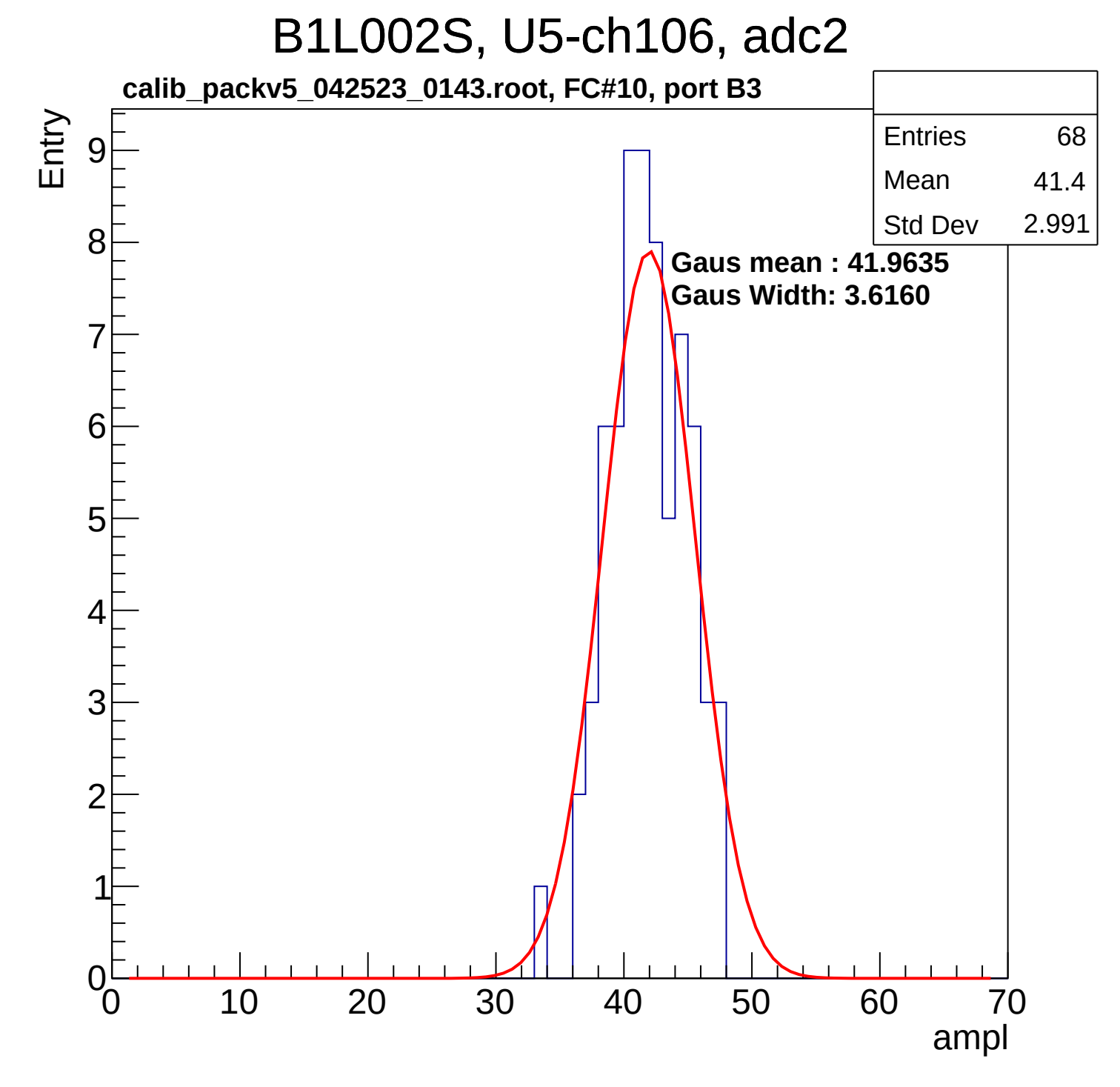

B1L002S, U5-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.62
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

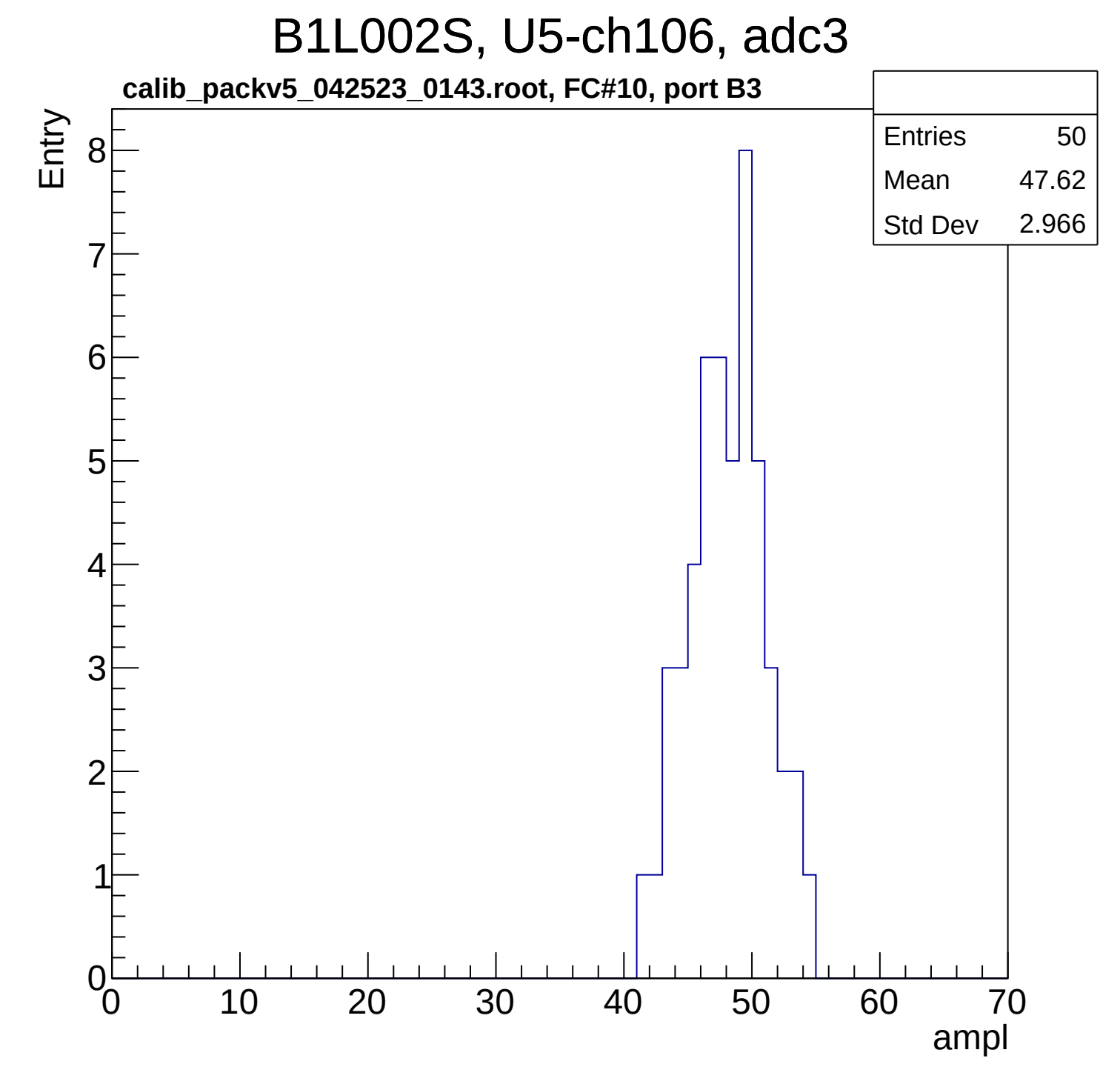

B1L002S, U5-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 59.09
Std Dev 10.77
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

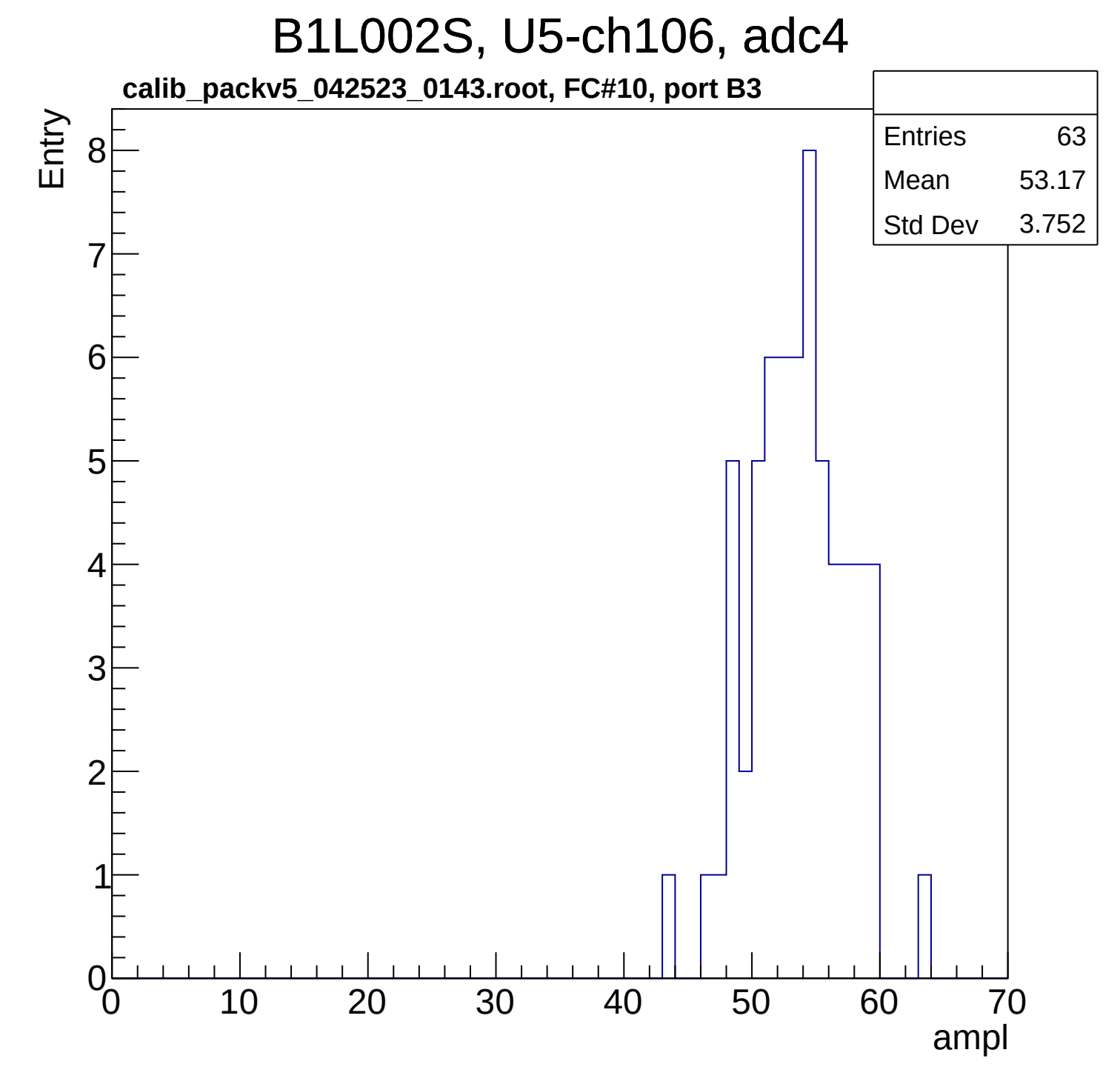

B1L002S, U5-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 26.65
Std Dev 7.103
Gaus mean : 28.3697
Gaus Width: 2.3558
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.49
Std Dev 3.175
Gaus mean : 35.2619
Gaus Width: 2.7987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.4
Std Dev 2.991
Gaus mean : 41.9635
Gaus Width: 3.6160
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.62
Std Dev 2.966
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.17
Std Dev 3.752
Entry
ampl
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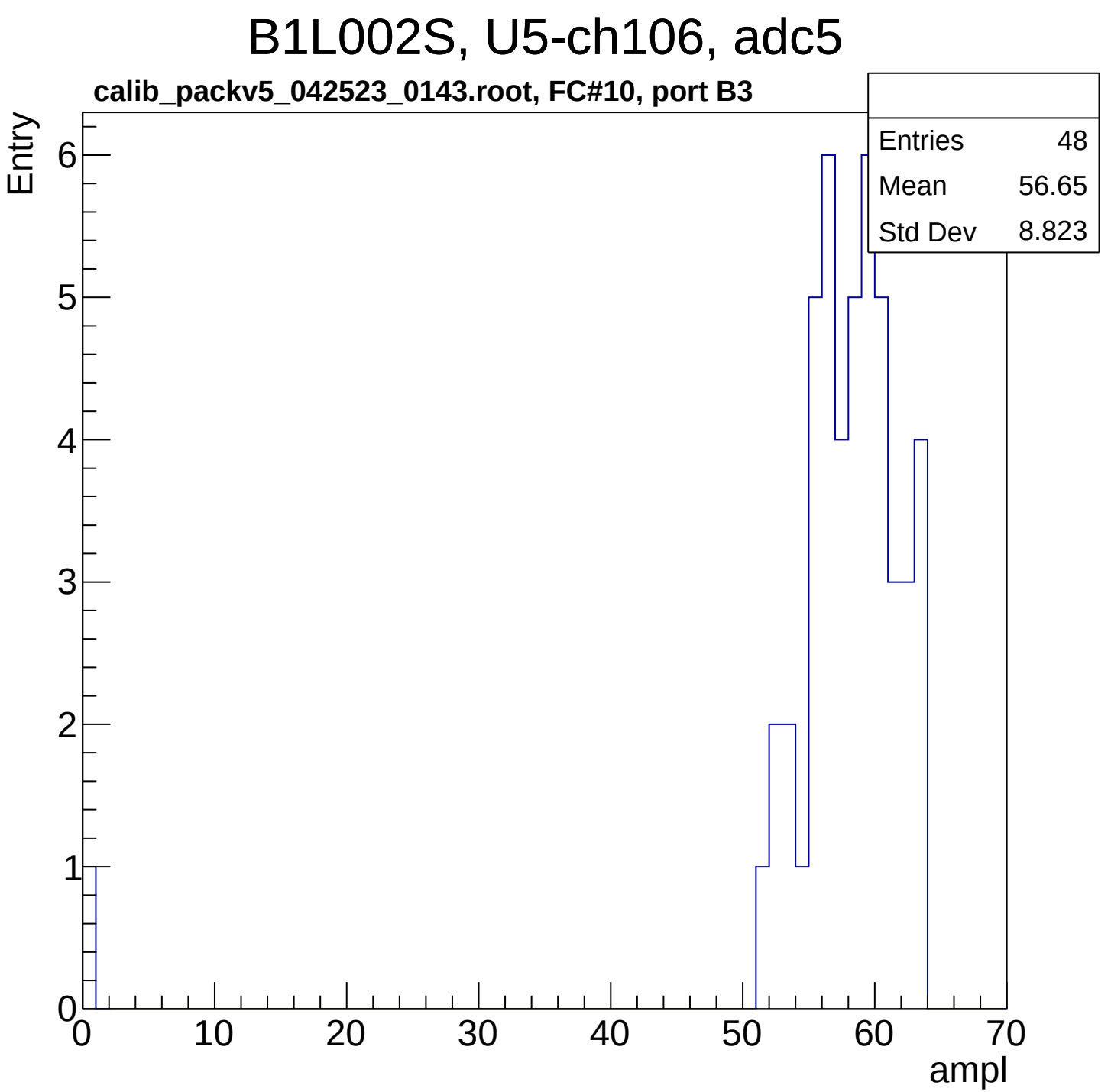
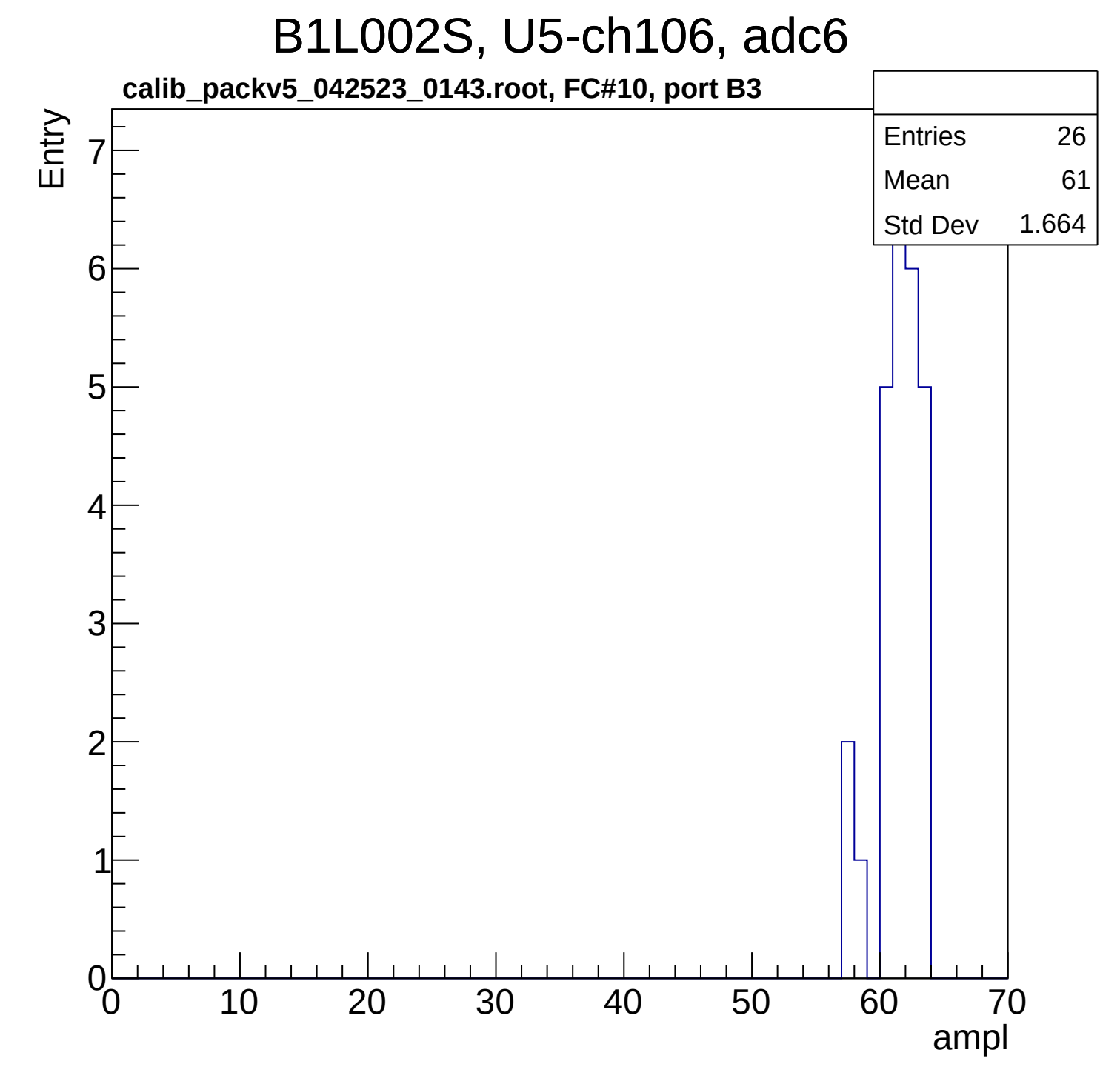
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 56.65
Std Dev 8.823
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61
Std Dev 1.664
Entry
ampl
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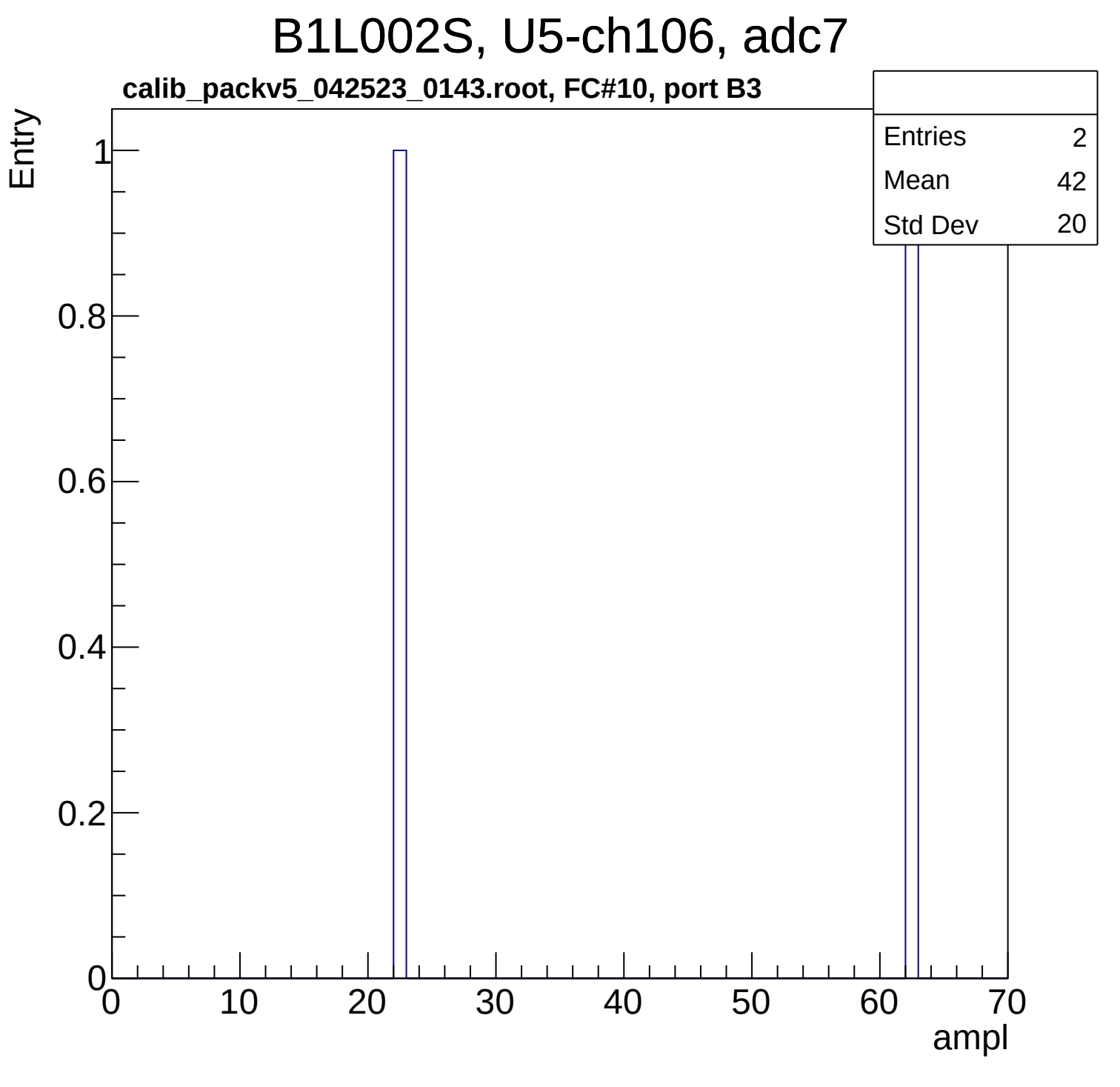
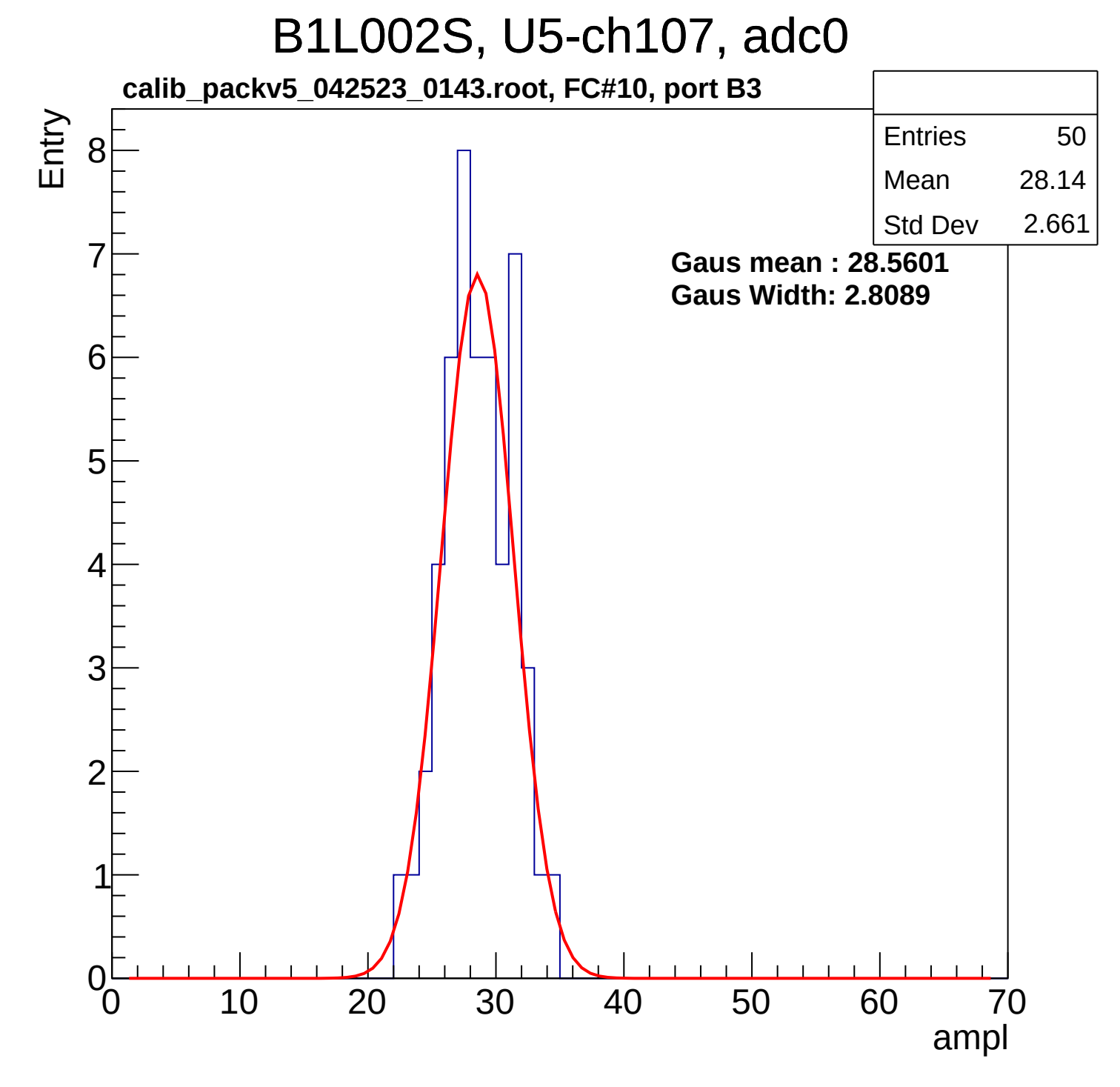
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 28.14
Std Dev 2.661
Gaus mean : 28.5601
Gaus Width: 2.8089
Entry
ampl
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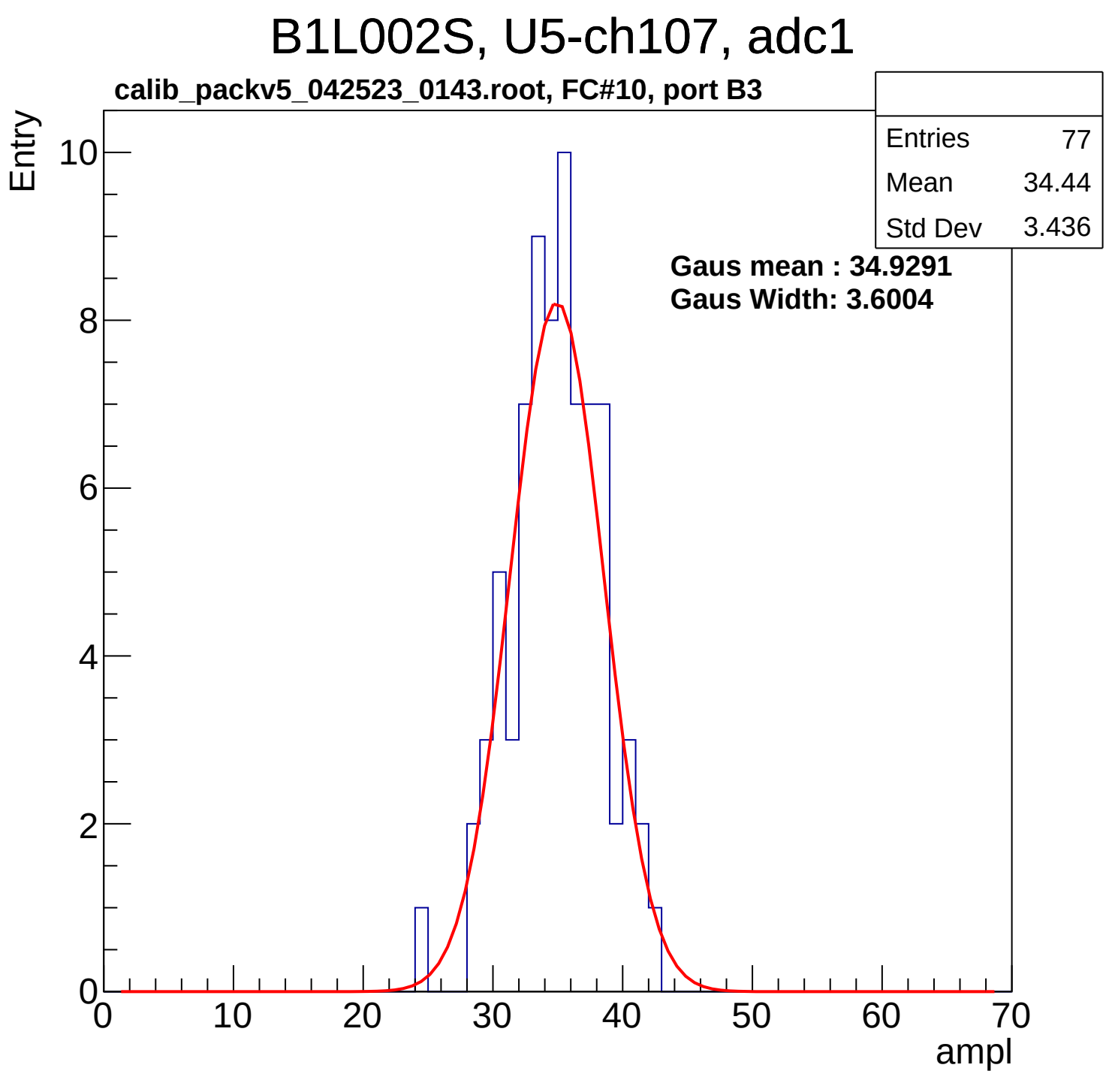
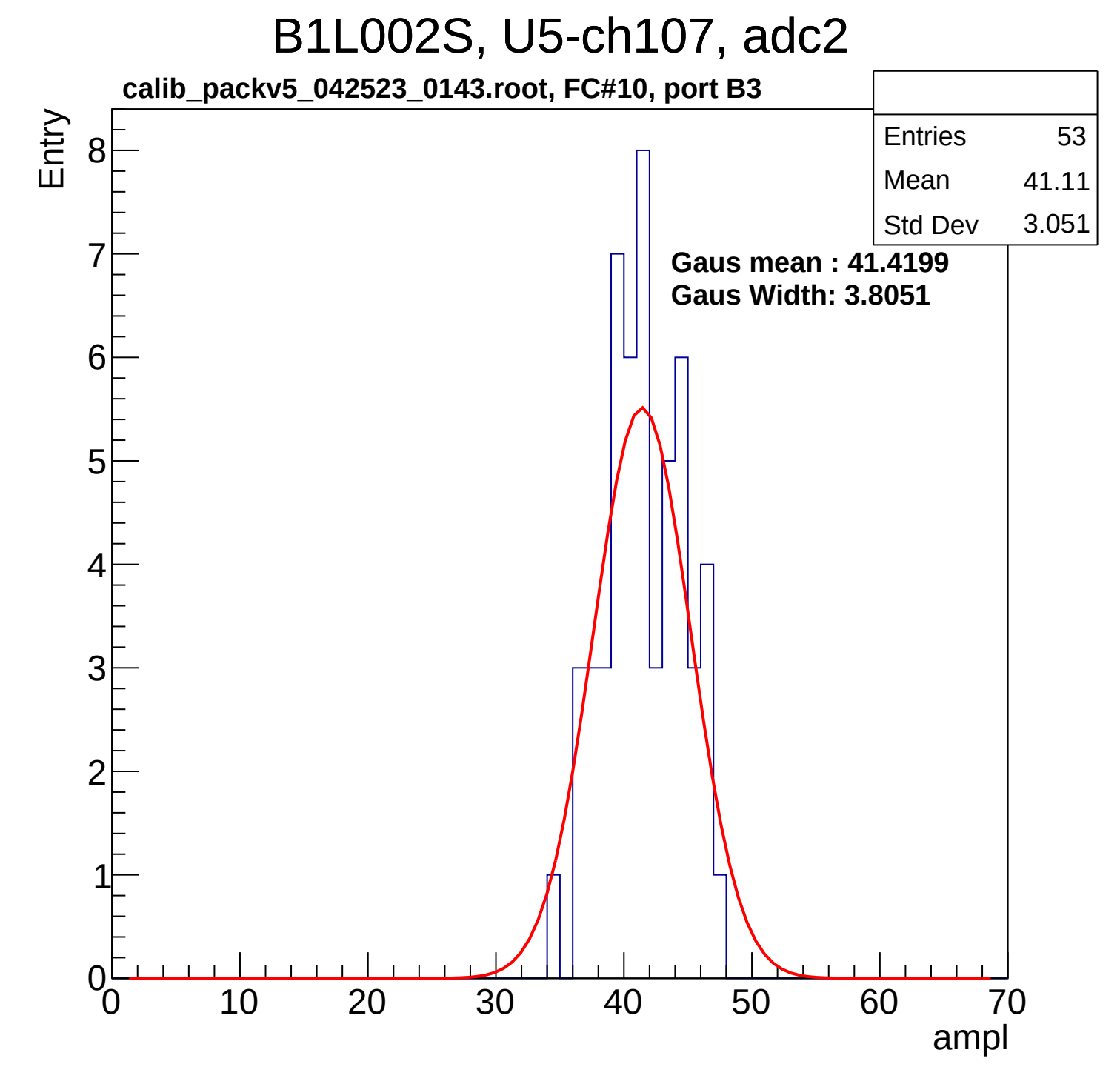
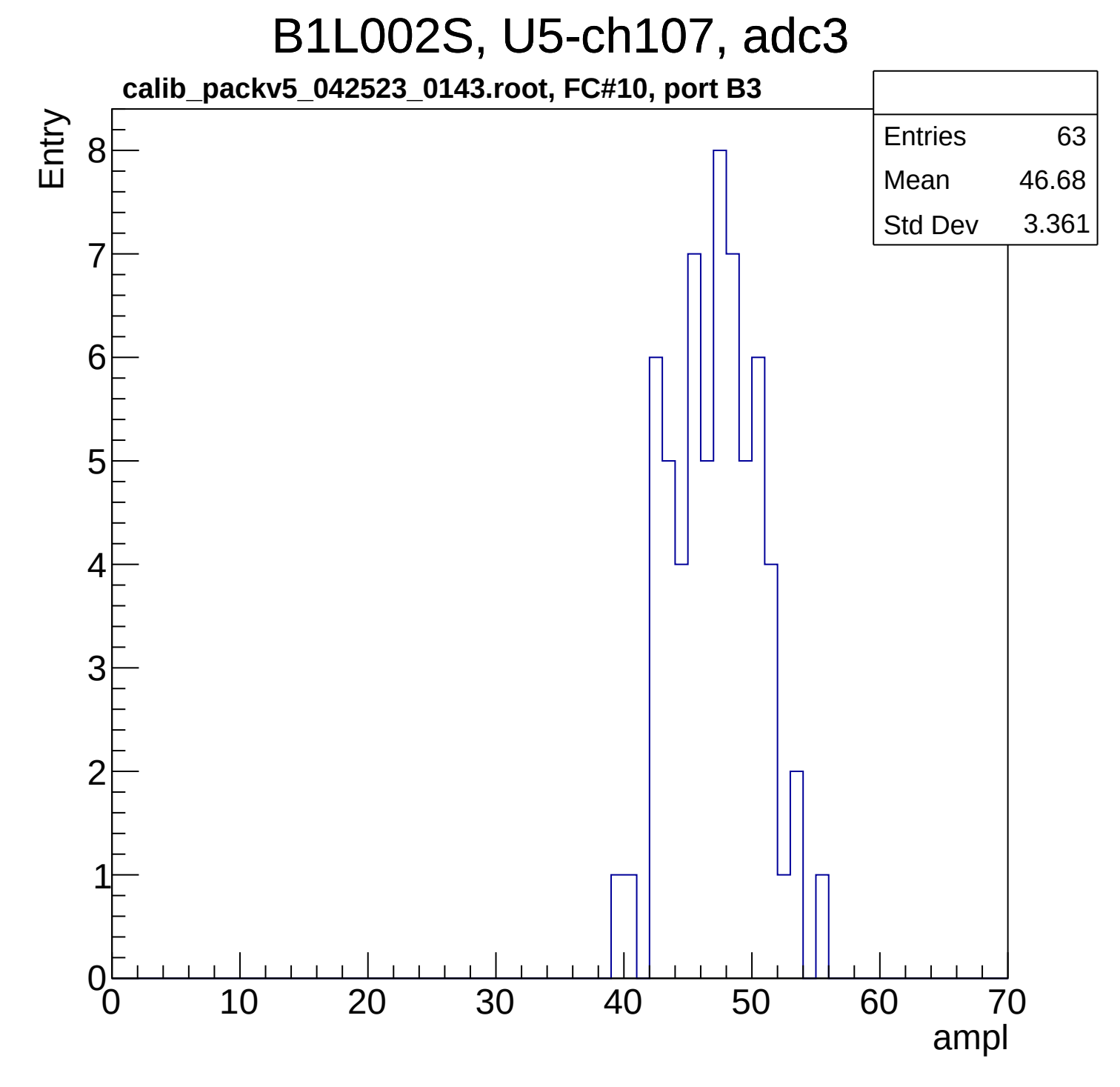
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.44
Std Dev 3.436
Gaus mean : 34.9291
Gaus Width: 3.6004
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.11
Std Dev 3.051
Gaus mean : 41.4199
Gaus Width: 3.8051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.68
Std Dev 3.361
Entry
ampl
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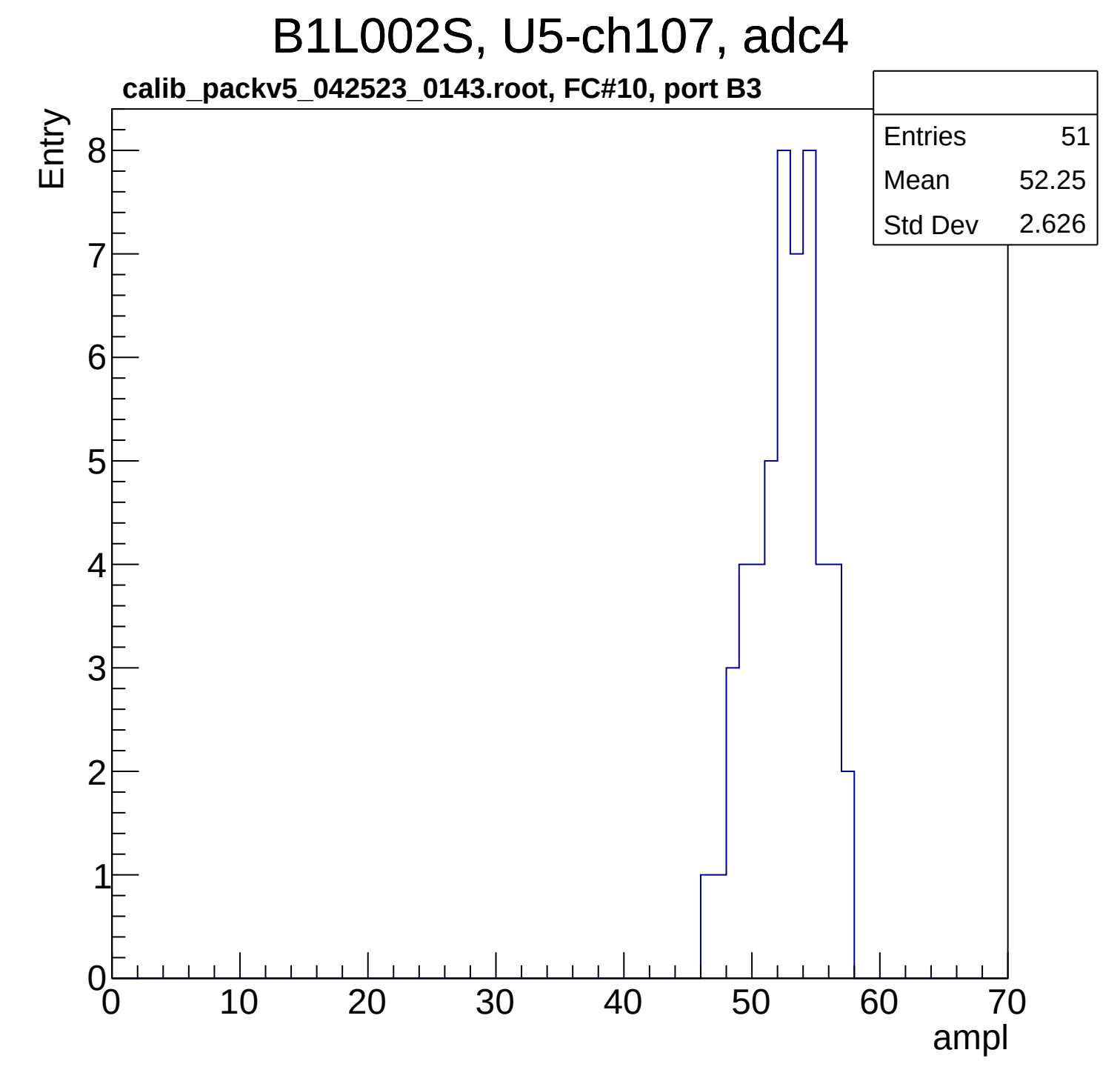
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.25
Std Dev 2.626
Entry
ampl
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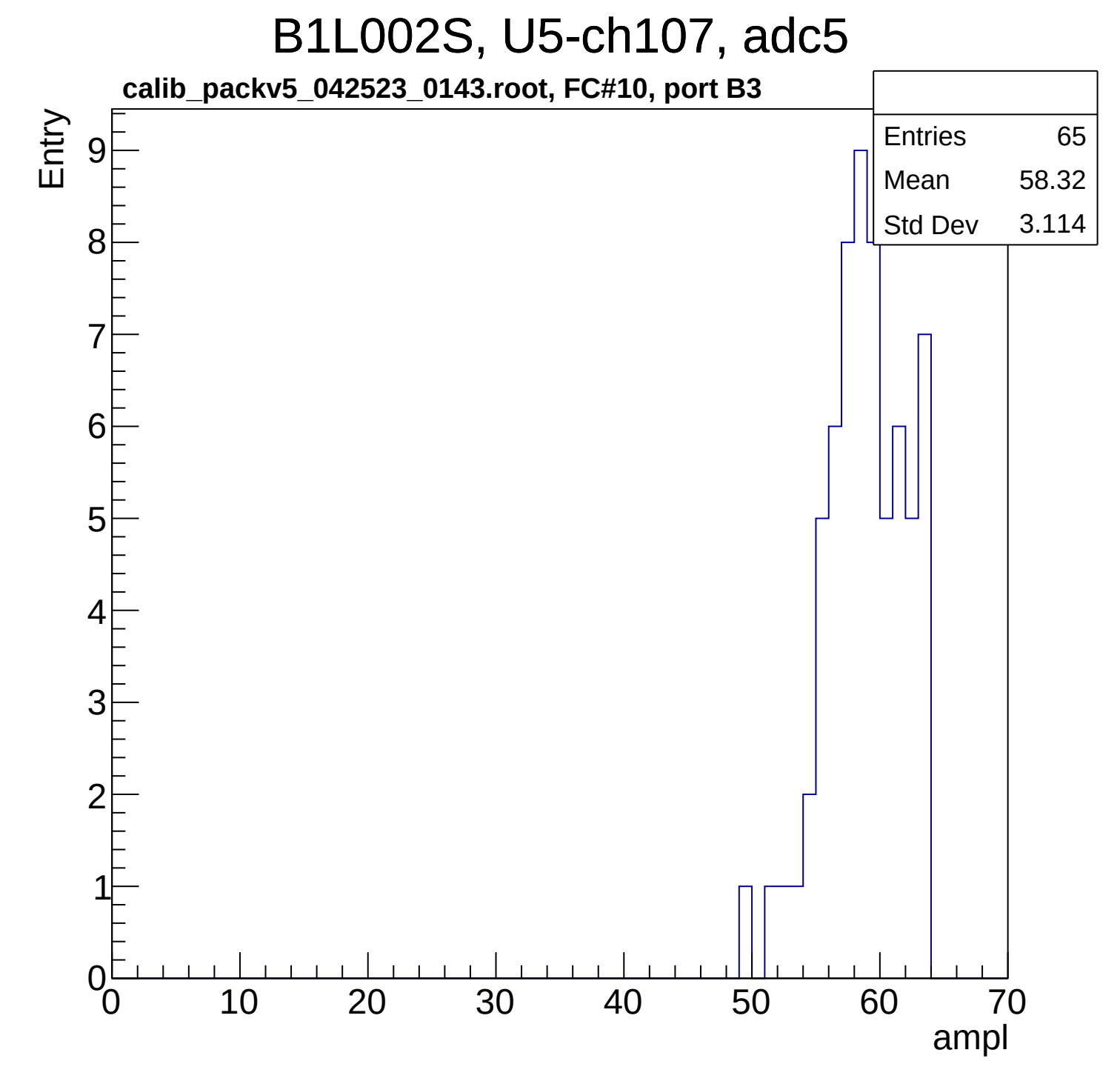
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.32
Std Dev 3.114
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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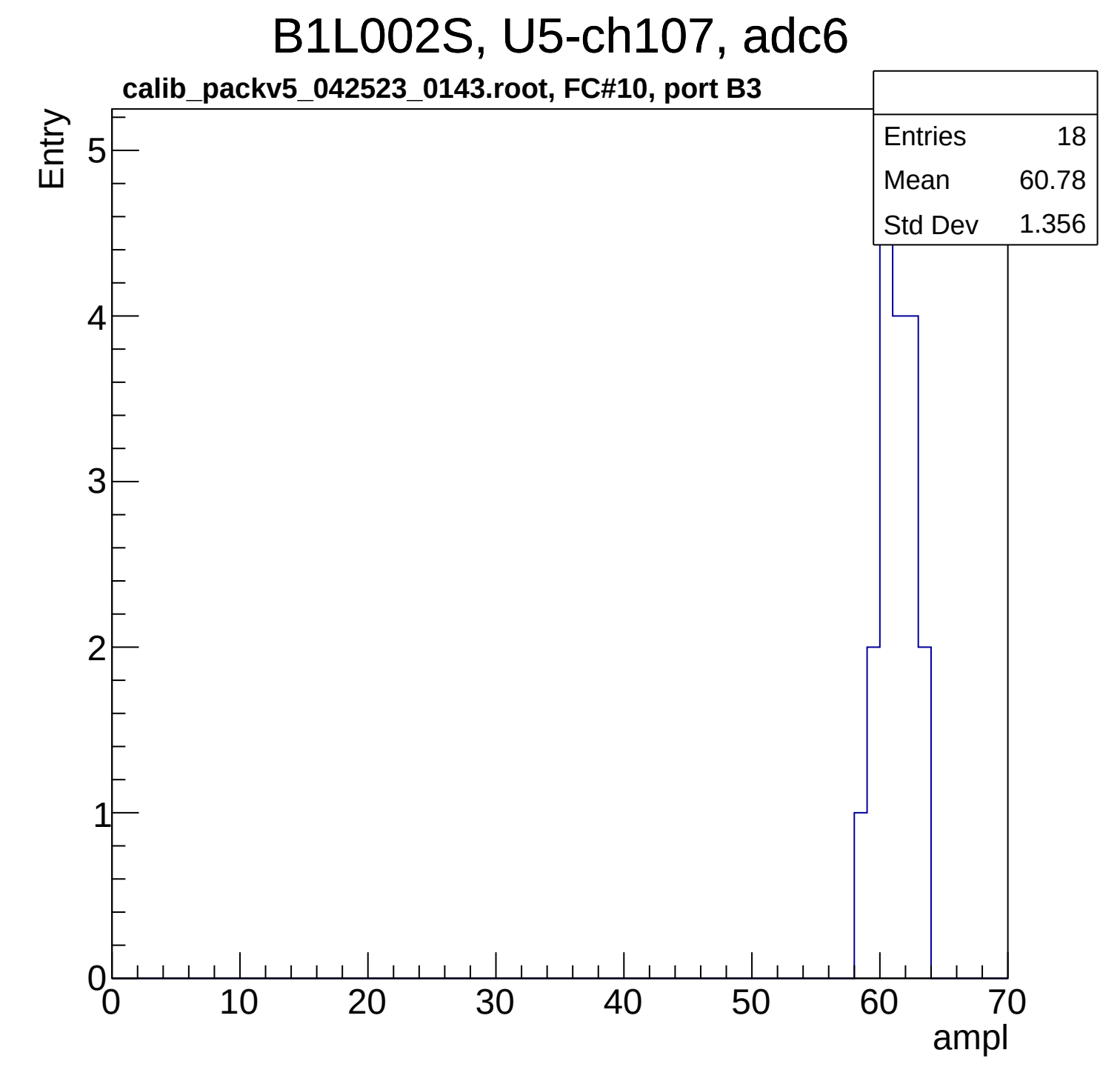
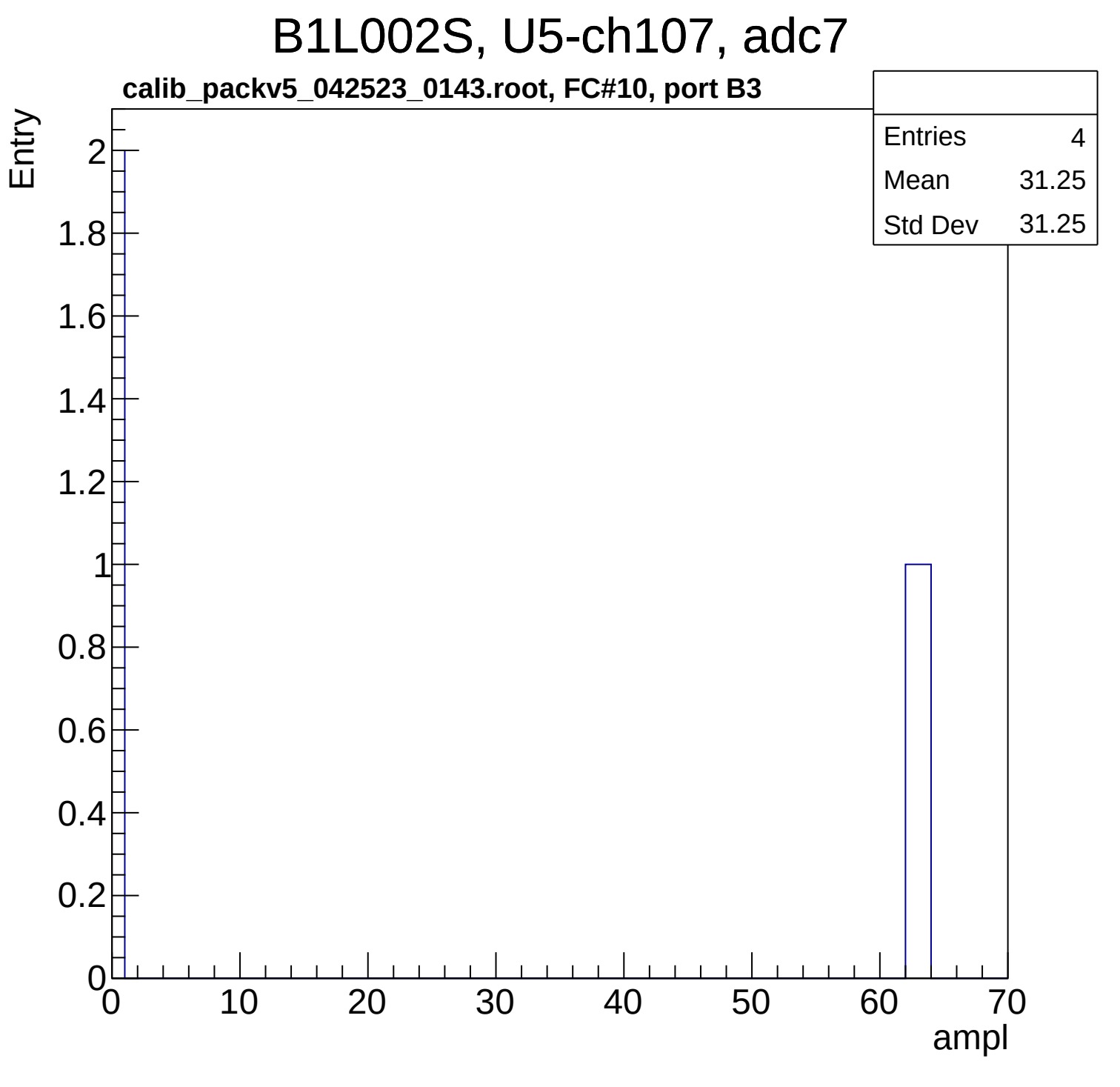
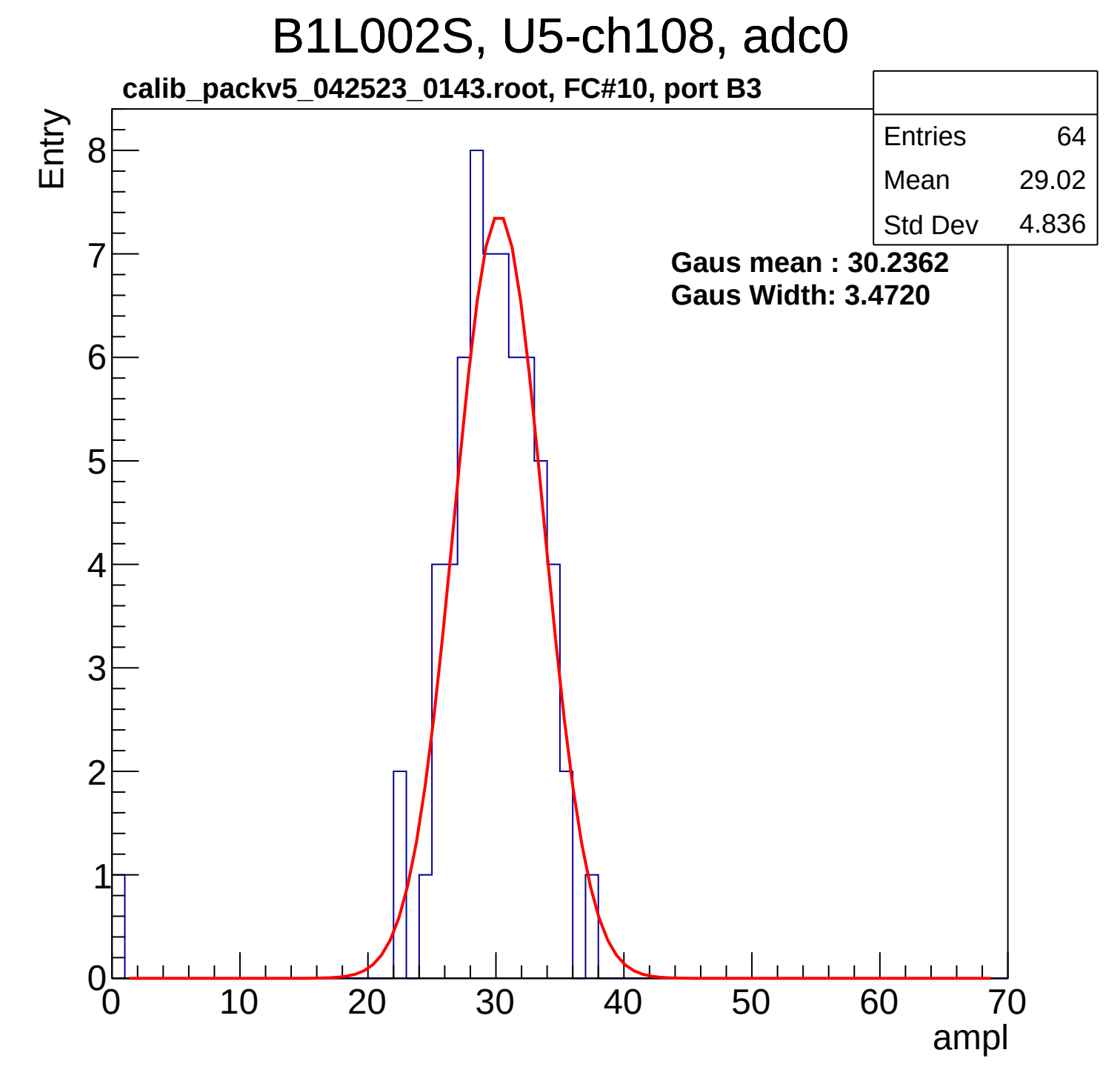

B1L002S, U5-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 60.78
Std Dev 1.356
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 29.02
Std Dev 4.836
Gaus mean : 30.2362
Gaus Width: 3.4720
Entry
ampl
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2
3
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5
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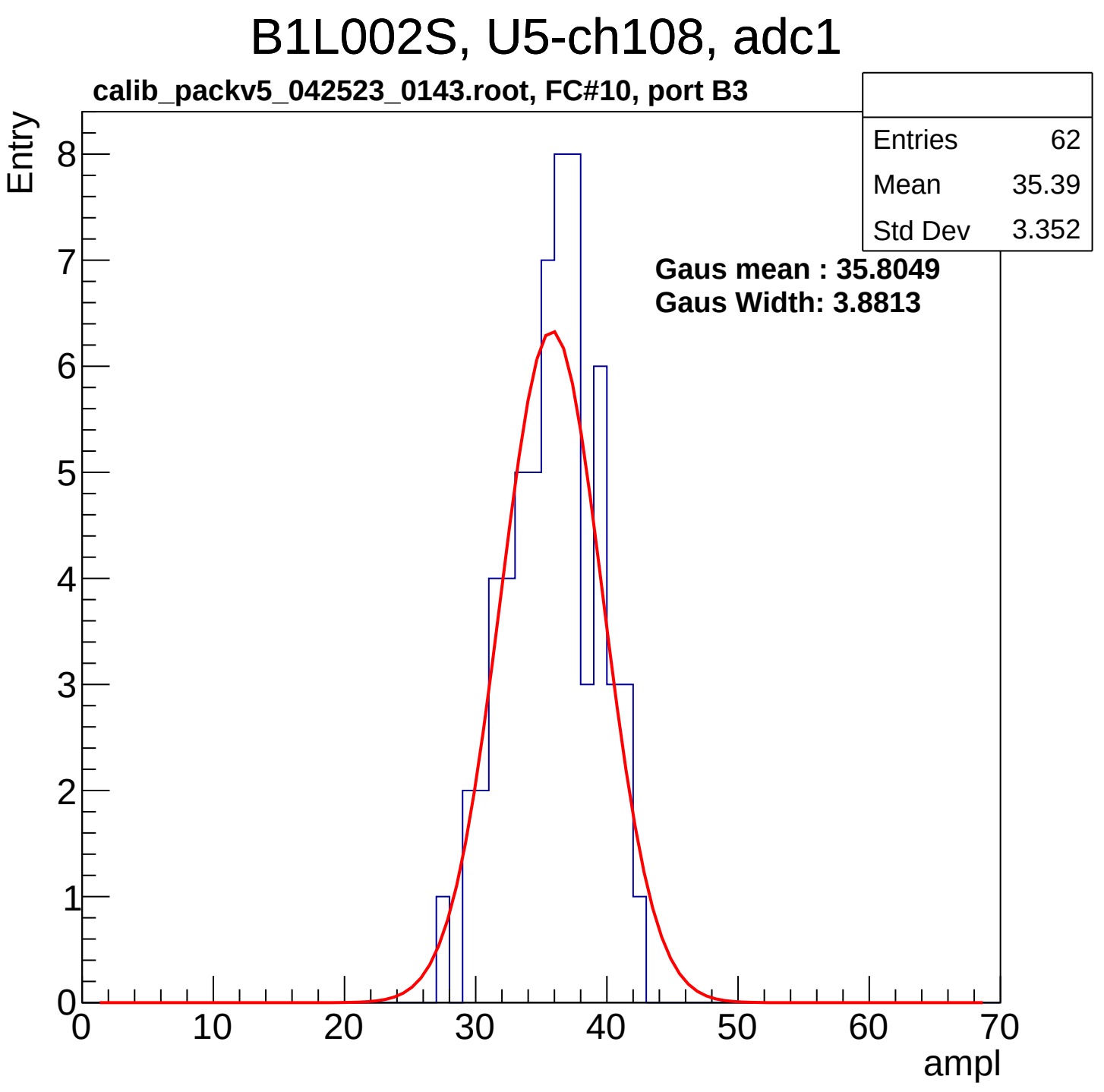
B1L002S, U5-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.39
Std Dev 3.352
Gaus mean : 35.8049
Gaus Width: 3.8813
Entry
ampl
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3
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5
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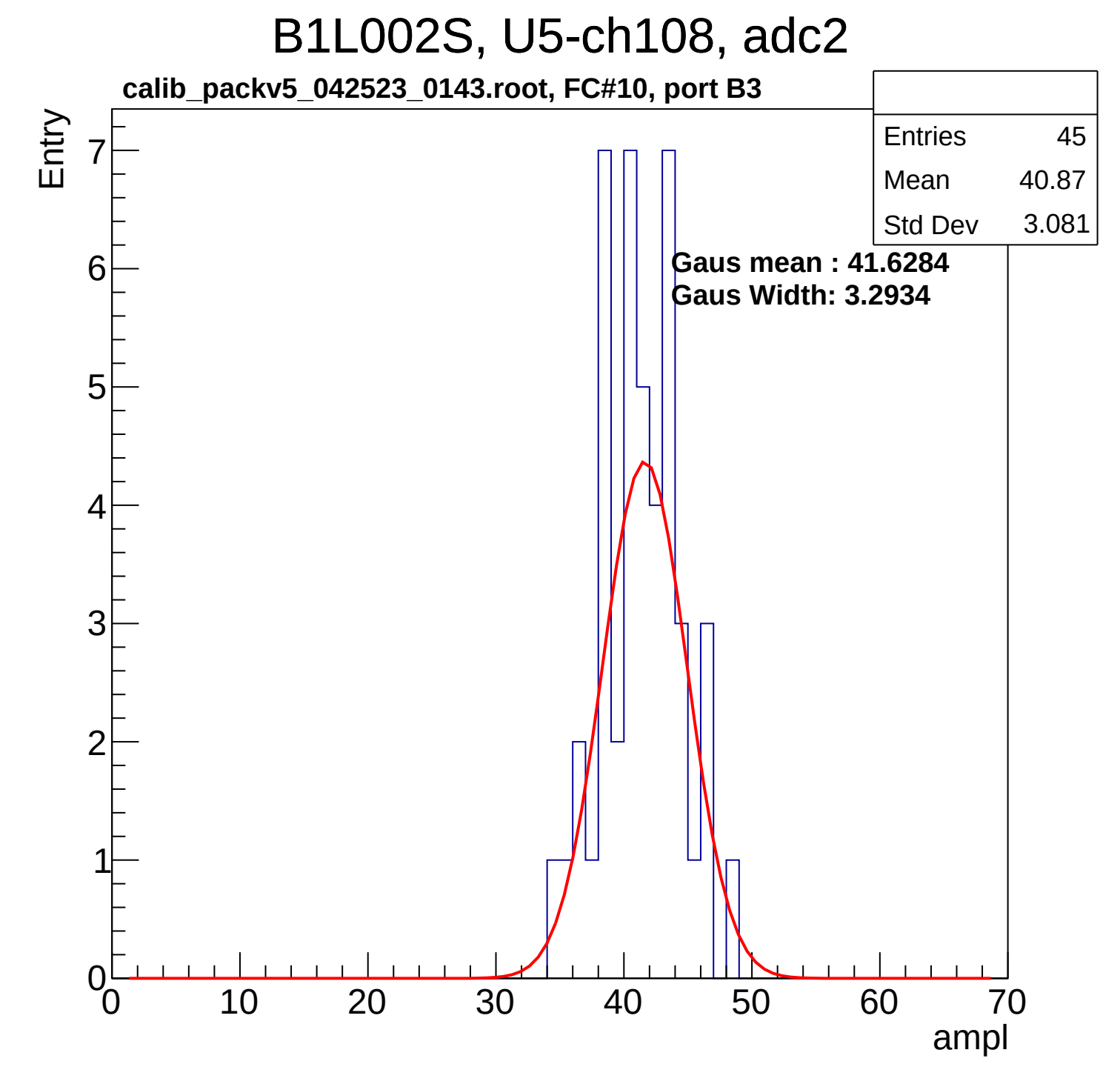
B1L002S, U5-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 40.87
Std Dev 3.081
Gaus mean : 41.6284
Gaus Width: 3.2934
Entry
ampl
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4
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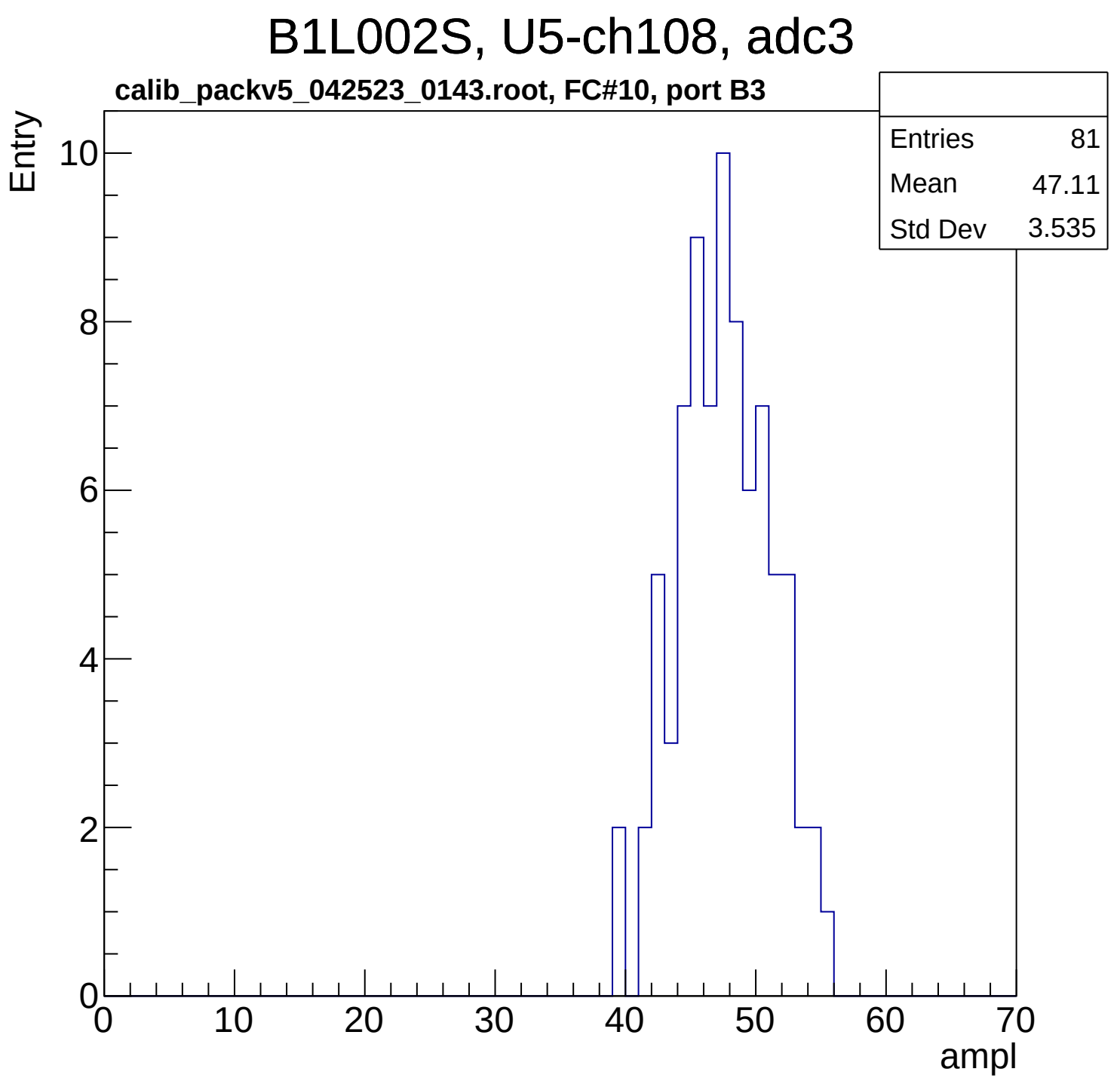
B1L002S, U5-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 47.11
Std Dev 3.535
Entry
ampl
0
2
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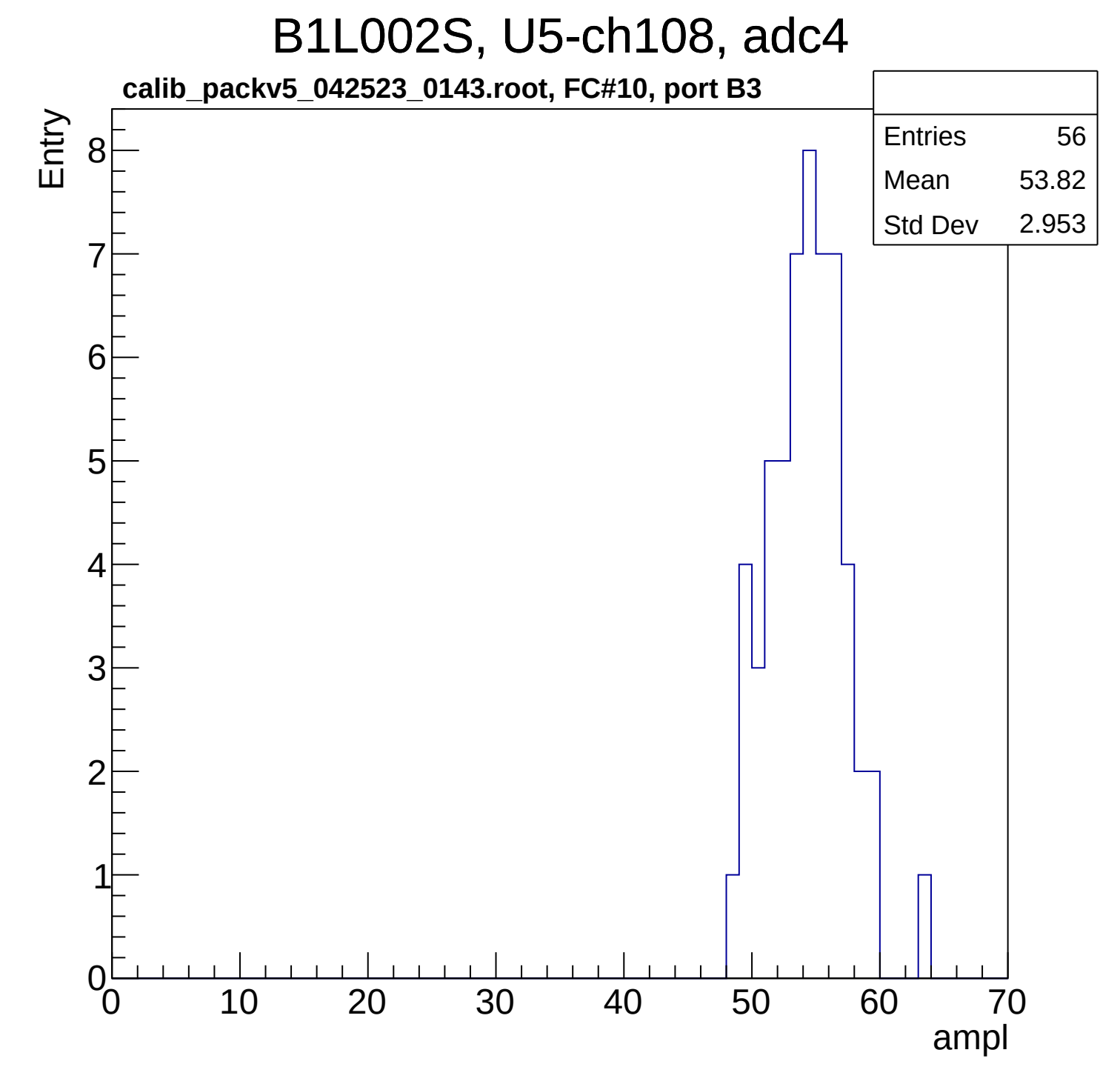
B1L002S, U5-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.82
Std Dev 2.953
Entry
ampl
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3
4
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7
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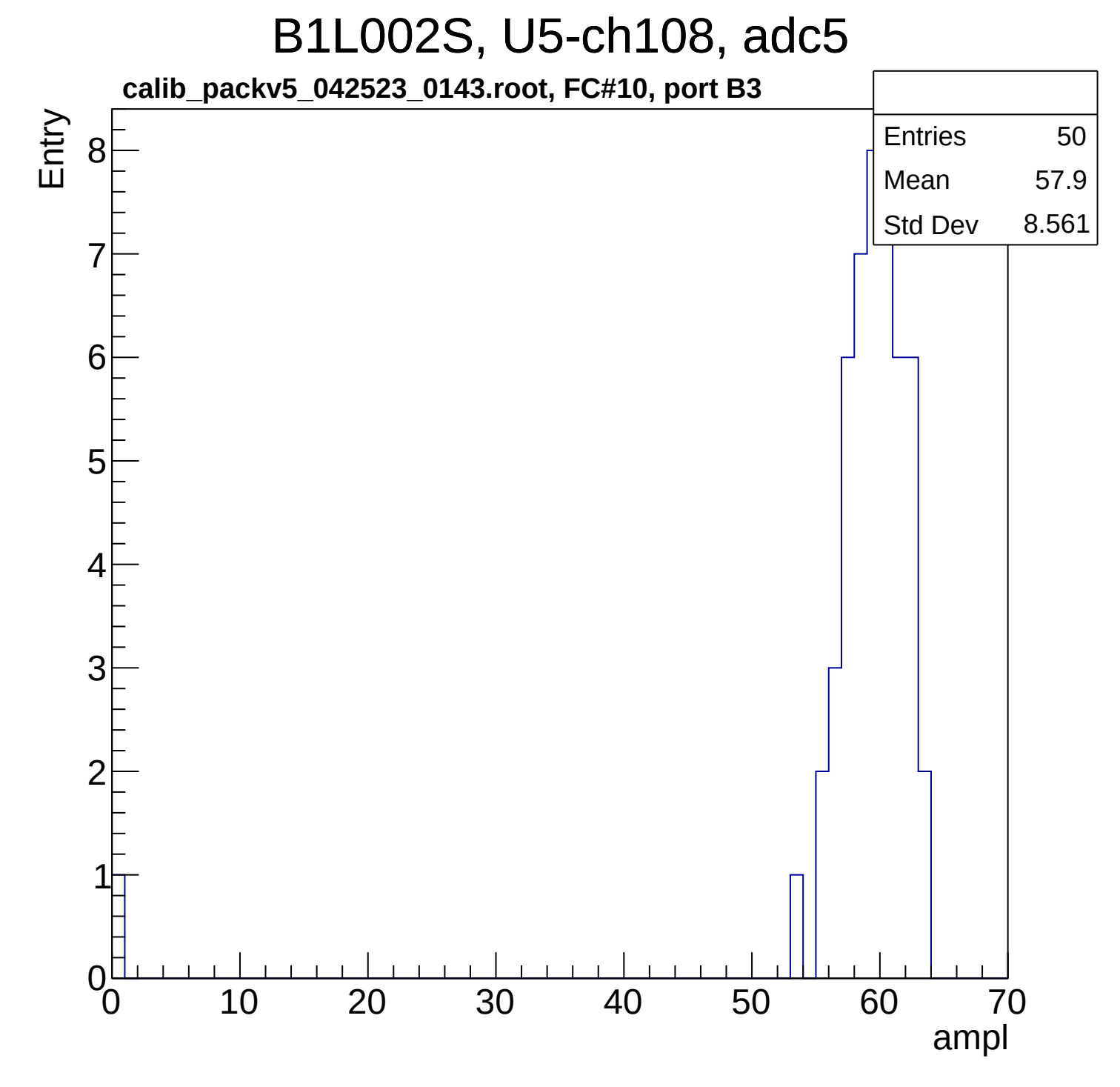
B1L002S, U5-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.9
Std Dev 8.561
Entry
ampl
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2
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4
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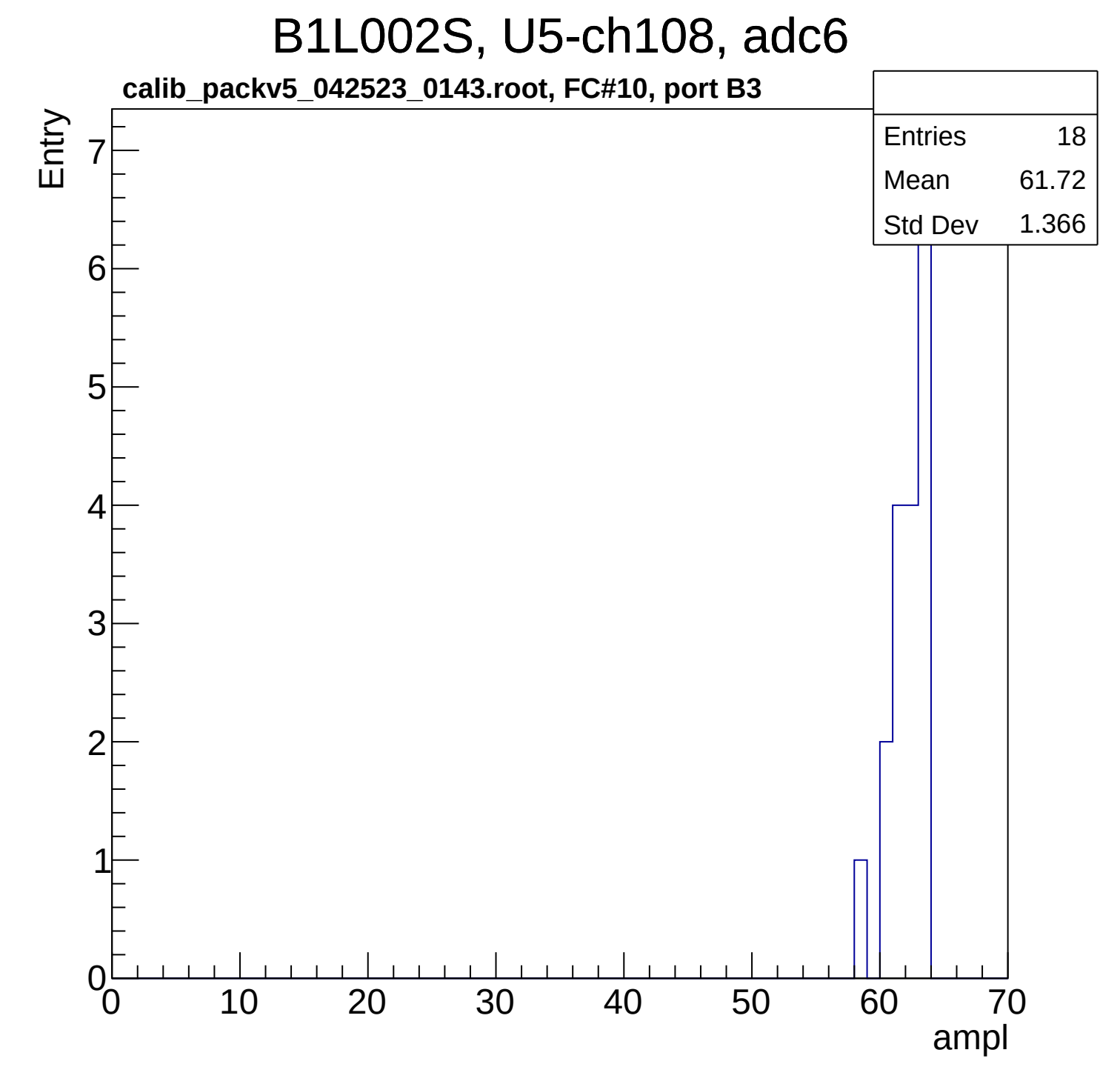
B1L002S, U5-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.72
Std Dev 1.366
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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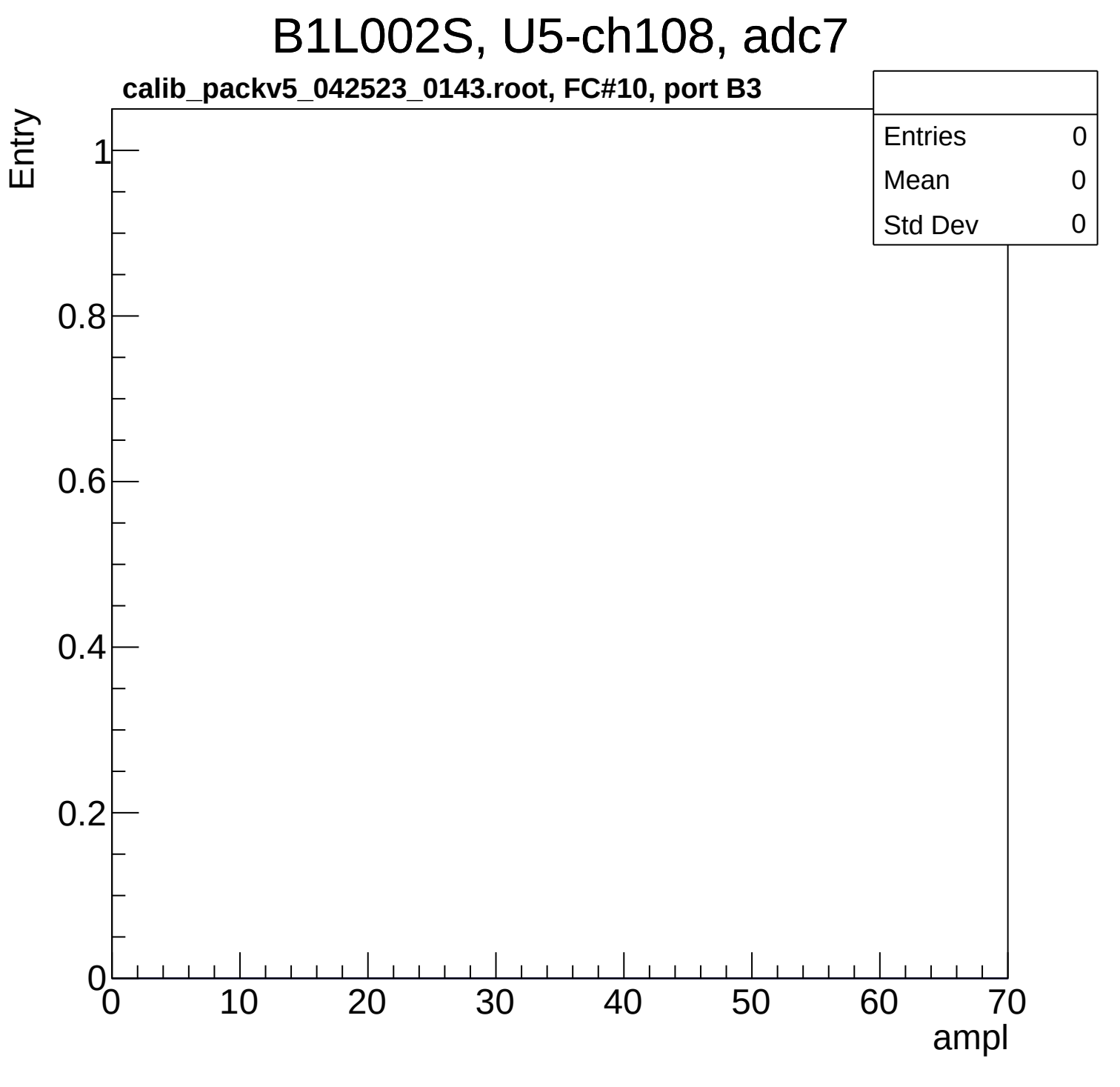
B1L002S, U5-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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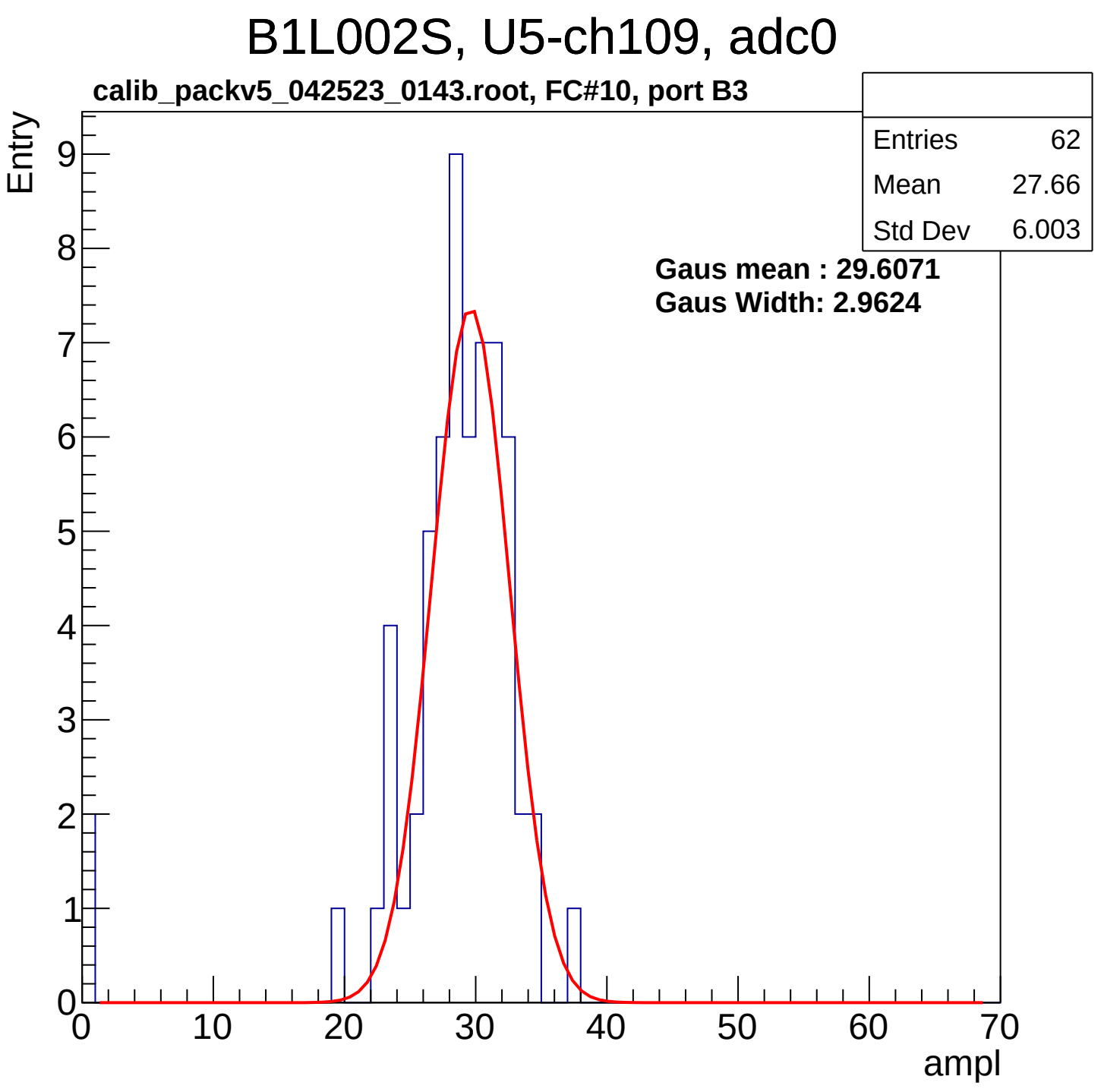
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.66
Std Dev 6.003
Gaus mean : 29.6071
Gaus Width: 2.9624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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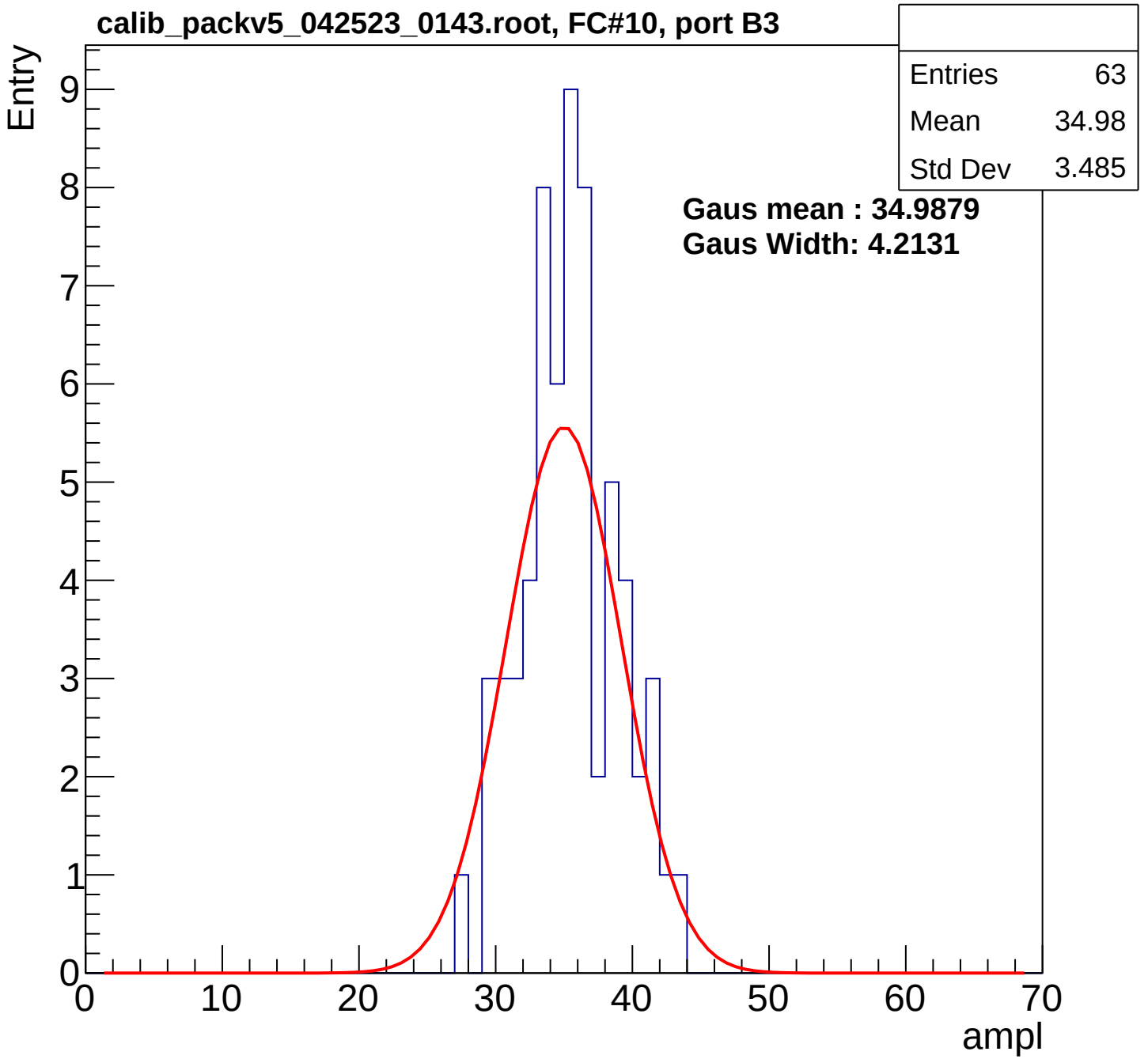
B1L002S, U5-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.98
Std Dev 3.485
Gaus mean : 34.9879
Gaus Width: 4.2131
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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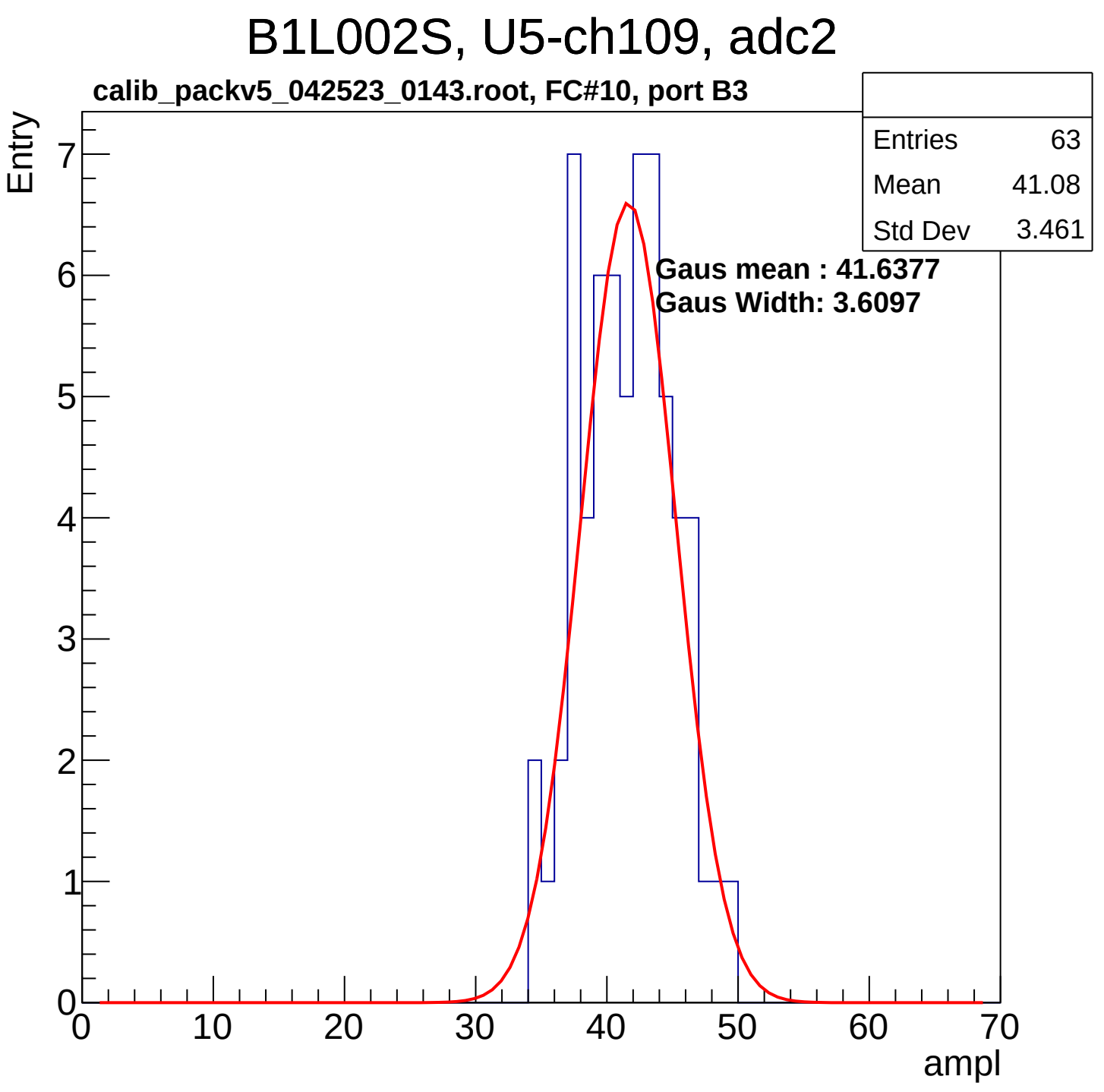
B1L002S, U5-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.08
Std Dev 3.461
Gaus mean : 41.6377
Gaus Width: 3.6097
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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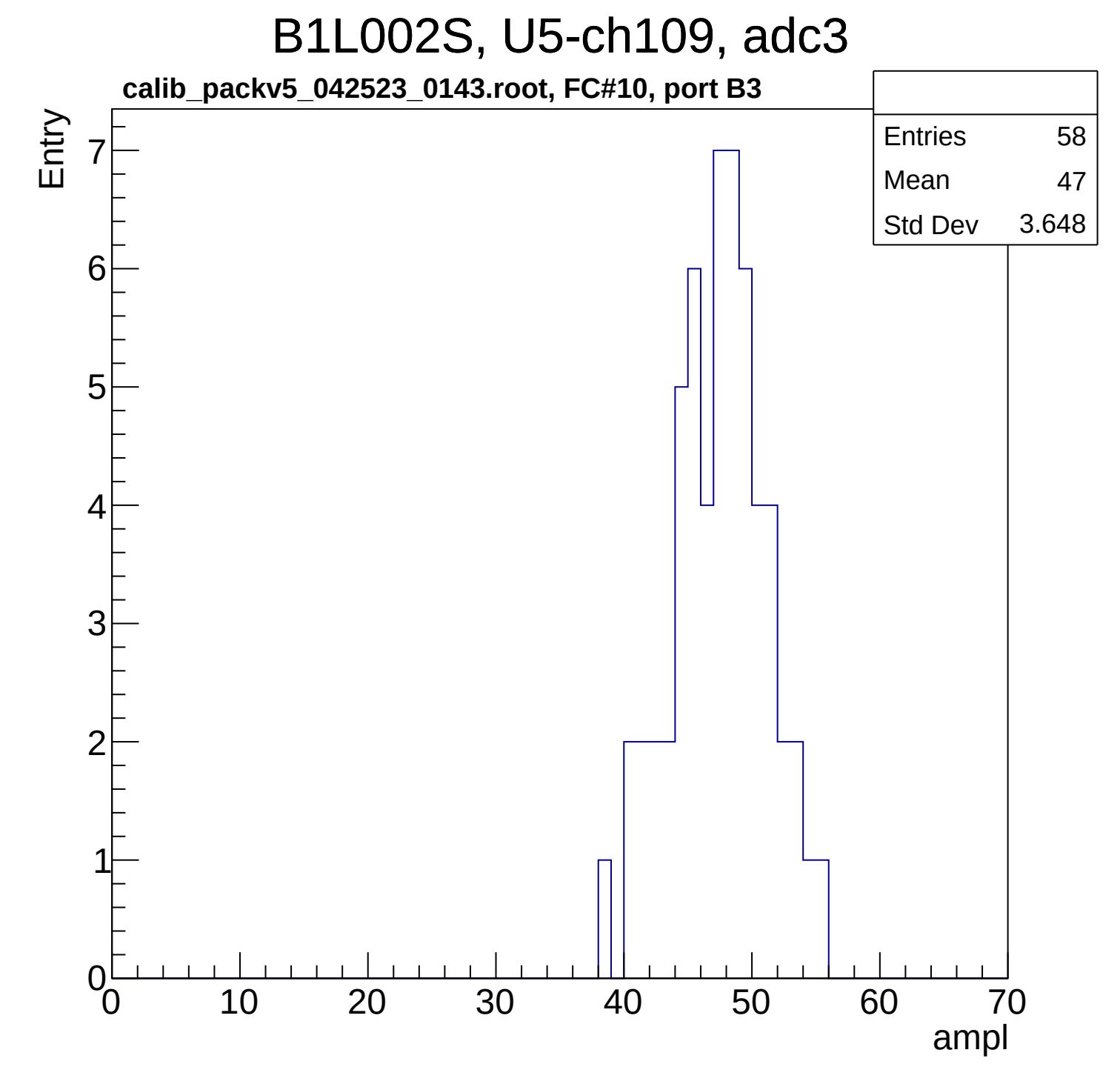
B1L002S, U5-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47
Std Dev 3.648
Entry
ampl
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2
3
4
5
6
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0
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40
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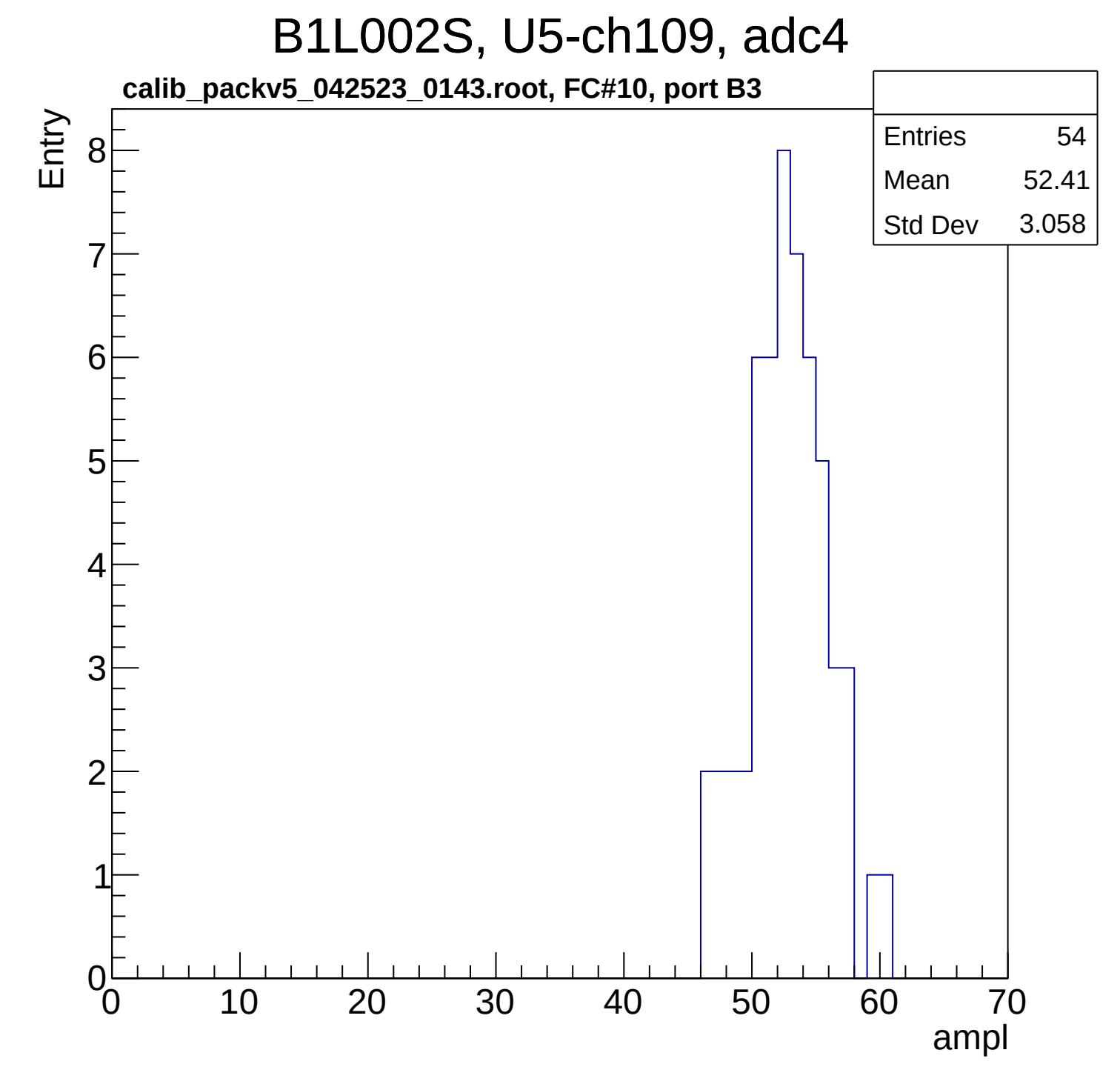
B1L002S, U5-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.41
Std Dev 3.058
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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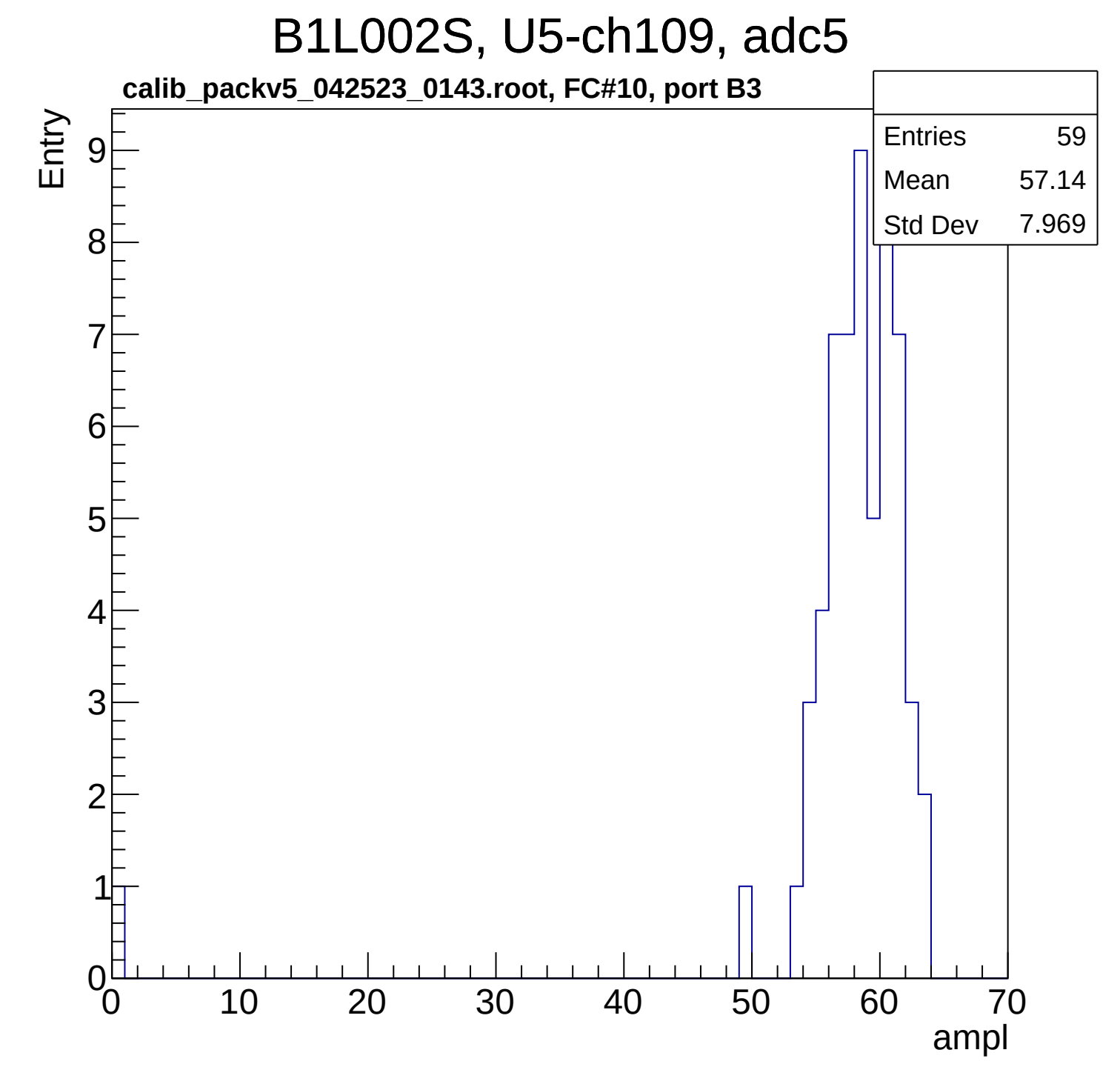
B1L002S, U5-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.14
Std Dev 7.969
Entry
ampl
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2
3
4
5
6
7
8
9
0
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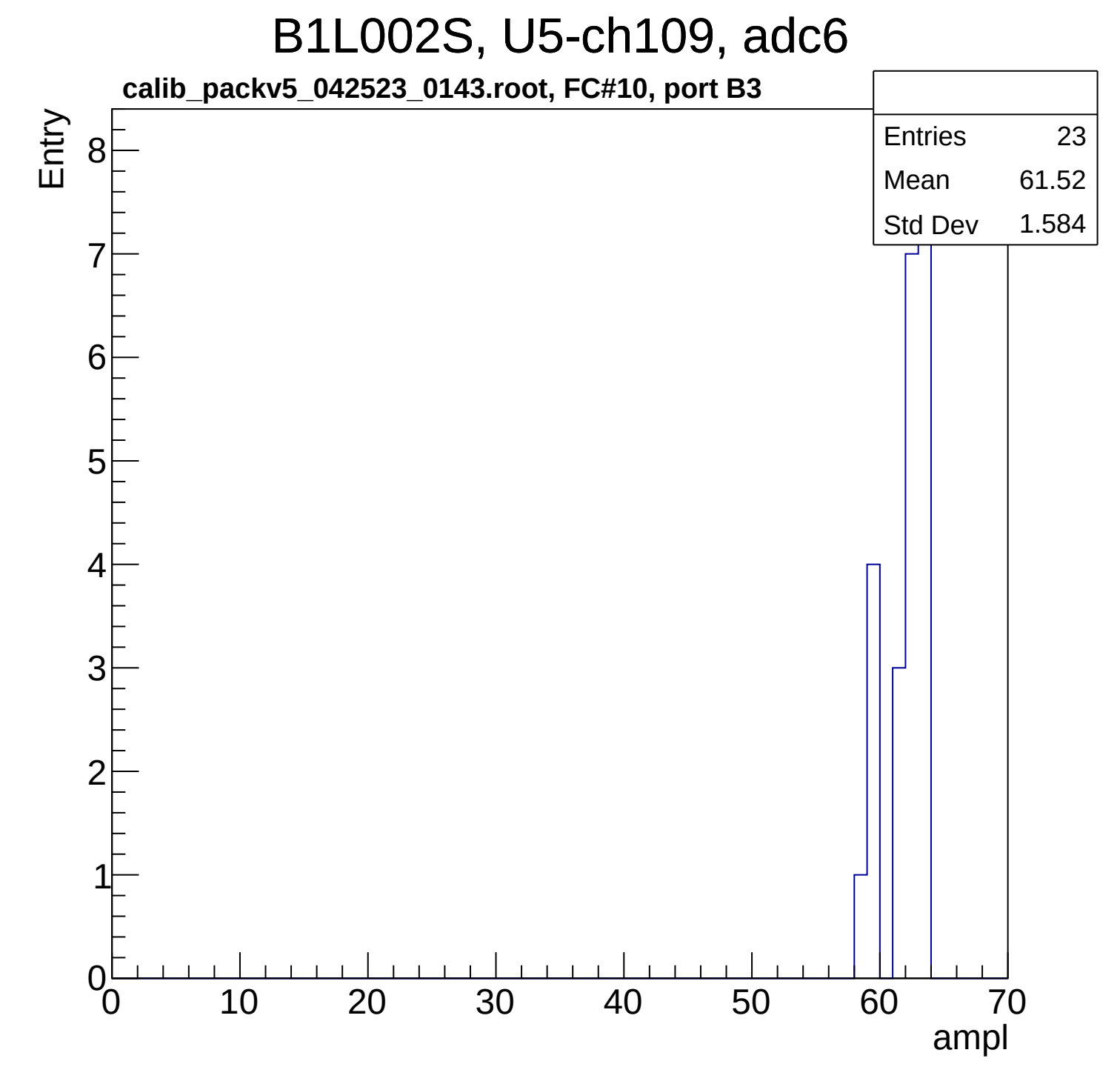
B1L002S, U5-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.52
Std Dev 1.584
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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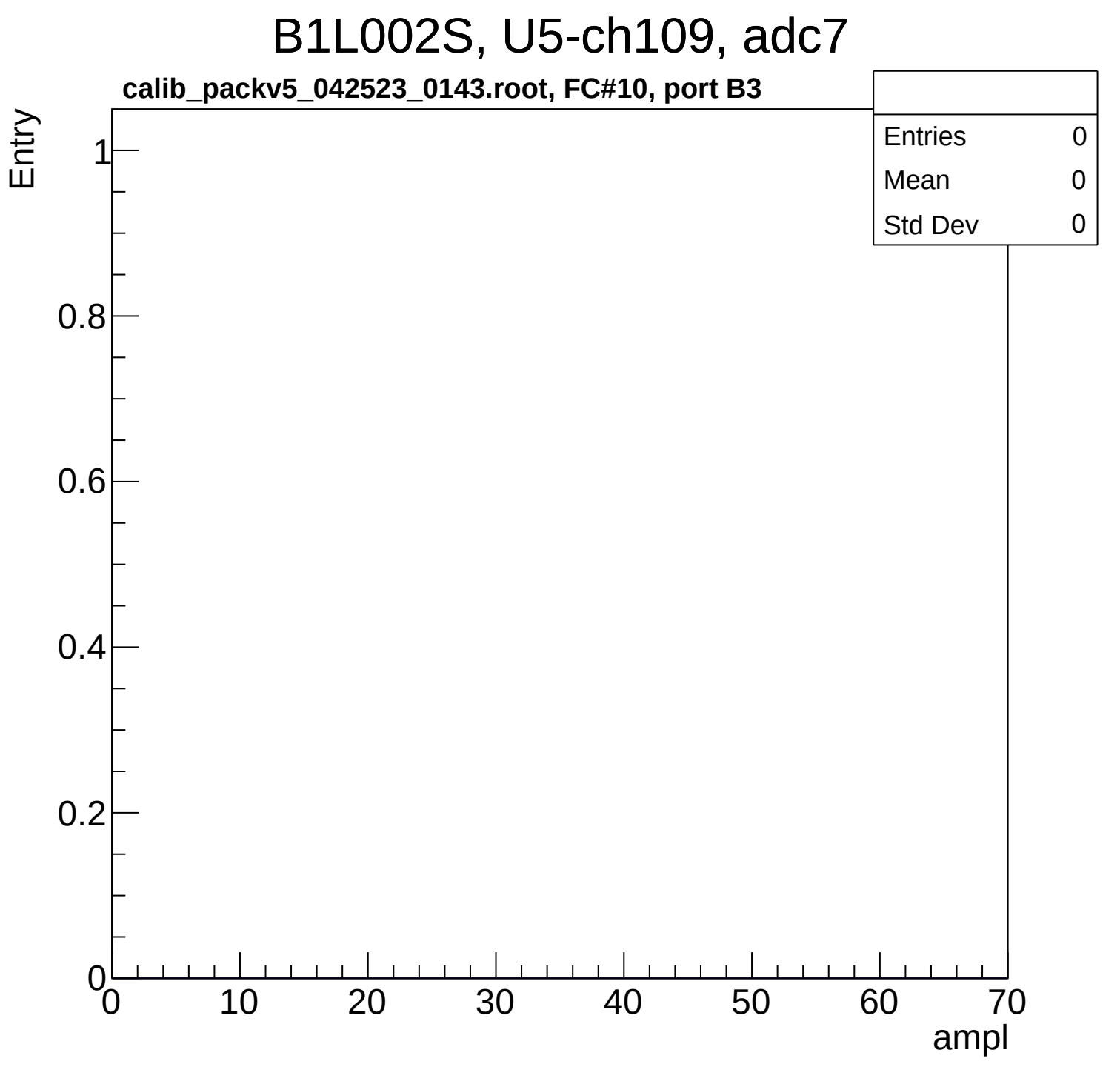
B1L002S, U5-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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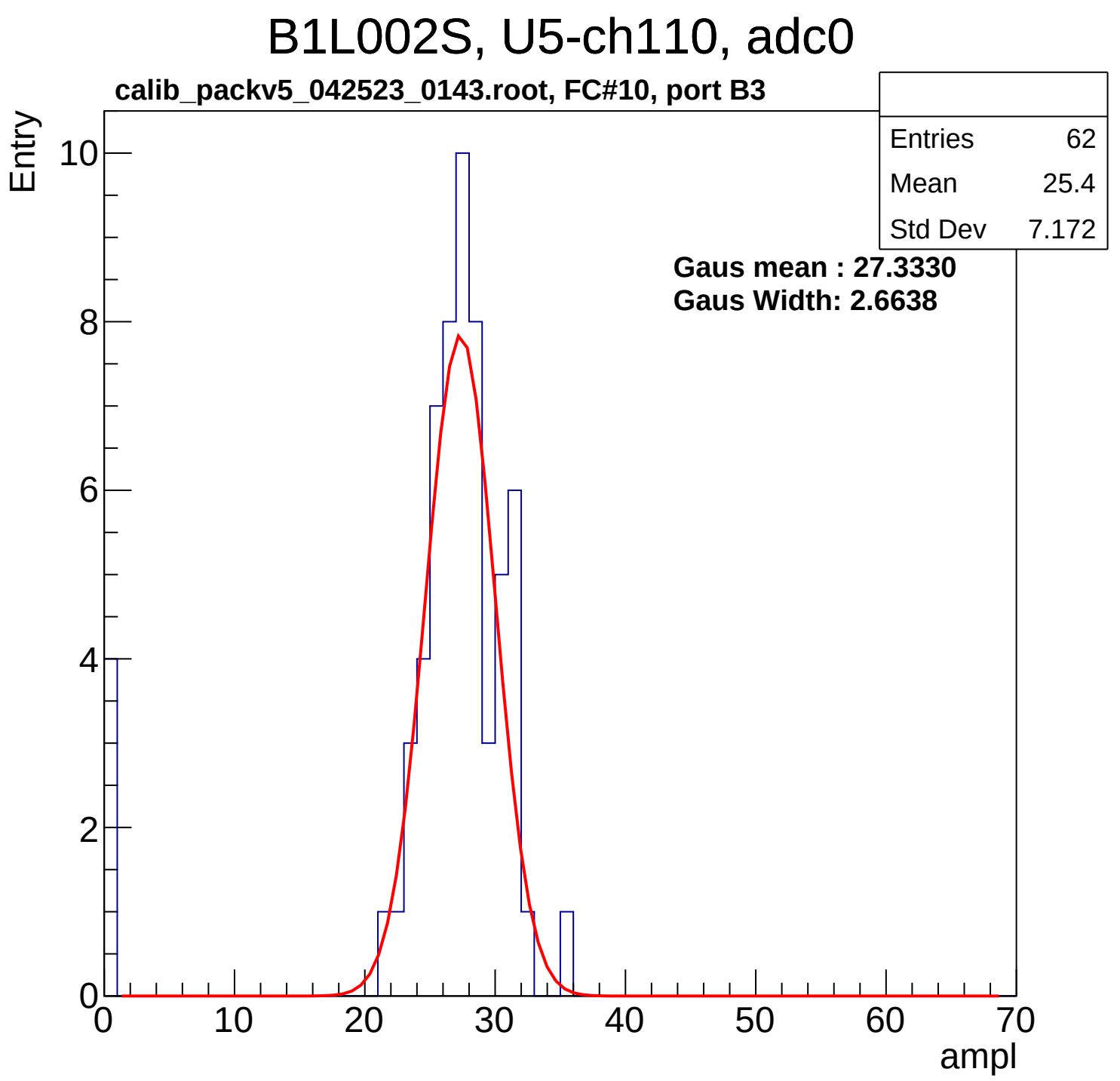
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 25.4
Std Dev 7.172
Gaus mean : 27.3330
Gaus Width: 2.6638
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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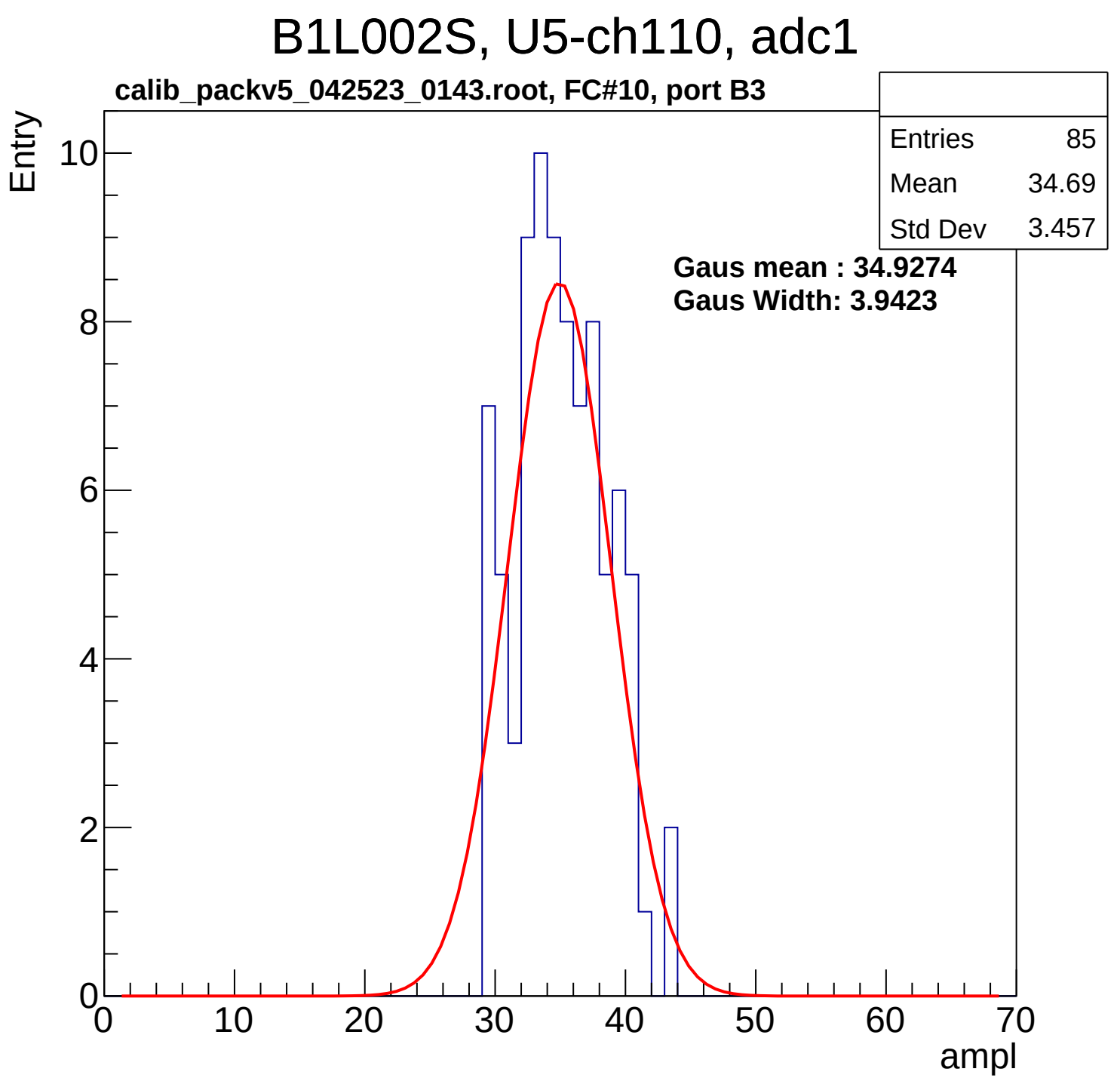
B1L002S, U5-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 34.69
Std Dev 3.457
Gaus mean : 34.9274
Gaus Width: 3.9423
Entry
ampl
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6
8
10
0
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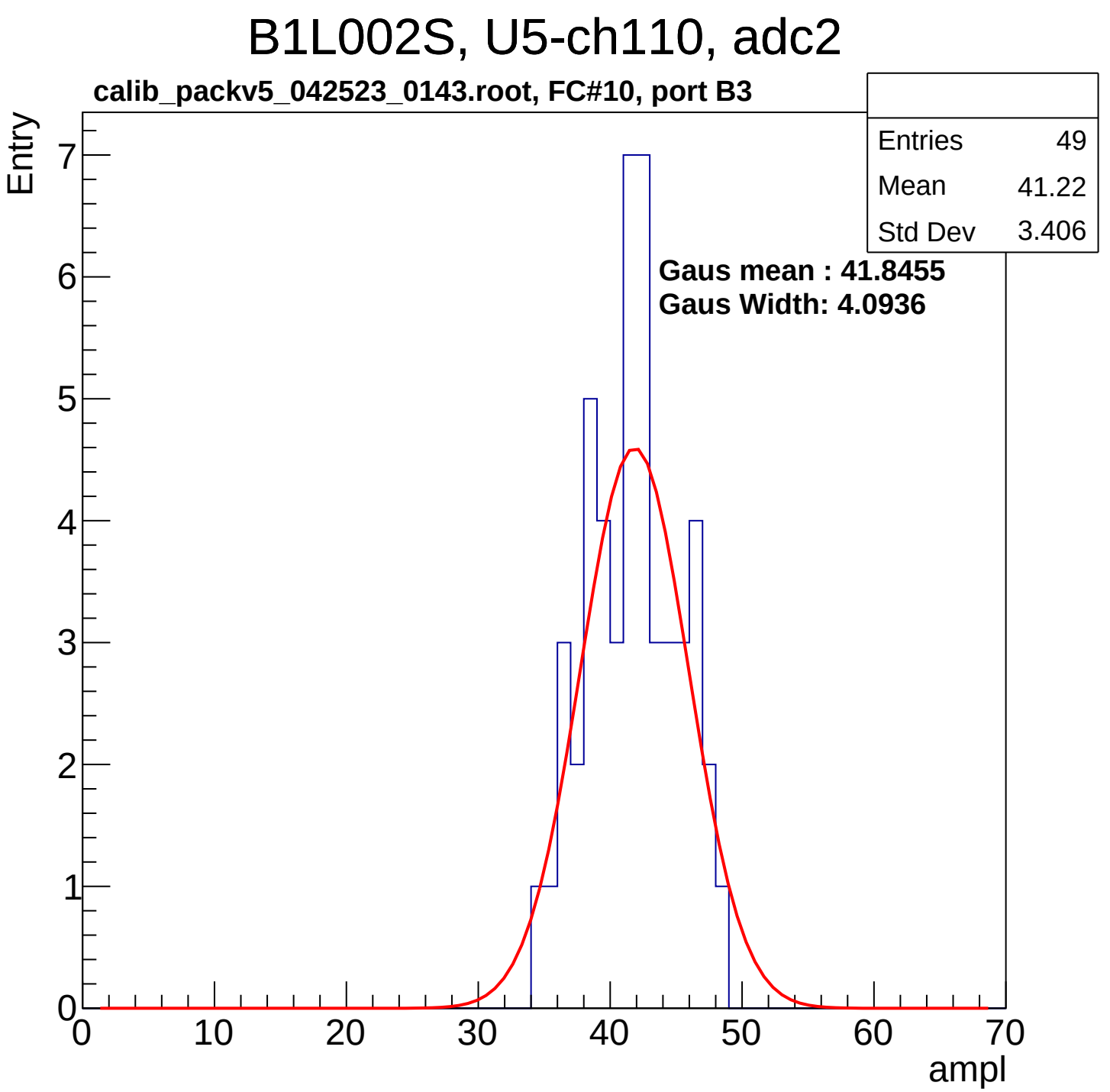
20
30
40
50
60
70


B1L002S, U5-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.22
Std Dev 3.406
Gaus mean : 41.8455
Gaus Width: 4.0936
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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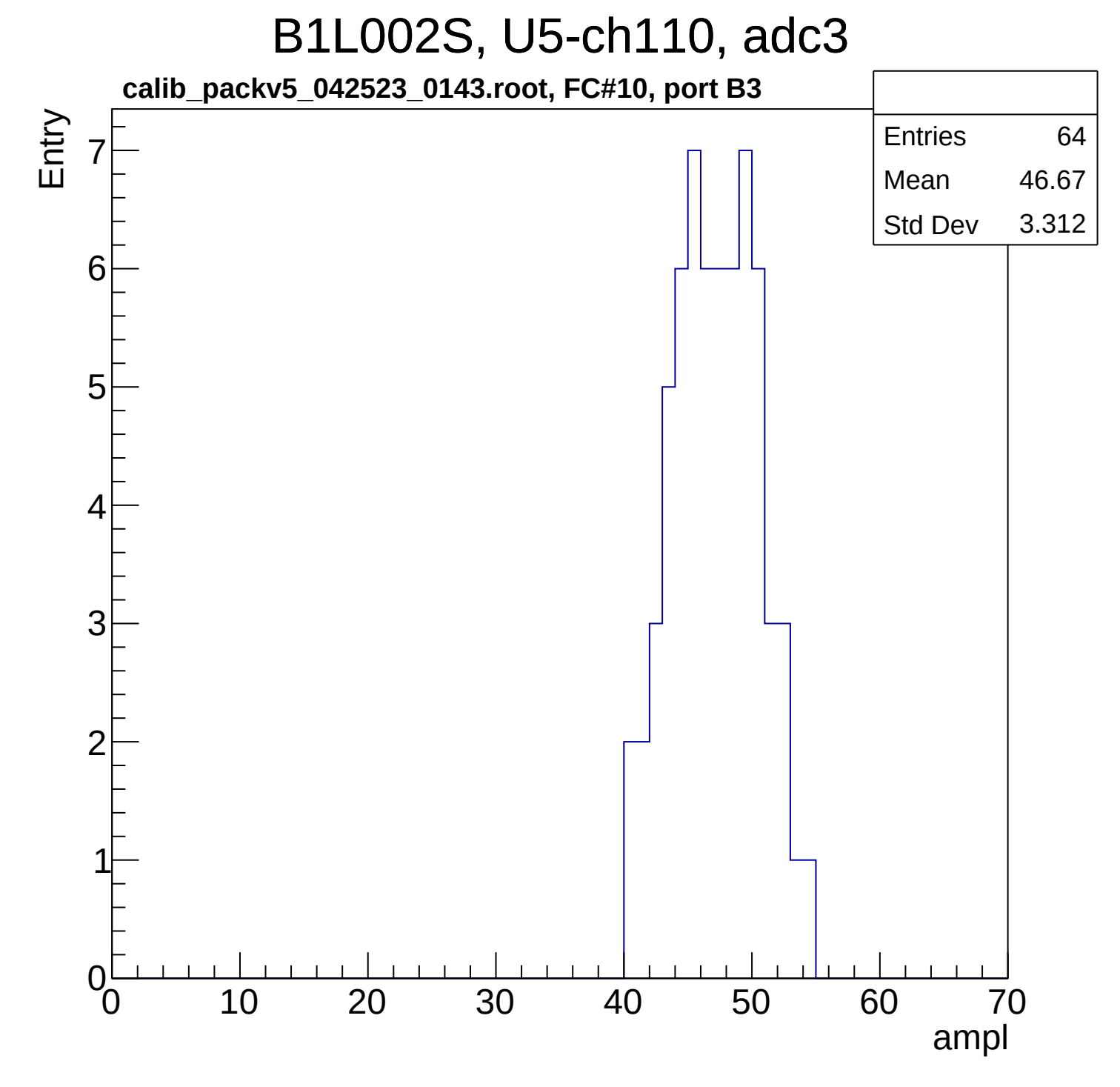
B1L002S, U5-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.67
Std Dev 3.312
Entry
ampl
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0
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60
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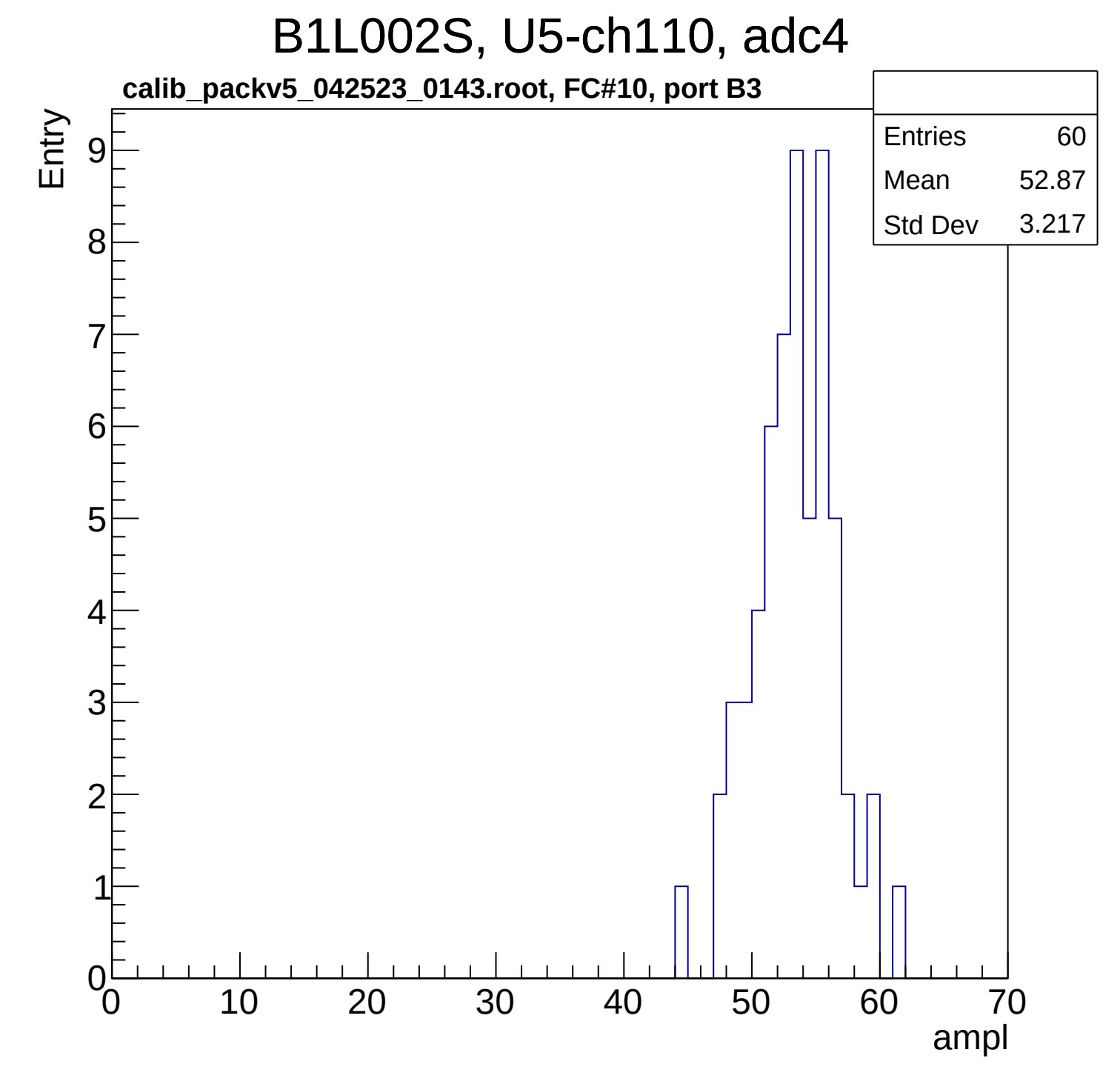
B1L002S, U5-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.87
Std Dev 3.217
Entry
ampl
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0
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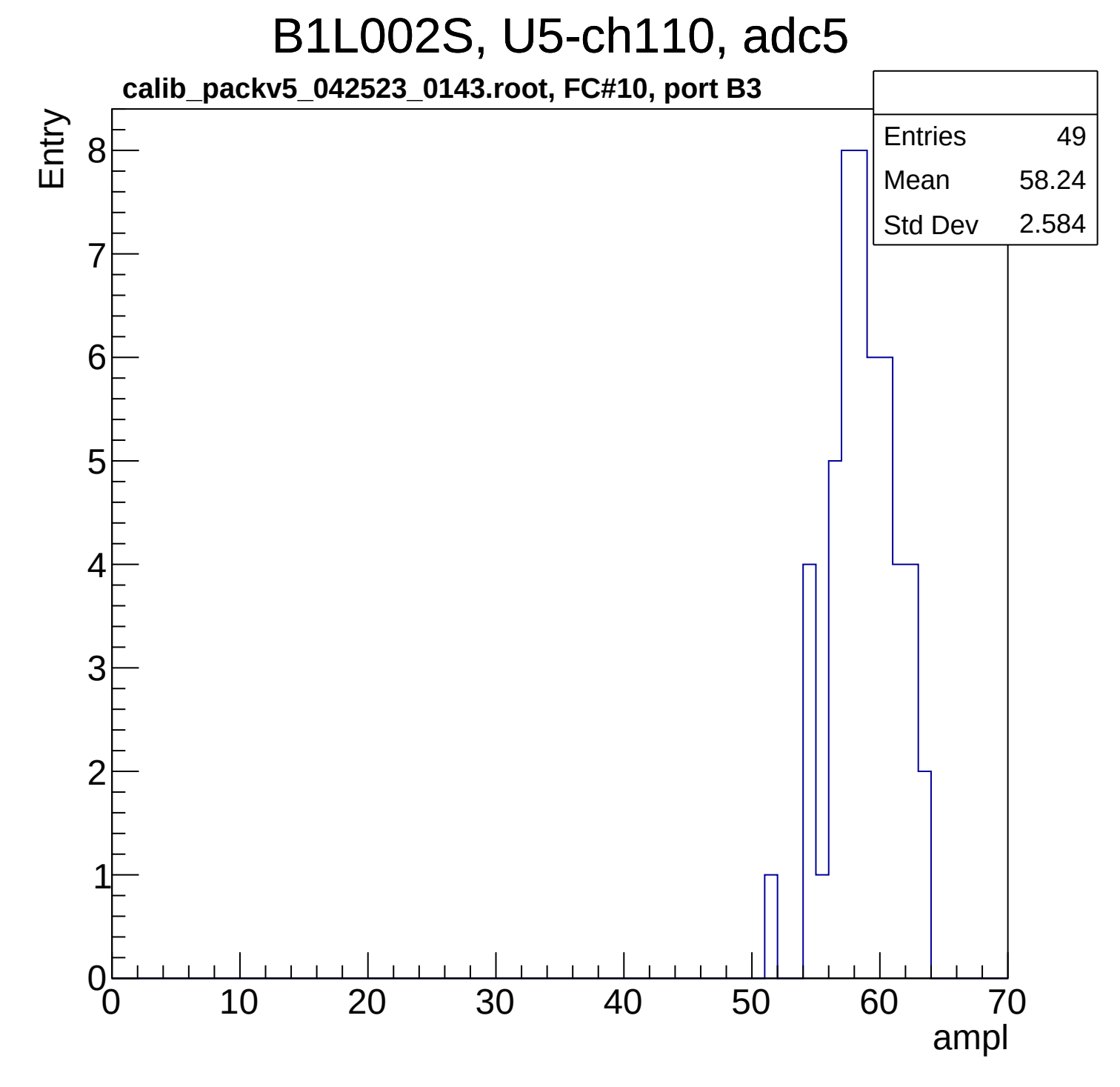
B1L002S, U5-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.24
Std Dev 2.584
Entry
ampl
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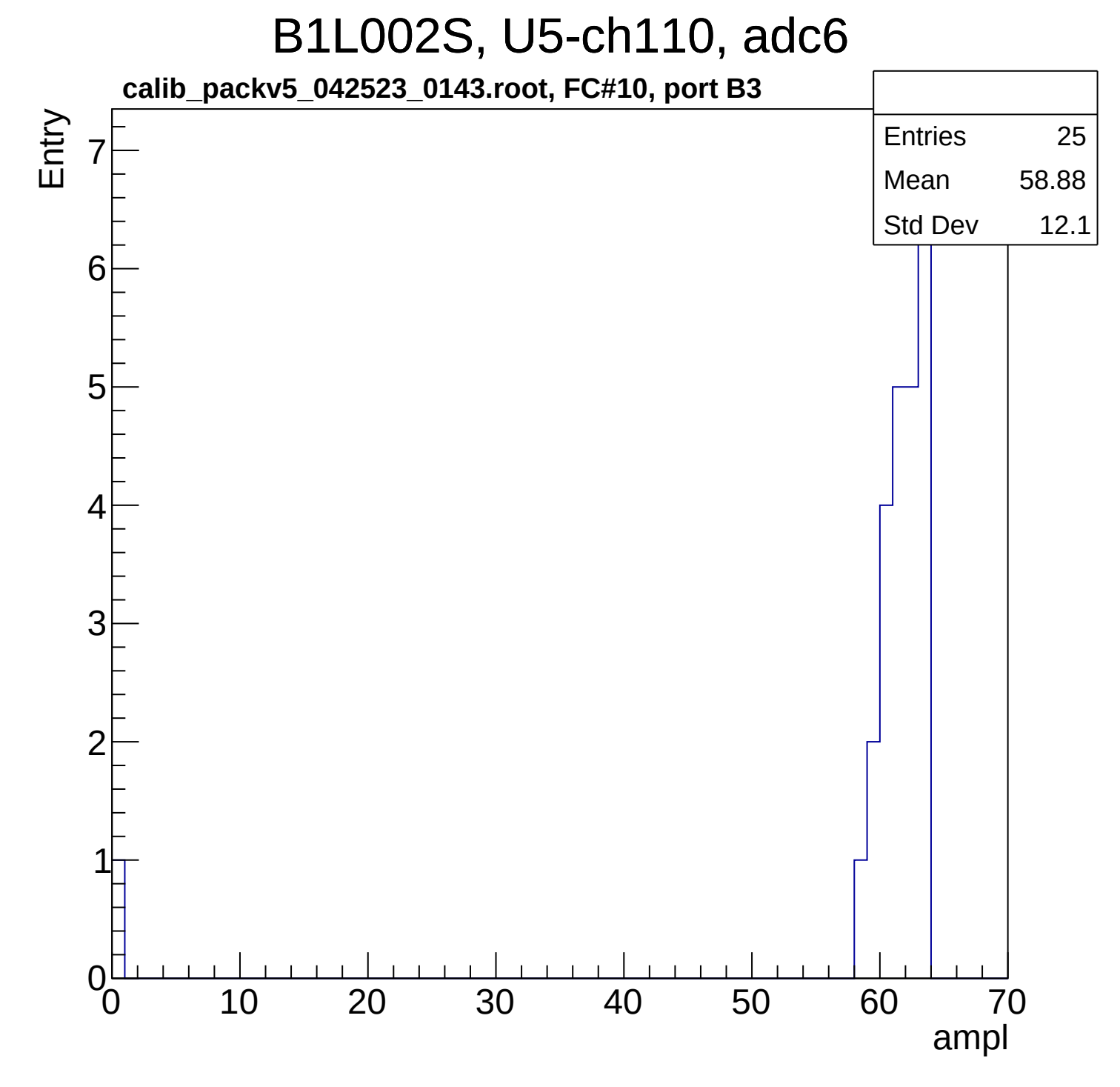
B1L002S, U5-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.88
Std Dev 12.1
Entry
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10
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30
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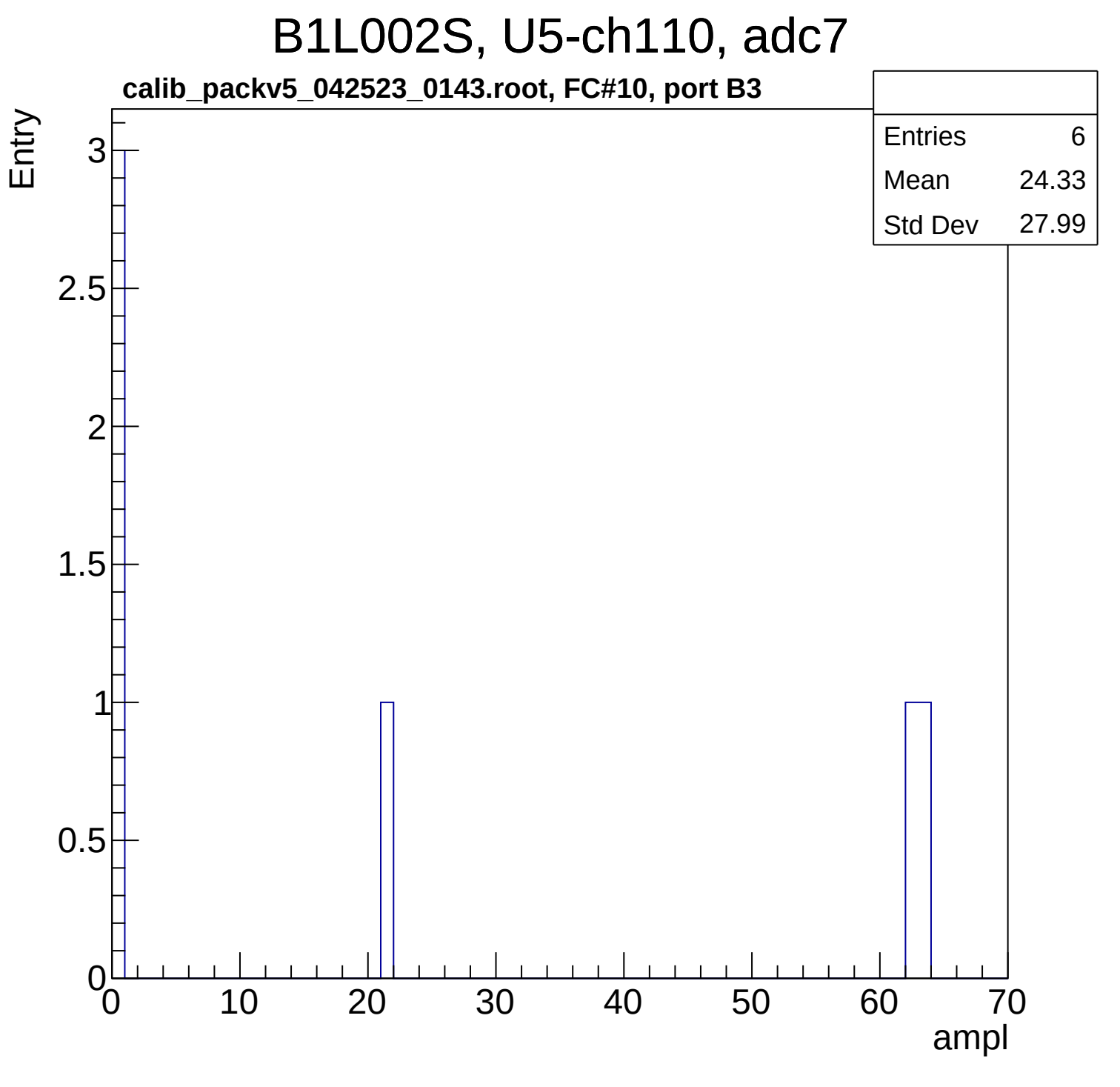
70
ampl


B1L002S, U5-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 24.33
Std Dev 27.99
Entry
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1.5
2
2.5
3
0
10
20
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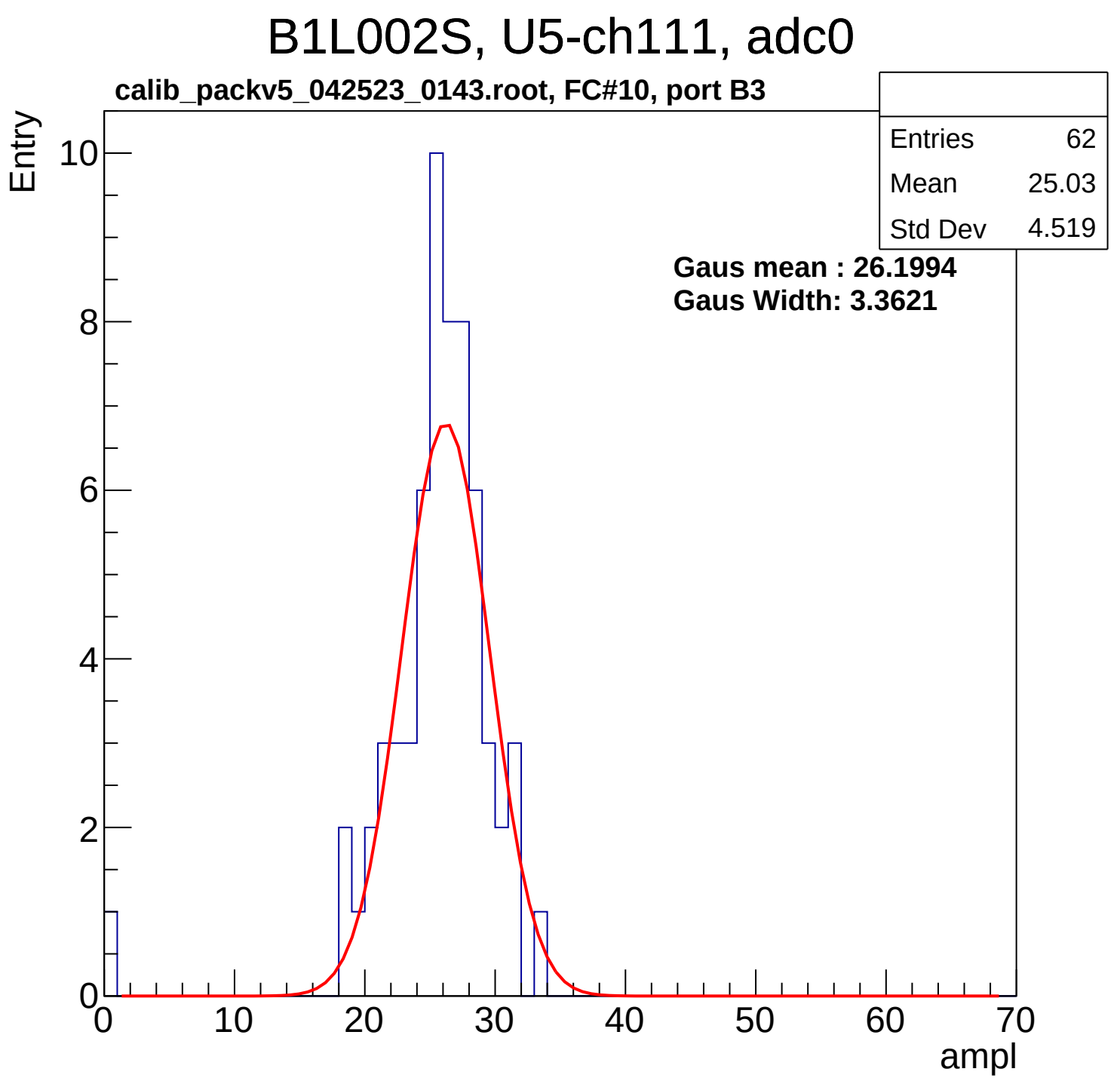
50
60
70
ampl


B1L002S, U5-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 25.03
Std Dev 4.519
Gaus mean : 26.1994
Gaus Width: 3.3621
Entry
ampl
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4
6
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0
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20
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40
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60
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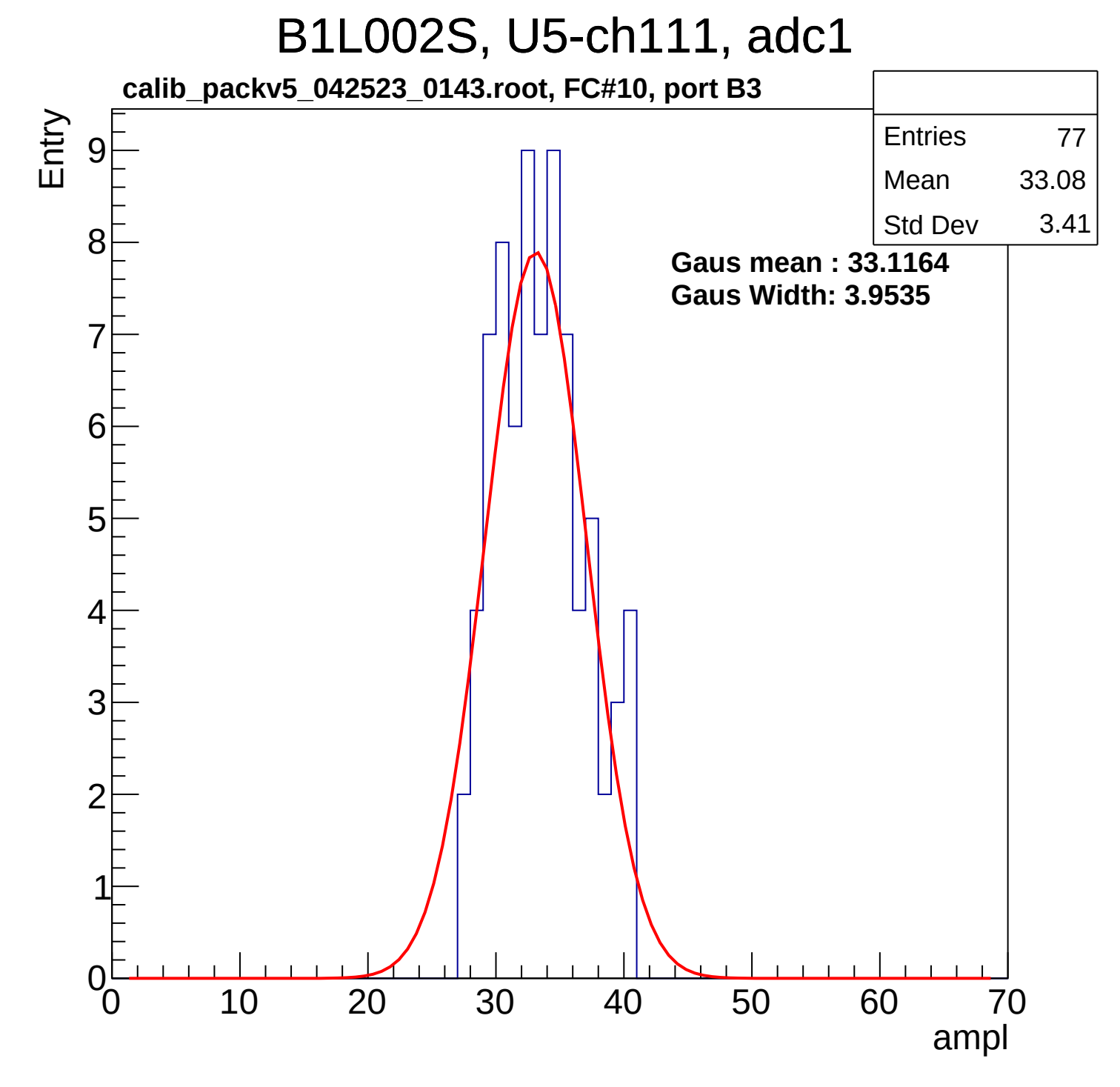
B1L002S, U5-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 33.08
Std Dev 3.41
Gaus mean : 33.1164
Gaus Width: 3.9535
Entry
ampl
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2
3
4
5
6
7
8
9
0
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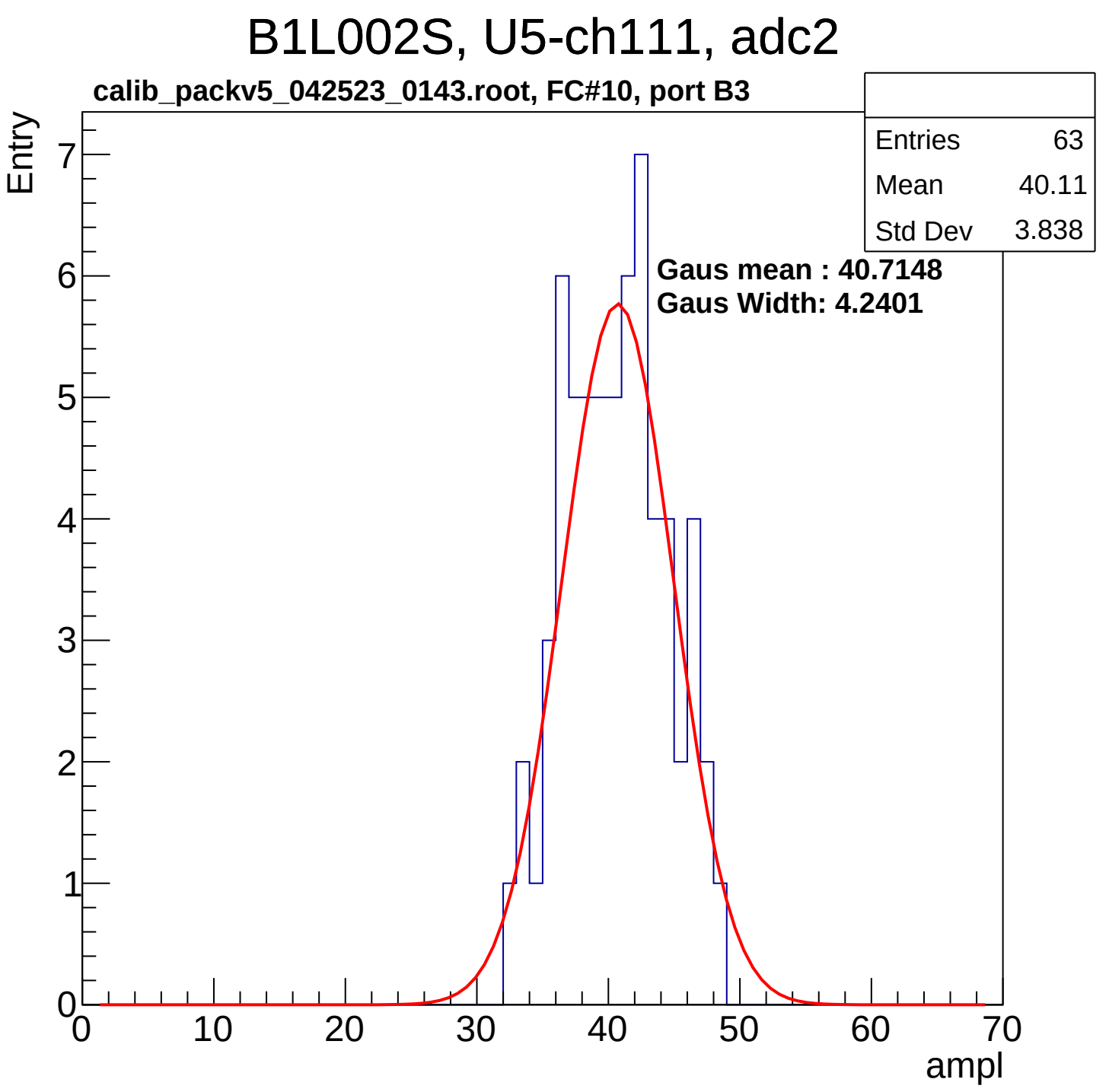
B1L002S, U5-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.11
Std Dev 3.838
Gaus mean : 40.7148
Gaus Width: 4.2401
Entry
ampl
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3
4
5
6
7
0
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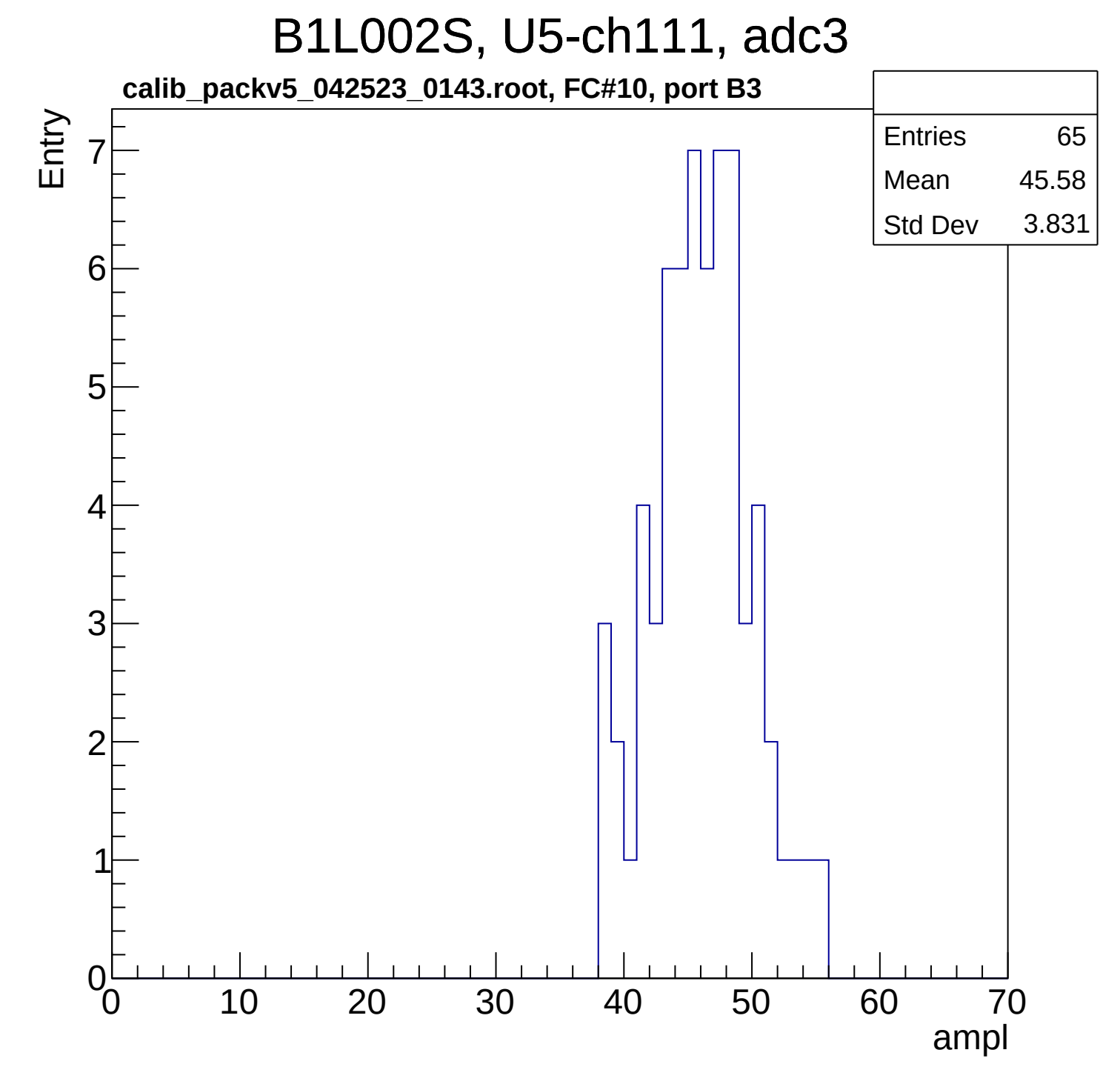
B1L002S, U5-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.58
Std Dev 3.831
Entry
ampl
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2
3
4
5
6
7
0
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60
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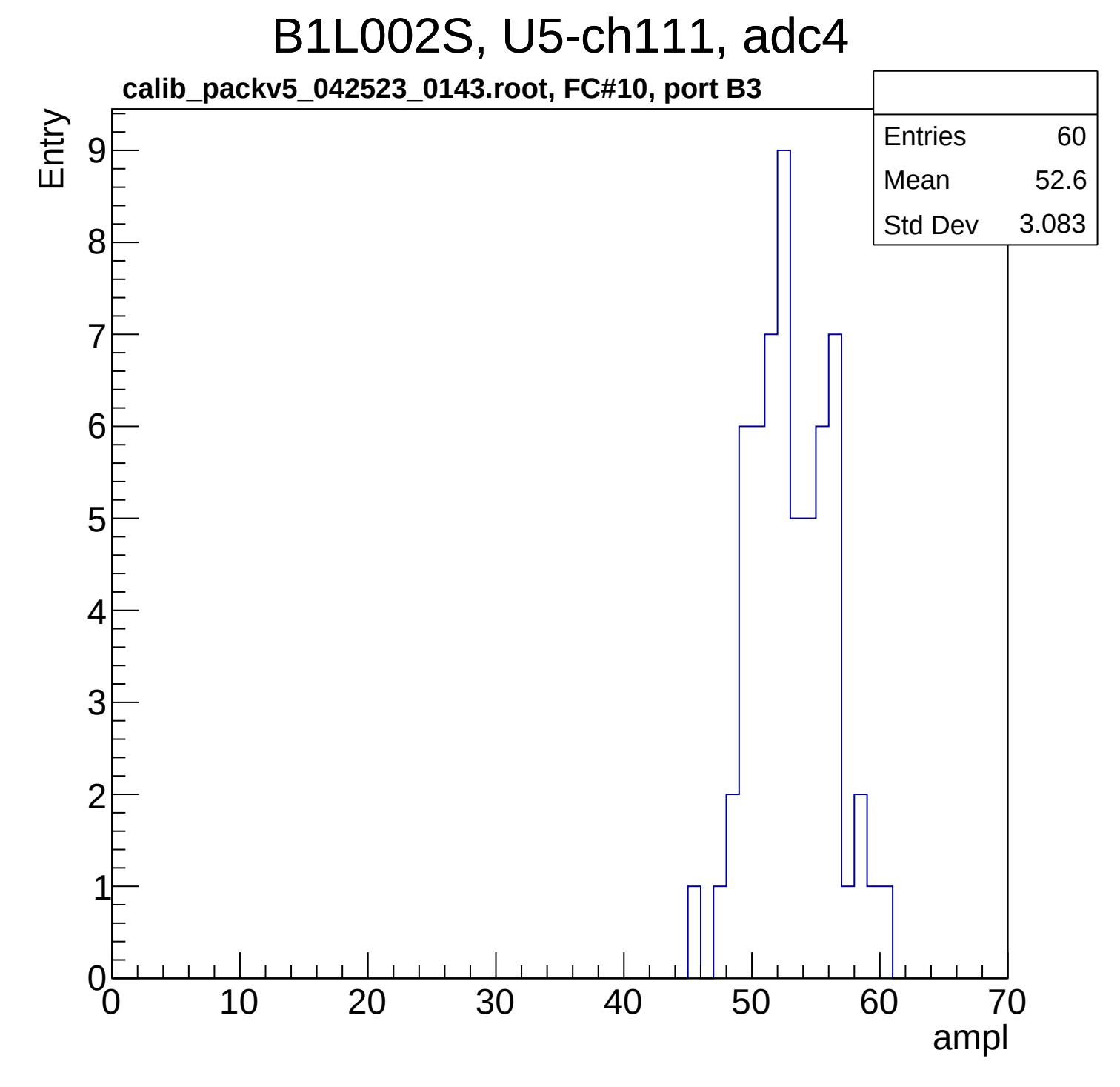
B1L002S, U5-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.6
Std Dev 3.083
Entry
ampl
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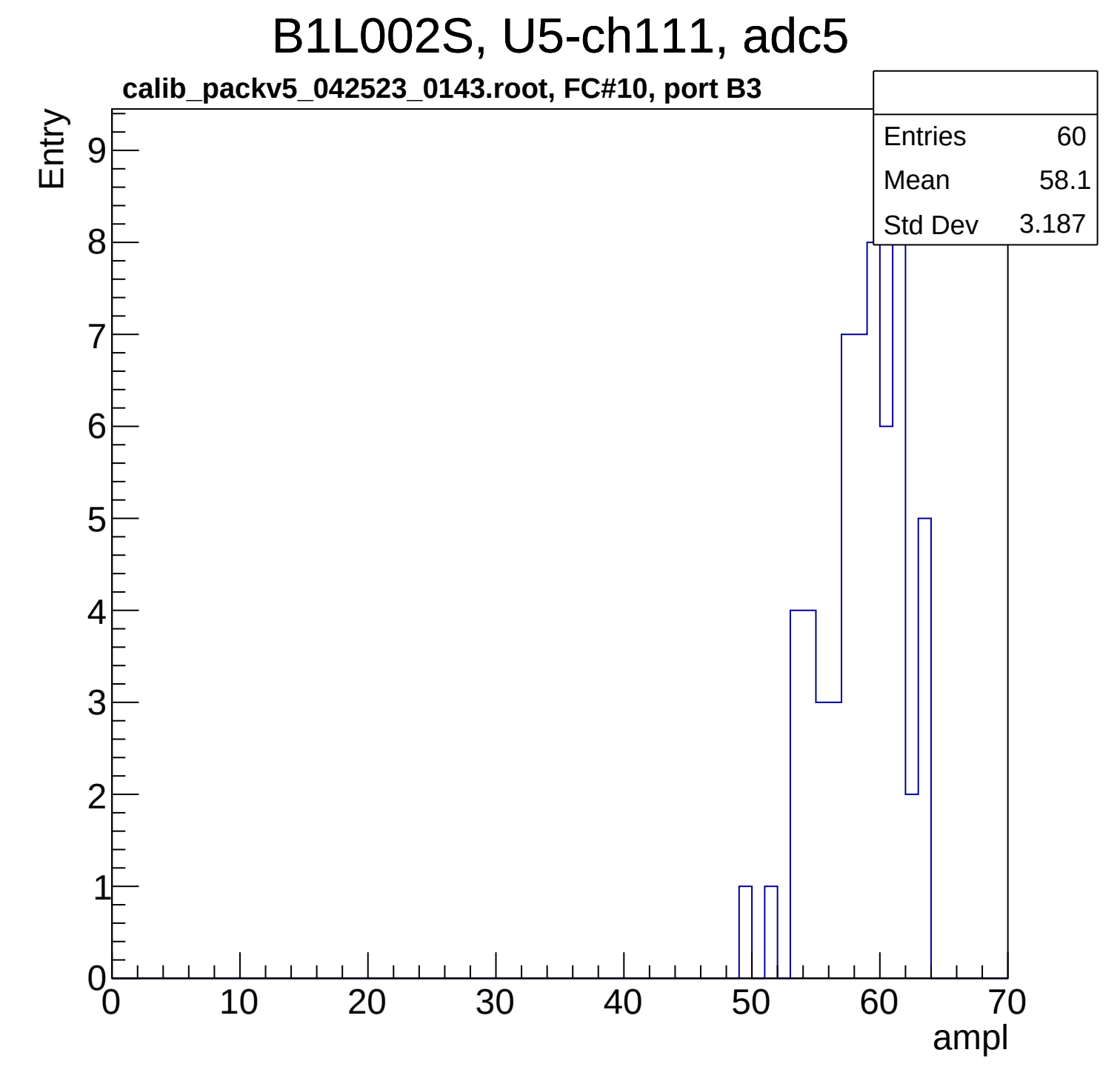
B1L002S, U5-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.1
Std Dev 3.187
Entry
ampl
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3
4
5
6
7
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0
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60
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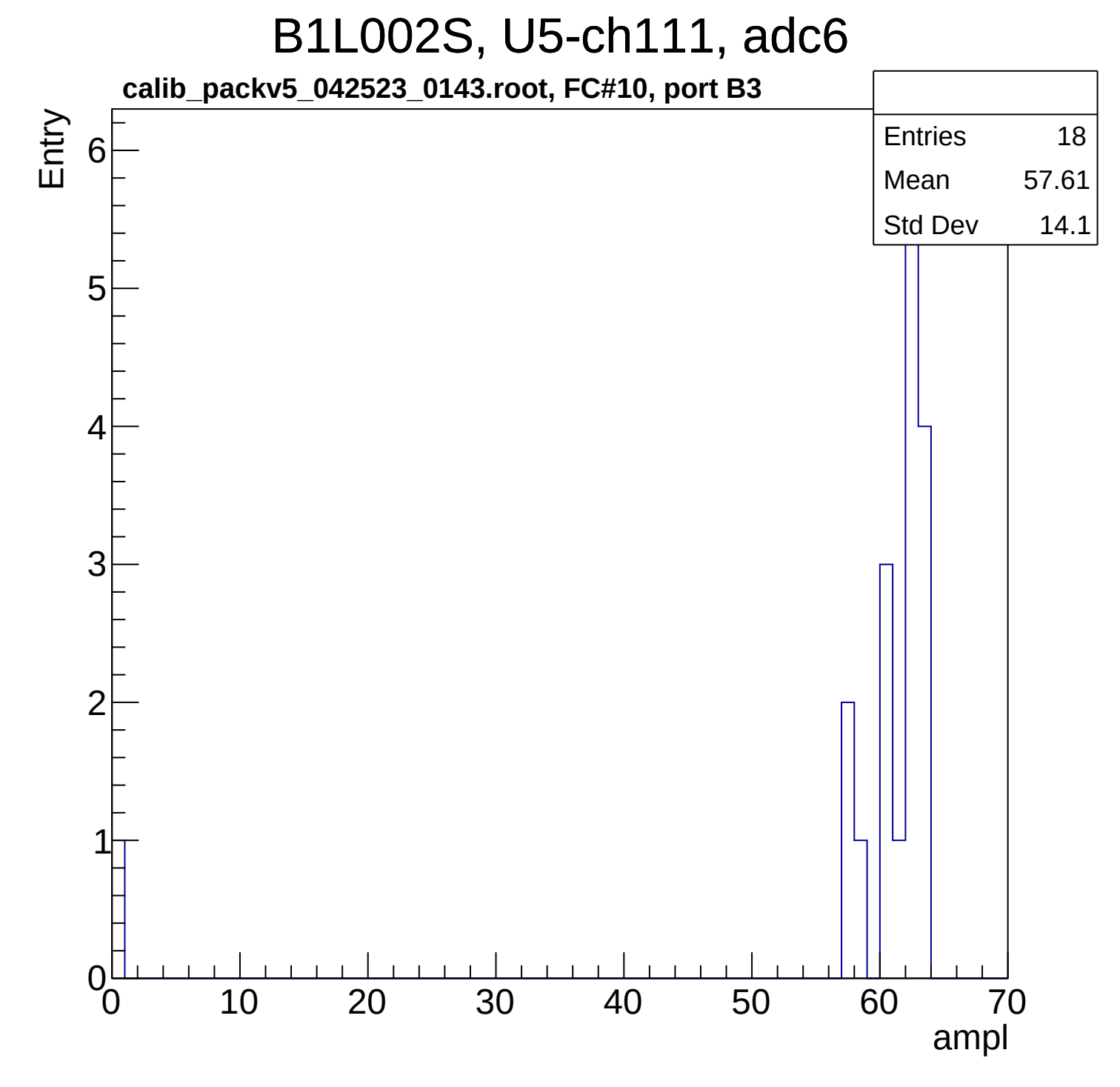
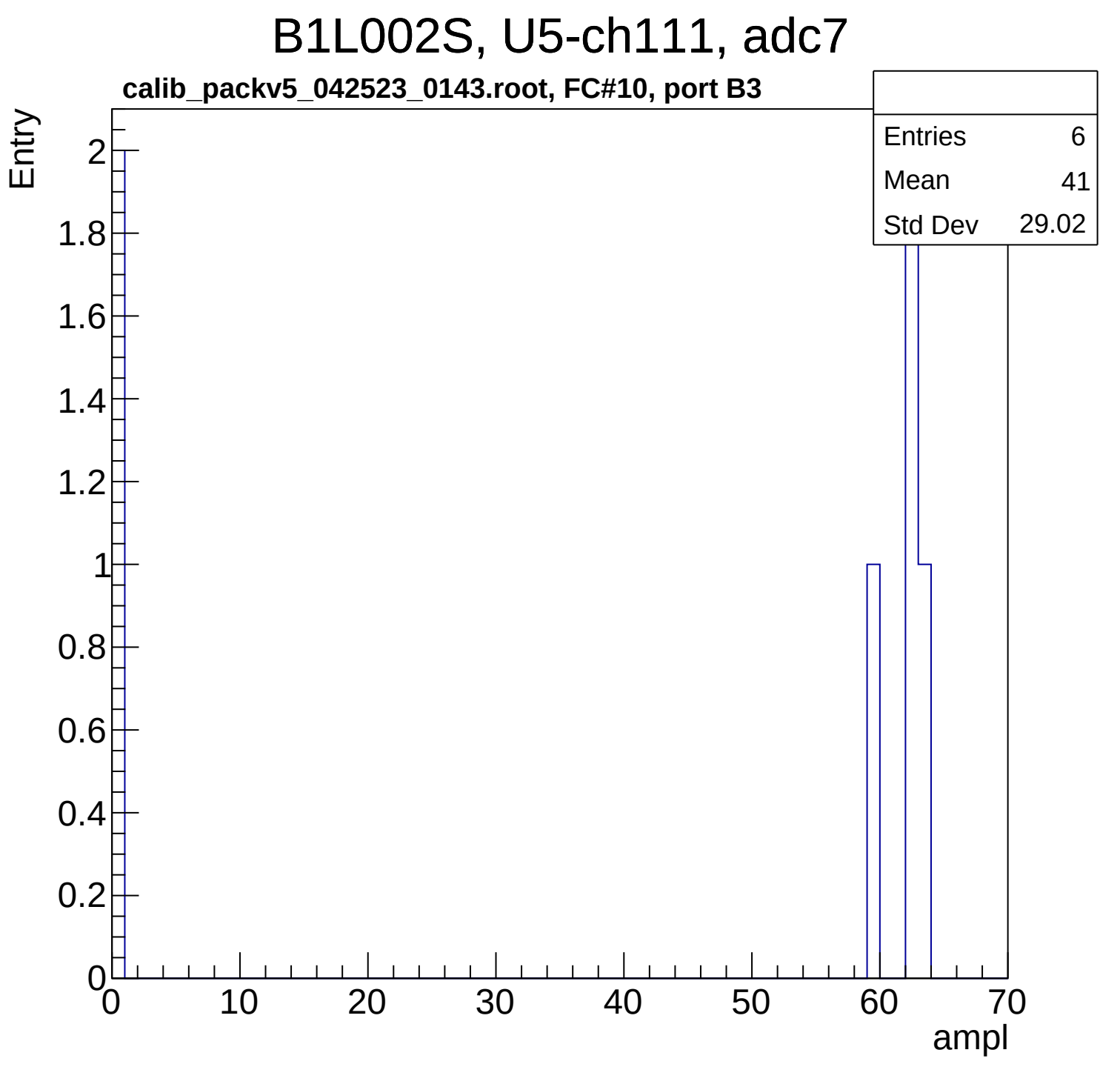

B1L002S, U5-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.61
Std Dev 14.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41
Std Dev 29.02
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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60
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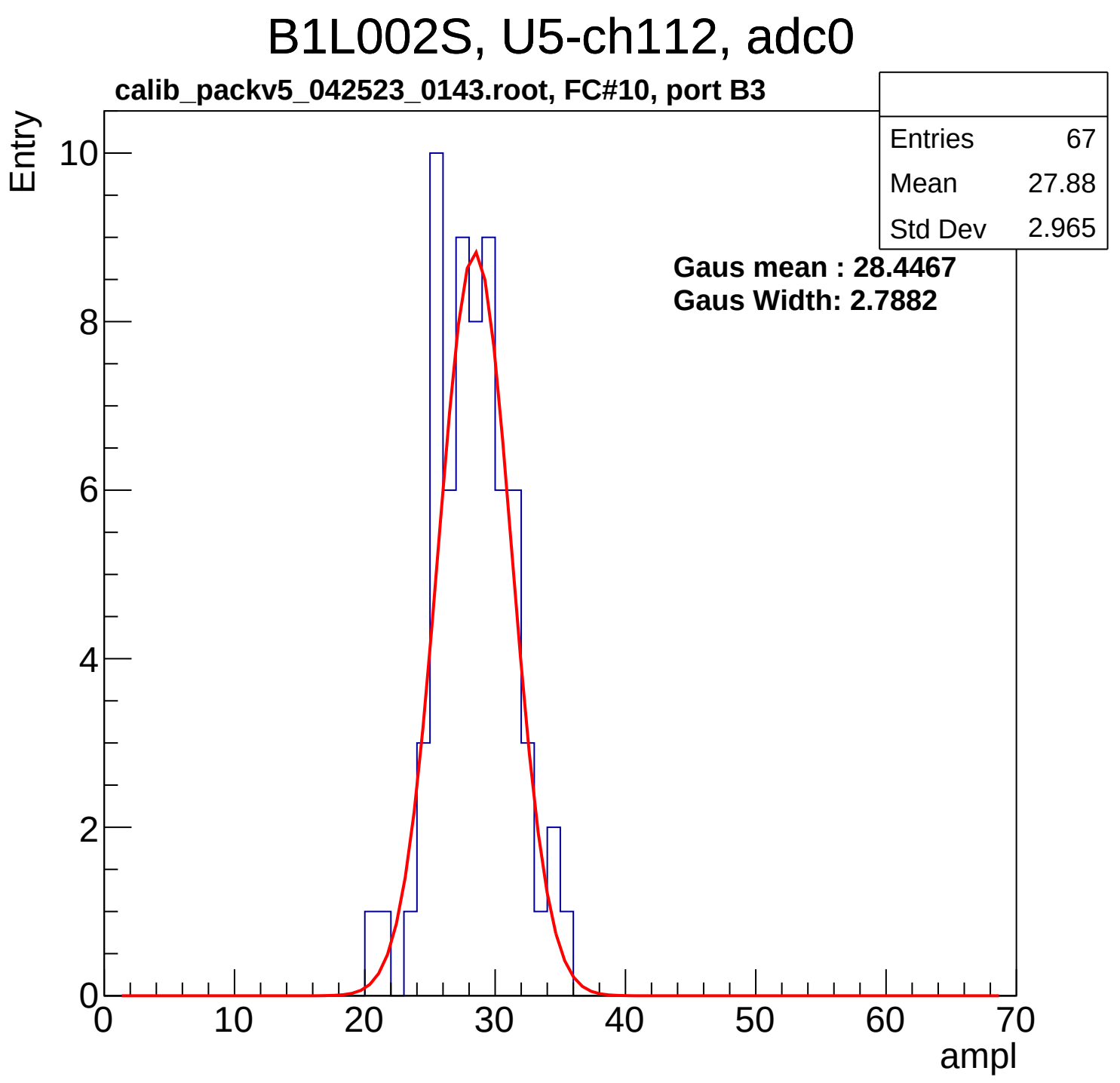
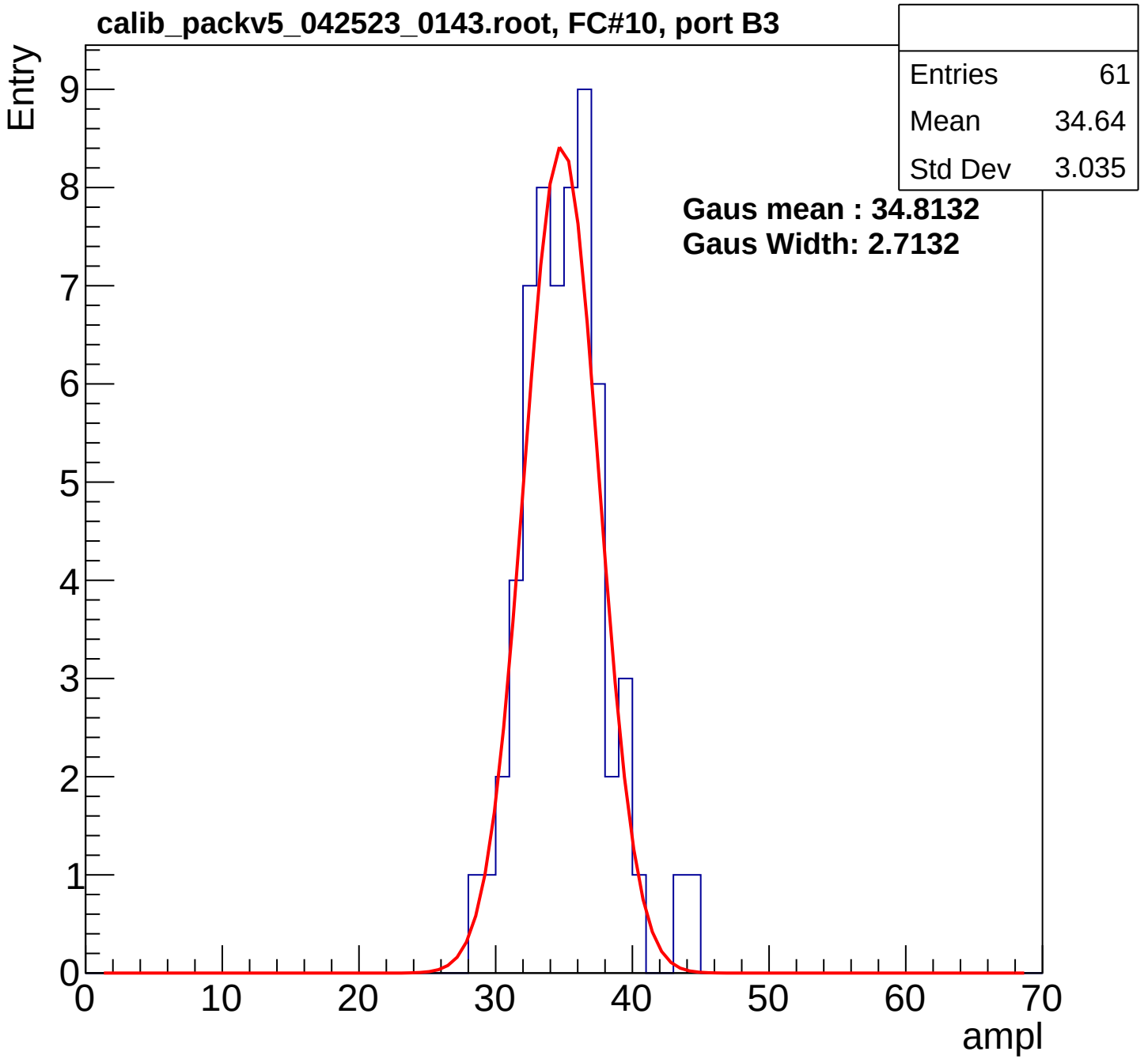

B1L002S, U5-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.88
Std Dev 2.965
Gaus mean : 28.4467
Gaus Width: 2.7882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.64
Std Dev 3.035
Gaus mean : 34.8132
Gaus Width: 2.7132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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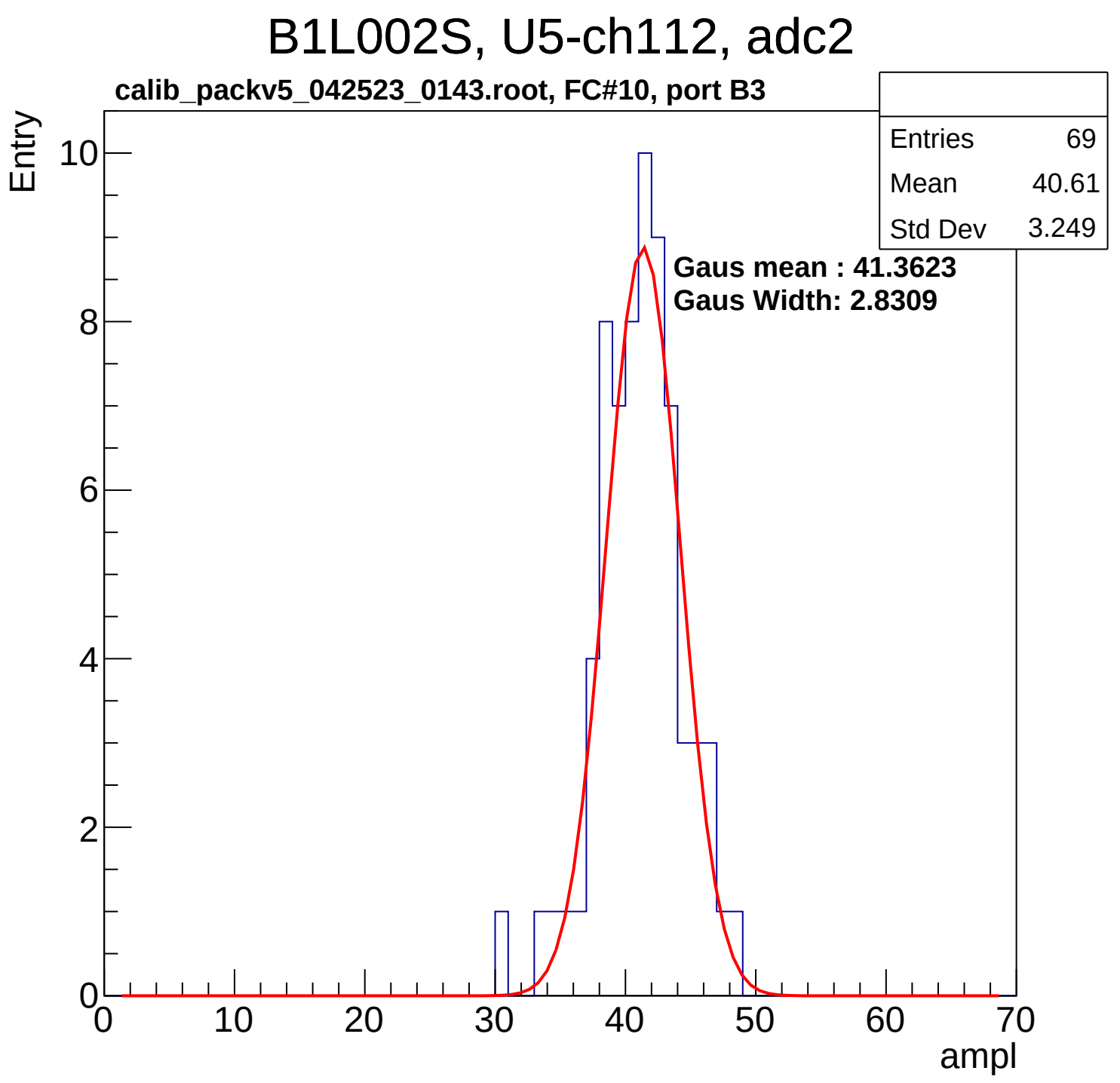
B1L002S, U5-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.61
Std Dev 3.249
Gaus mean : 41.3623
Gaus Width: 2.8309
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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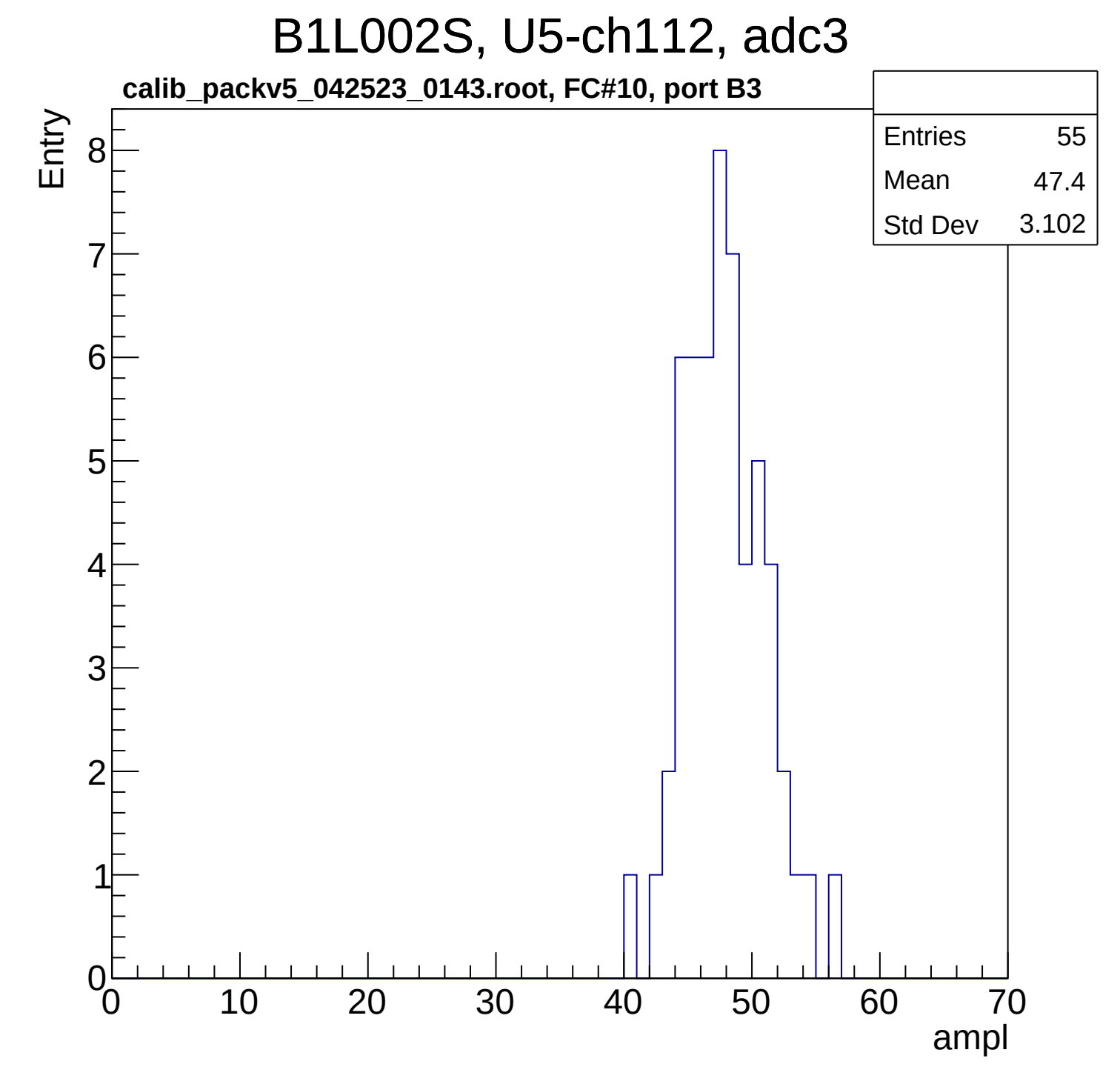
B1L002S, U5-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.4
Std Dev 3.102
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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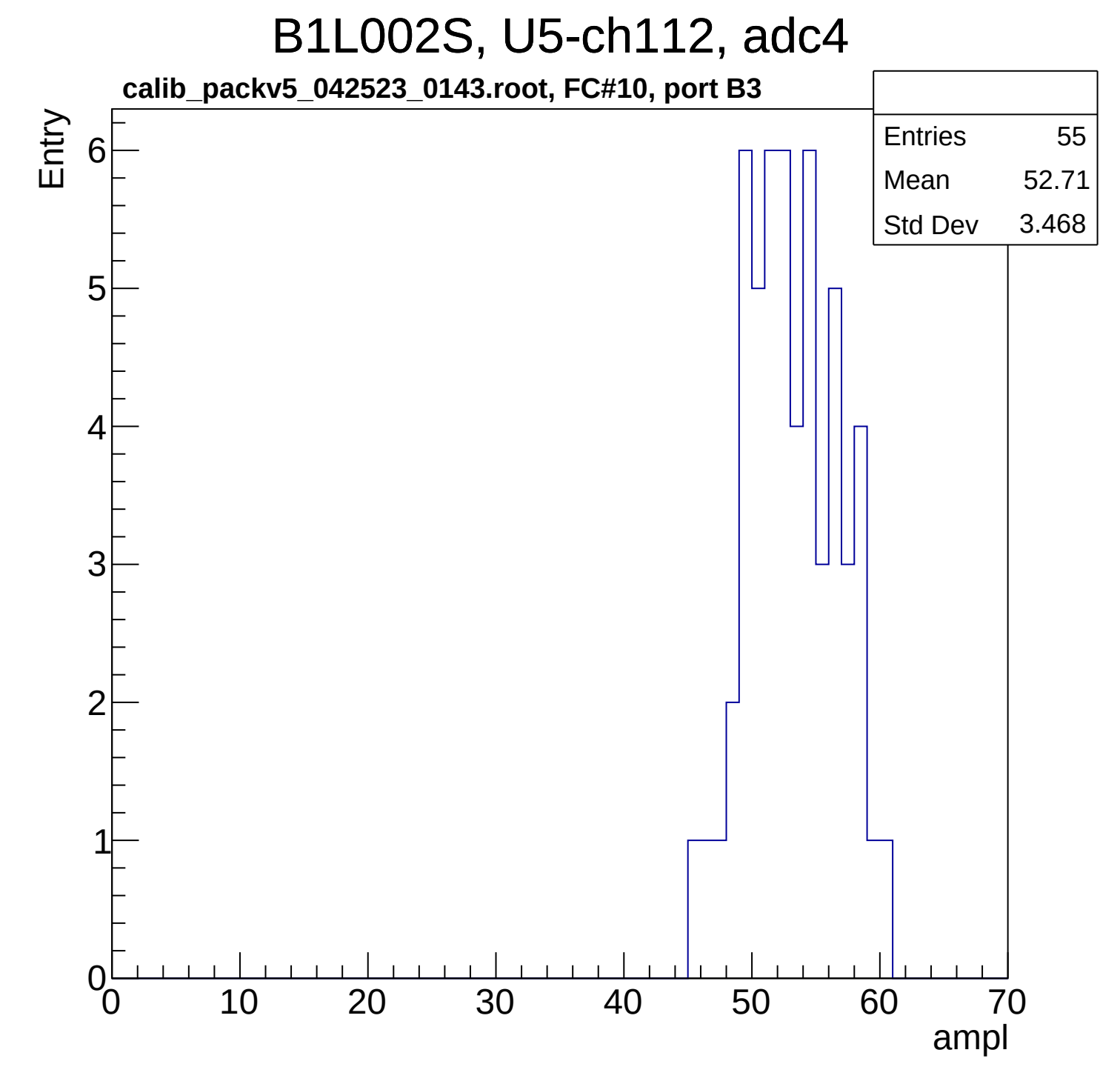
B1L002S, U5-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.71
Std Dev 3.468
Entry
ampl
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1
2
3
4
5
6
0
10
20
30
40
50
60
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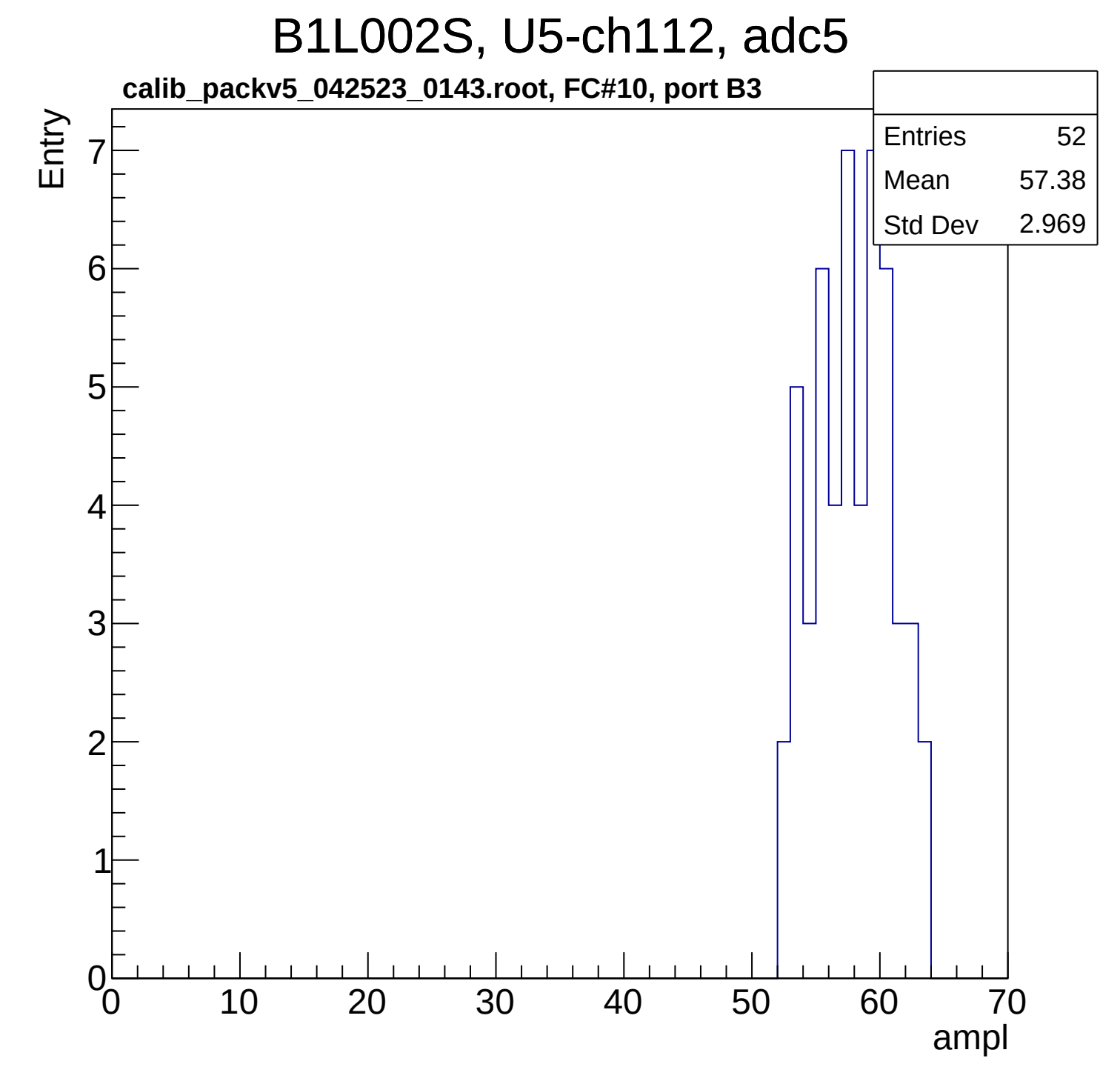
B1L002S, U5-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.38
Std Dev 2.969
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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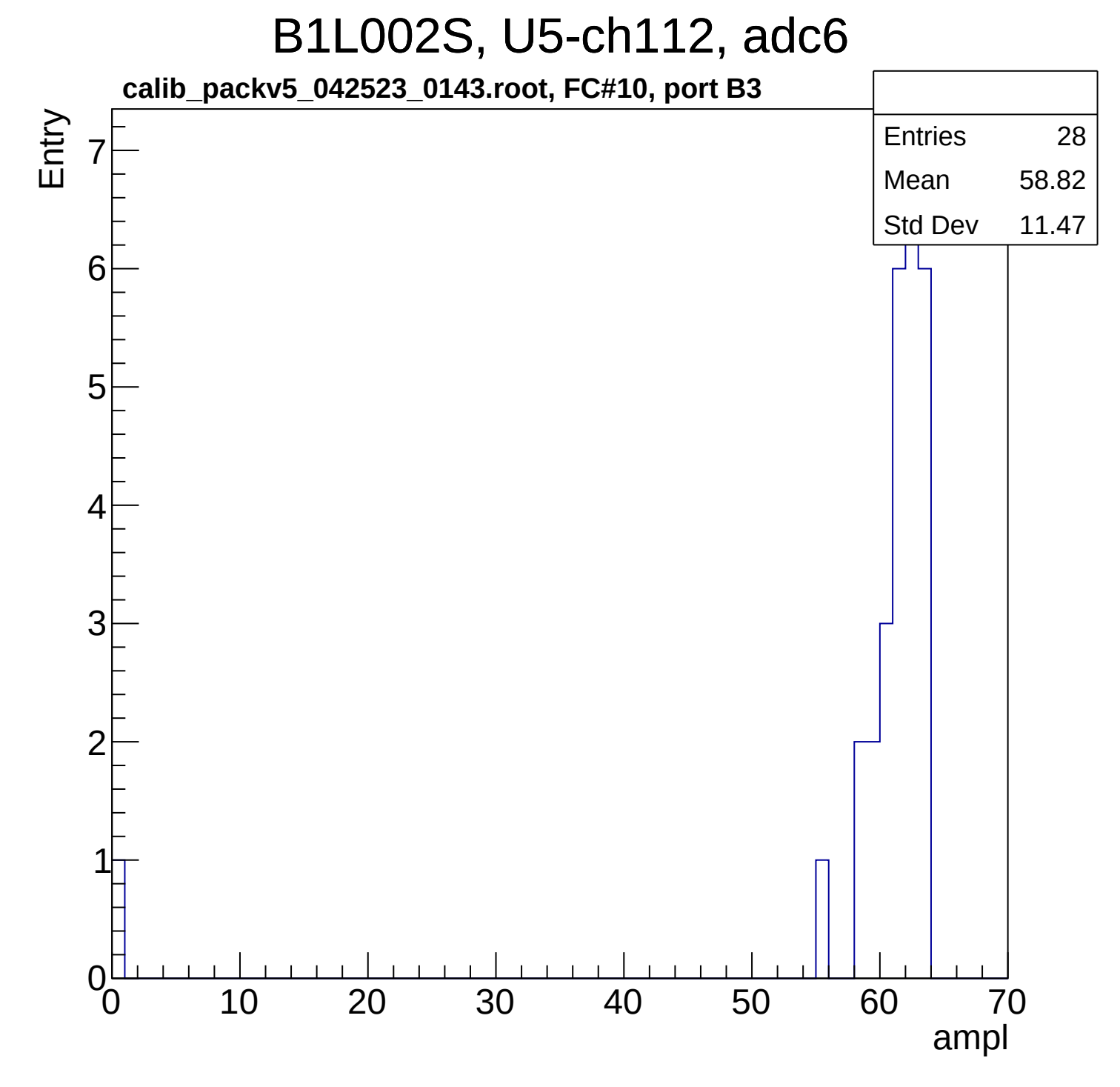
B1L002S, U5-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.82
Std Dev 11.47
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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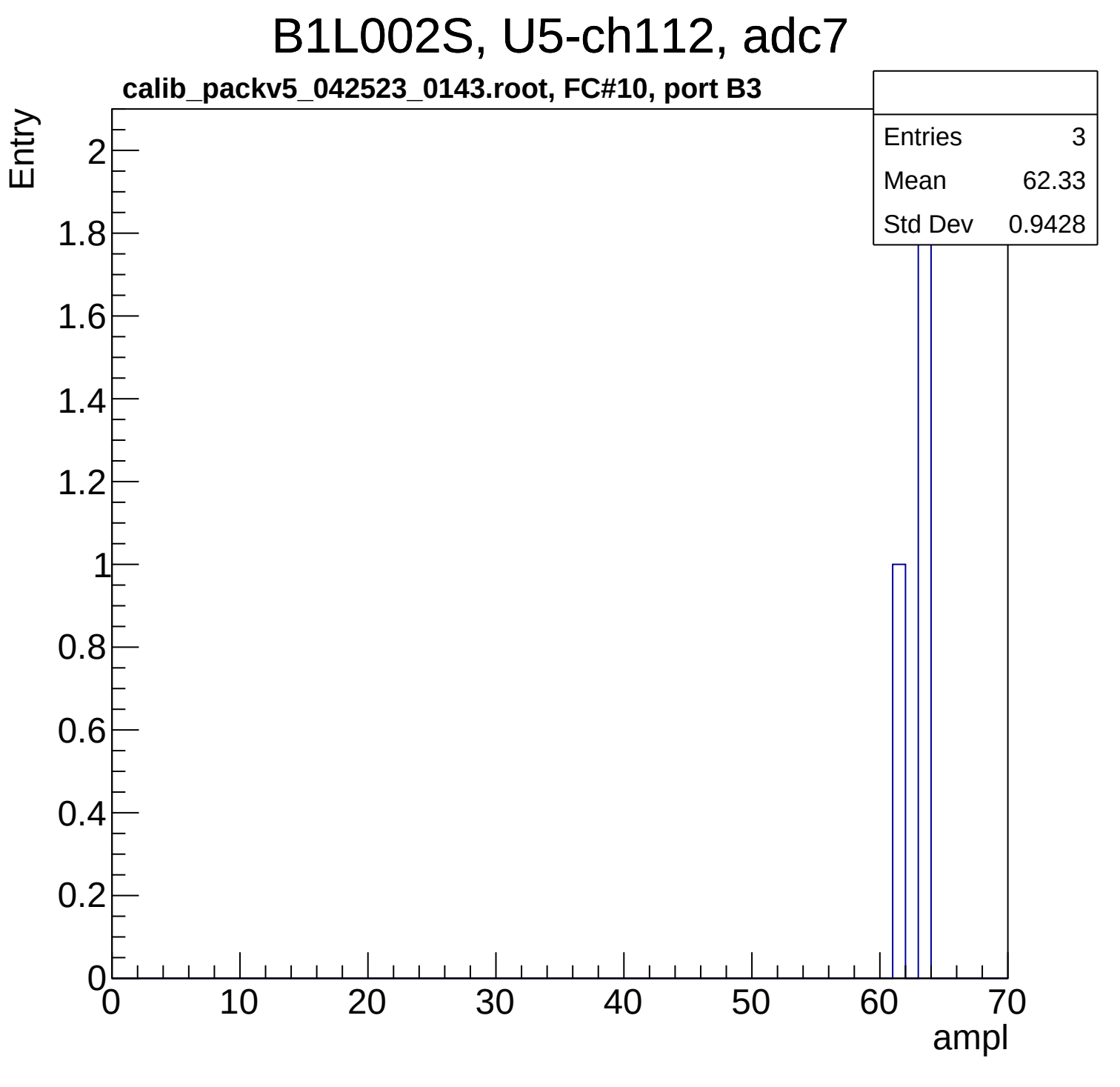
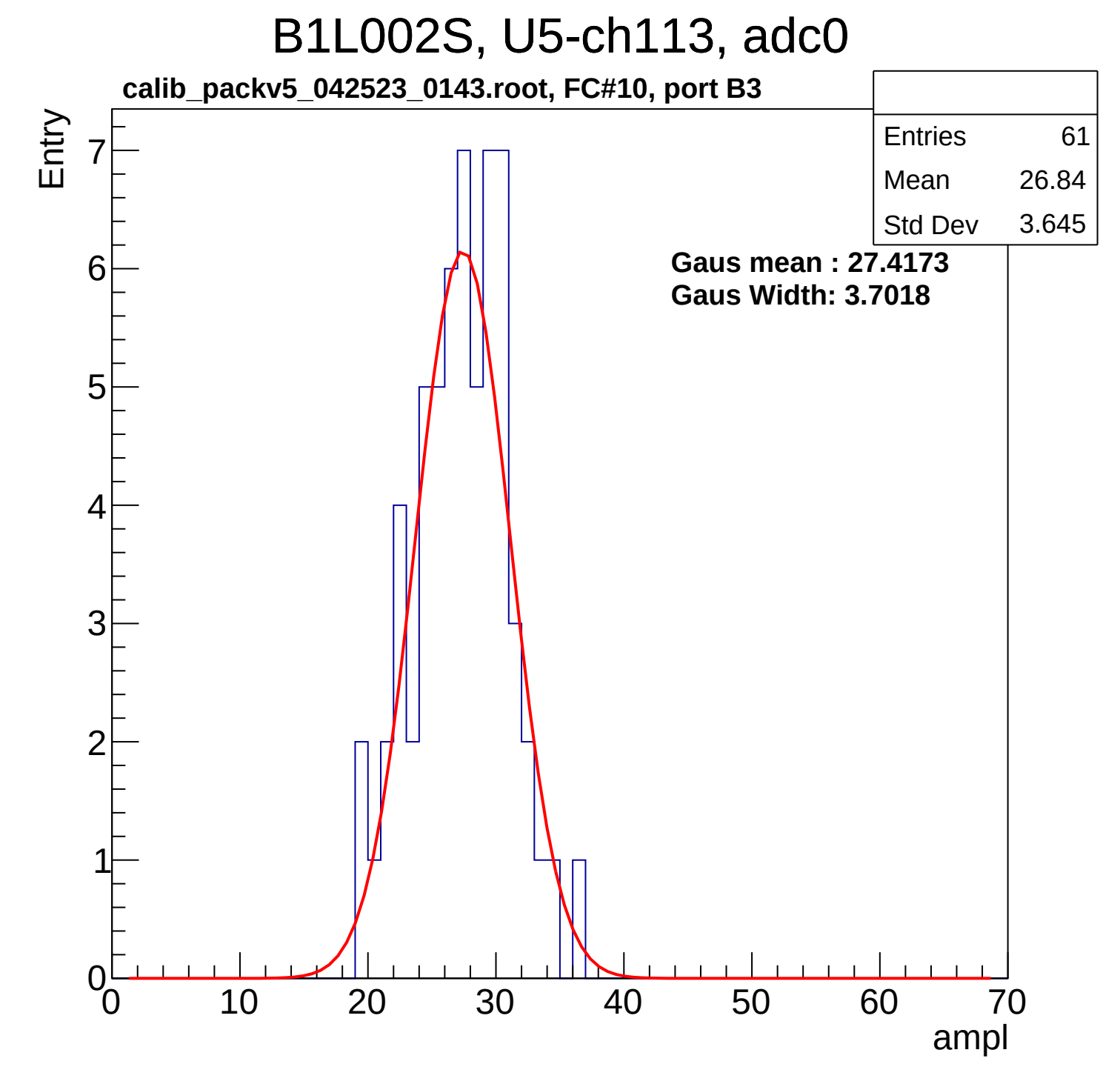

B1L002S, U5-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.84
Std Dev 3.645
Gaus mean : 27.4173
Gaus Width: 3.7018
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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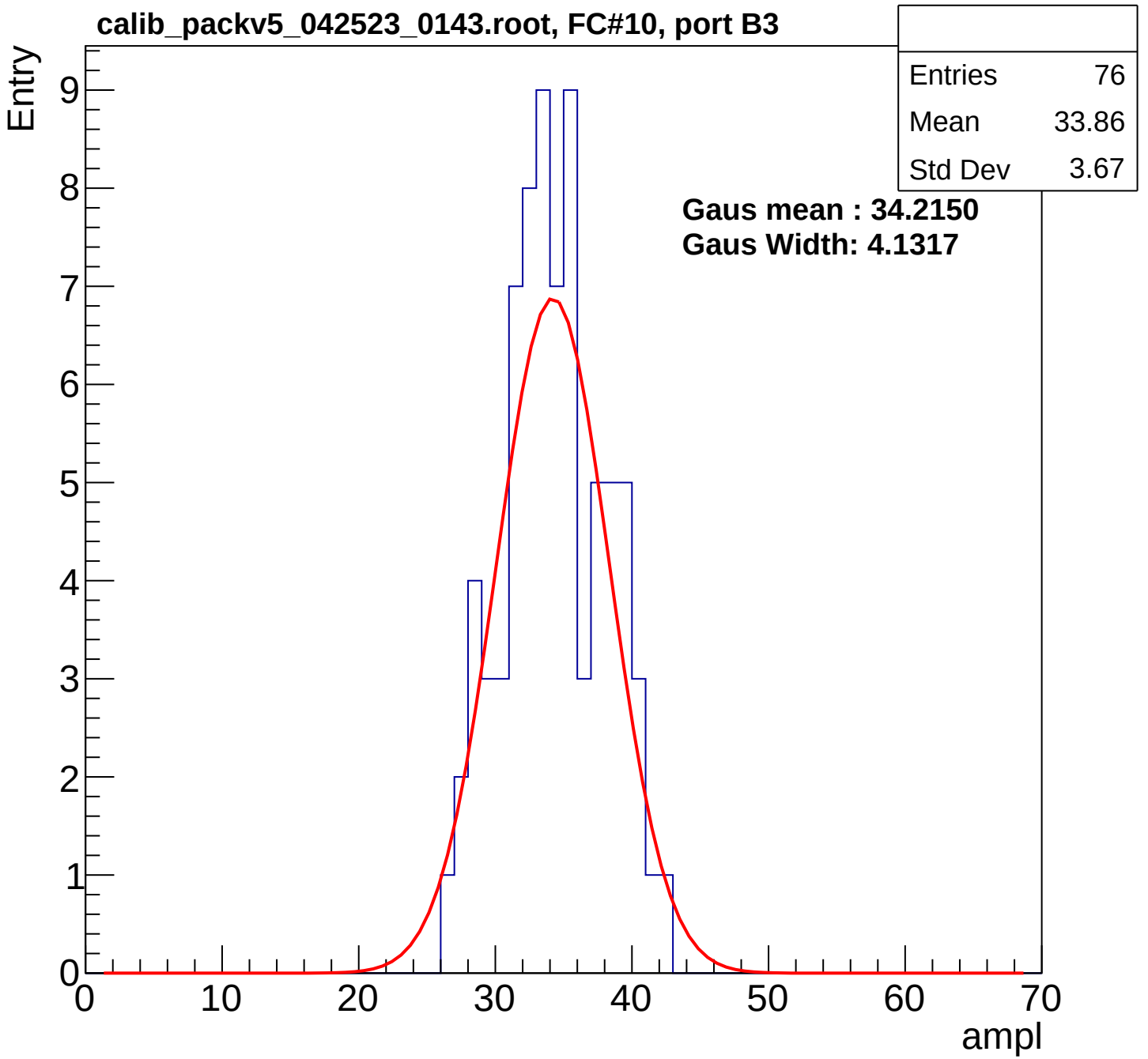
B1L002S, U5-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.86
Std Dev 3.67
Gaus mean : 34.2150
Gaus Width: 4.1317
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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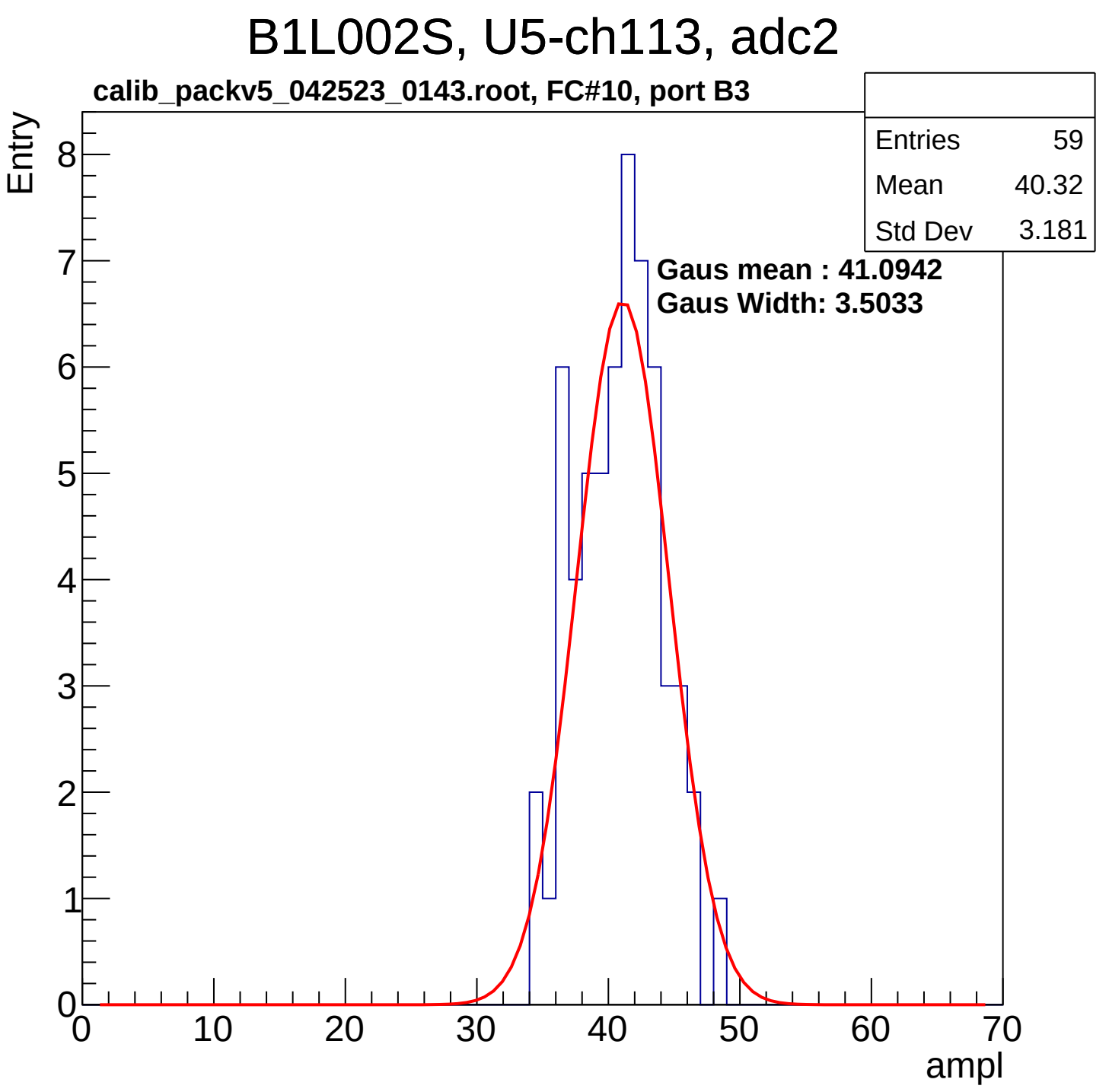
B1L002S, U5-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.32
Std Dev 3.181
Gaus mean : 41.0942
Gaus Width: 3.5033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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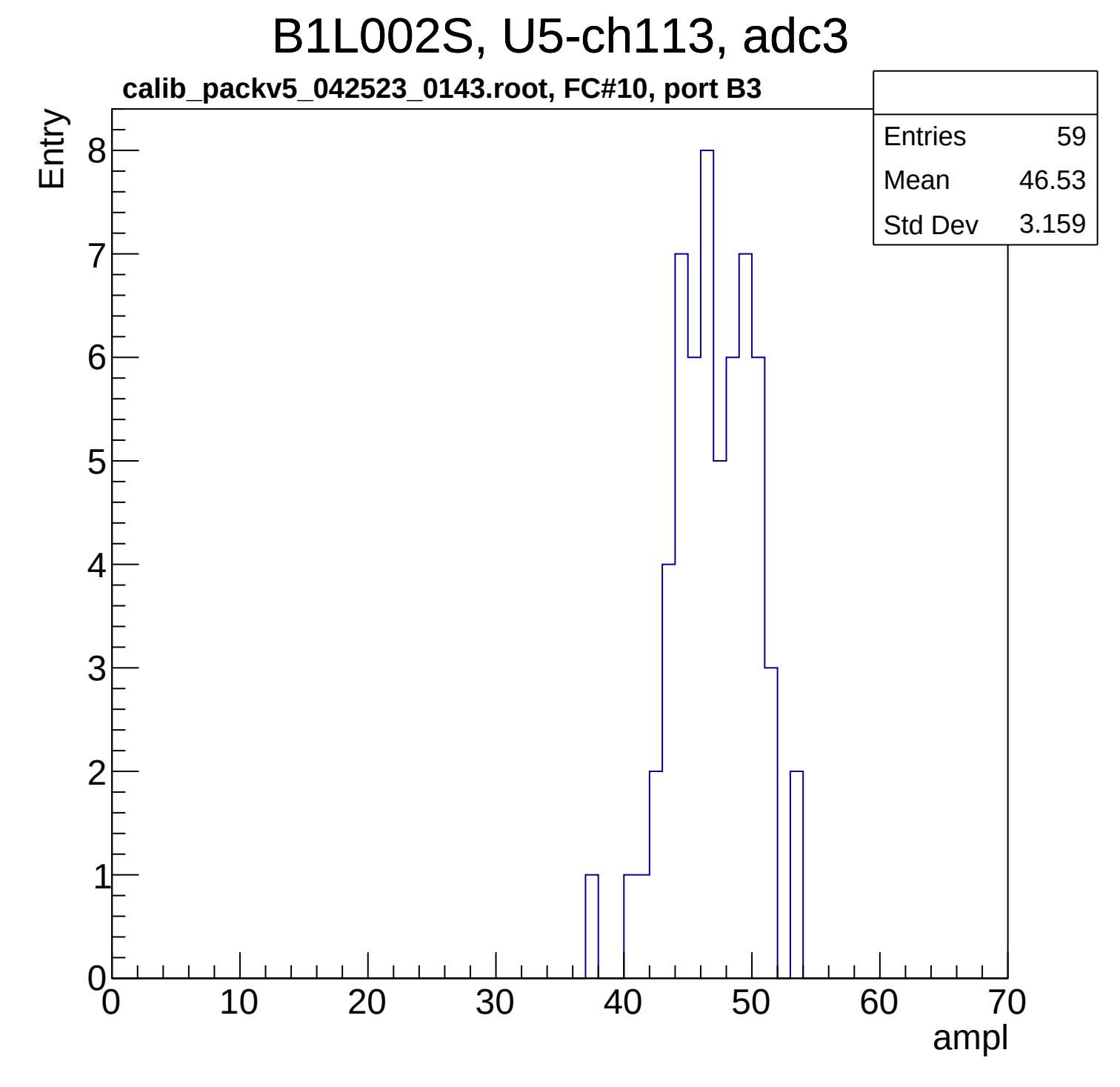
B1L002S, U5-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.53
Std Dev 3.159
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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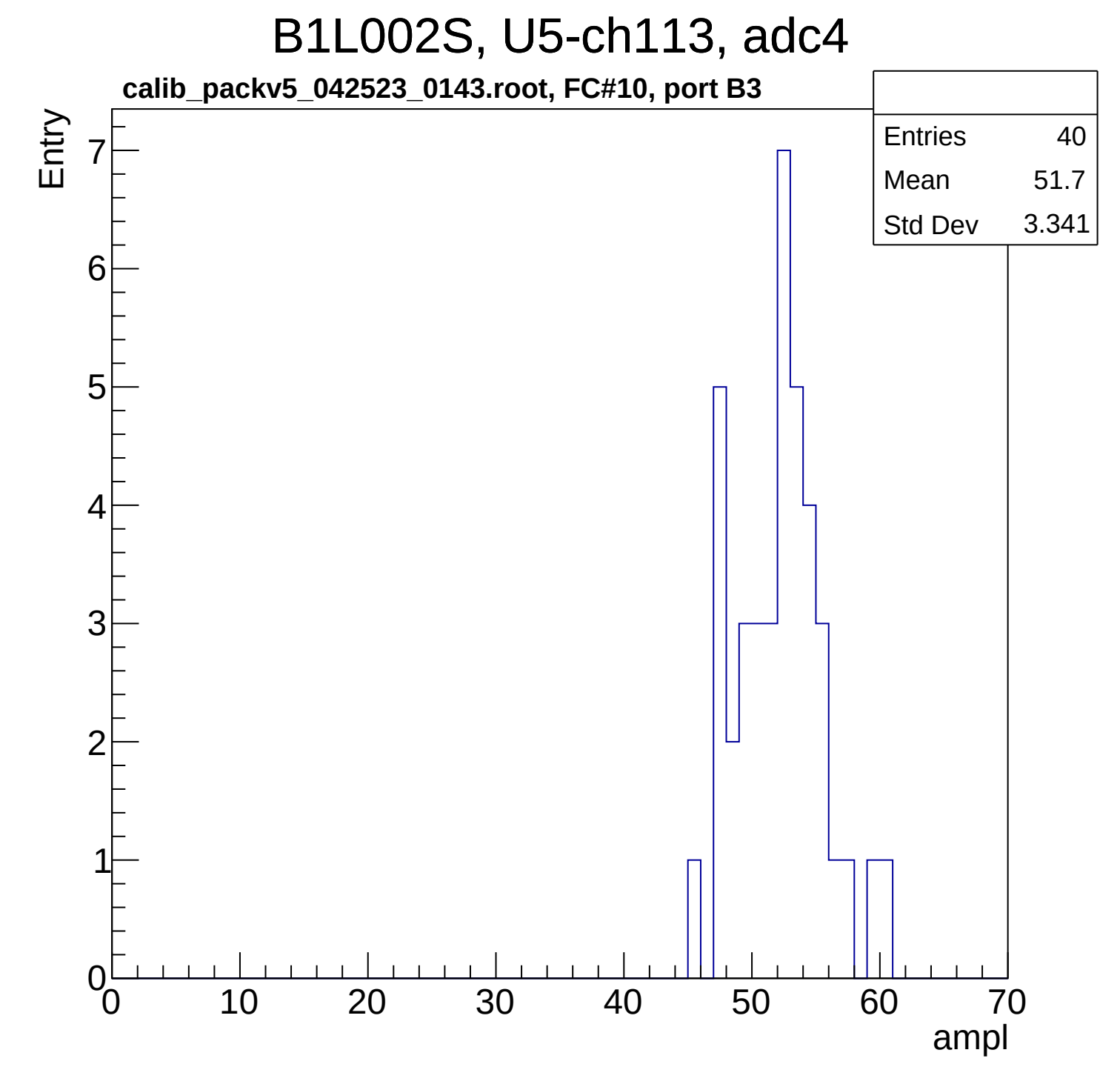
B1L002S, U5-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 51.7
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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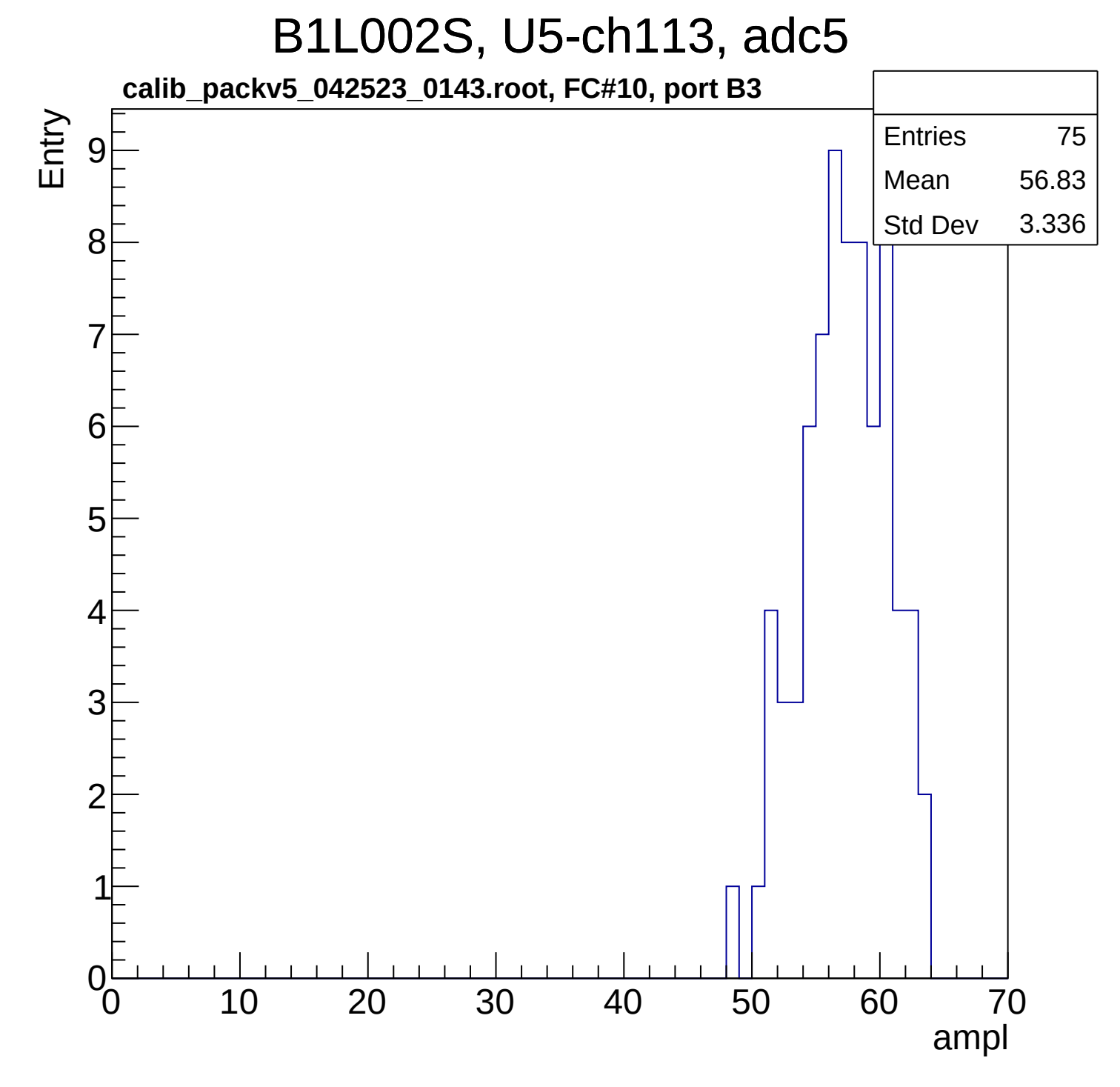
B1L002S, U5-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 56.83
Std Dev 3.336
Entry
ampl
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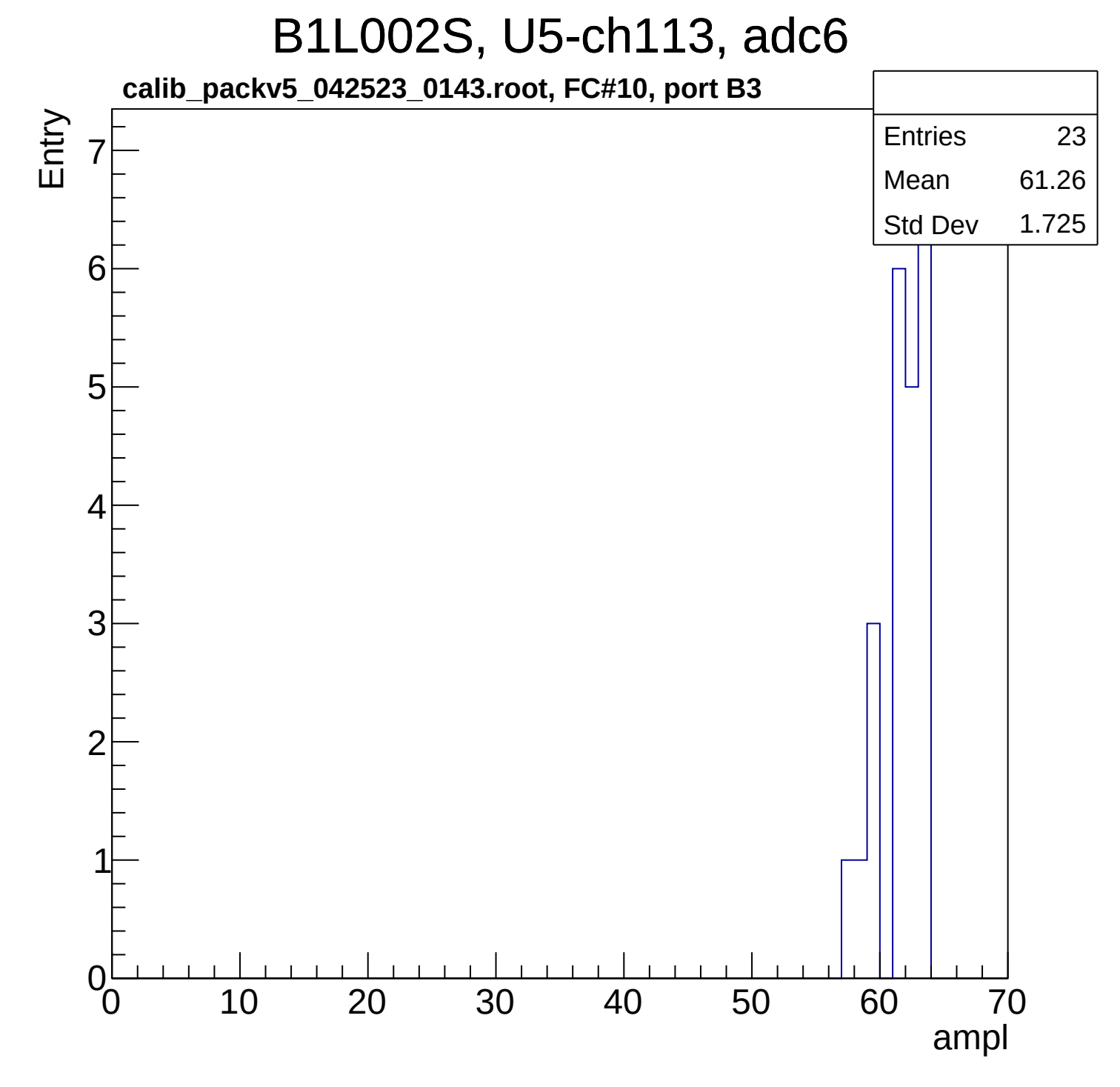
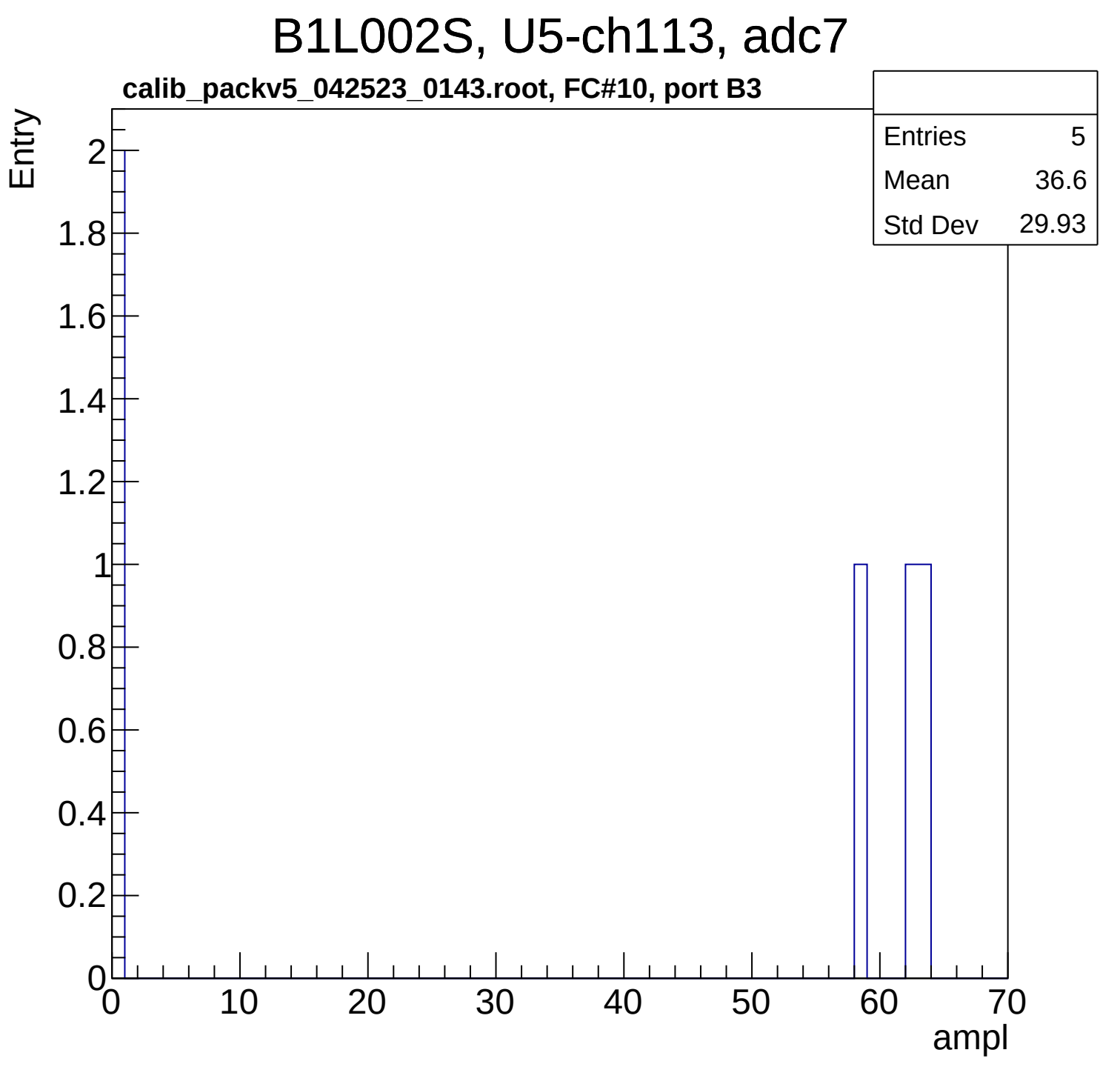
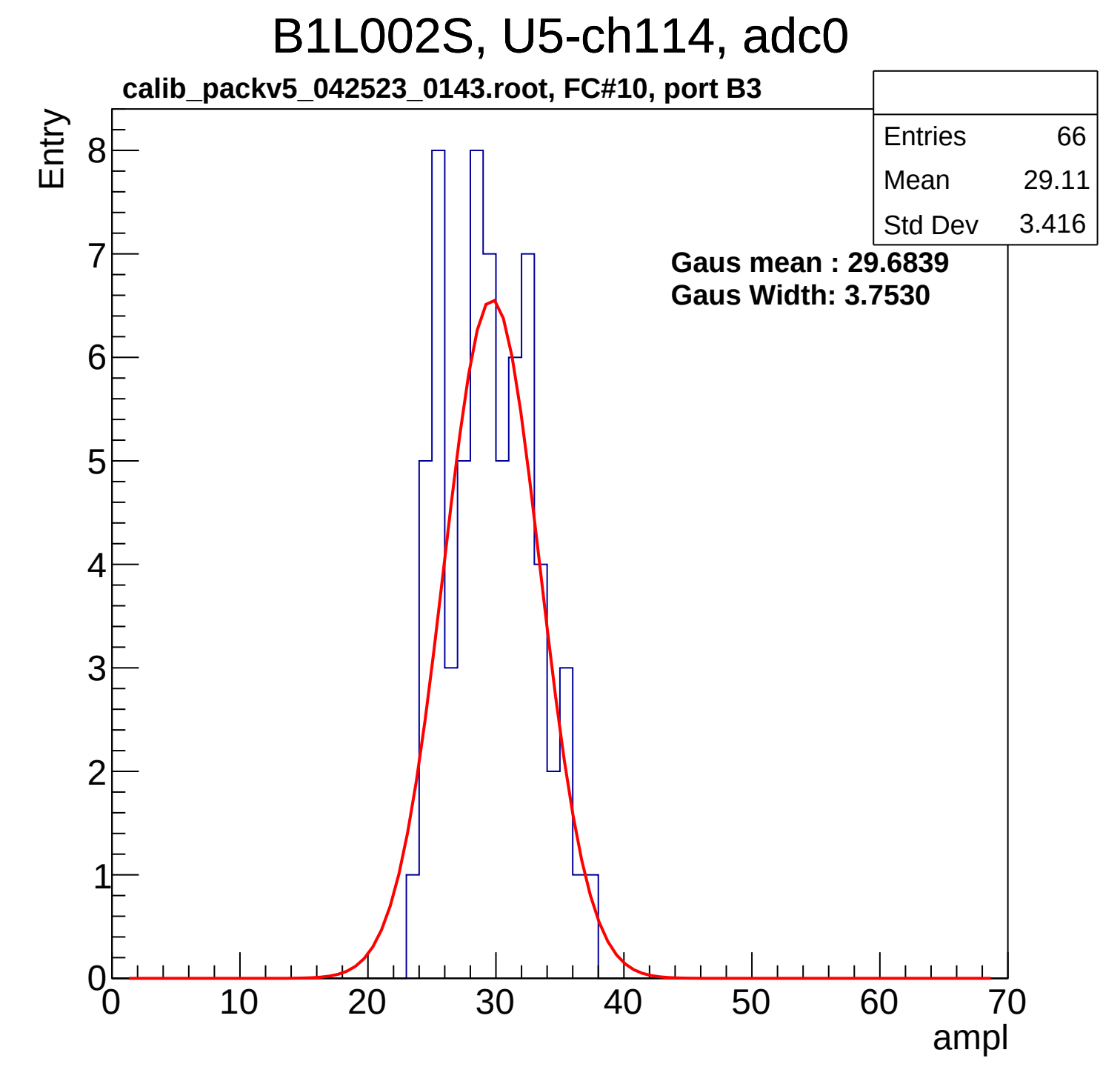
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.26
Std Dev 1.725
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 36.6
Std Dev 29.93
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U5-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.11
Std Dev 3.416
Gaus mean : 29.6839
Gaus Width: 3.7530
Entry
ampl
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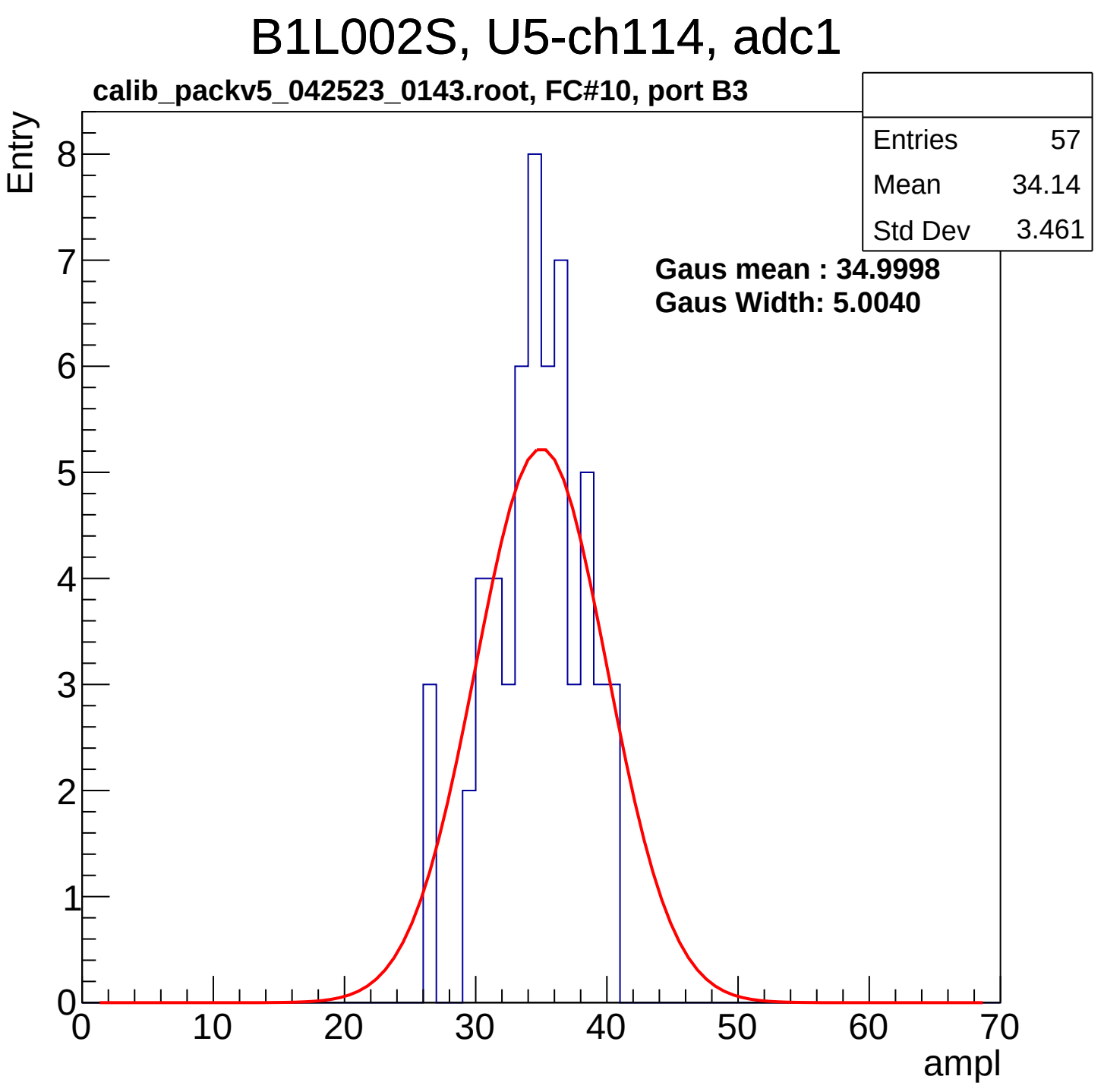
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.14
Std Dev 3.461
Gaus mean : 34.9998
Gaus Width: 5.0040
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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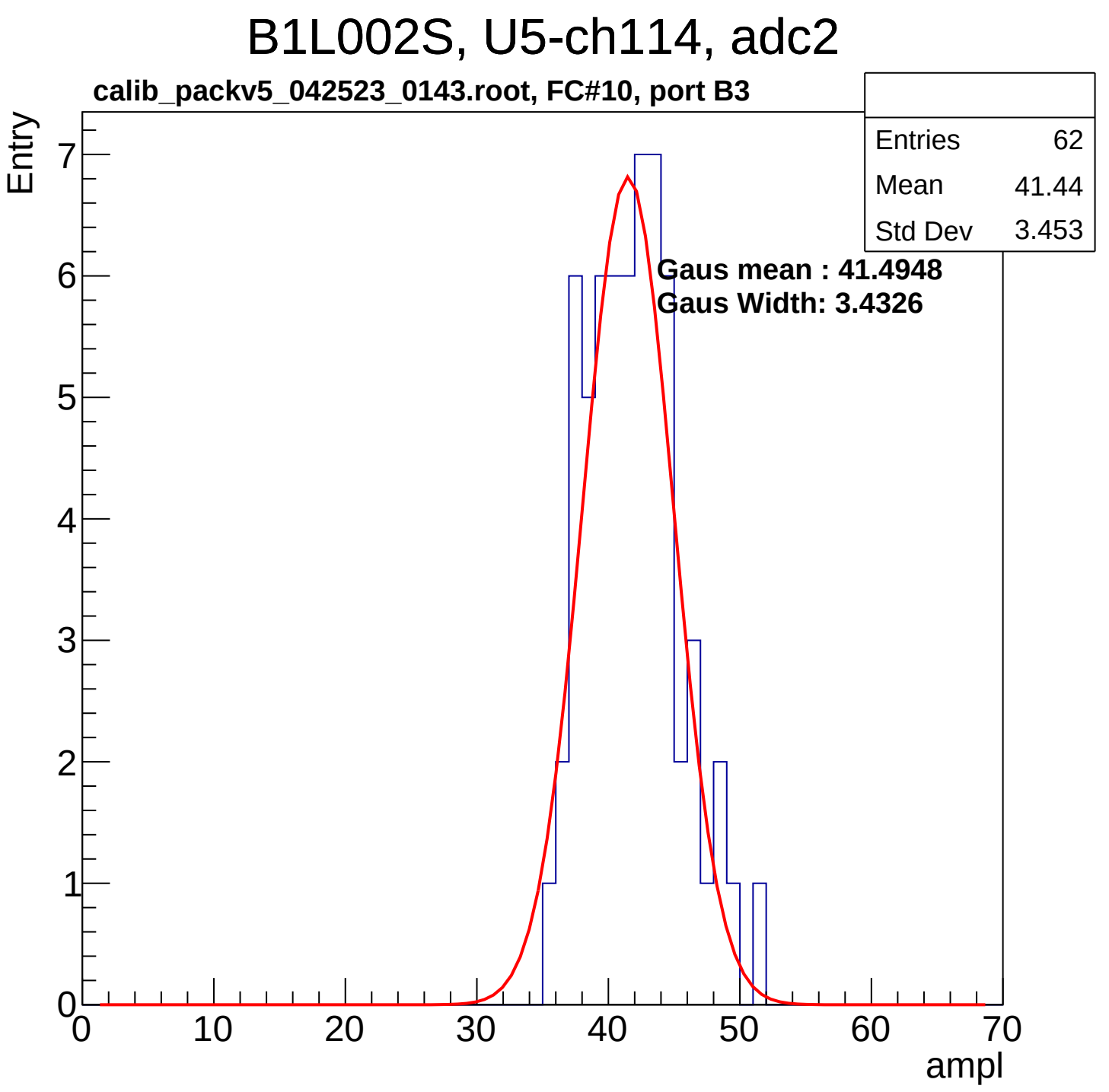
B1L002S, U5-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.44
Std Dev 3.453
Gaus mean : 41.4948
Gaus Width: 3.4326
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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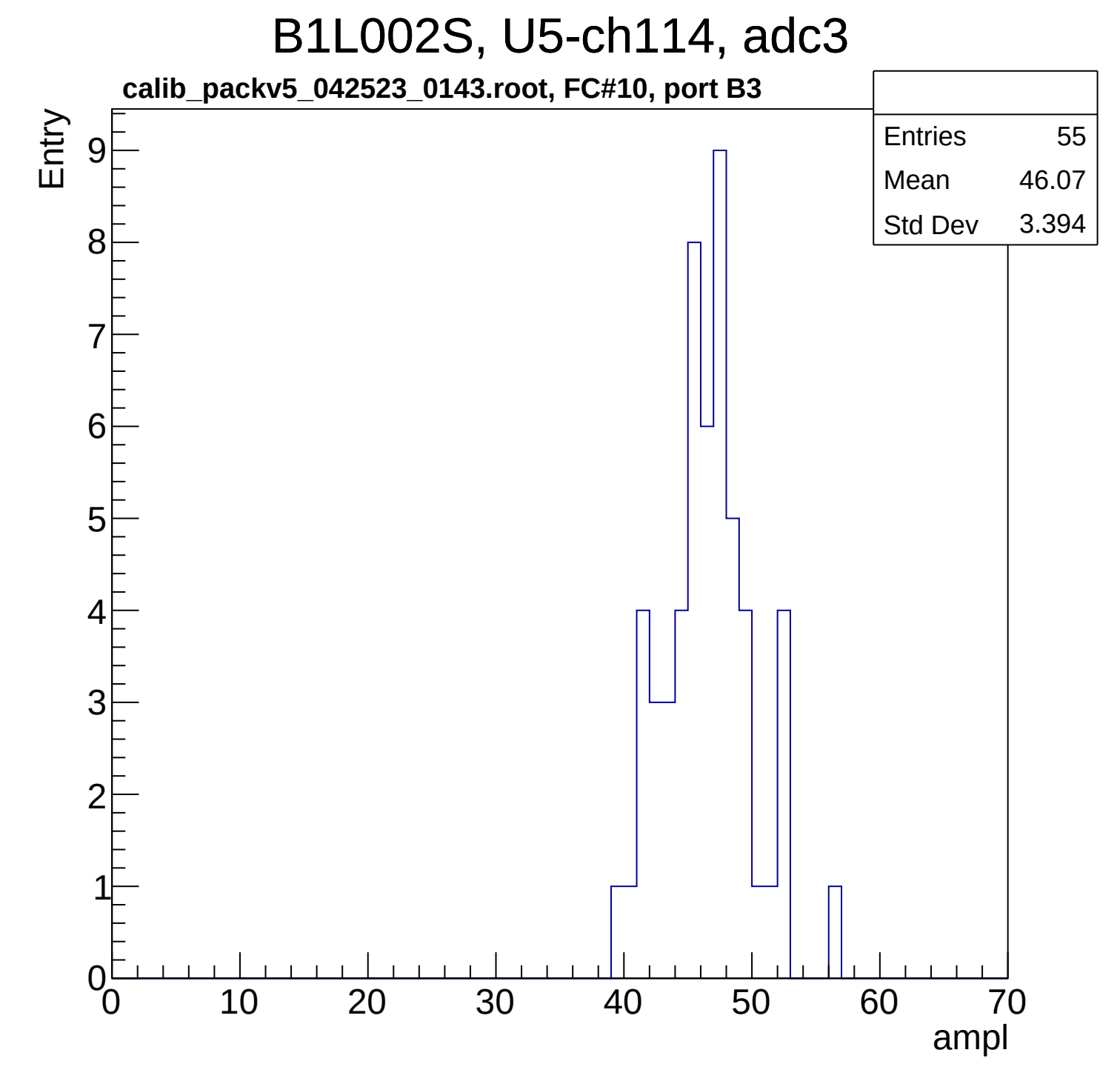
B1L002S, U5-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.07
Std Dev 3.394
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
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60
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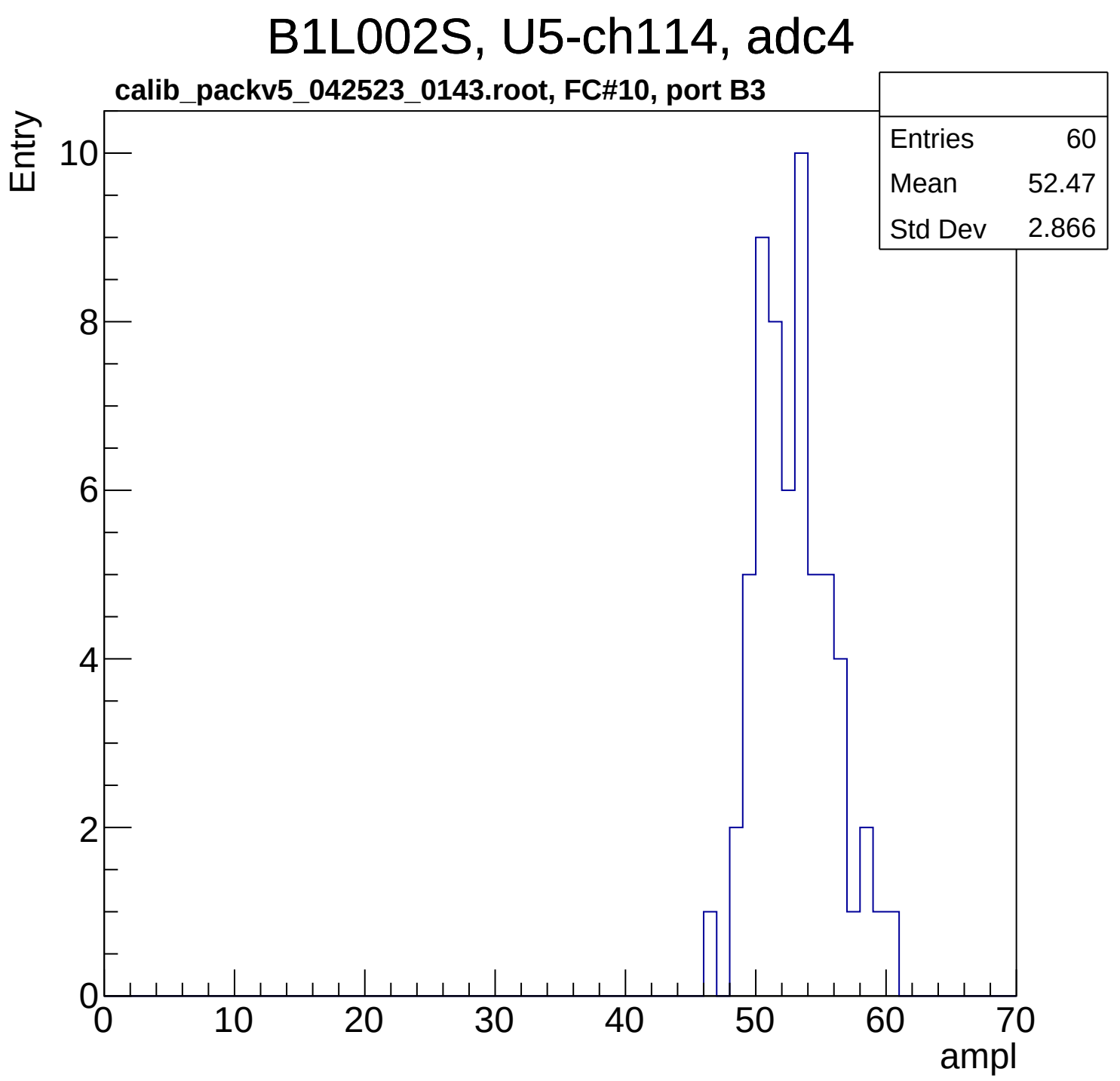
B1L002S, U5-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.47
Std Dev 2.866
Entry
ampl
0
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4
6
8
10
0
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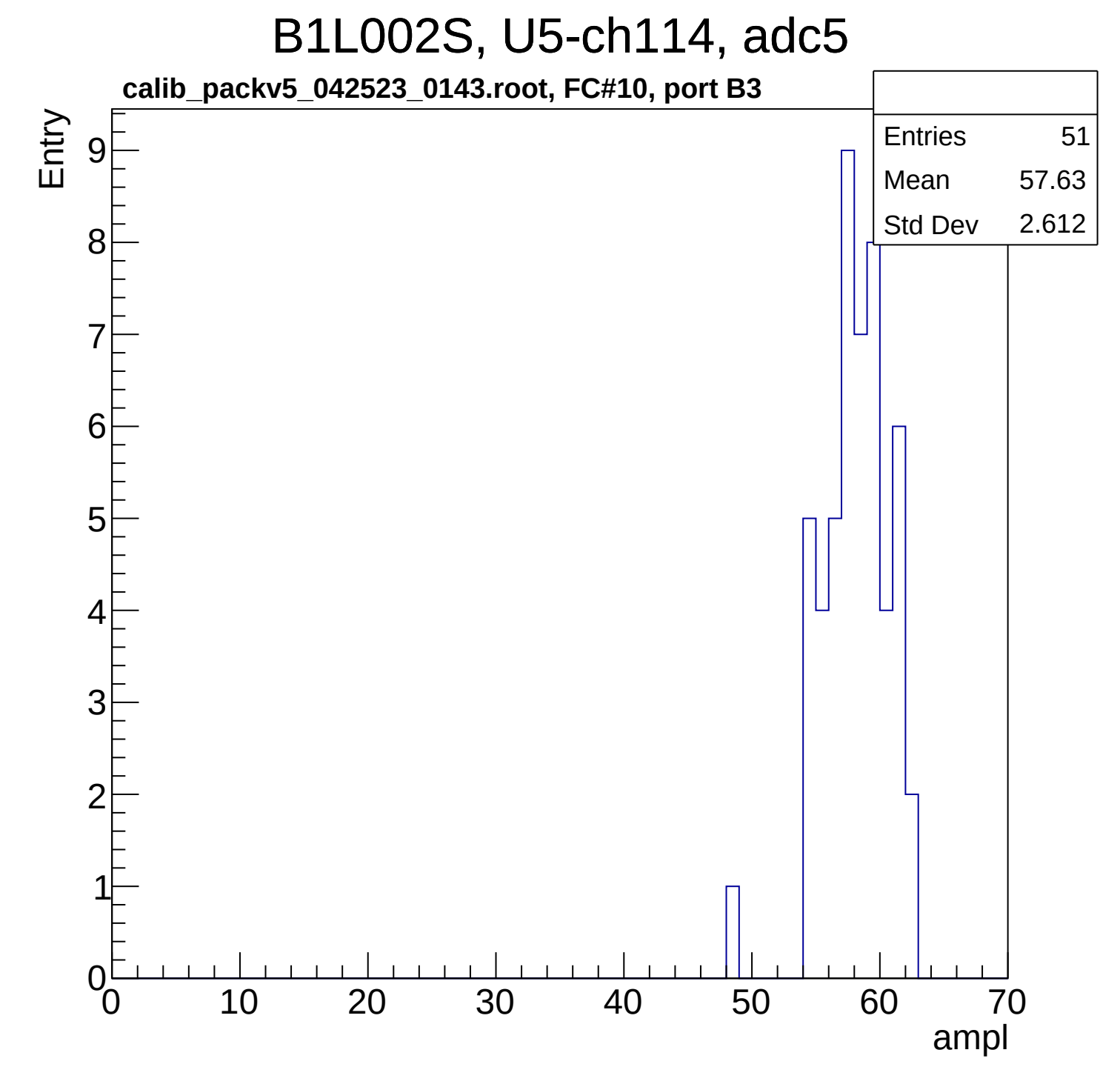
20
30
40
50
60
70


B1L002S, U5-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.63
Std Dev 2.612
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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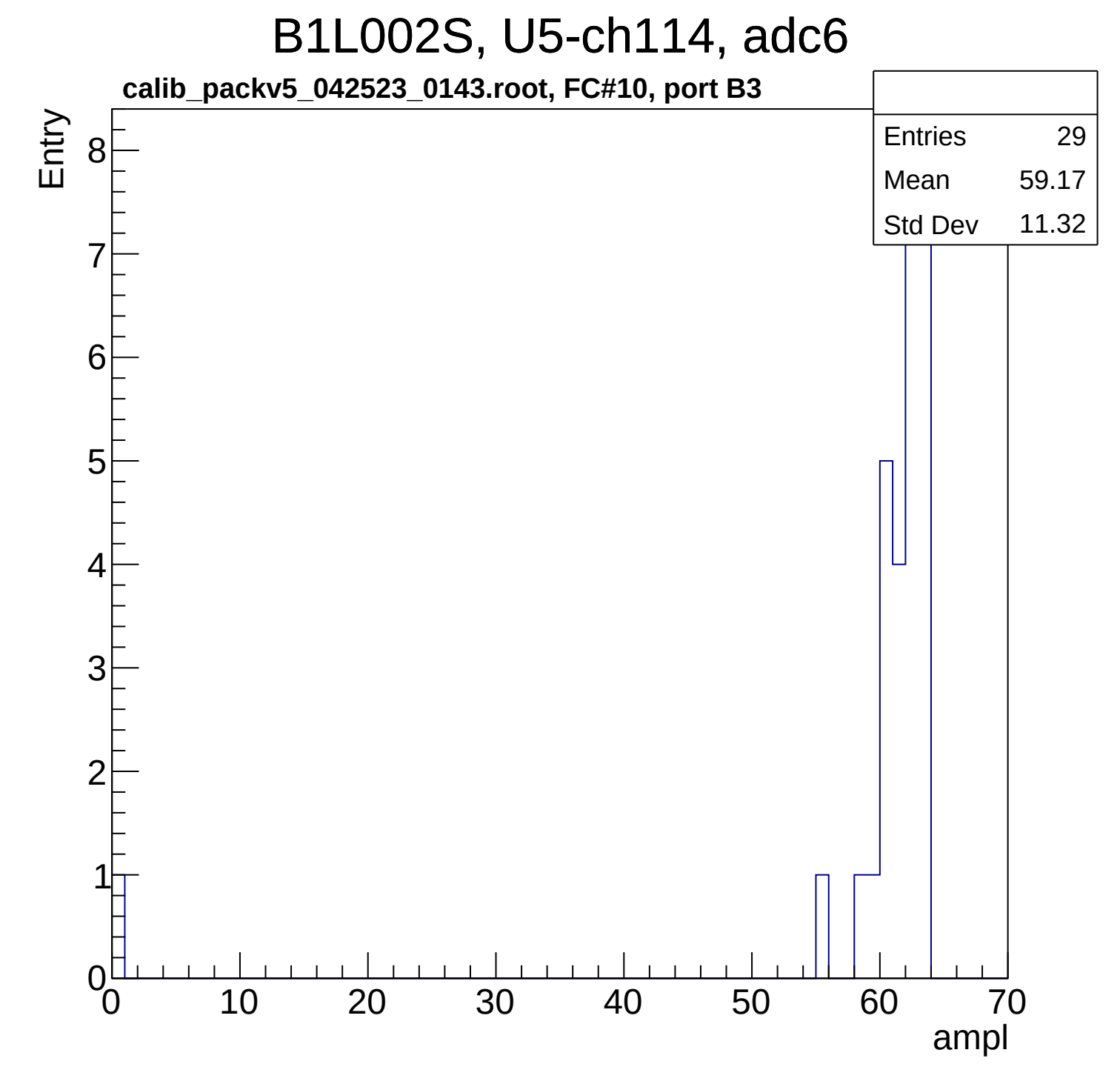
B1L002S, U5-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.17
Std Dev 11.32
Entry
ampl
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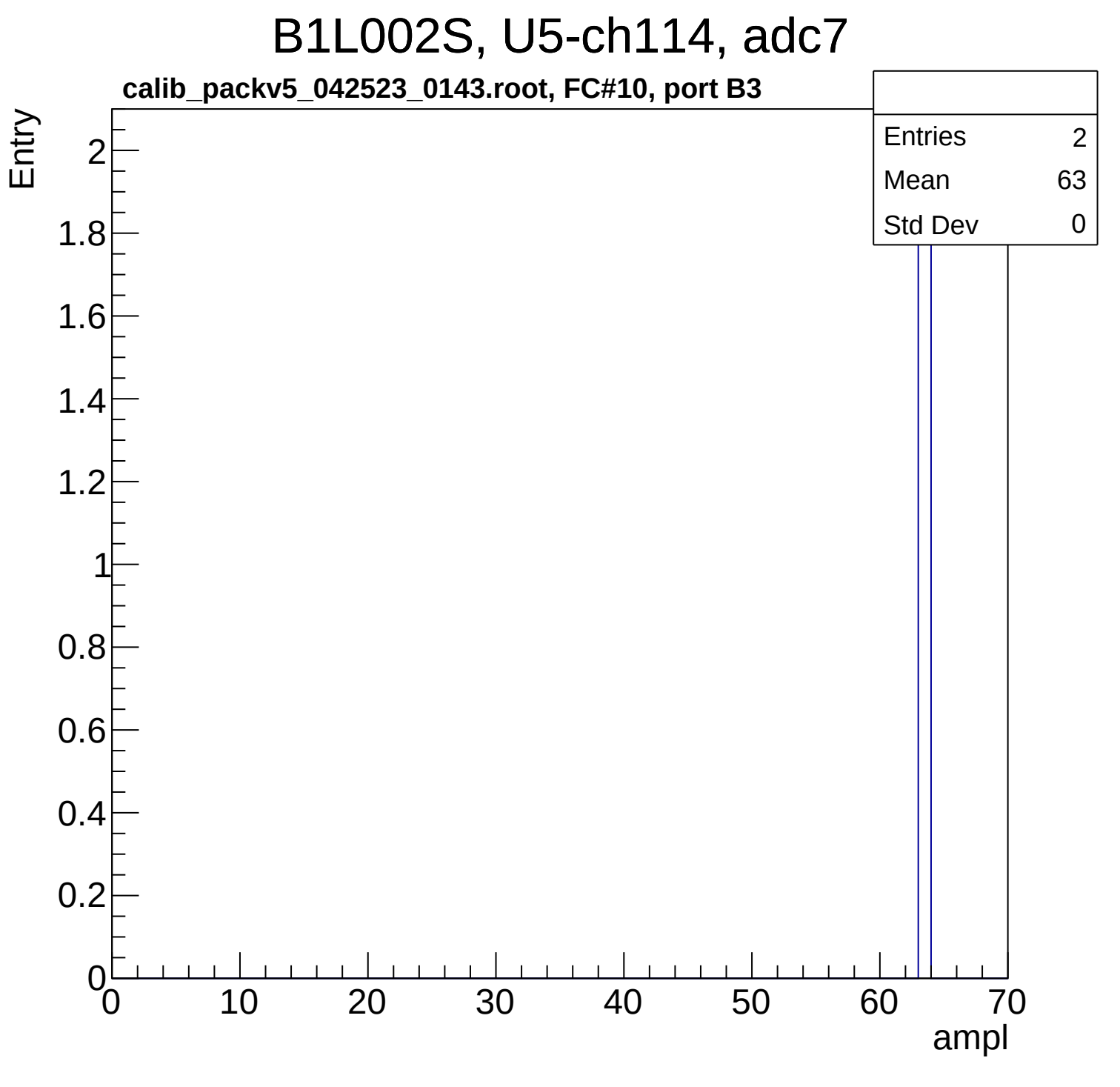
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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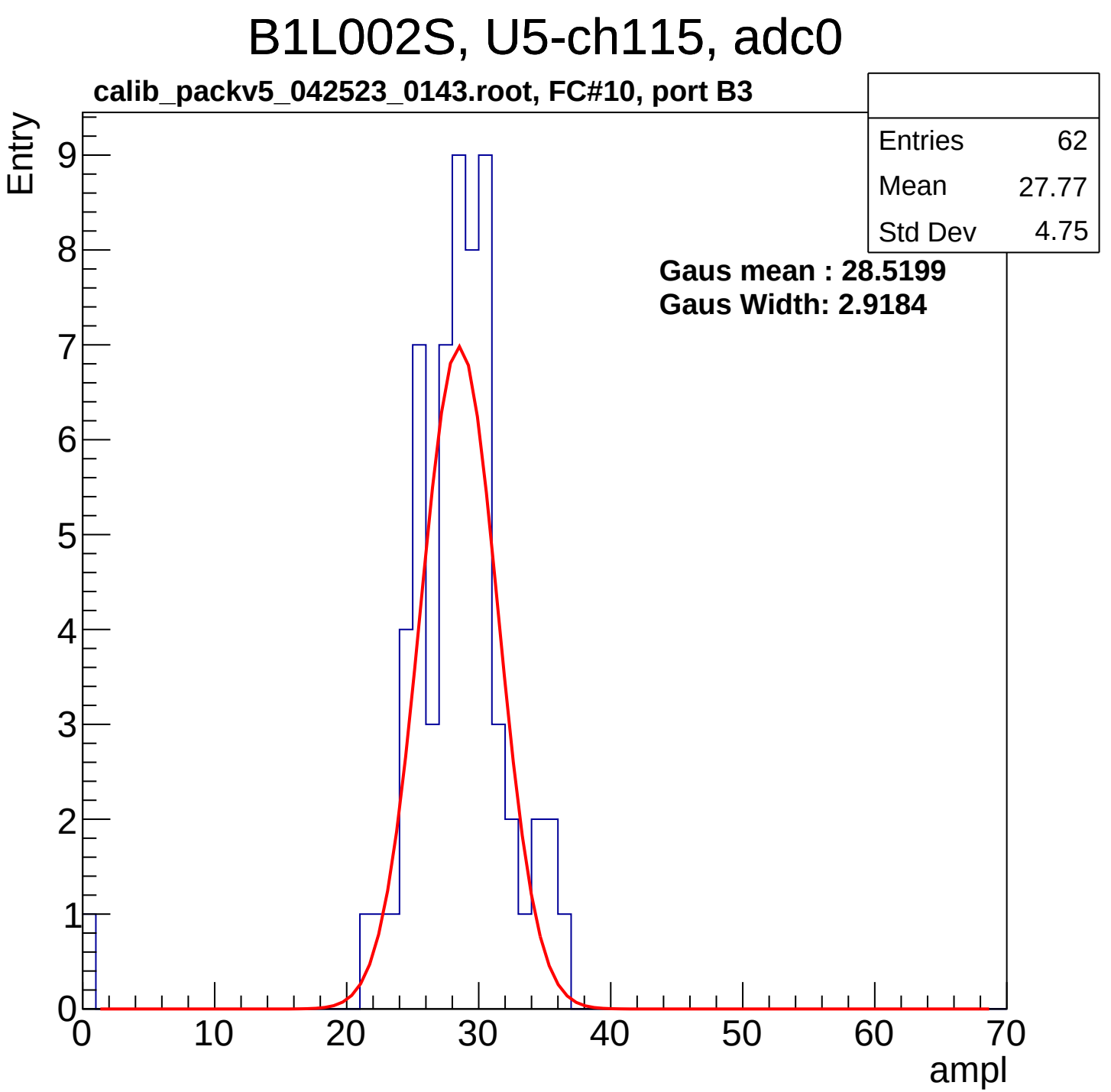
B1L002S, U5-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.77
Std Dev 4.75
Gaus mean : 28.5199
Gaus Width: 2.9184
Entry
ampl
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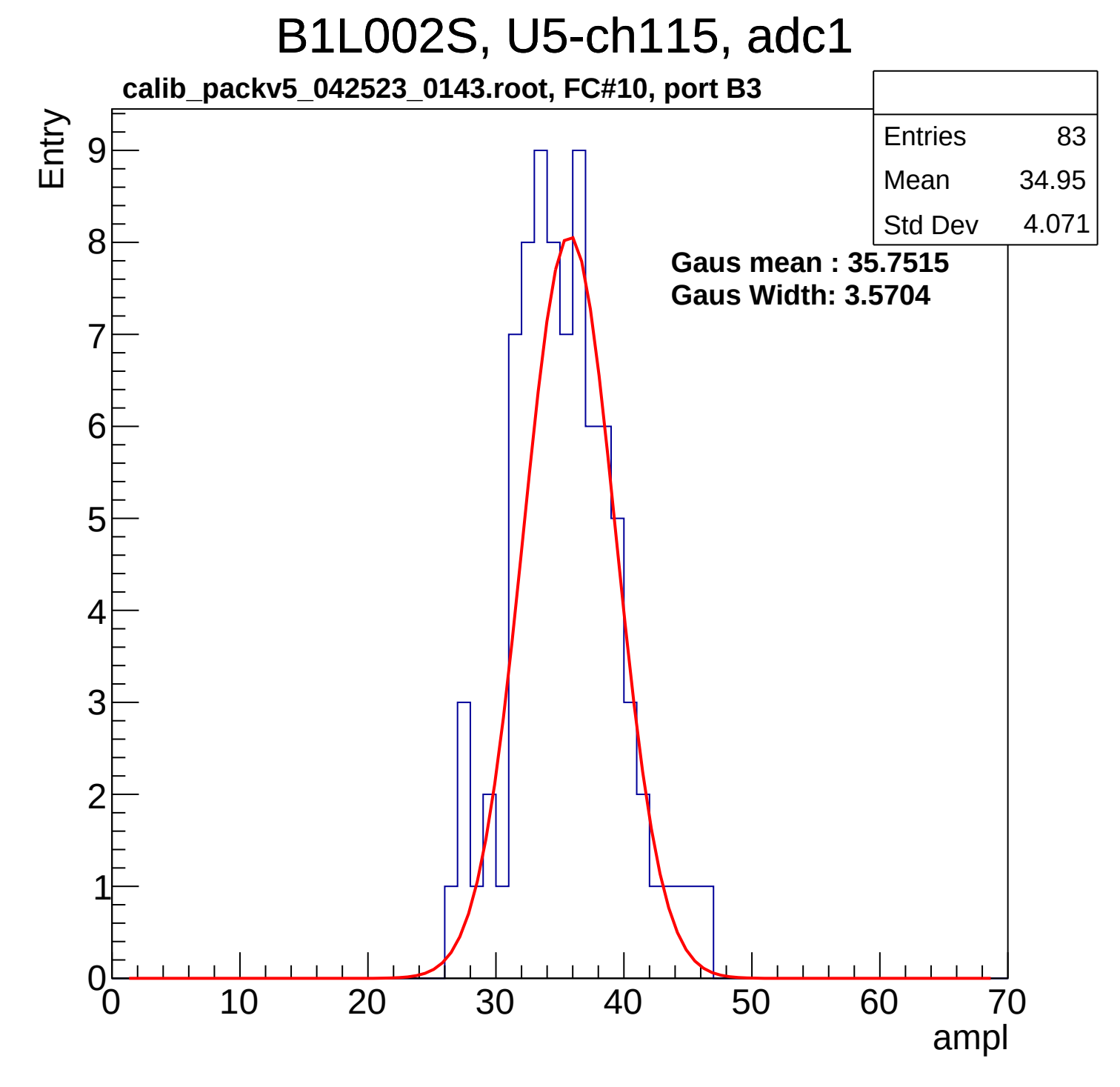
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 34.95
Std Dev 4.071
Gaus mean : 35.7515
Gaus Width: 3.5704
Entry
ampl
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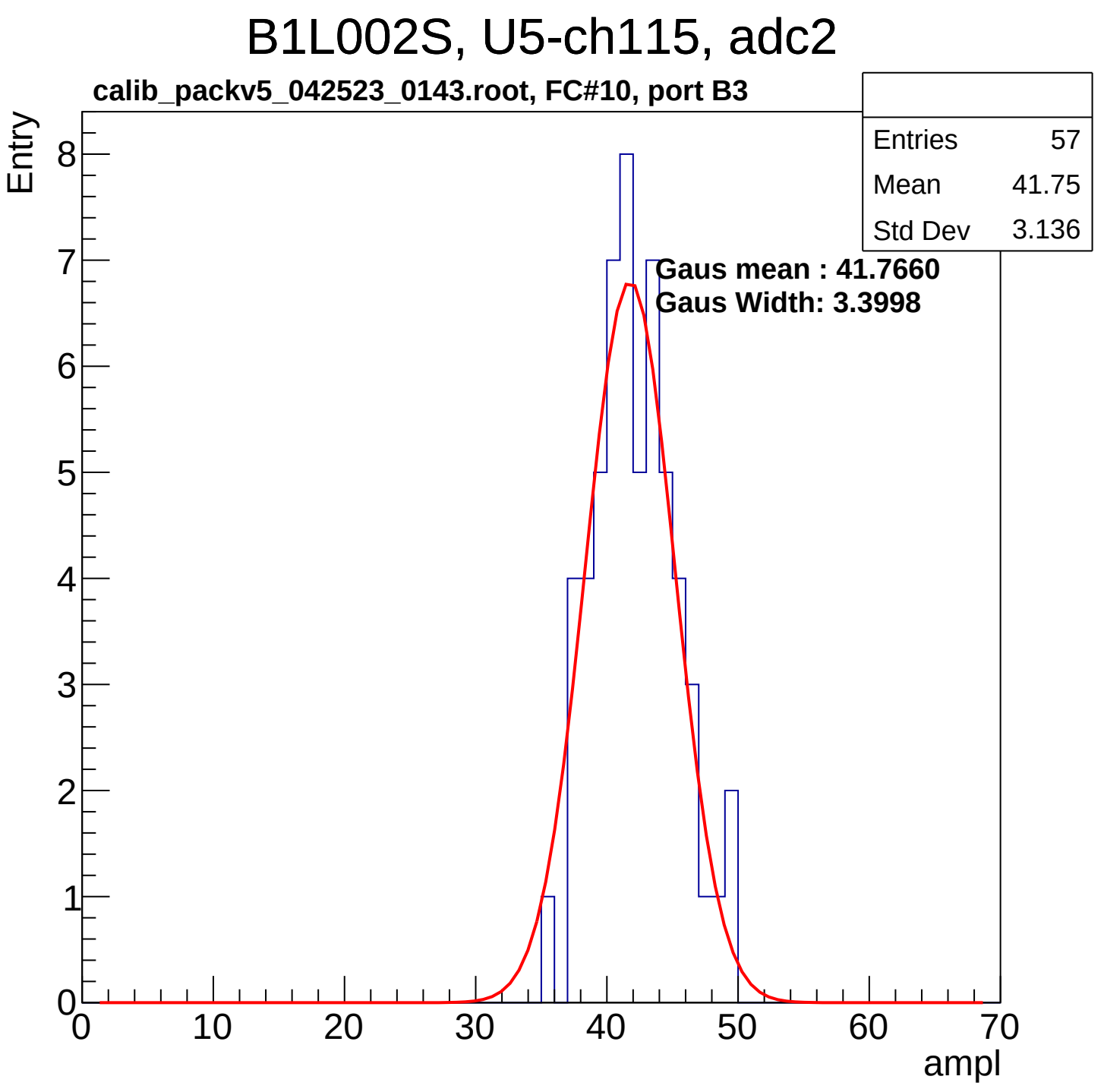
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.75
Std Dev 3.136
Gaus mean : 41.7660
Gaus Width: 3.3998
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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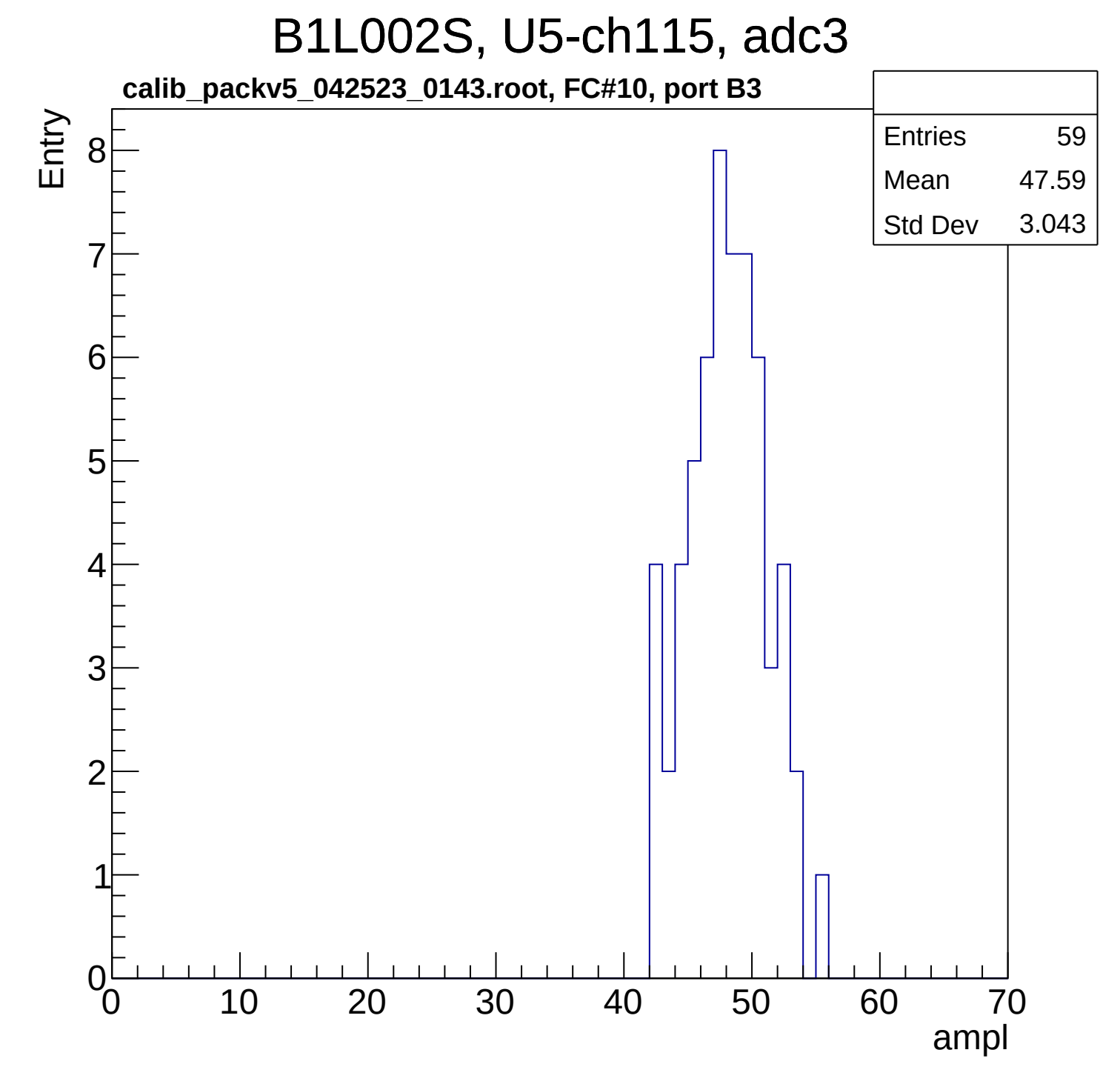
B1L002S, U5-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.59
Std Dev 3.043
Entry
0
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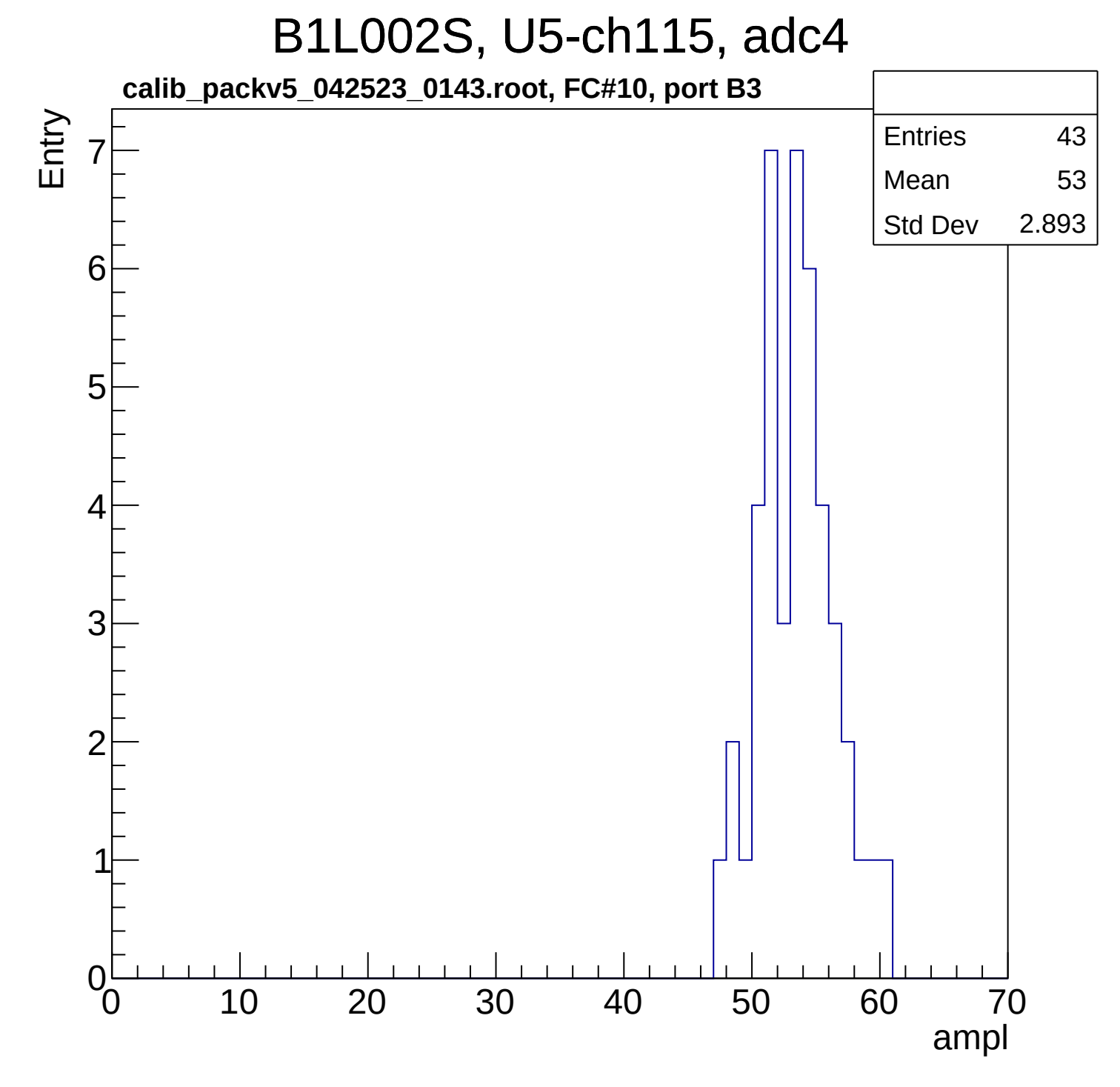
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53
Std Dev 2.893
Entry
ampl
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5
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7
0
10
20
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40
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60
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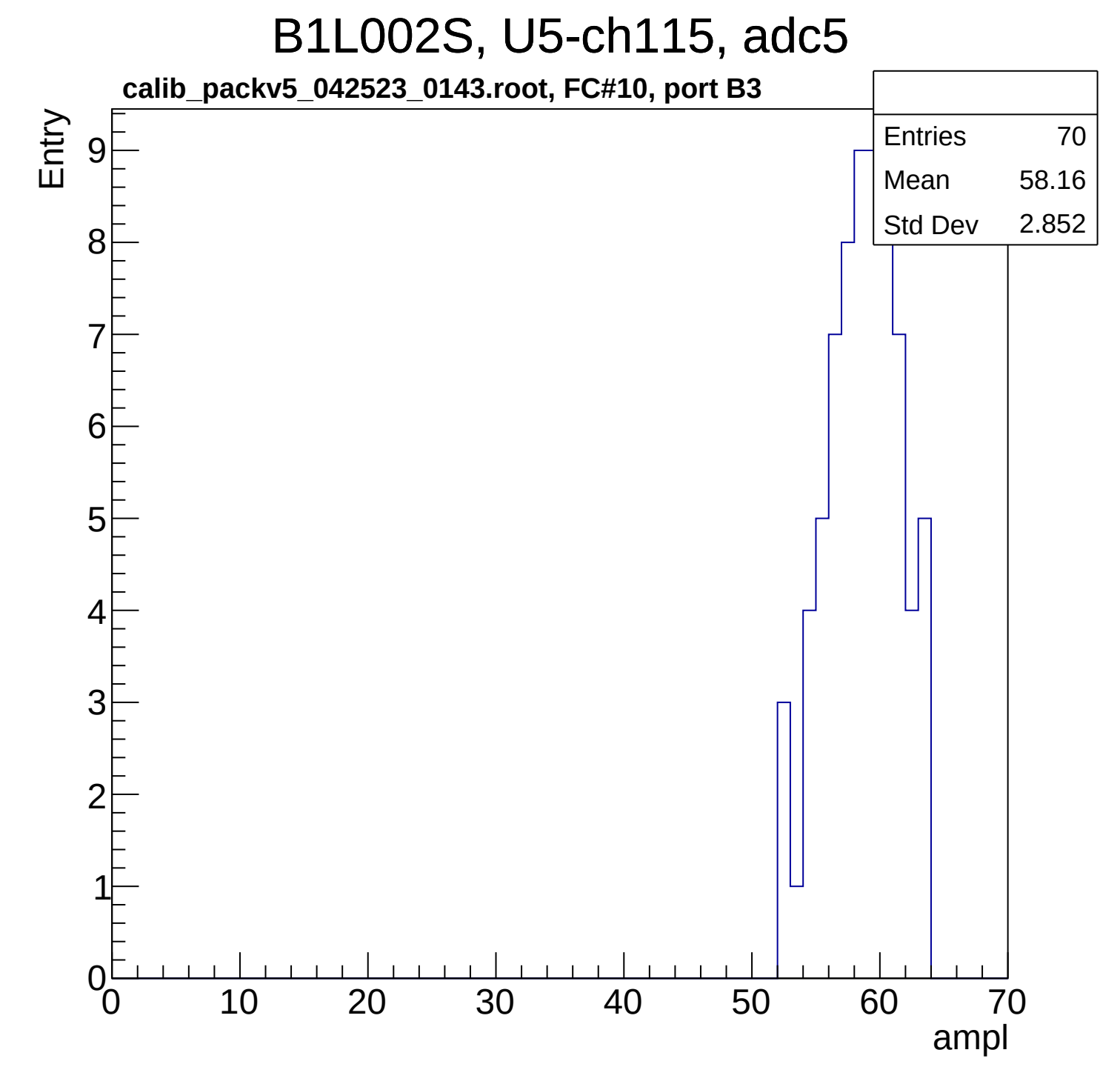
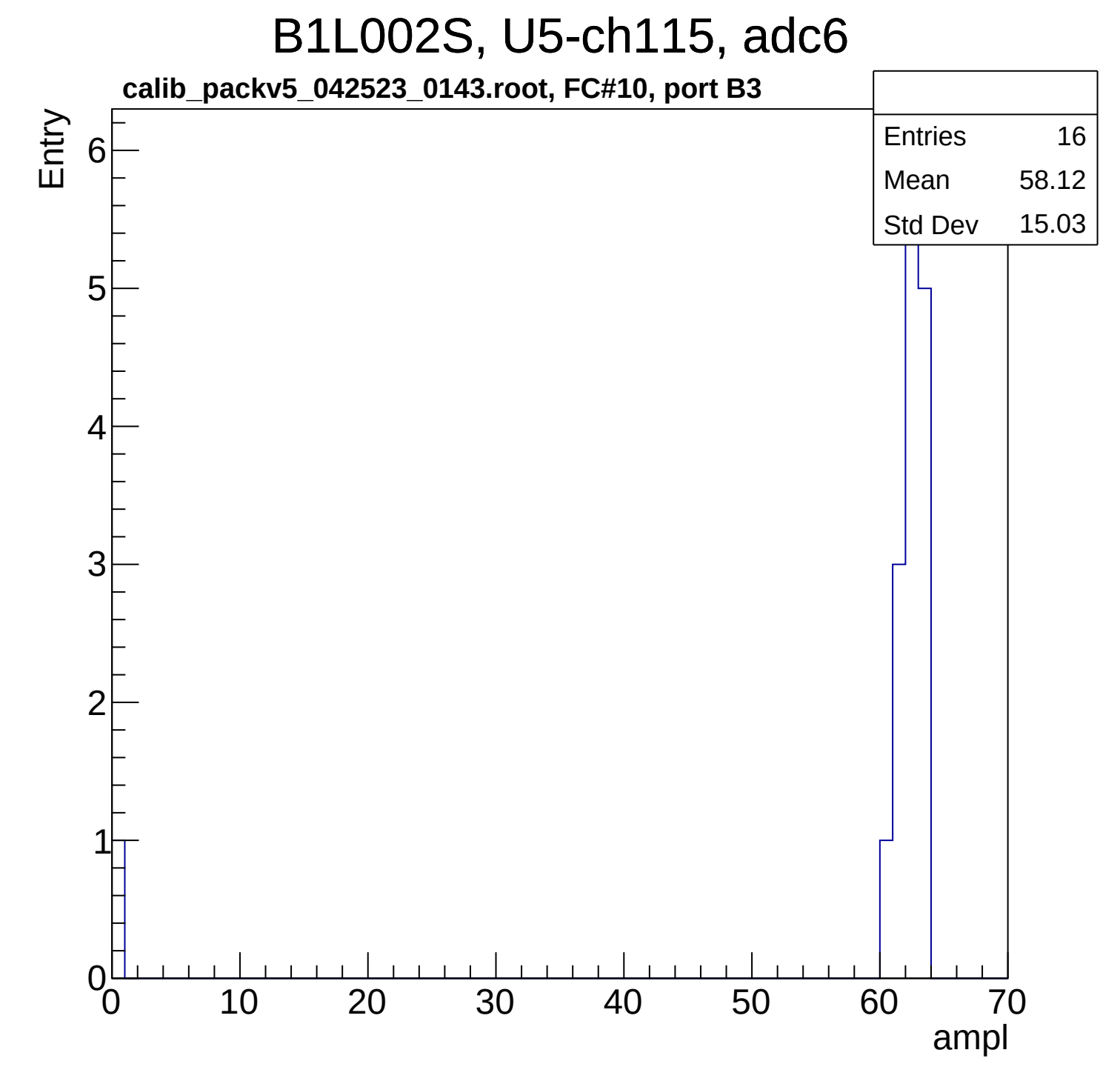
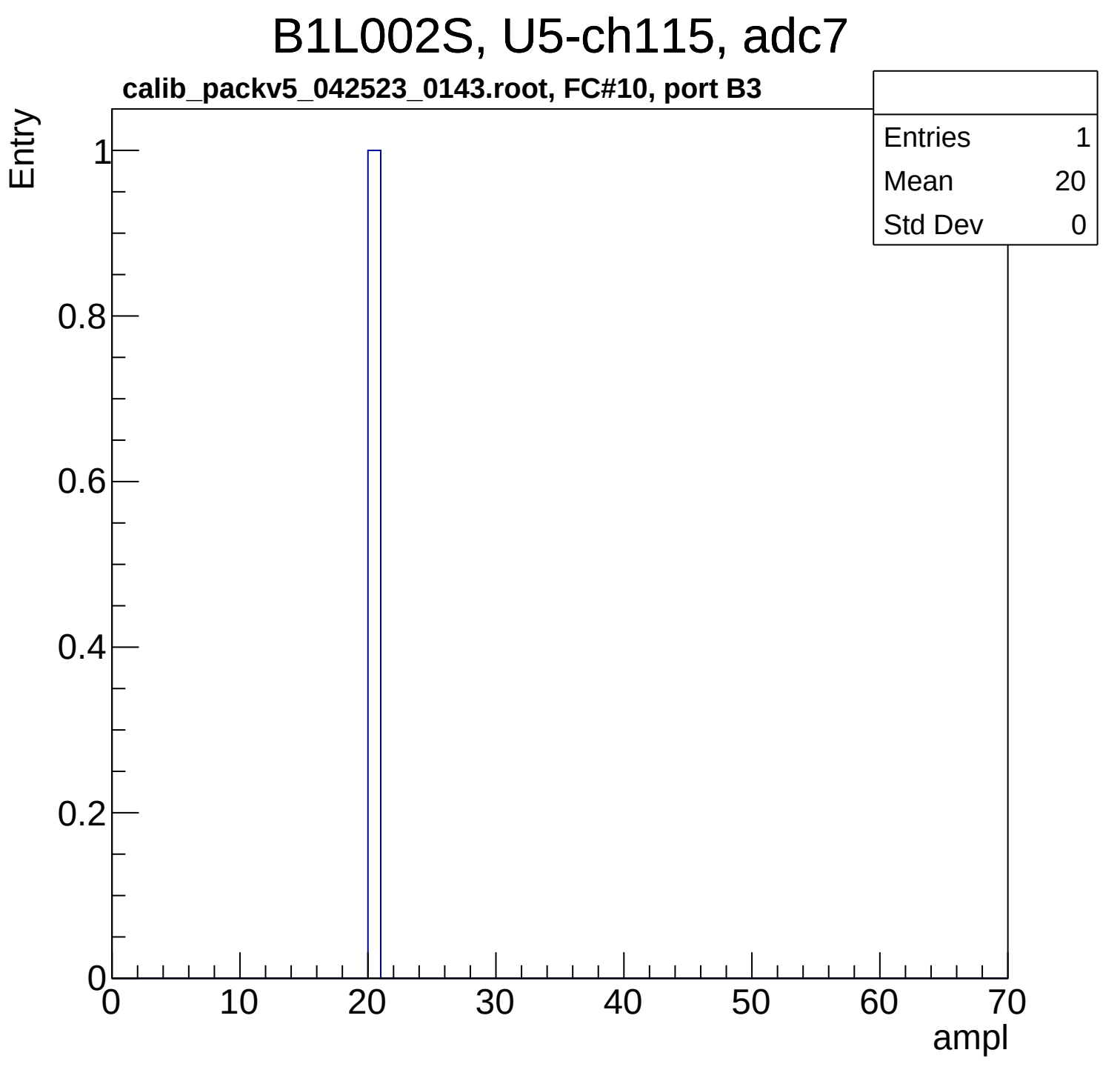

B1L002S, U5-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 58.16
Std Dev 2.852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 58.12
Std Dev 15.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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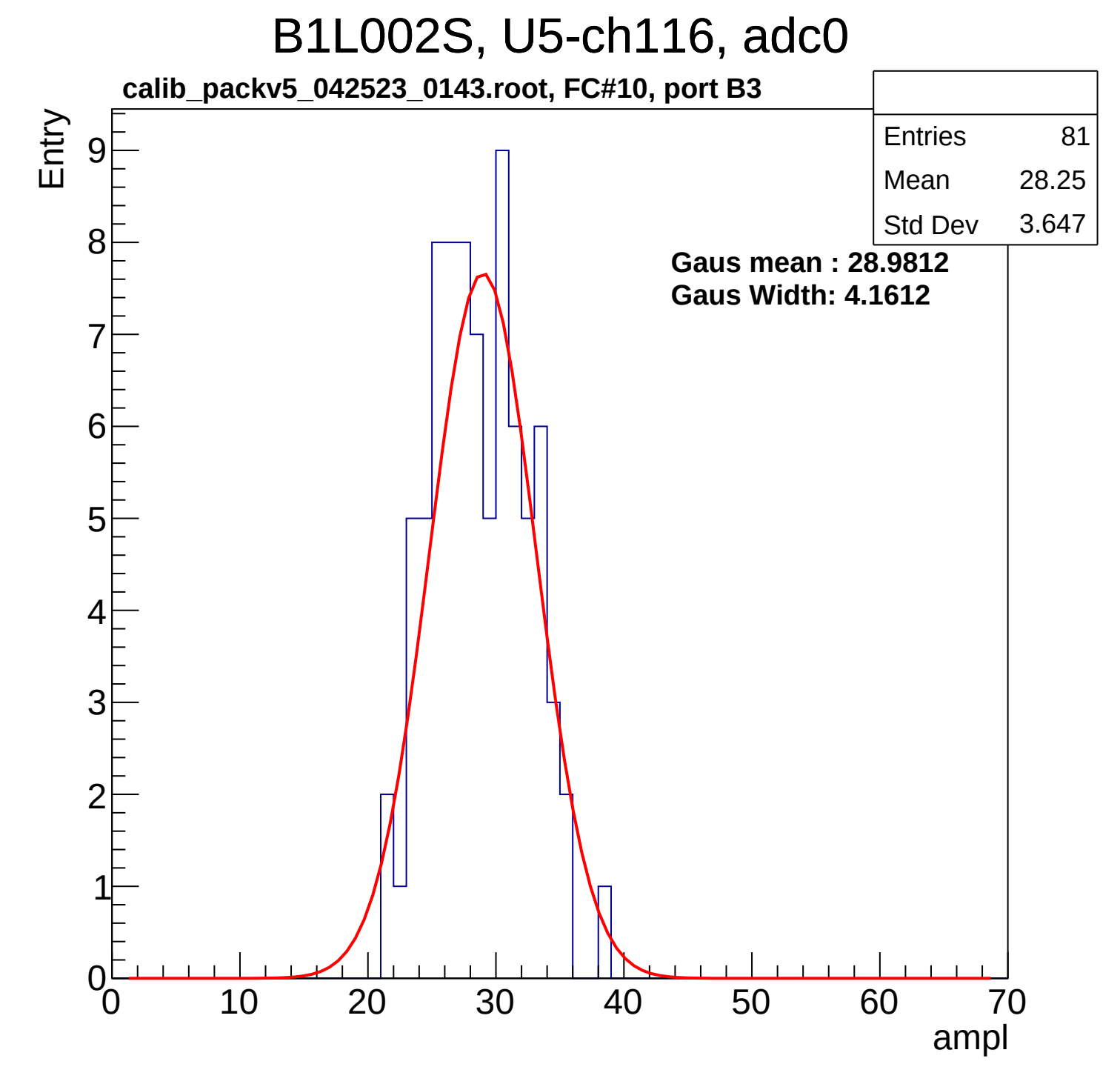
B1L002S, U5-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 28.25
Std Dev 3.647
Gaus mean : 28.9812
Gaus Width: 4.1612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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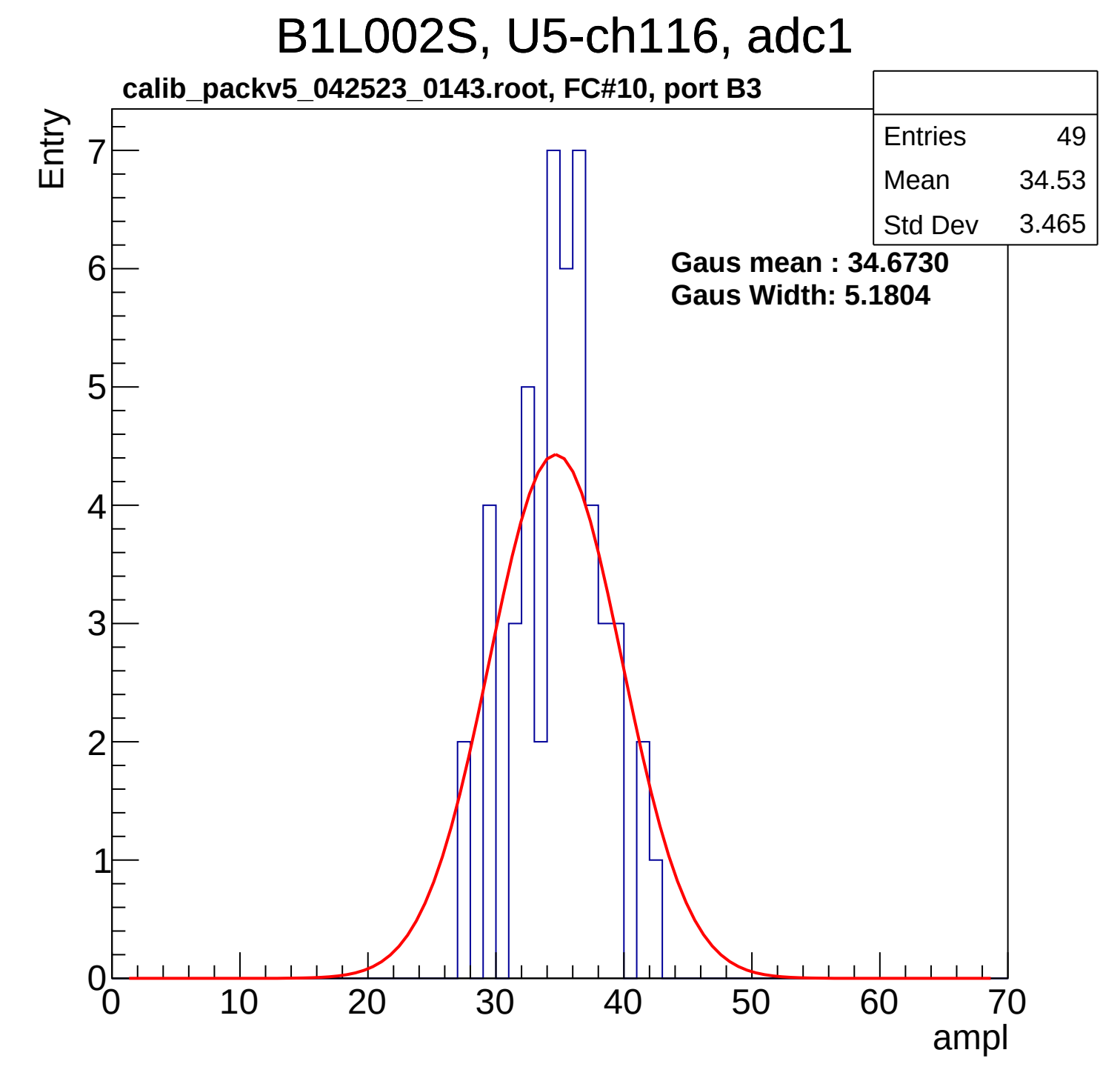
B1L002S, U5-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 34.53
Std Dev 3.465
Gaus mean : 34.6730
Gaus Width: 5.1804
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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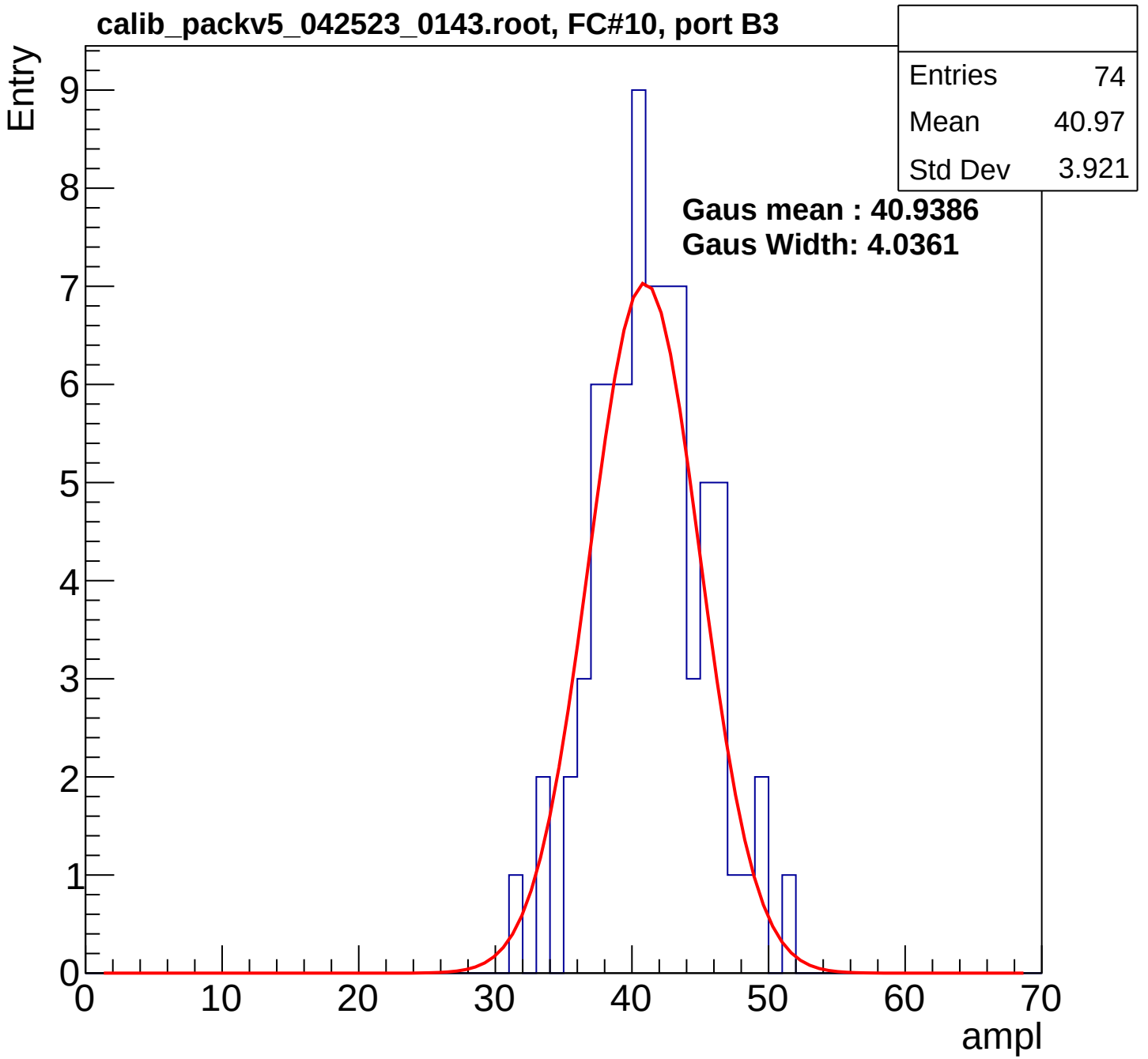
B1L002S, U5-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.97
Std Dev 3.921
Gaus mean : 40.9386
Gaus Width: 4.0361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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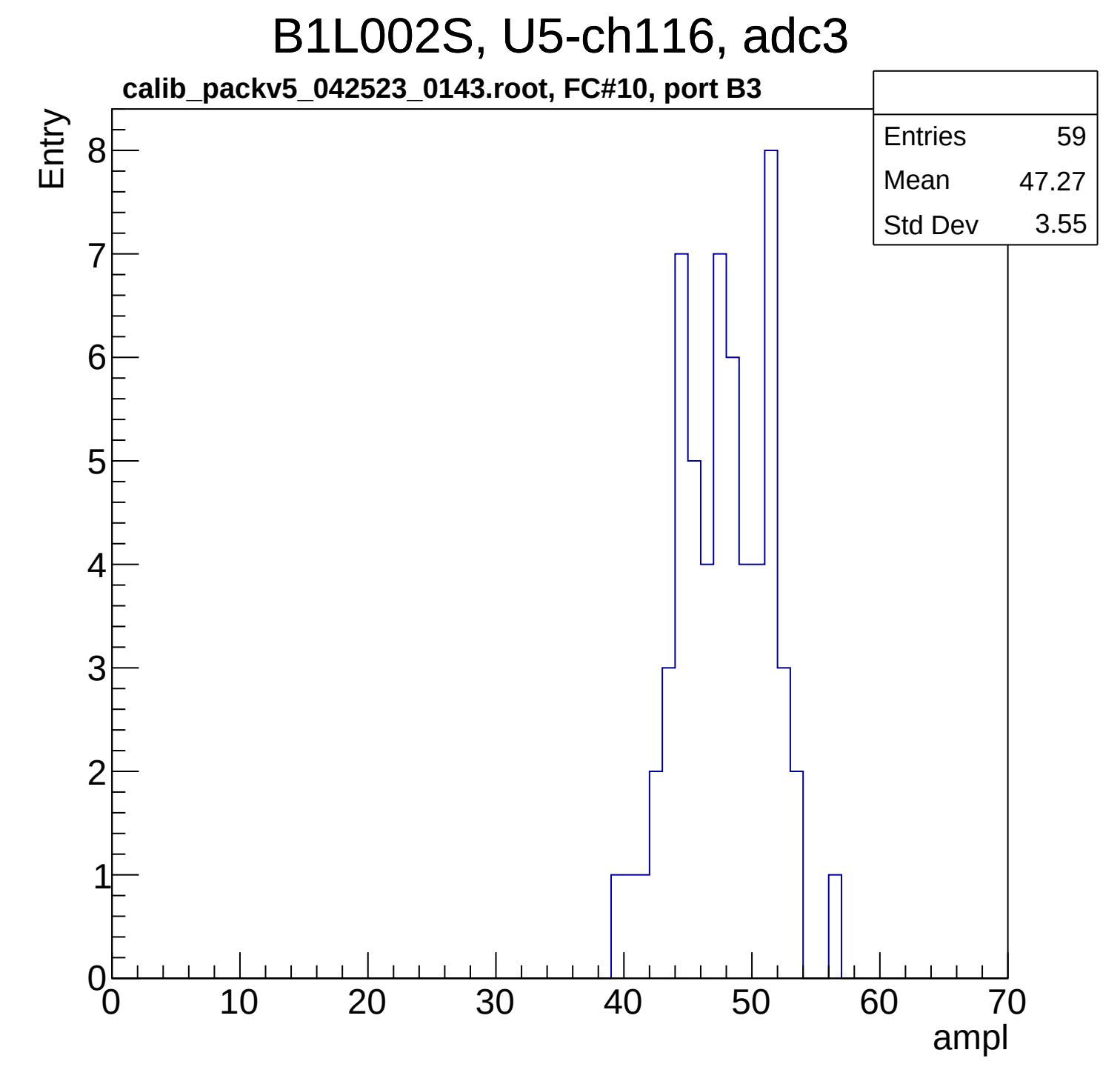
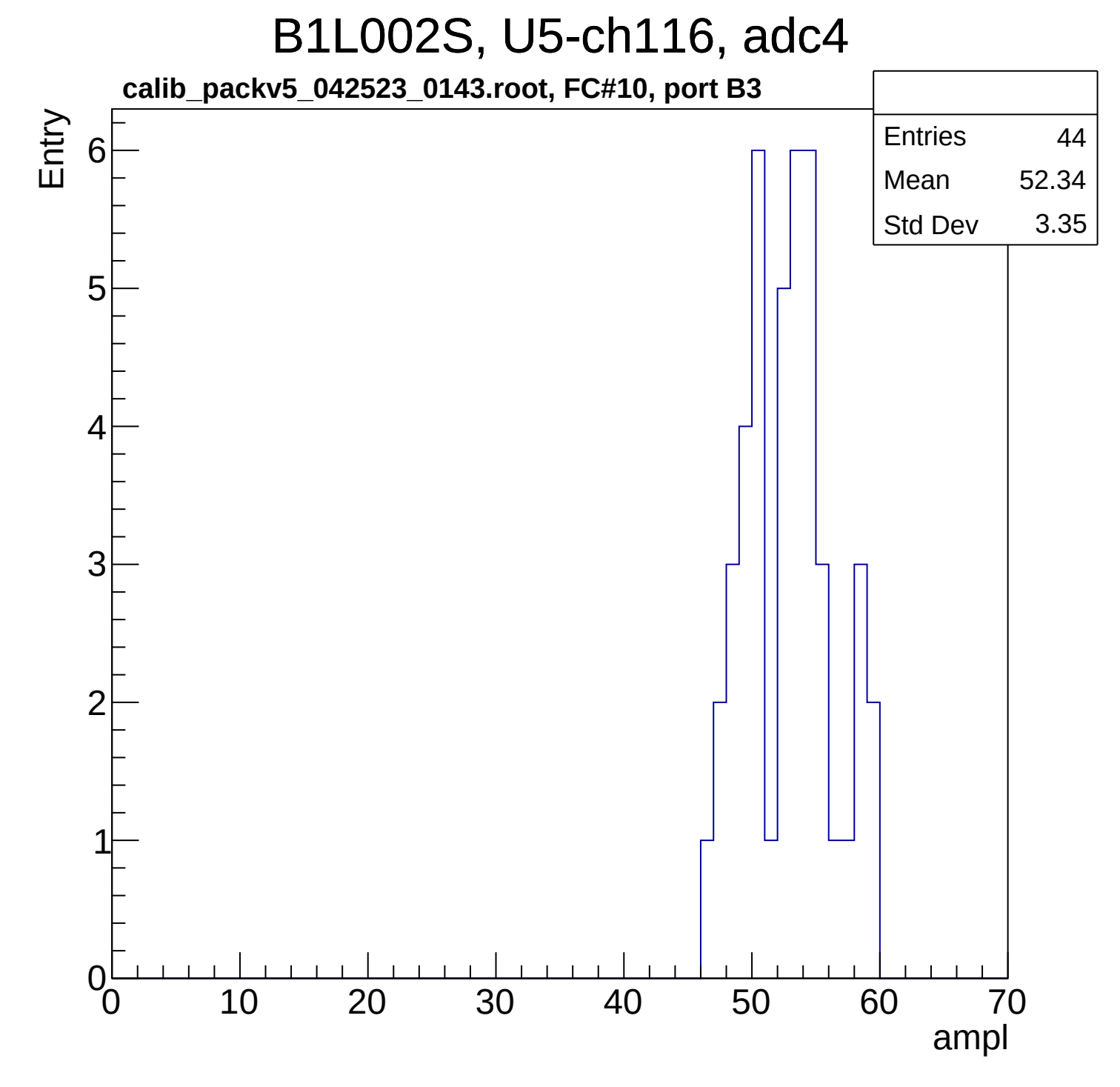
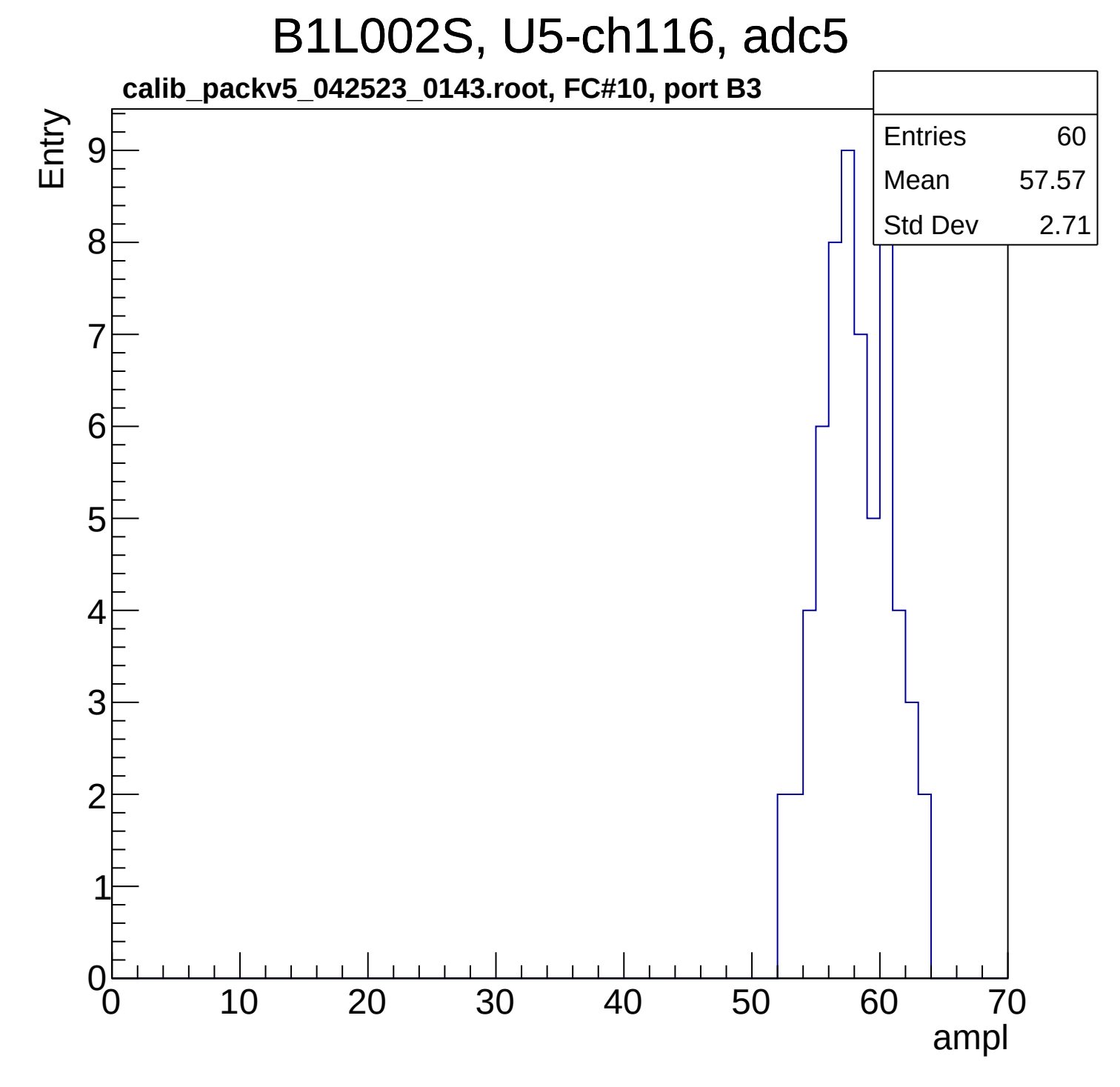
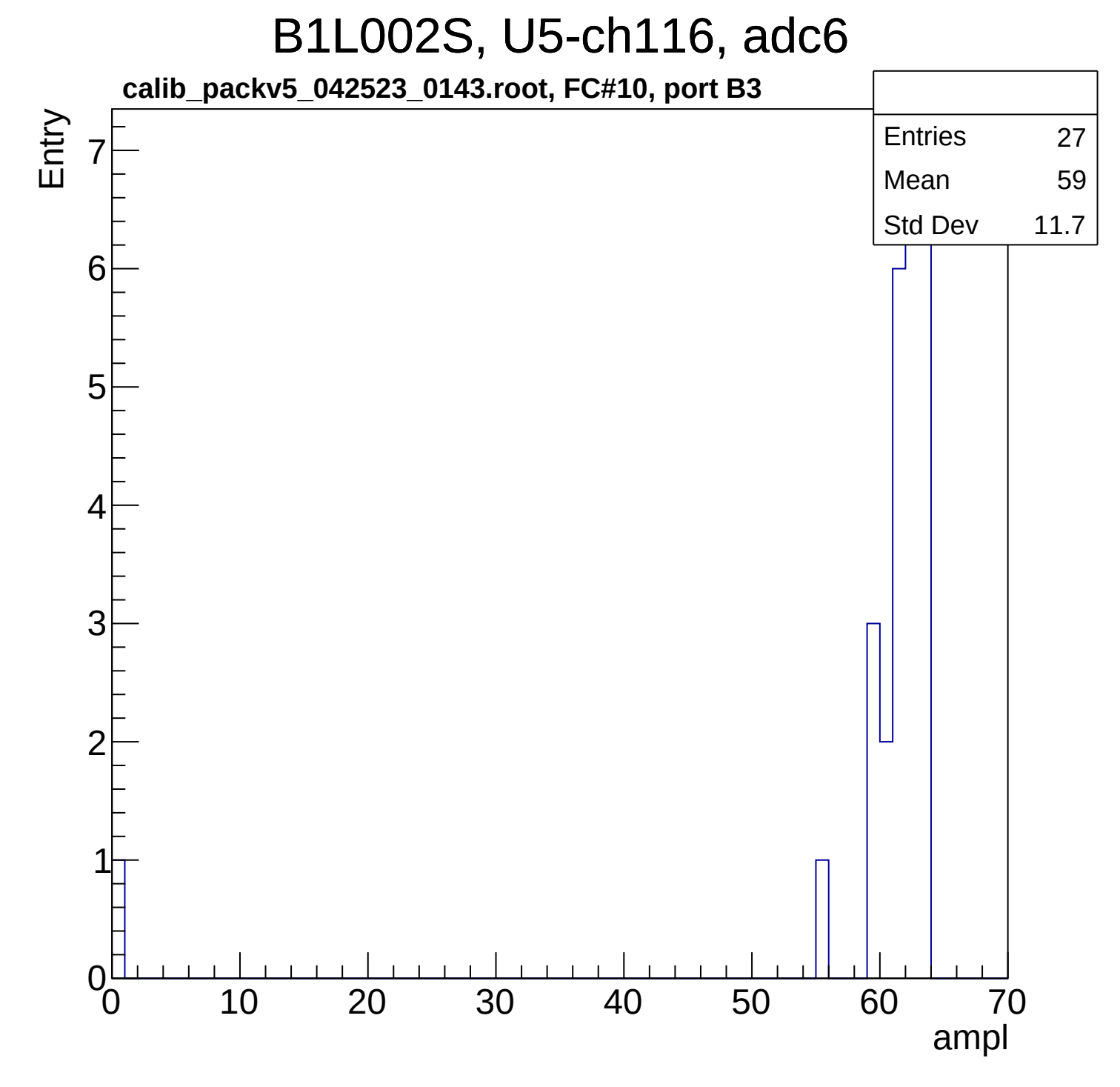
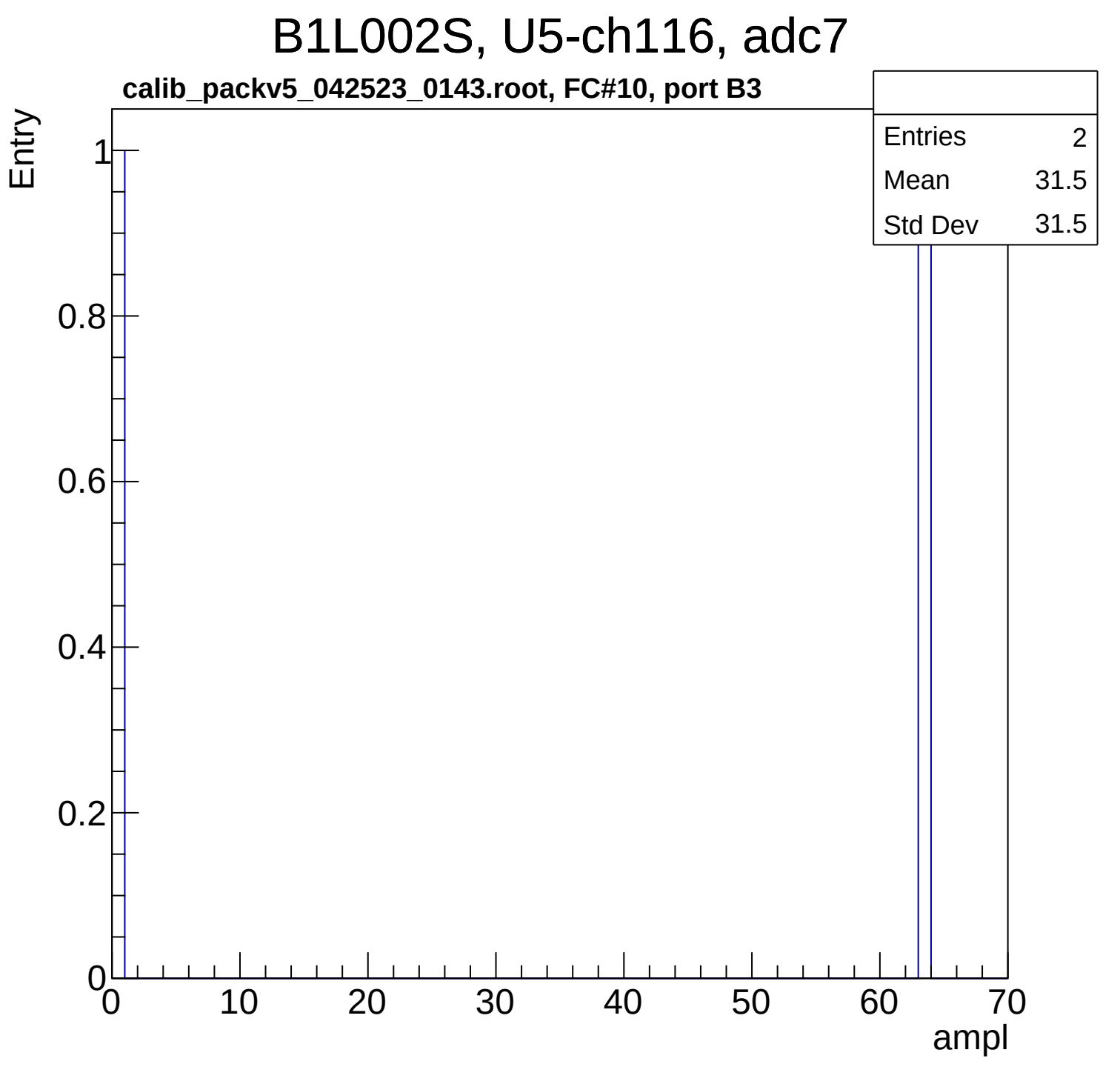
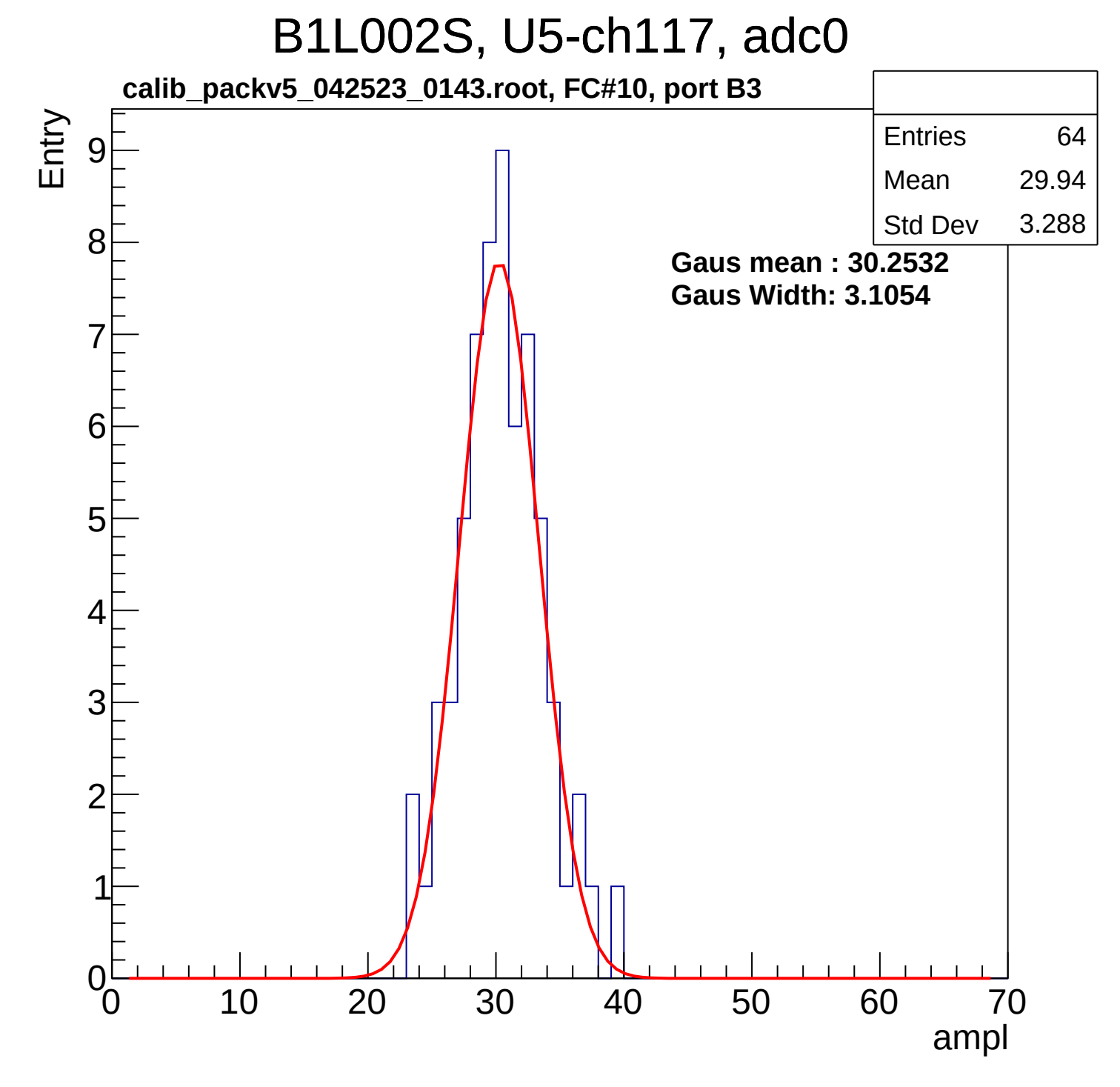

B1L002S, U5-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.27
Std Dev 3.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

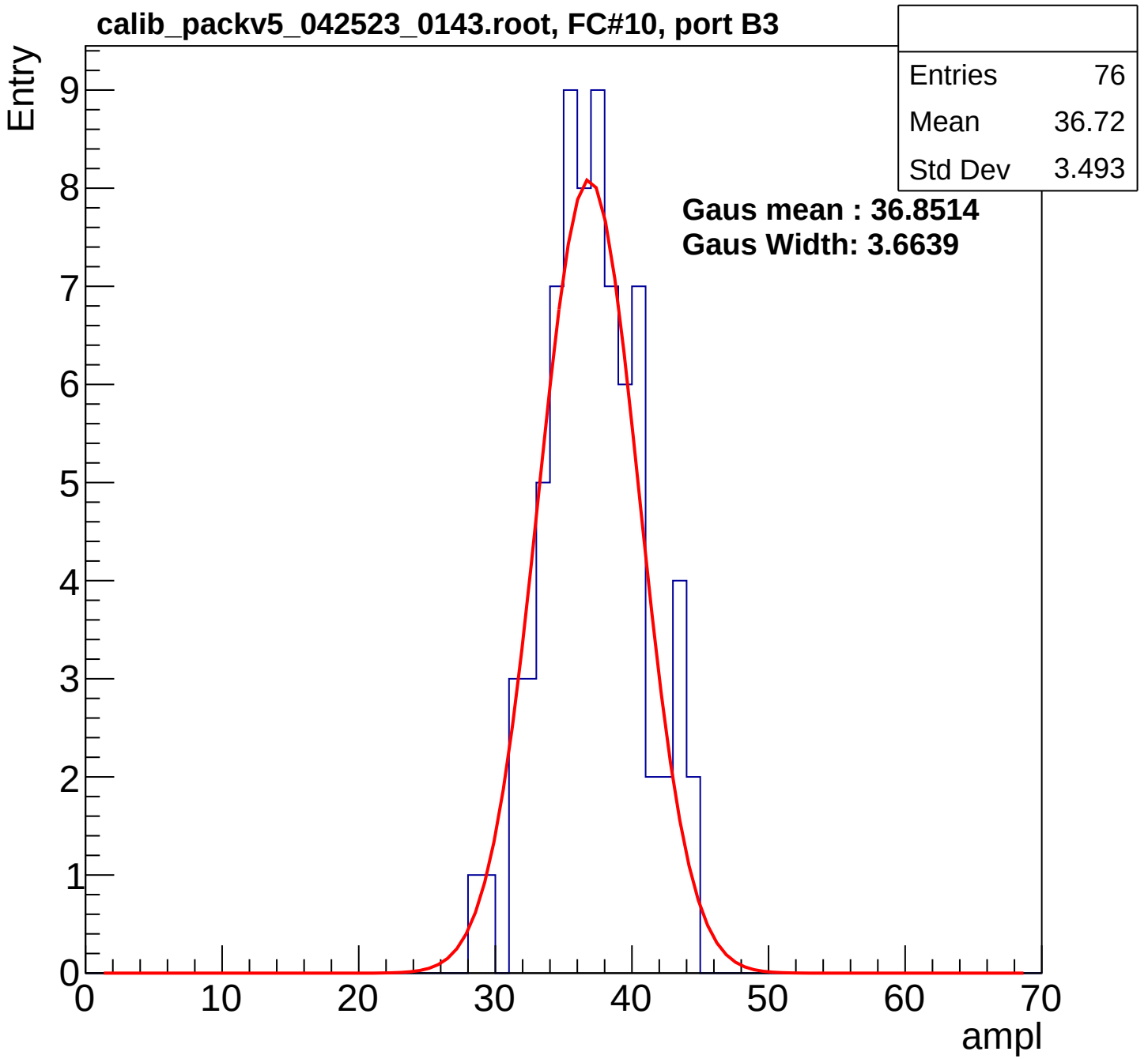

B1L002S, U5-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.34
Std Dev 3.35
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

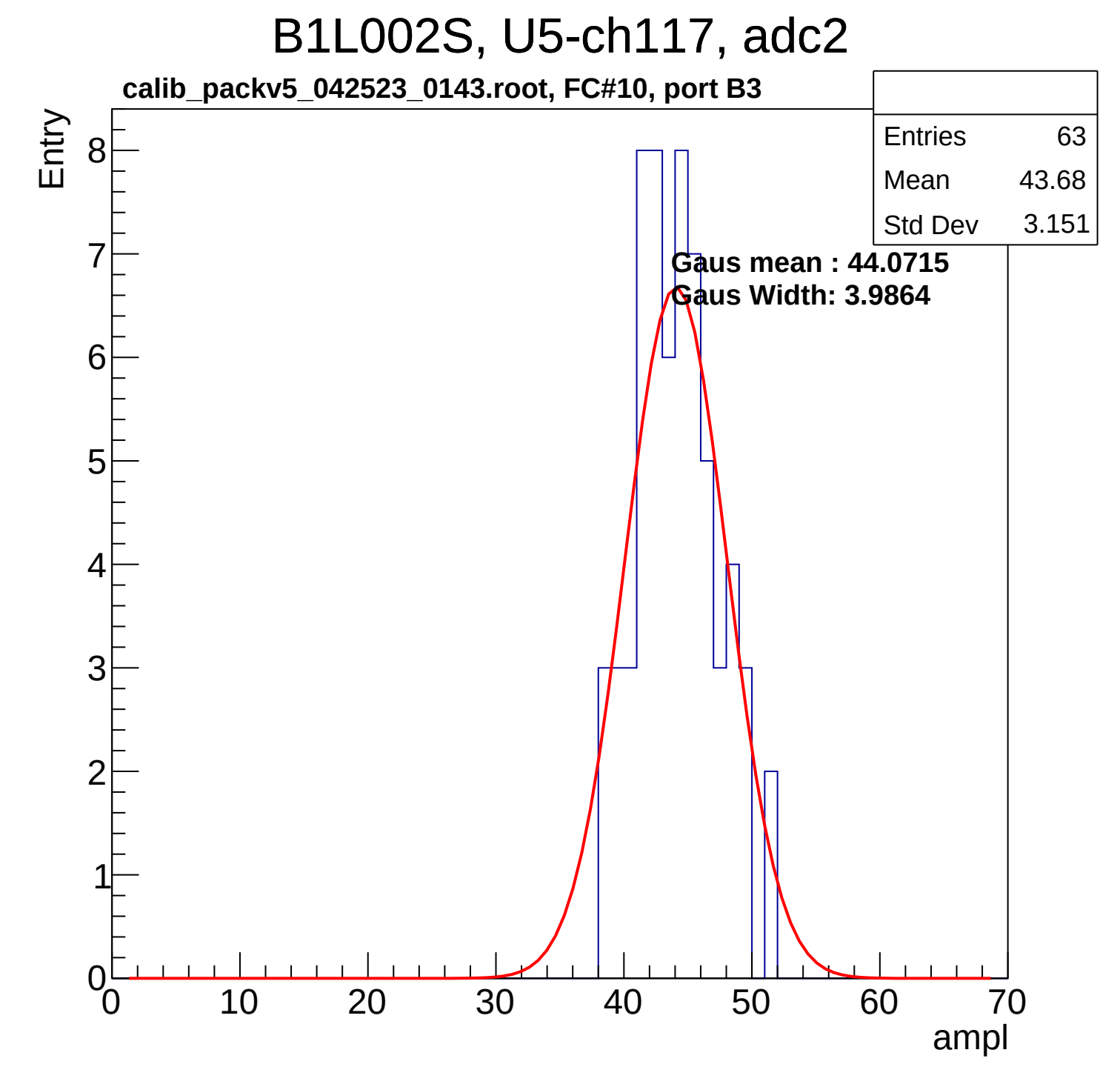

B1L002S, U5-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.57
Std Dev 2.71
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59
Std Dev 11.7
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 29.94
Std Dev 3.288
Gaus mean : 30.2532
Gaus Width: 3.1054
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 36.72
Std Dev 3.493
Gaus mean : 36.8514
Gaus Width: 3.6639
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U5-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 43.68
Std Dev 3.151
Gaus mean : 44.0715
Gaus Width: 3.9864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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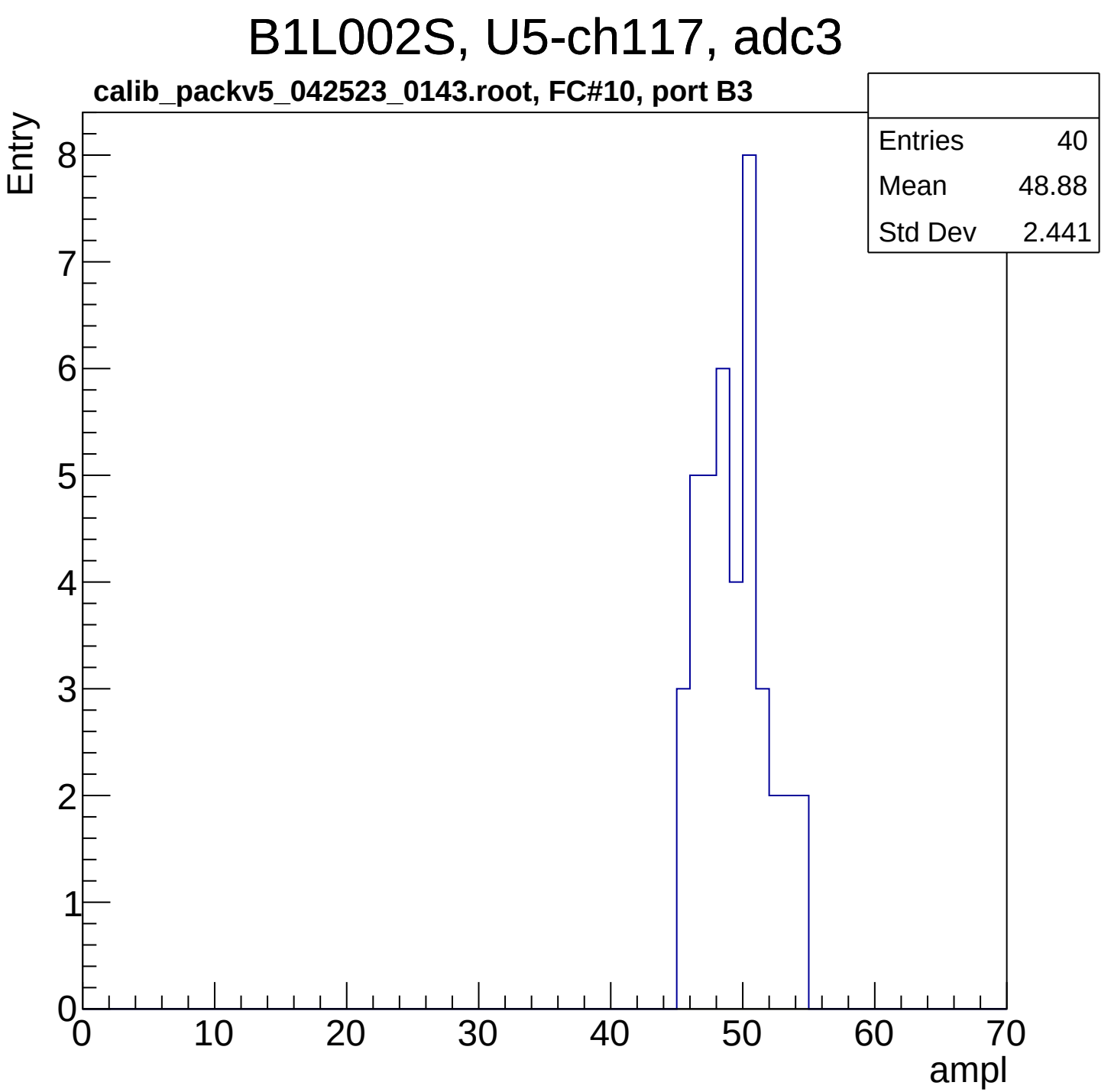
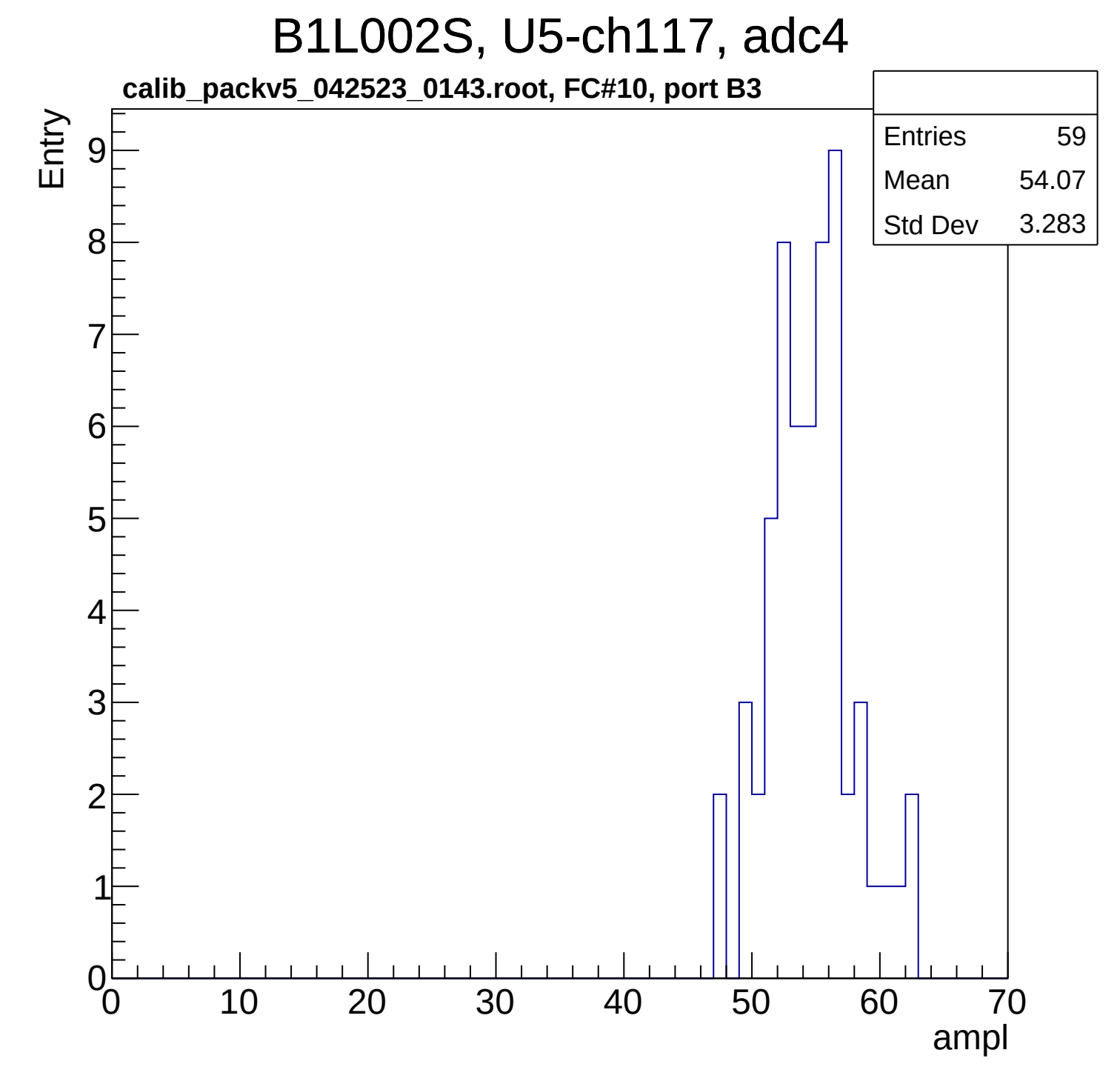
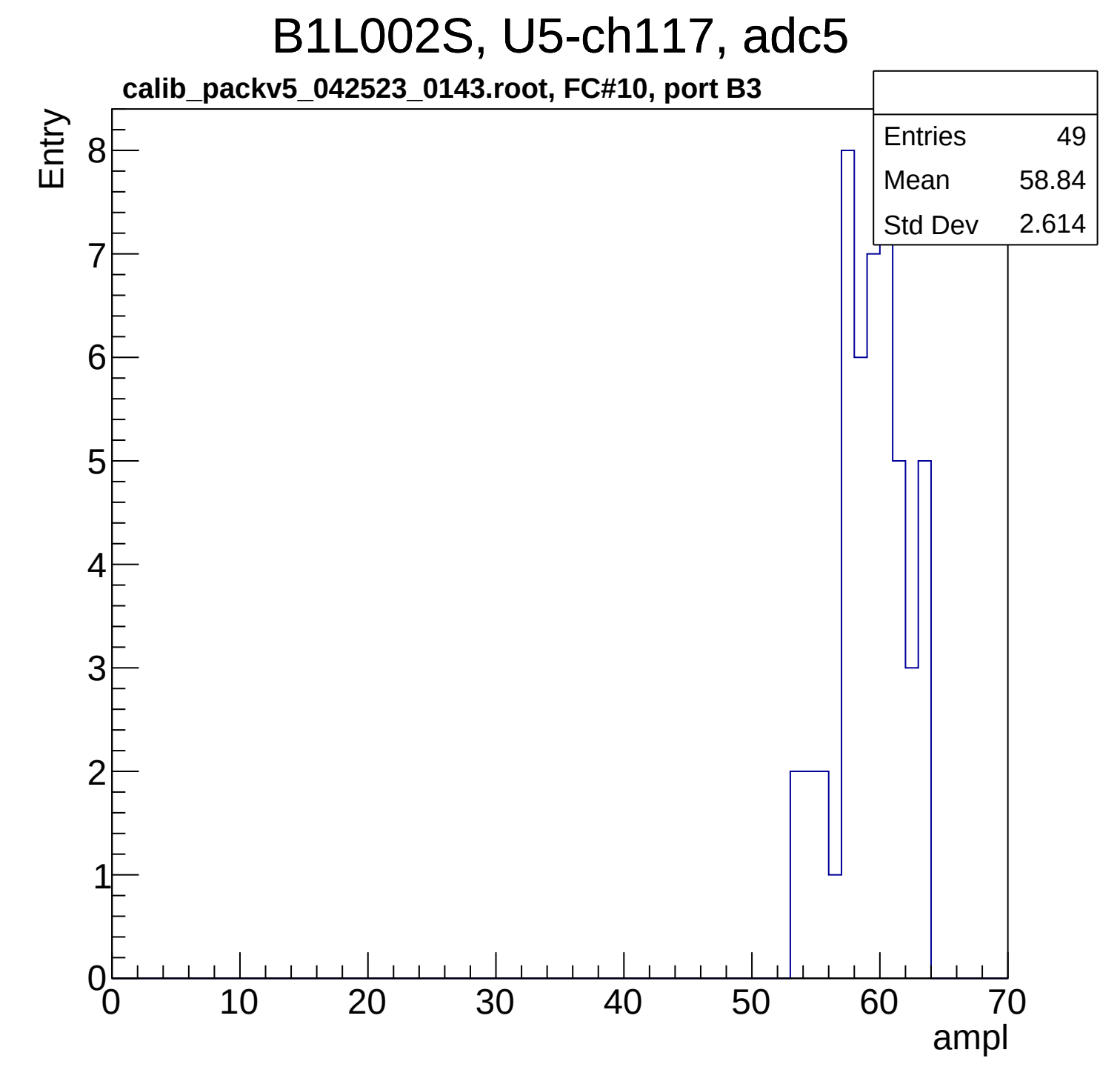
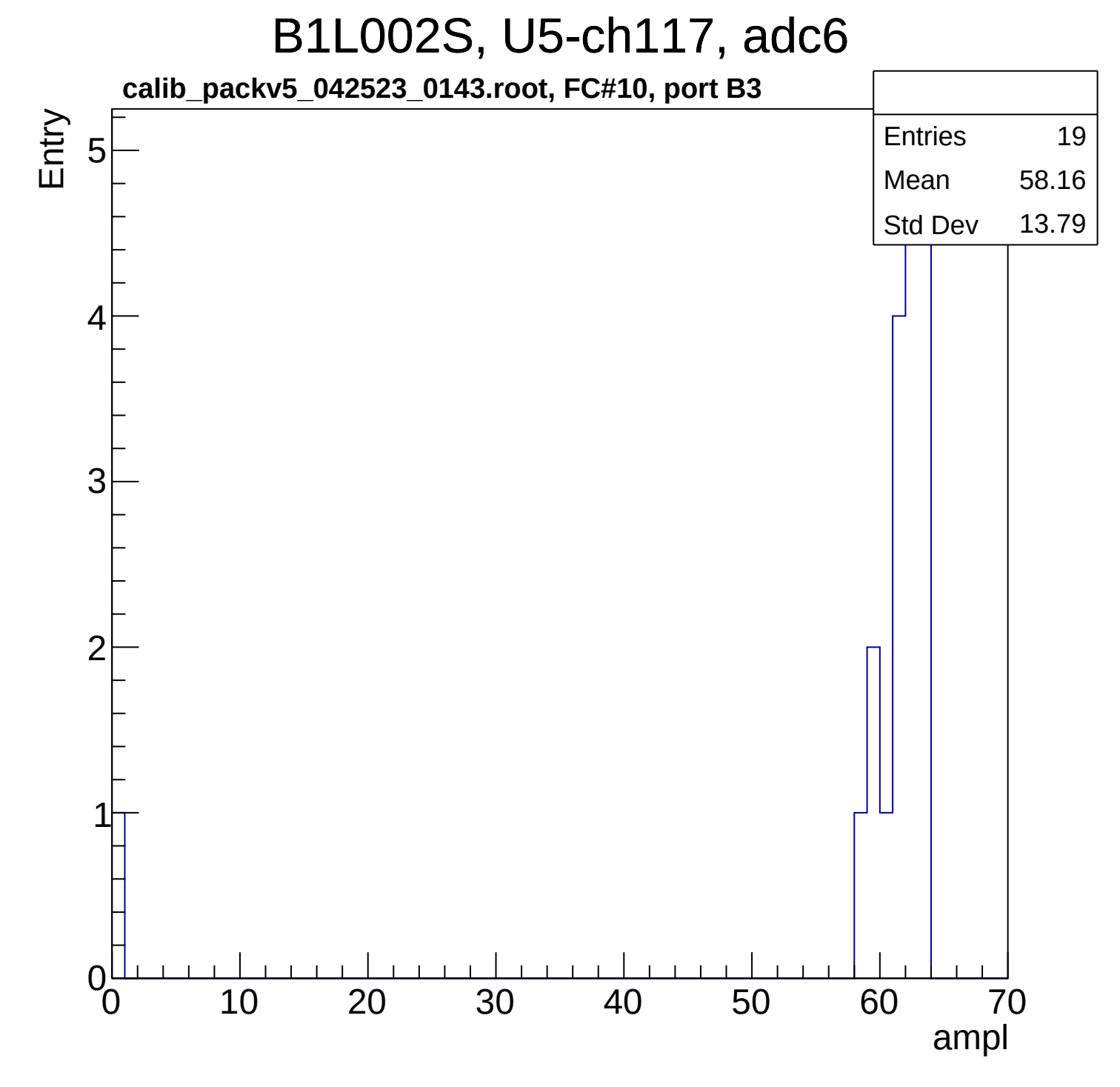
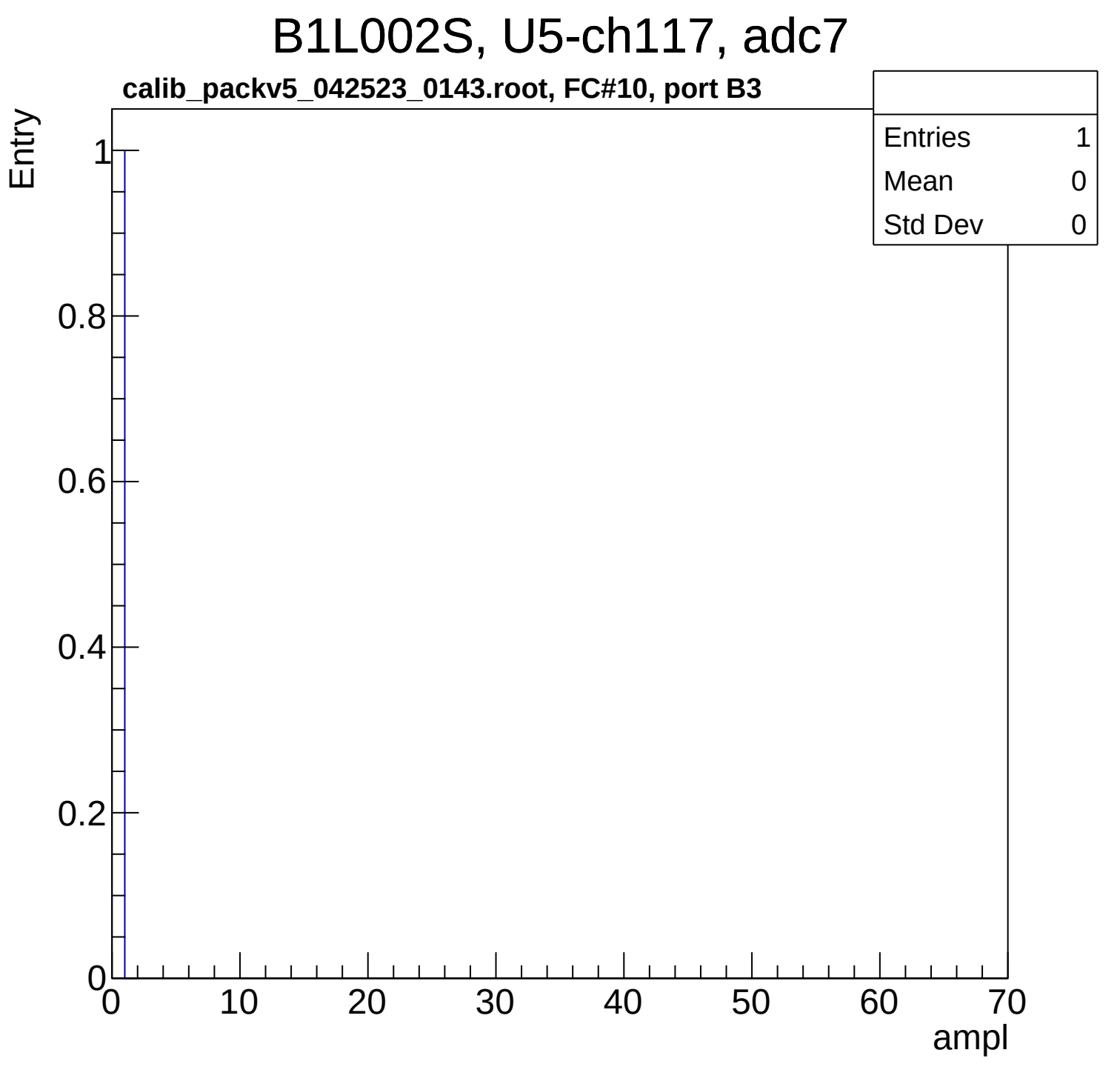
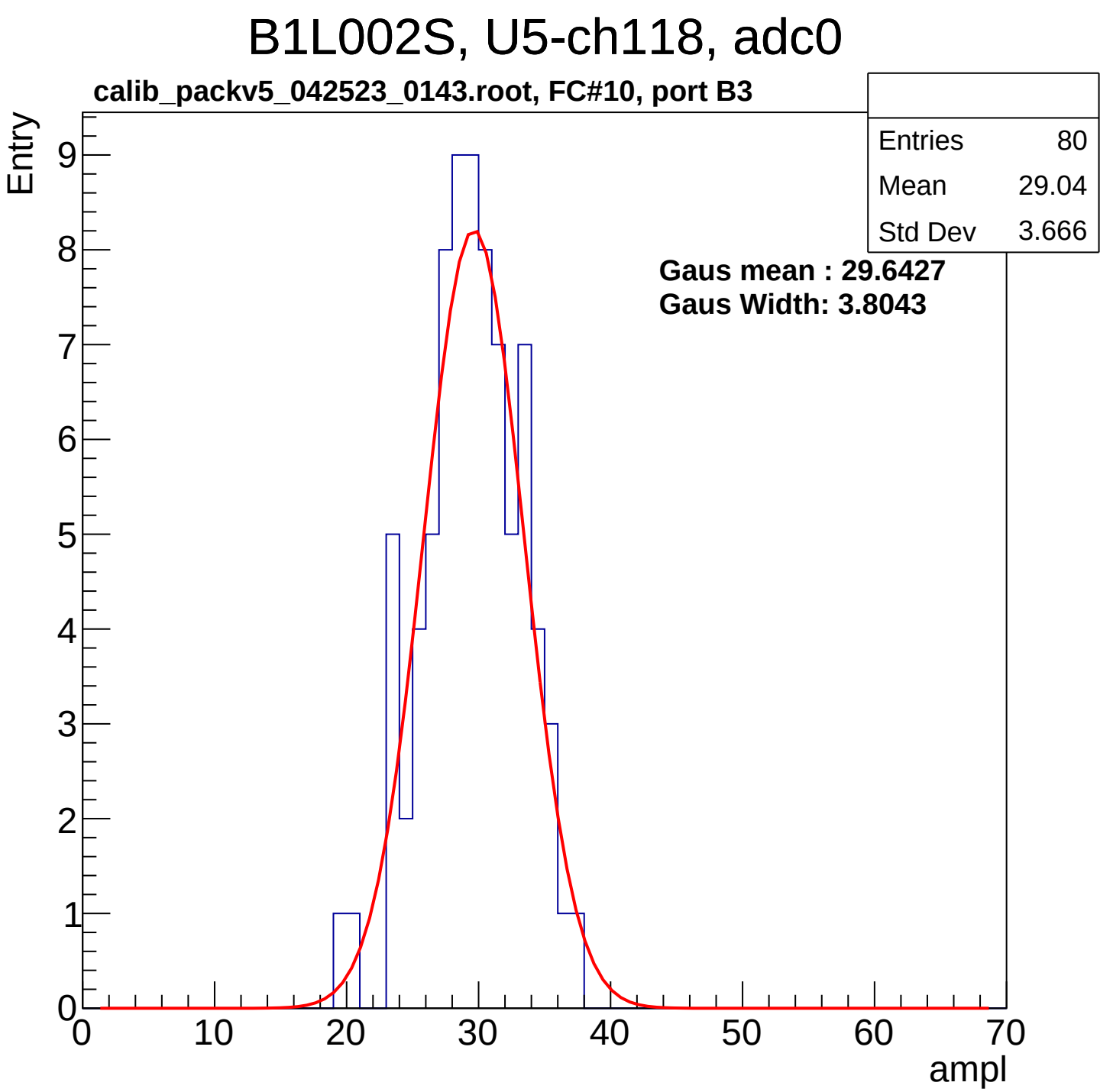

B1L002S, U5-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 48.88
Std Dev 2.441
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

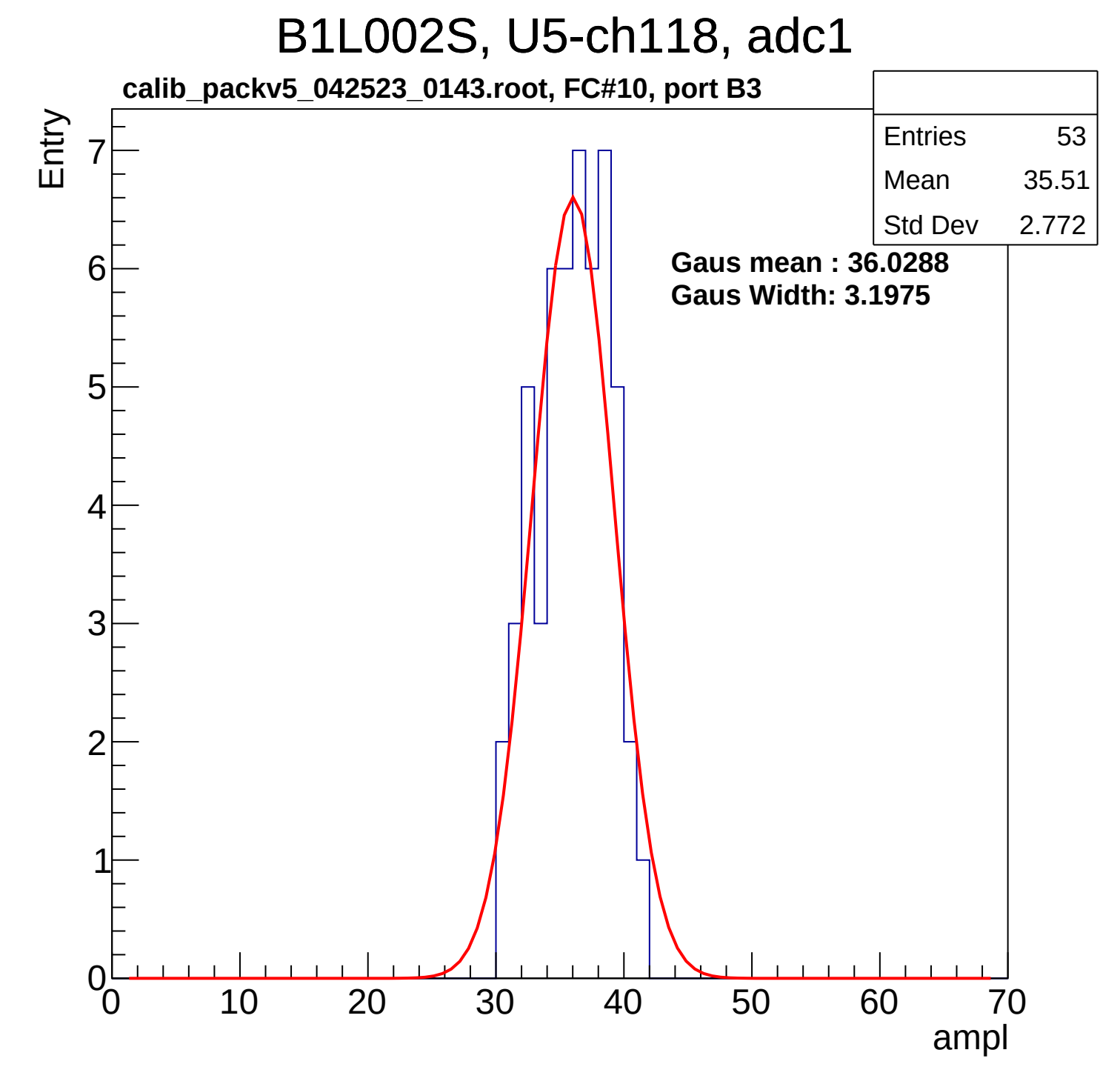

B1L002S, U5-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.07
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

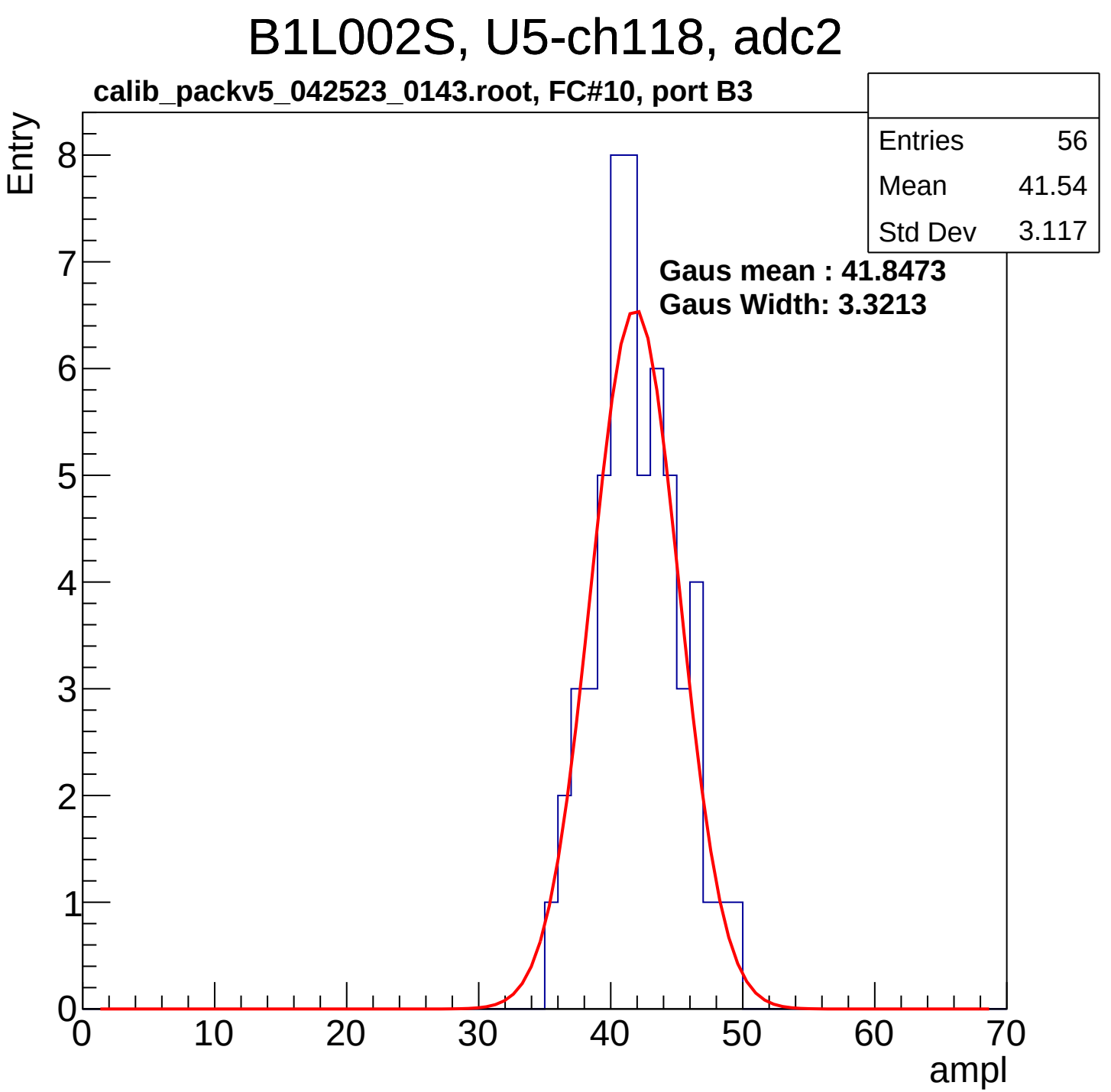

B1L002S, U5-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.84
Std Dev 2.614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

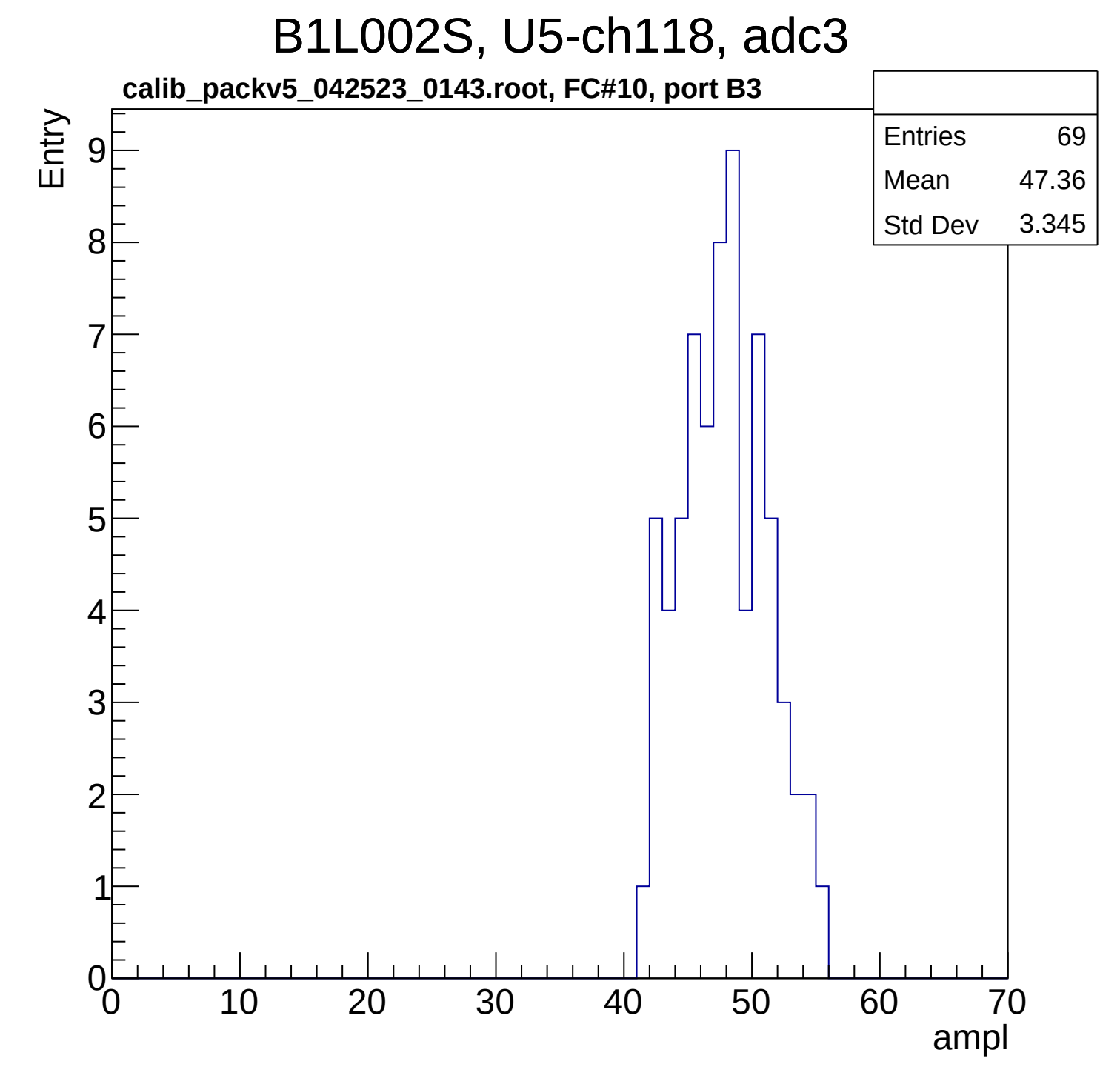

B1L002S, U5-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.16
Std Dev 13.79
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 29.04
Std Dev 3.666
Gaus mean : 29.6427
Gaus Width: 3.8043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 35.51
Std Dev 2.772
Gaus mean : 36.0288
Gaus Width: 3.1975
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.54
Std Dev 3.117
Gaus mean : 41.8473
Gaus Width: 3.3213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.36
Std Dev 3.345
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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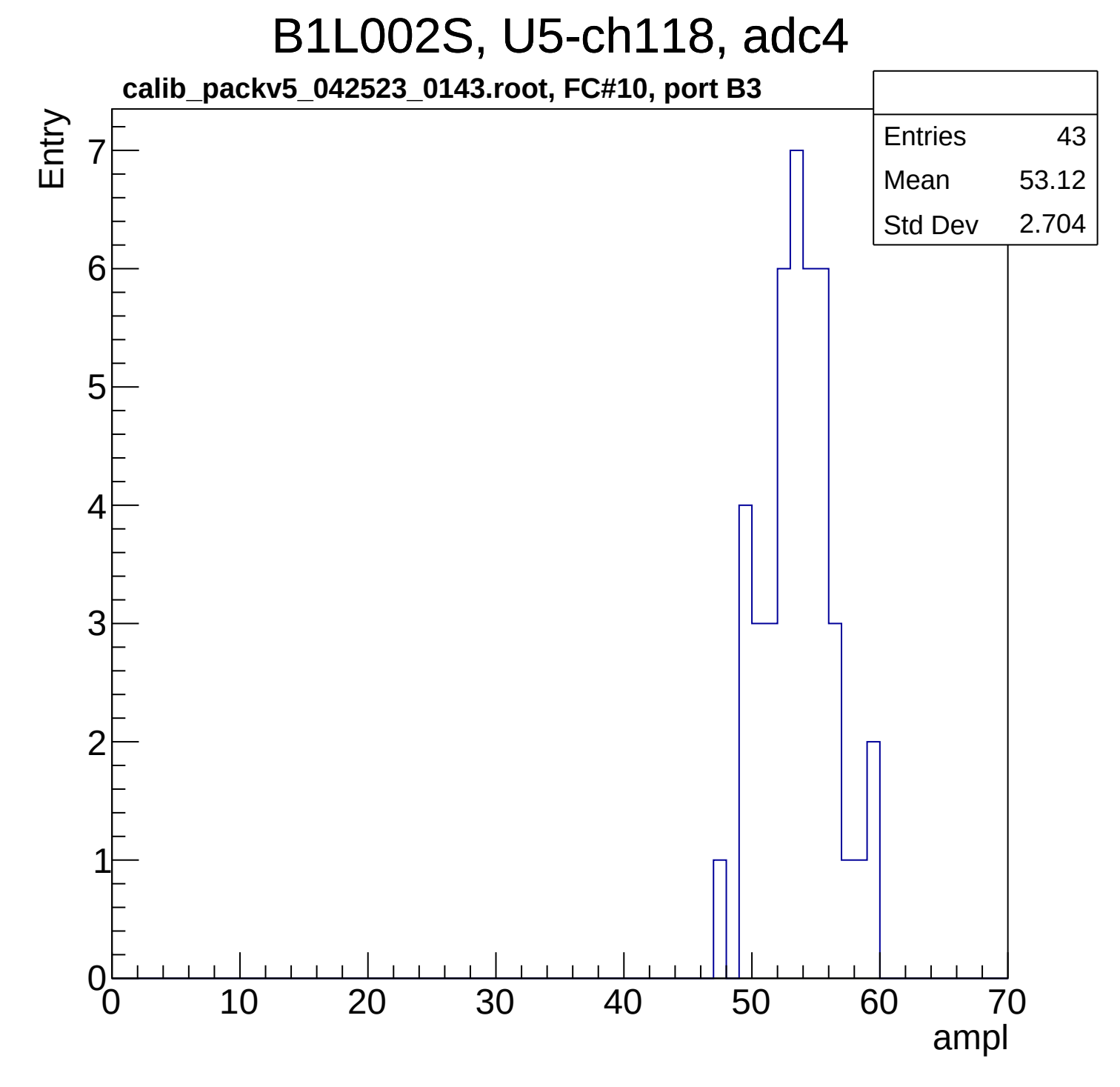
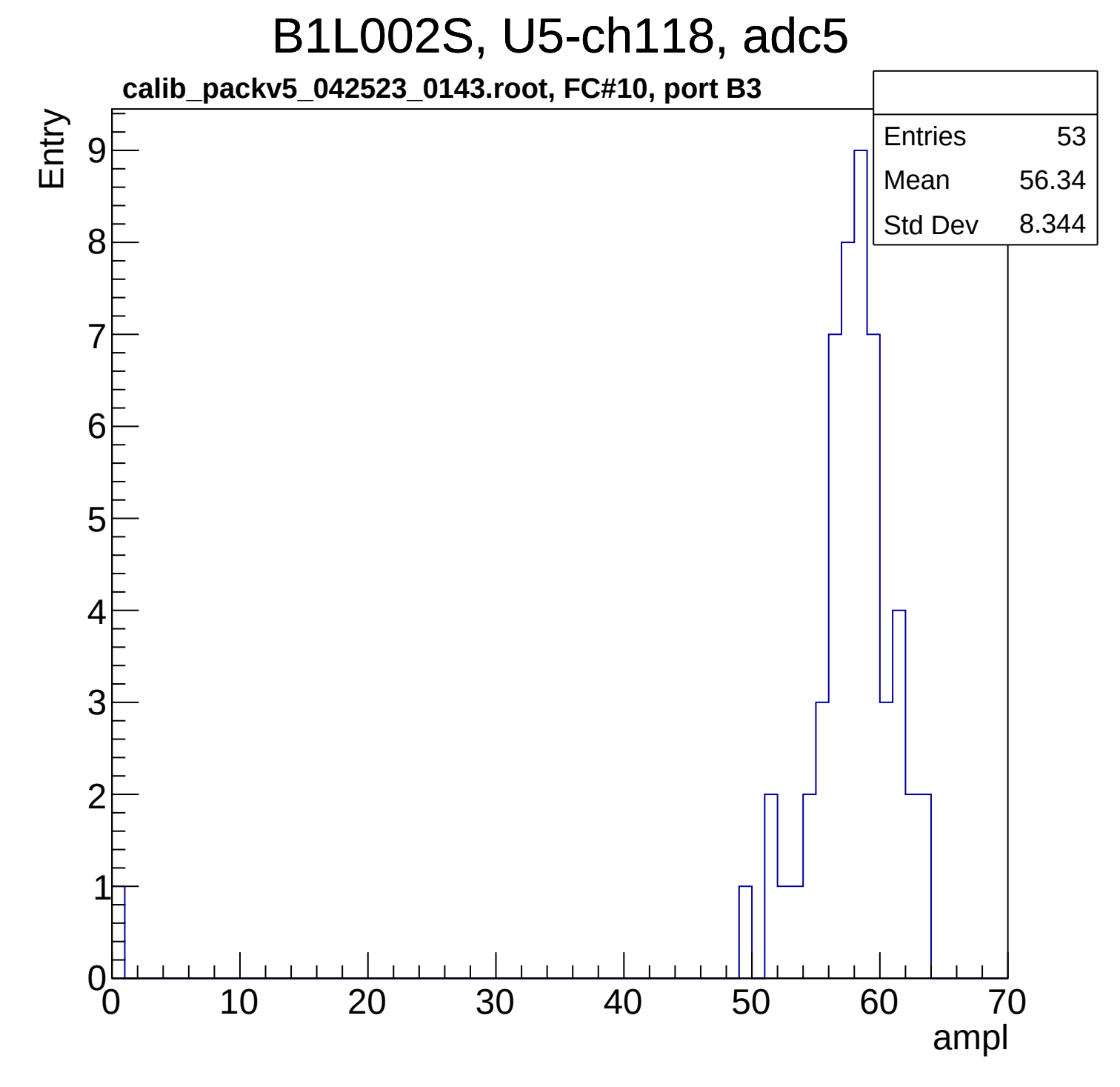
40
50
60
70


B1L002S, U5-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.12
Std Dev 2.704
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.34
Std Dev 8.344
Entry
ampl
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4
5
6
7
8
9
0
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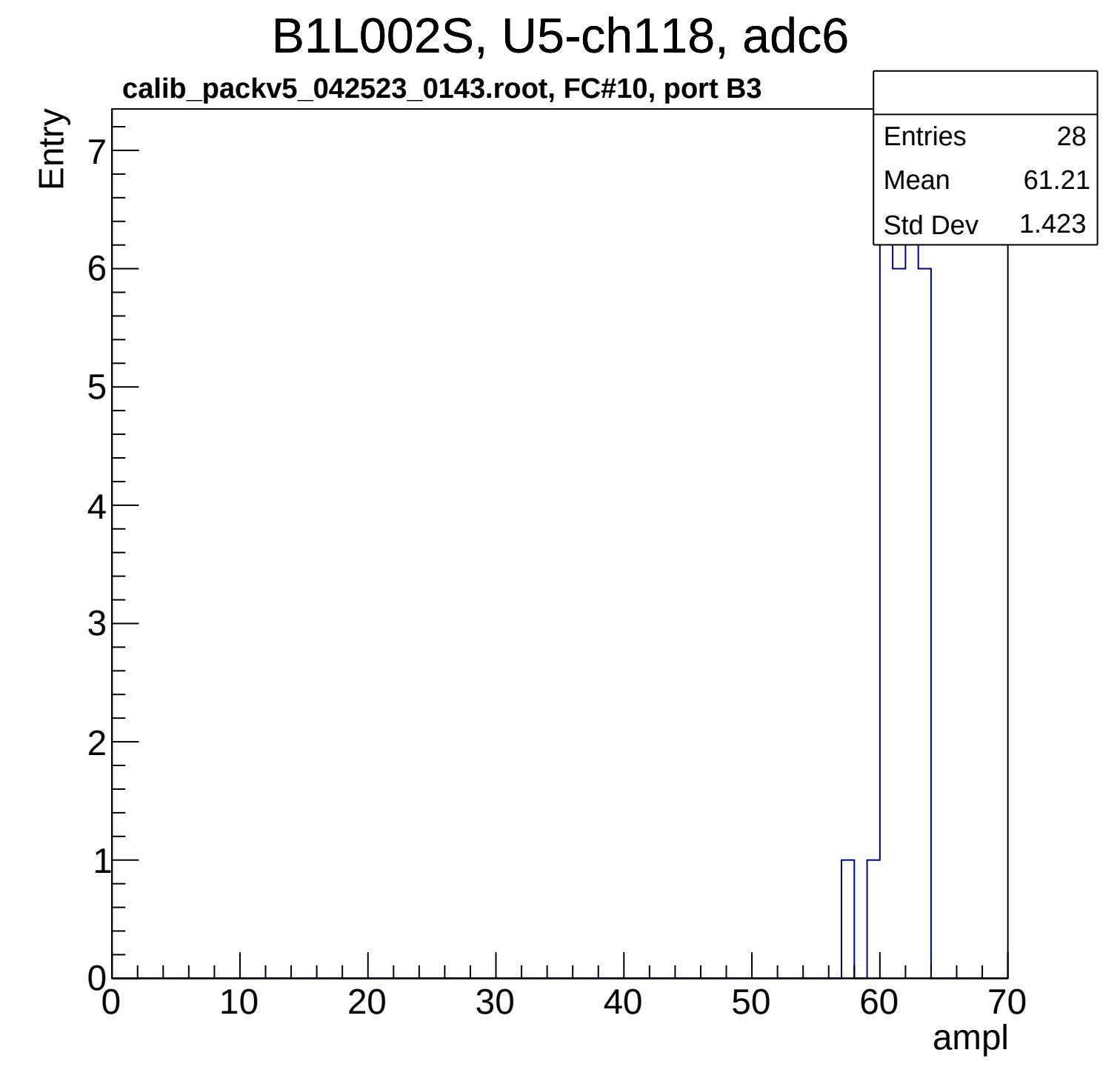
40
50
60
70


B1L002S, U5-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61.21
Std Dev 1.423
Entry
ampl
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2
3
4
5
6
7
0
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40
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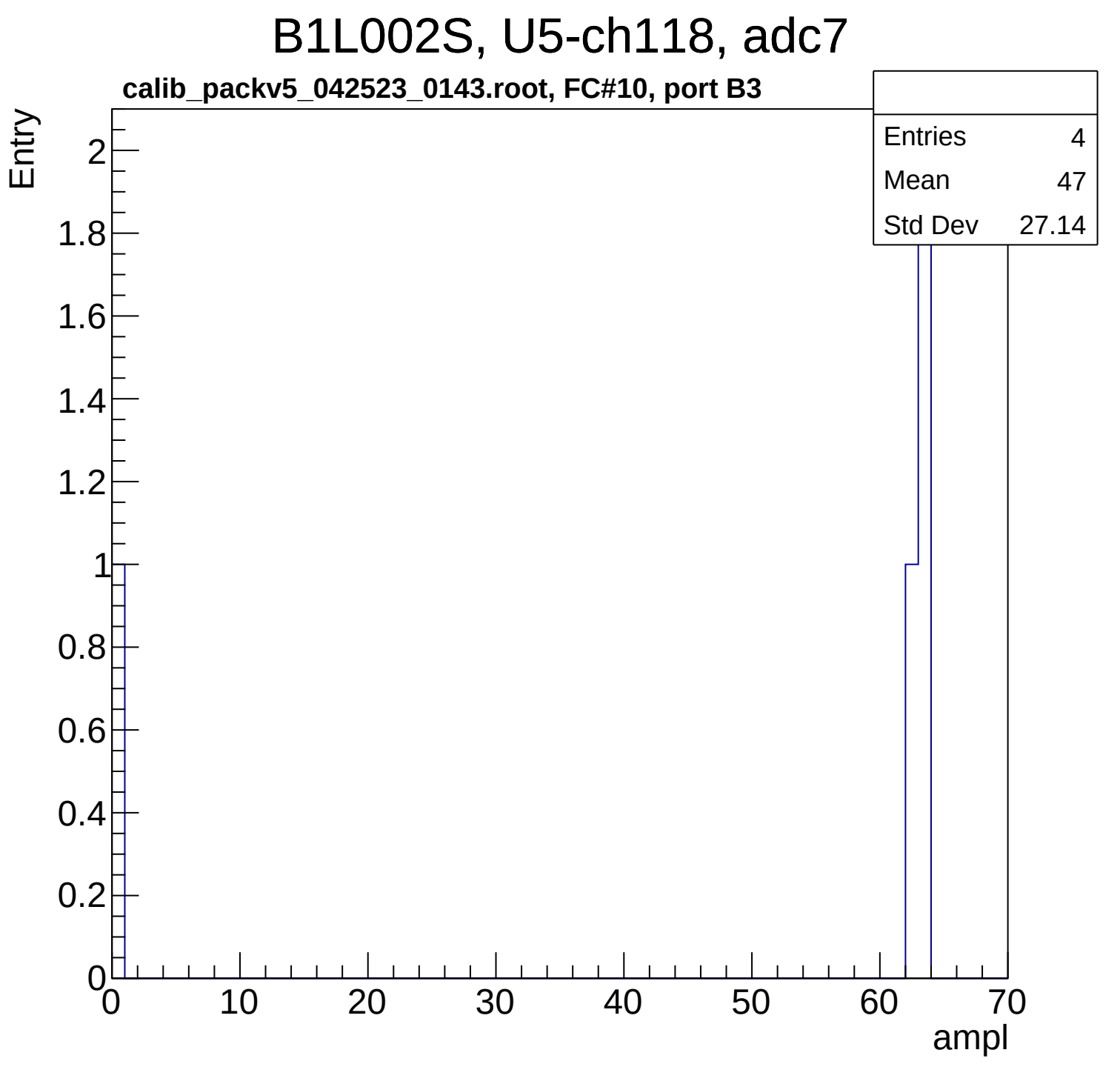
60
70


B1L002S, U5-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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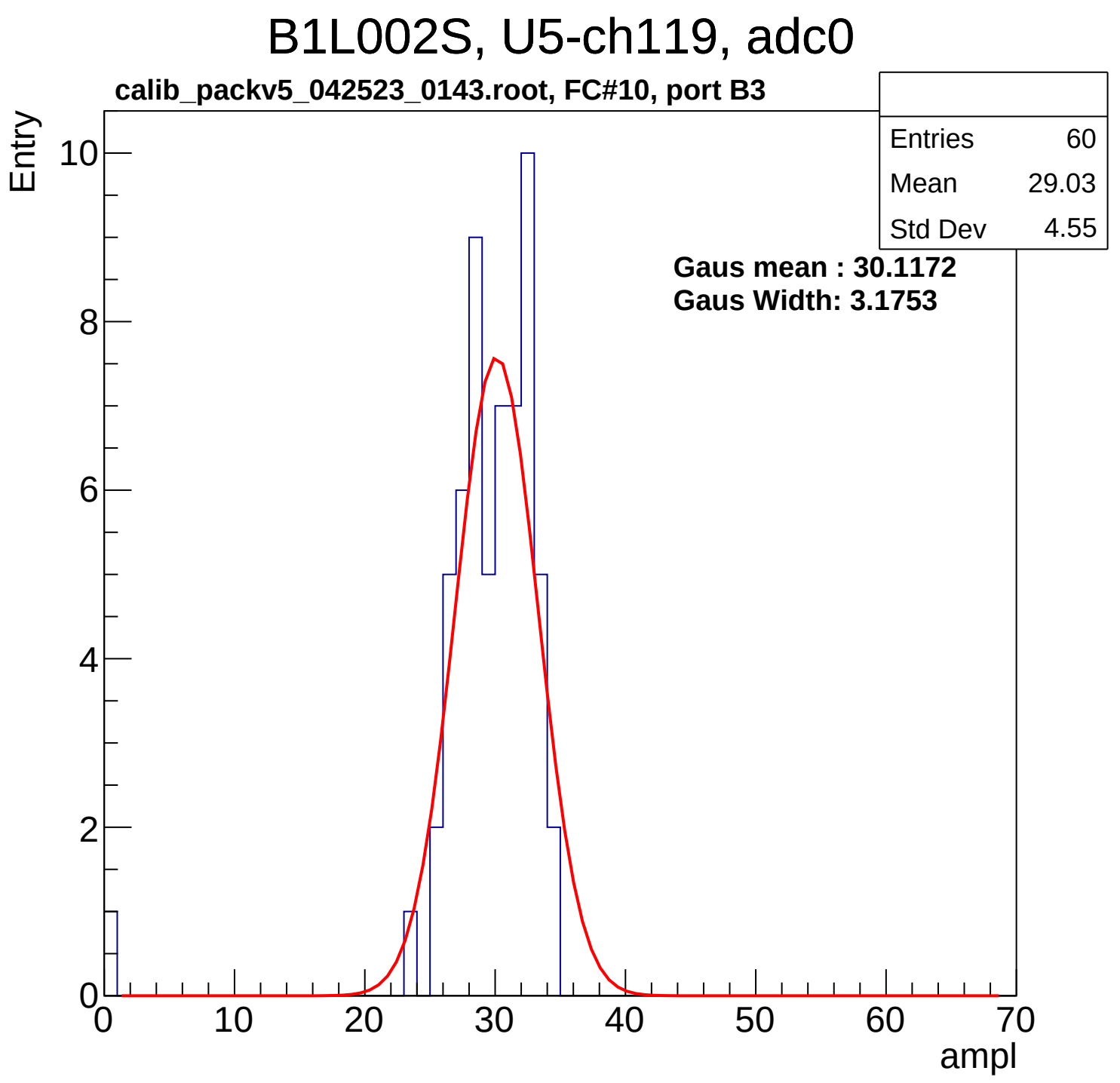
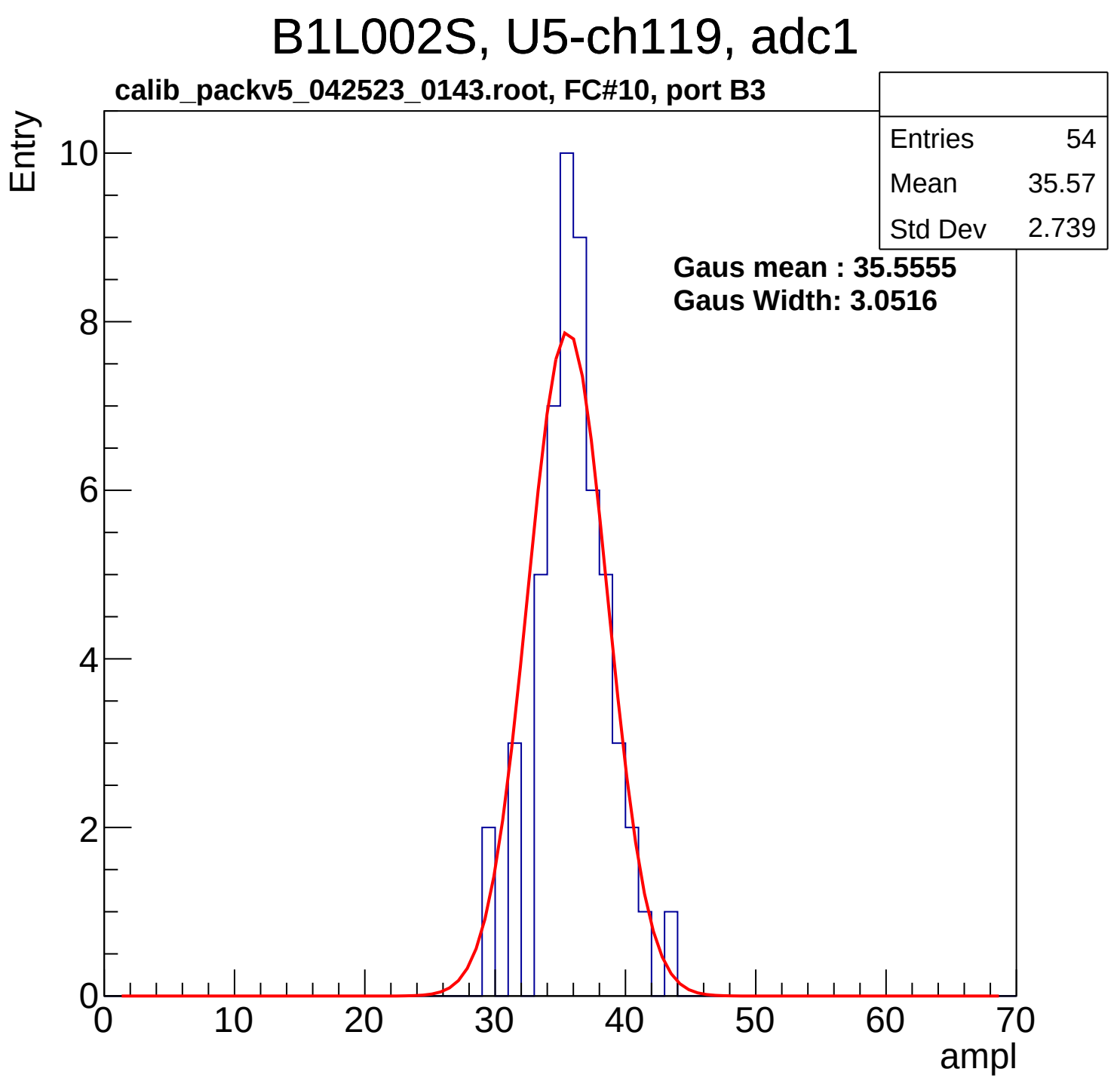

B1L002S, U5-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.03
Std Dev 4.55
Gaus mean : 30.1172
Gaus Width: 3.1753
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.57
Std Dev 2.739
Gaus mean : 35.5555
Gaus Width: 3.0516
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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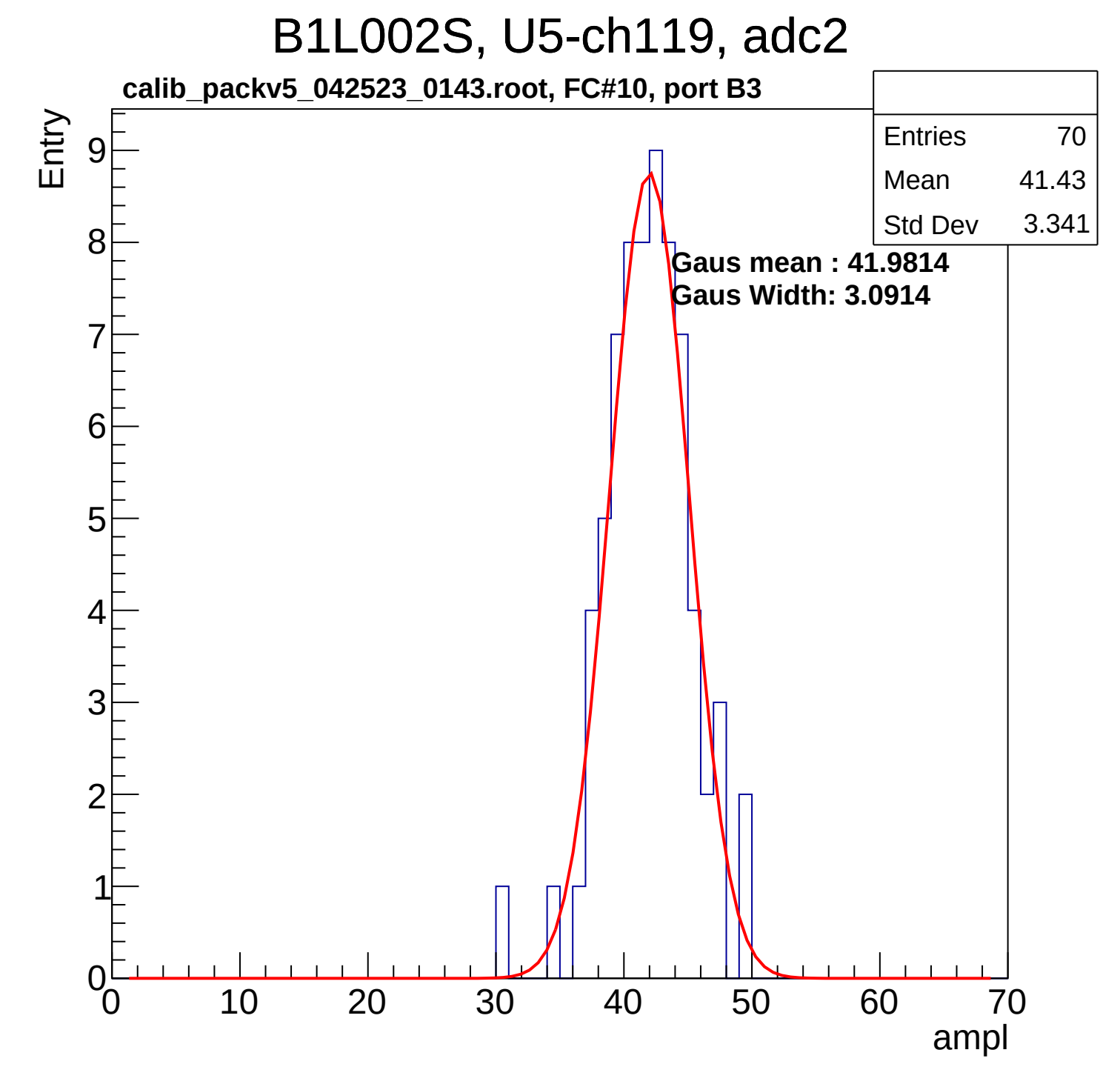
B1L002S, U5-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.43
Std Dev 3.341
Gaus mean : 41.9814
Gaus Width: 3.0914
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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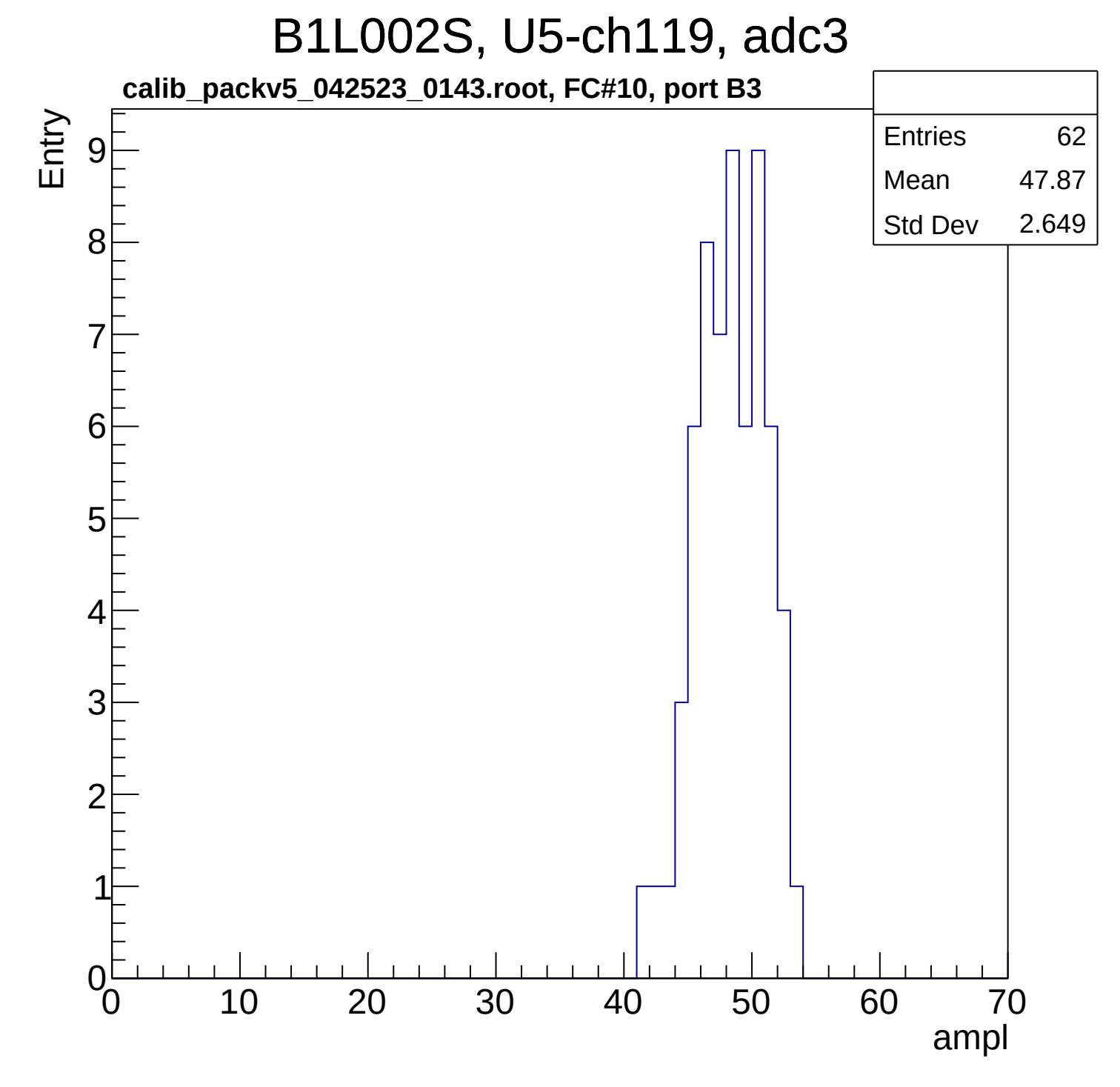
B1L002S, U5-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.87
Std Dev 2.649
Entry
ampl
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3
4
5
6
7
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0
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60
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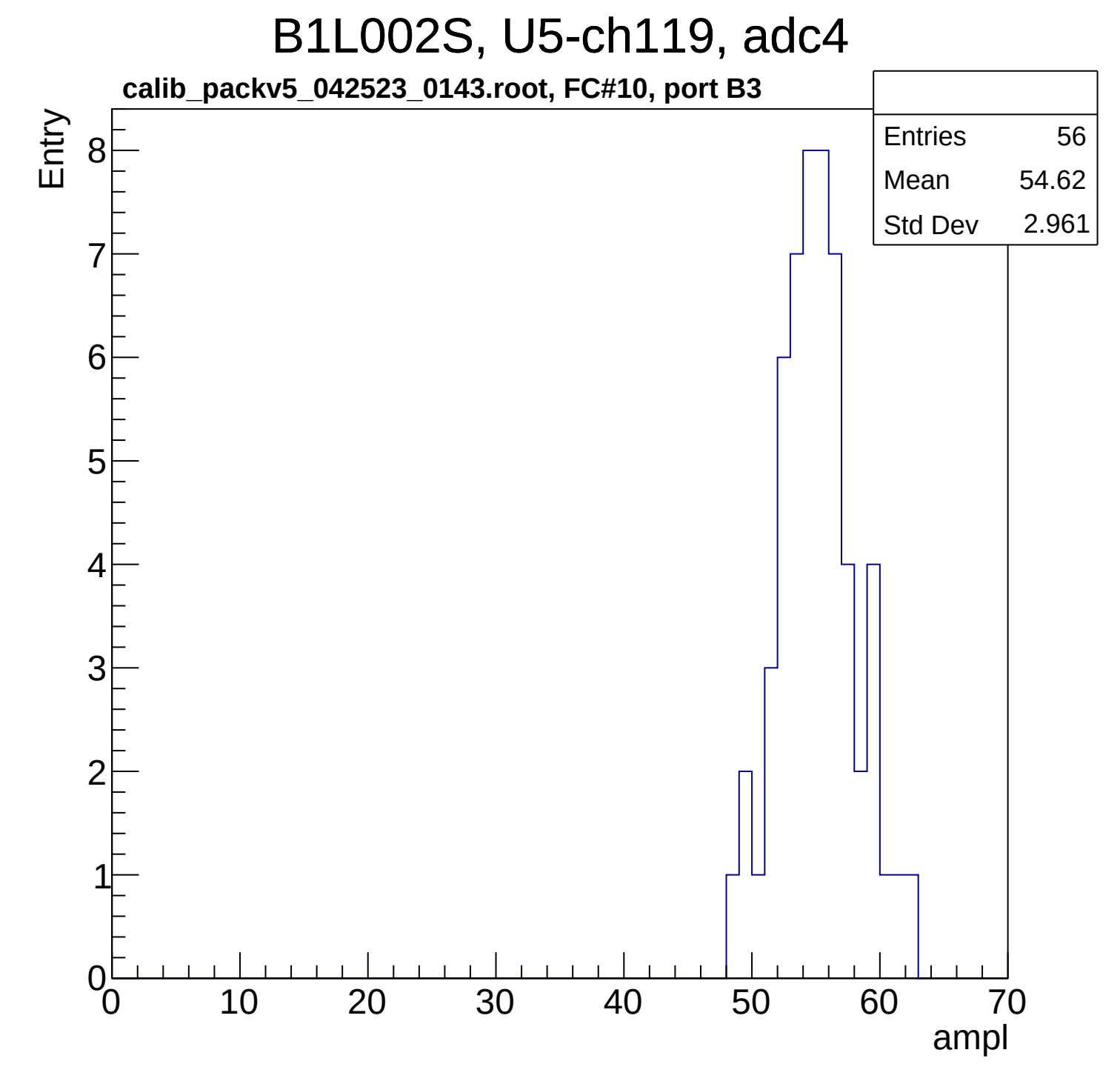
B1L002S, U5-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.62
Std Dev 2.961
Entry
0
1
2
3
4
5
6
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8
0
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20
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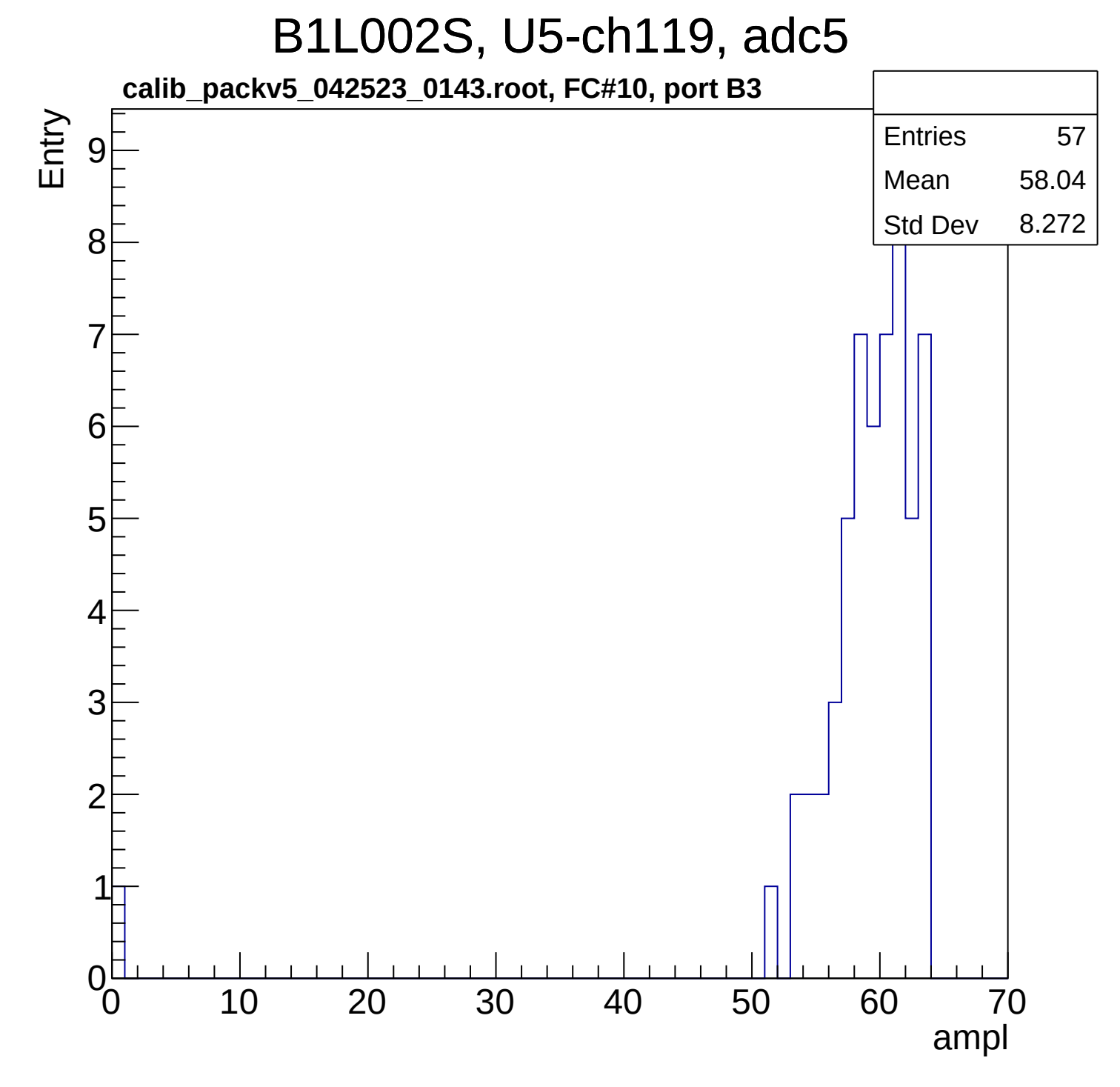
70
ampl


B1L002S, U5-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.04
Std Dev 8.272
Entry
ampl
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1
2
3
4
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6
7
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0
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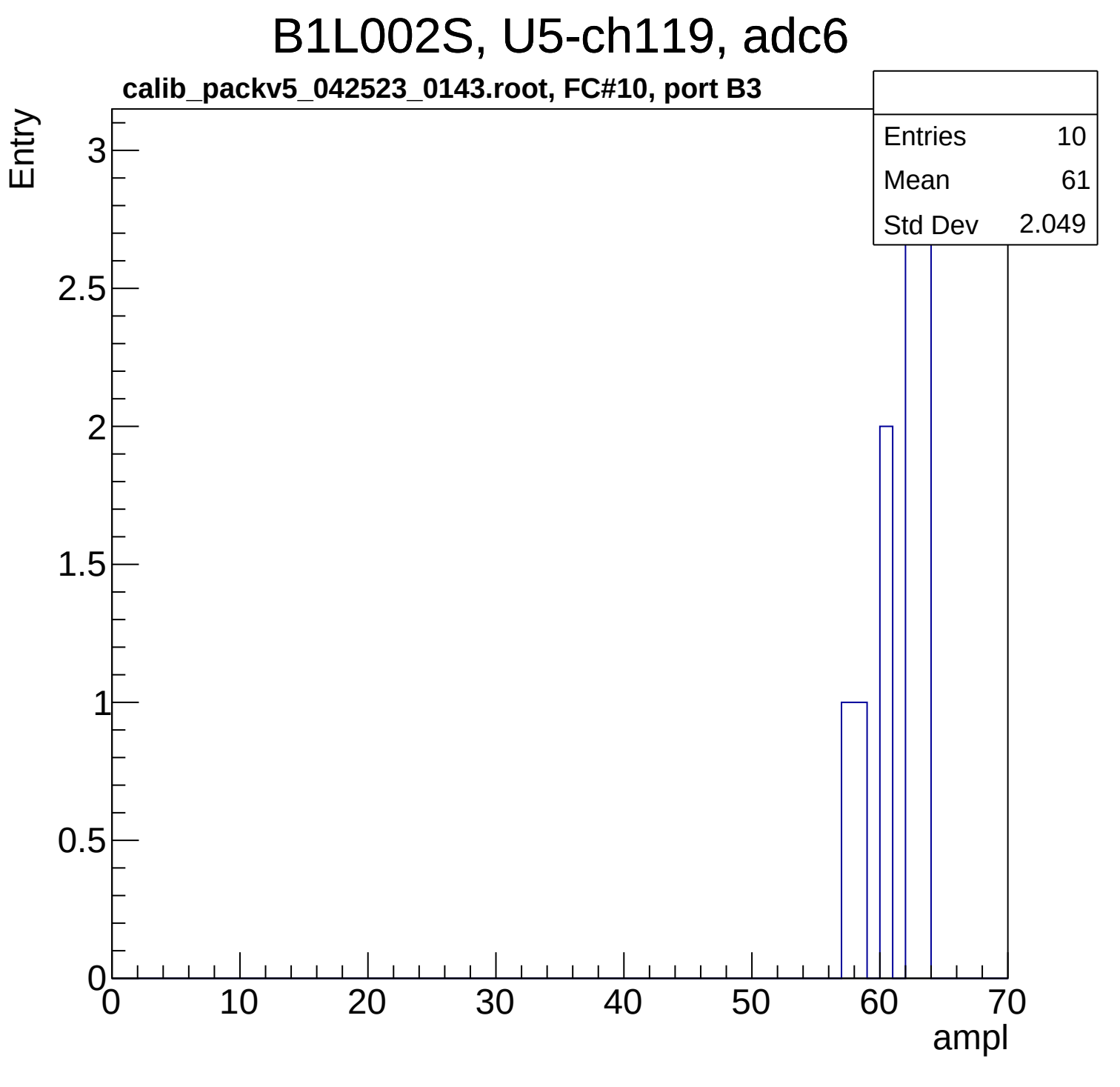
B1L002S, U5-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61
Std Dev 2.049
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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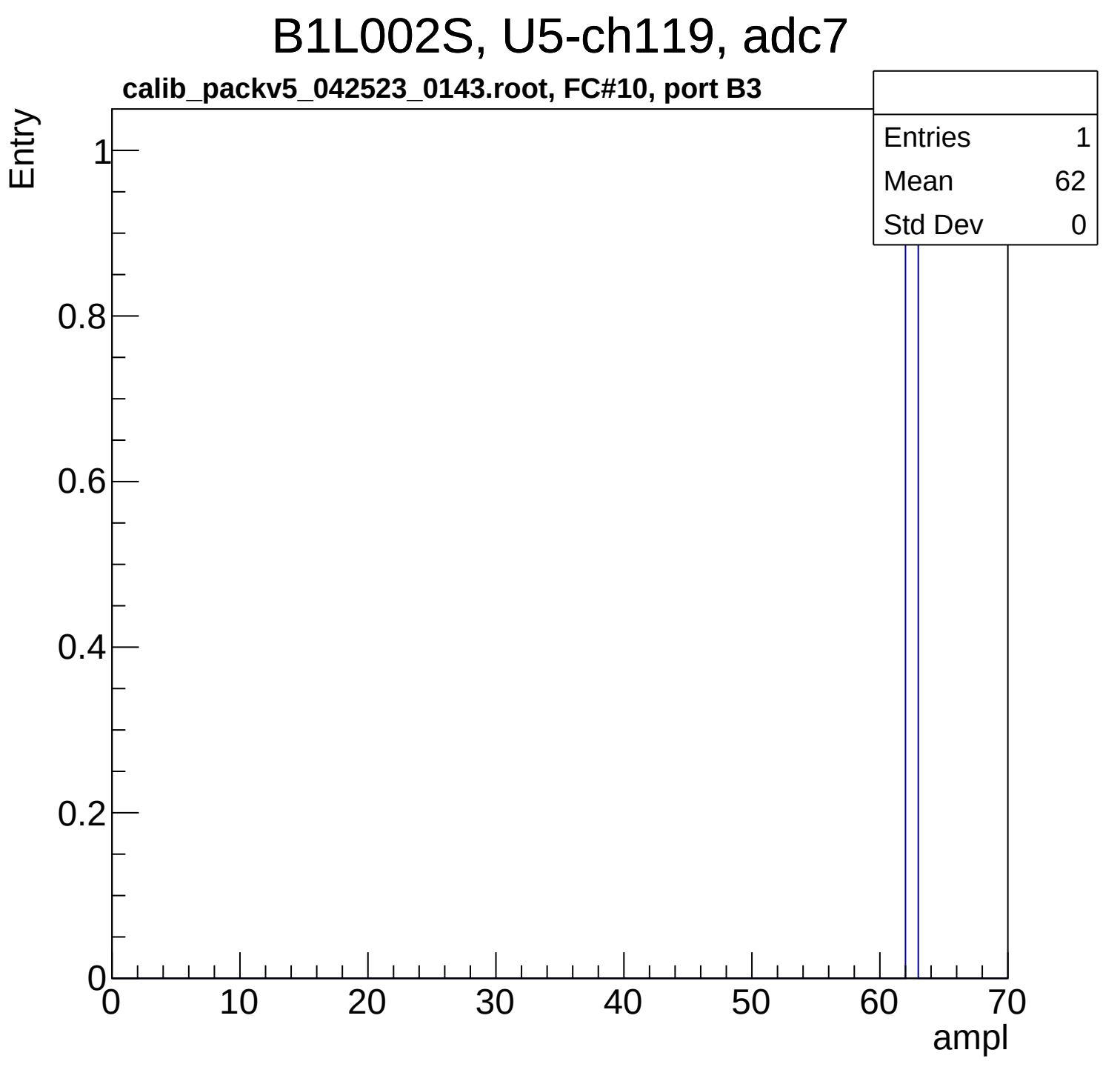
B1L002S, U5-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
30
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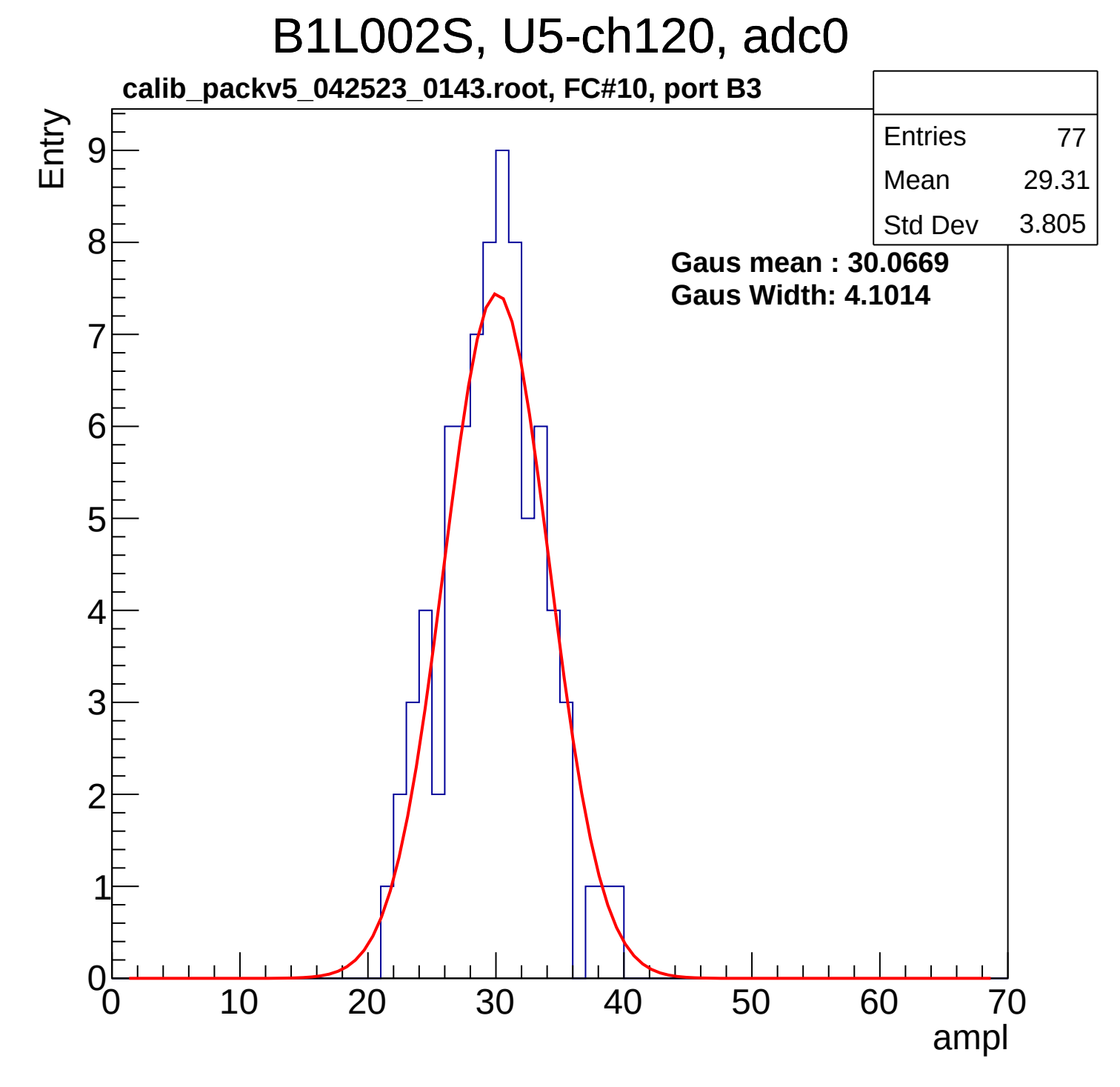
50
60
70
ampl


B1L002S, U5-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.31
Std Dev 3.805
Gaus mean : 30.0669
Gaus Width: 4.1014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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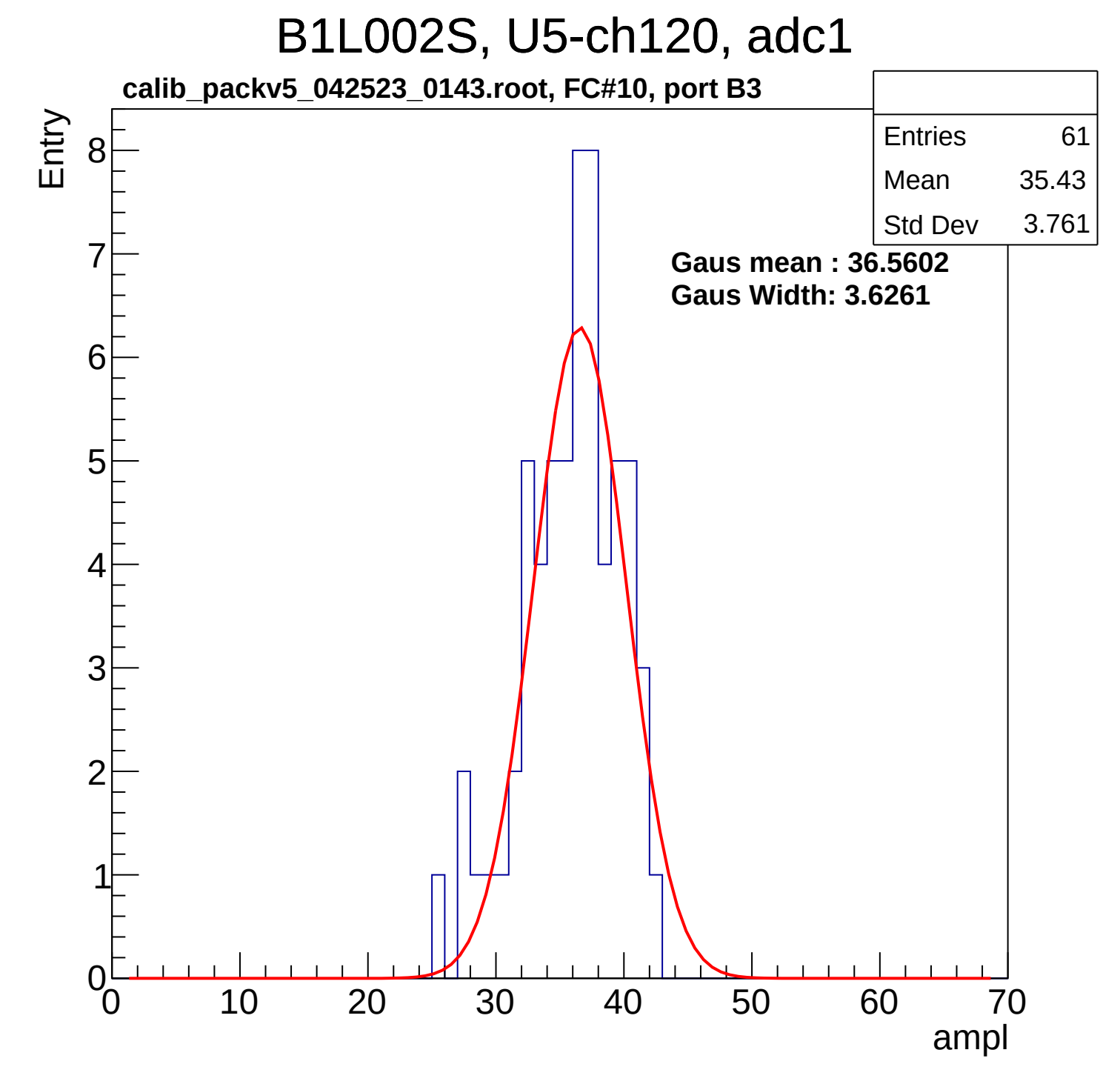
B1L002S, U5-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.43
Std Dev 3.761
Gaus mean : 36.5602
Gaus Width: 3.6261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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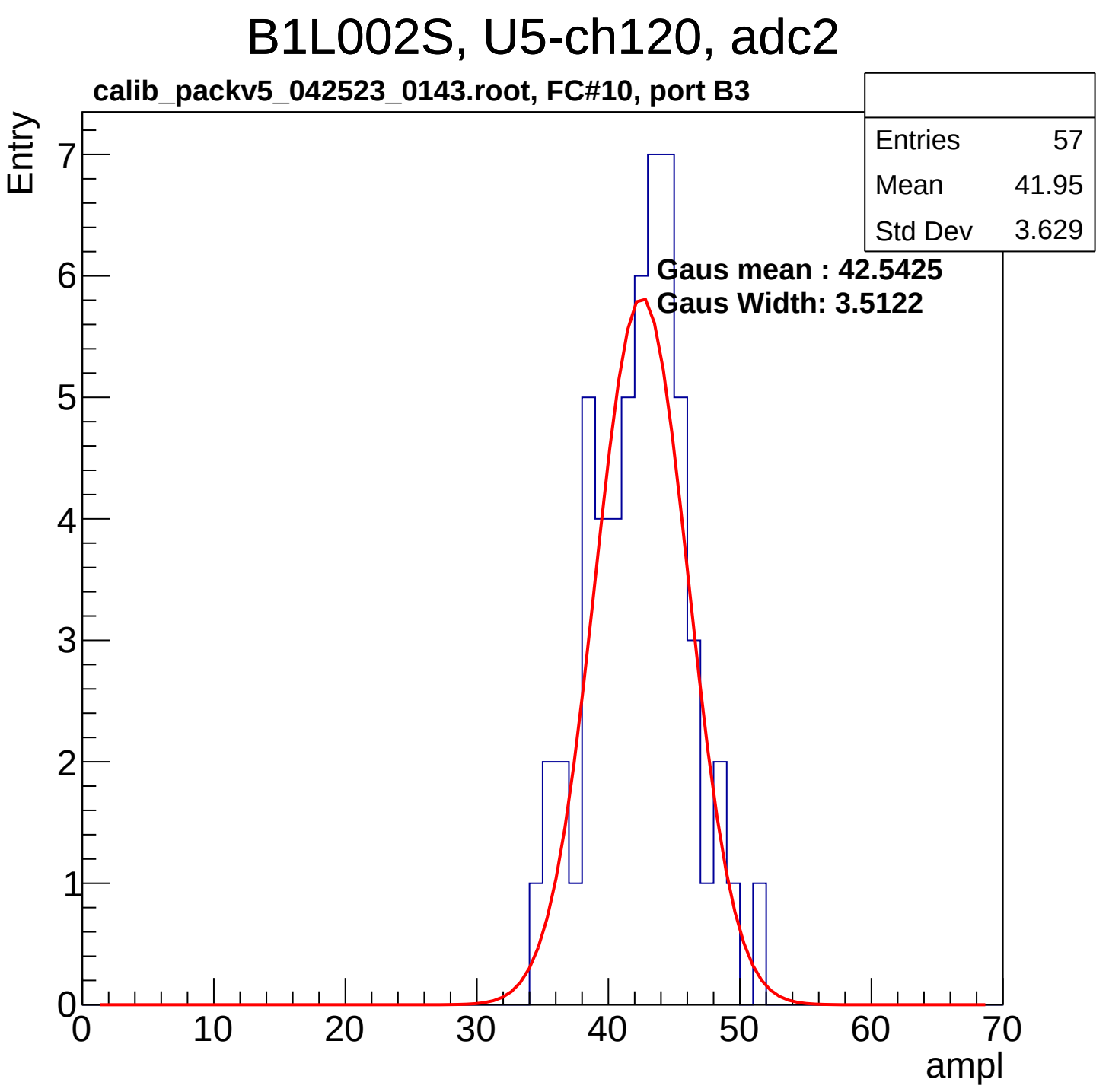
B1L002S, U5-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.95
Std Dev 3.629
Gaus mean : 42.5425
Gaus Width: 3.5122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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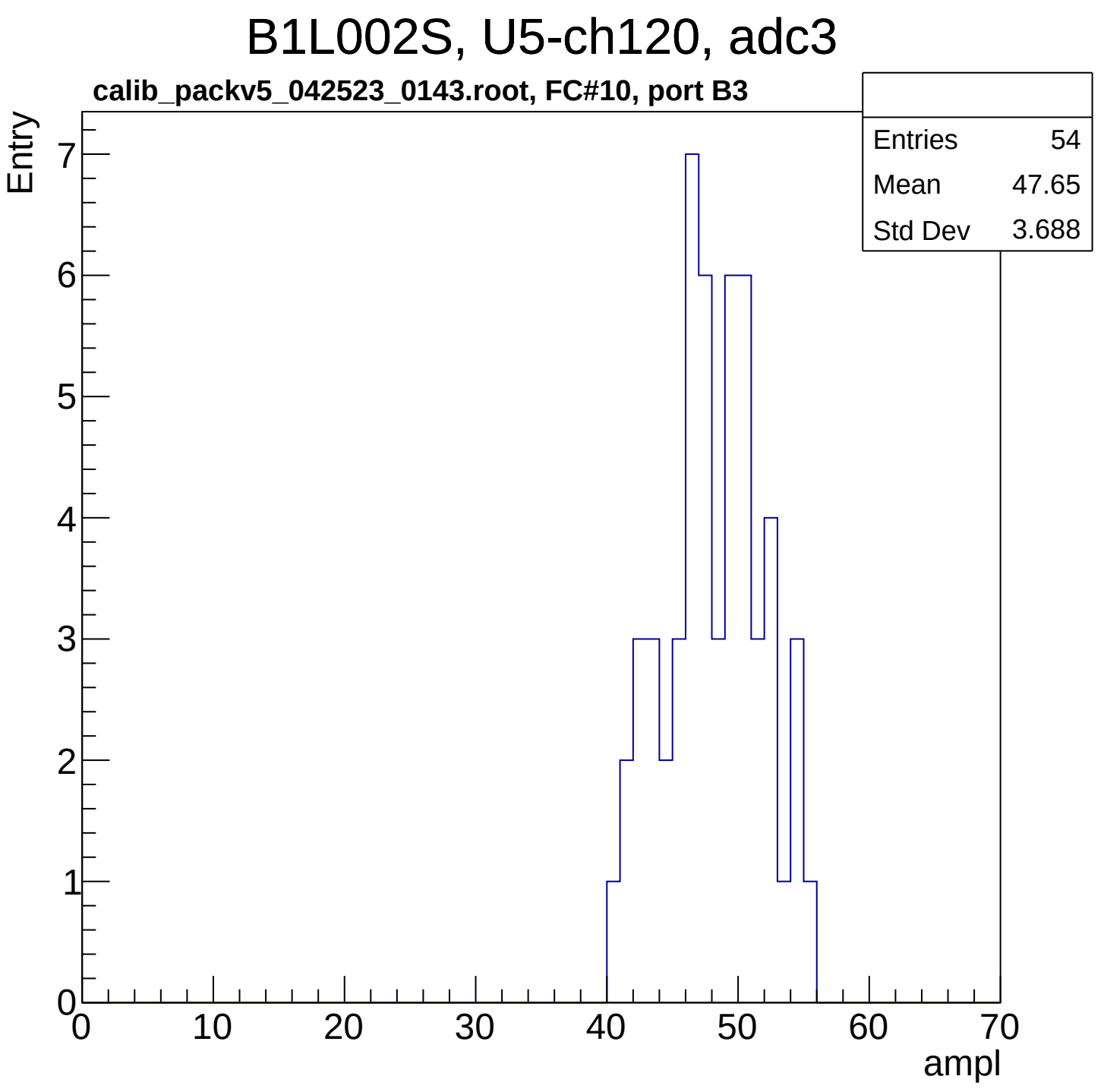
B1L002S, U5-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.65
Std Dev 3.688
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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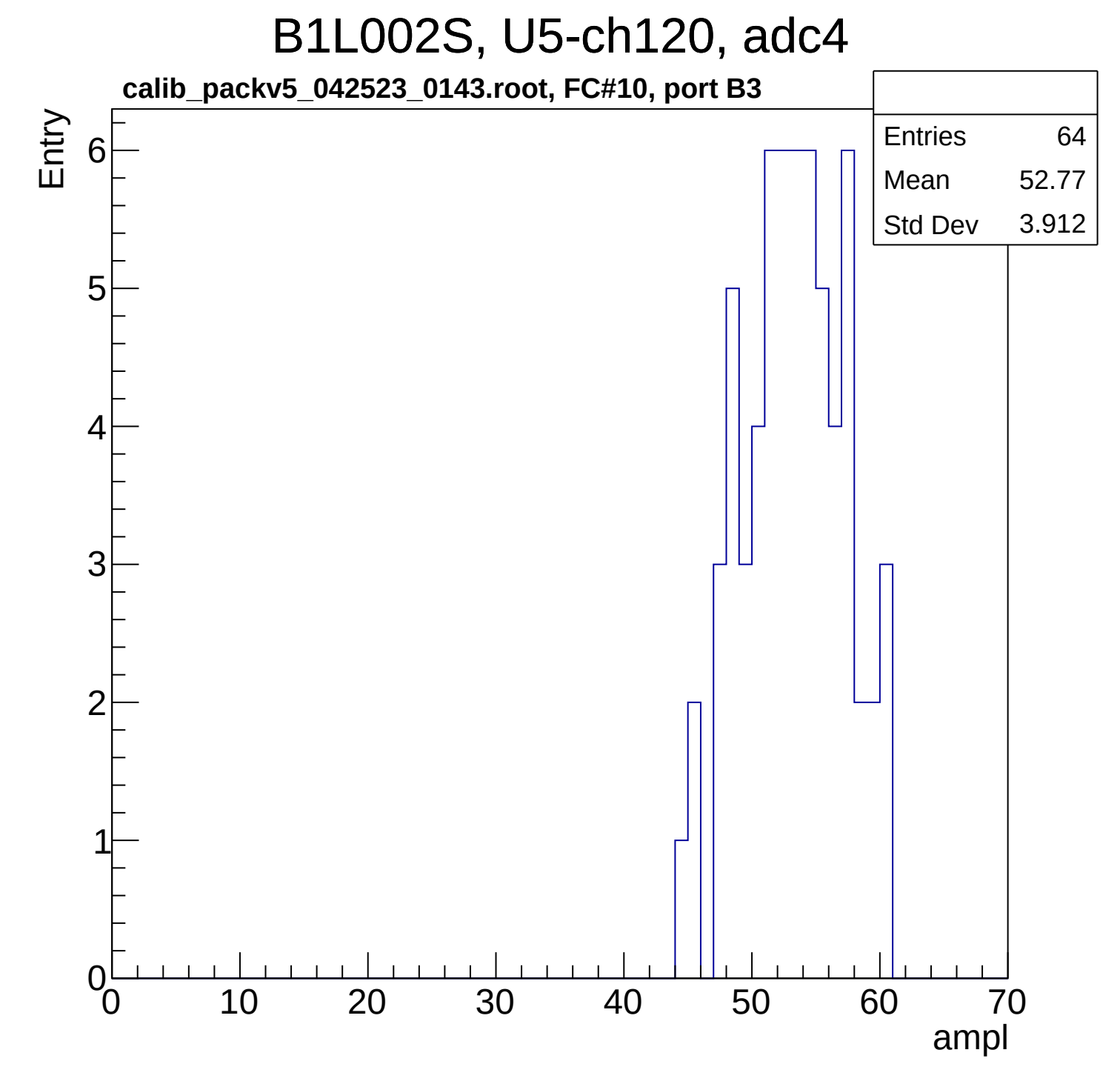
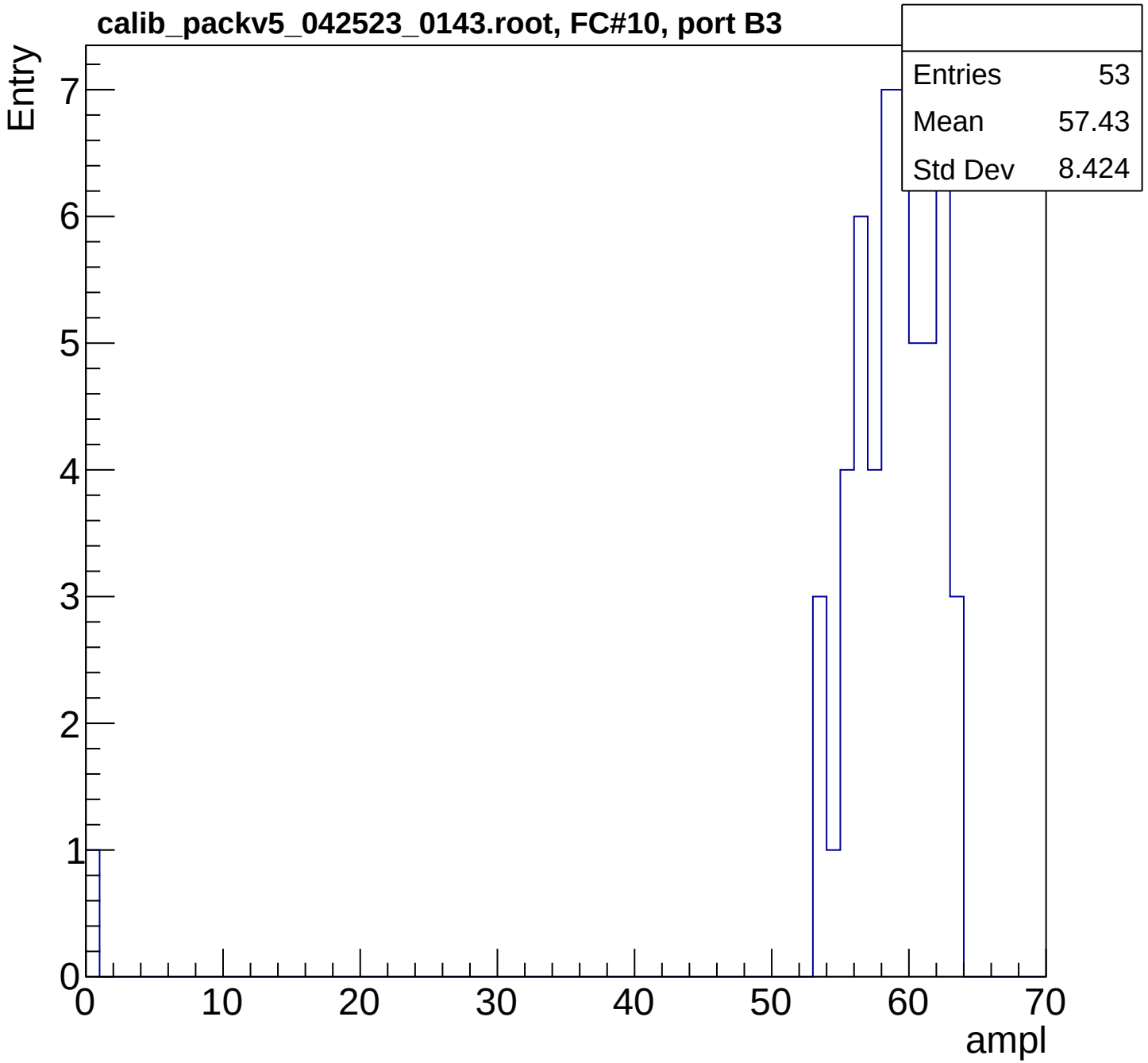
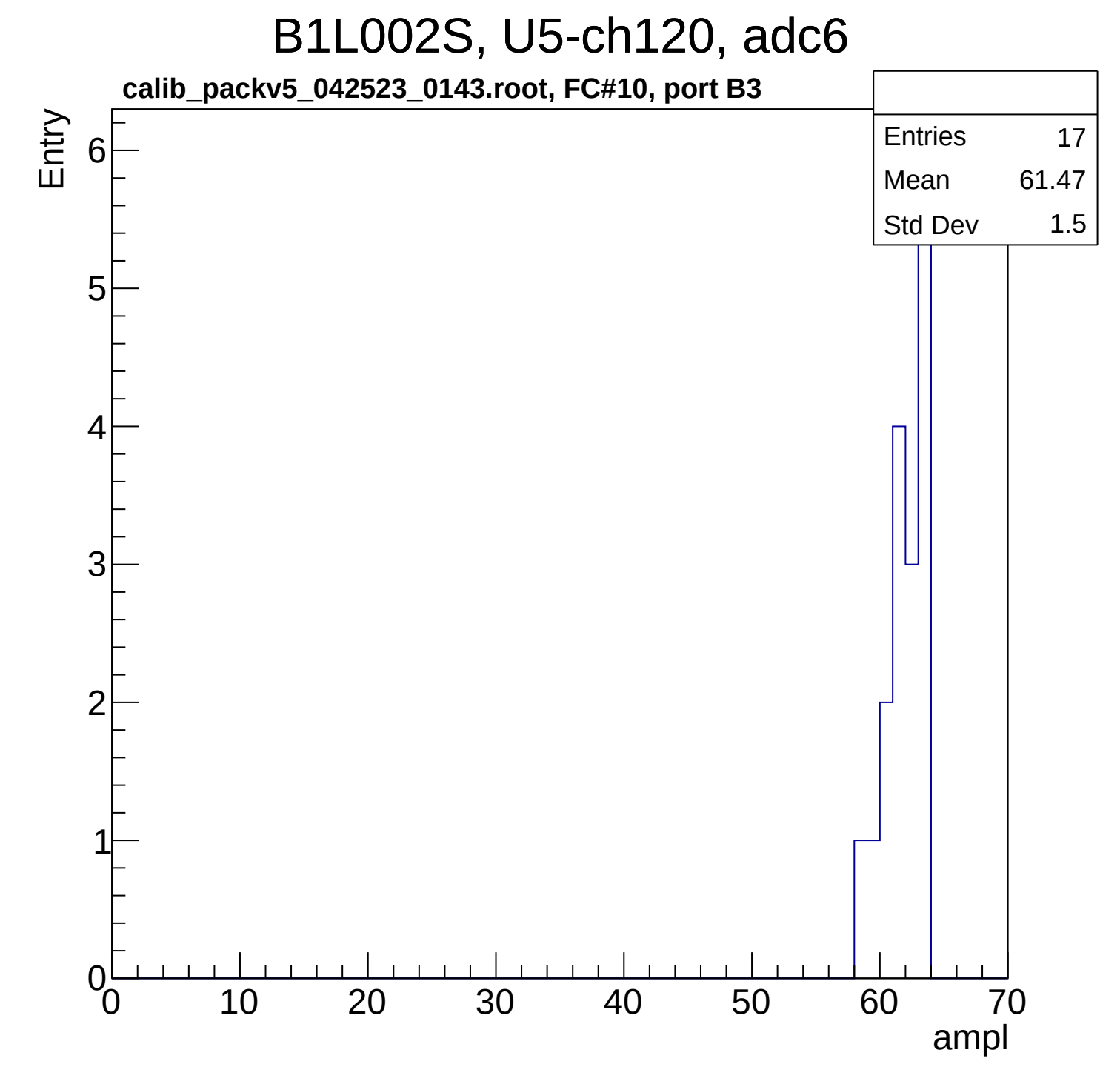
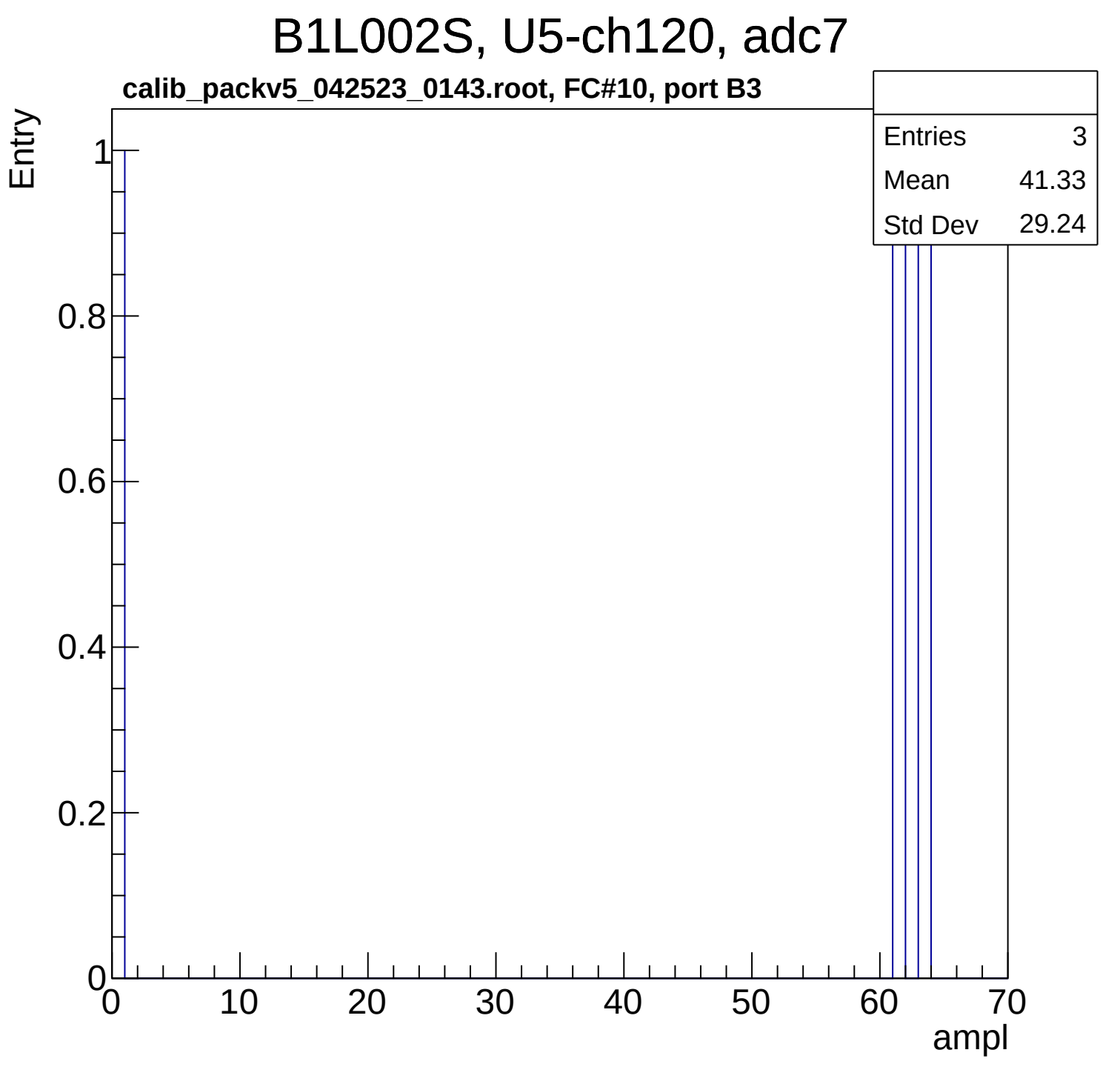
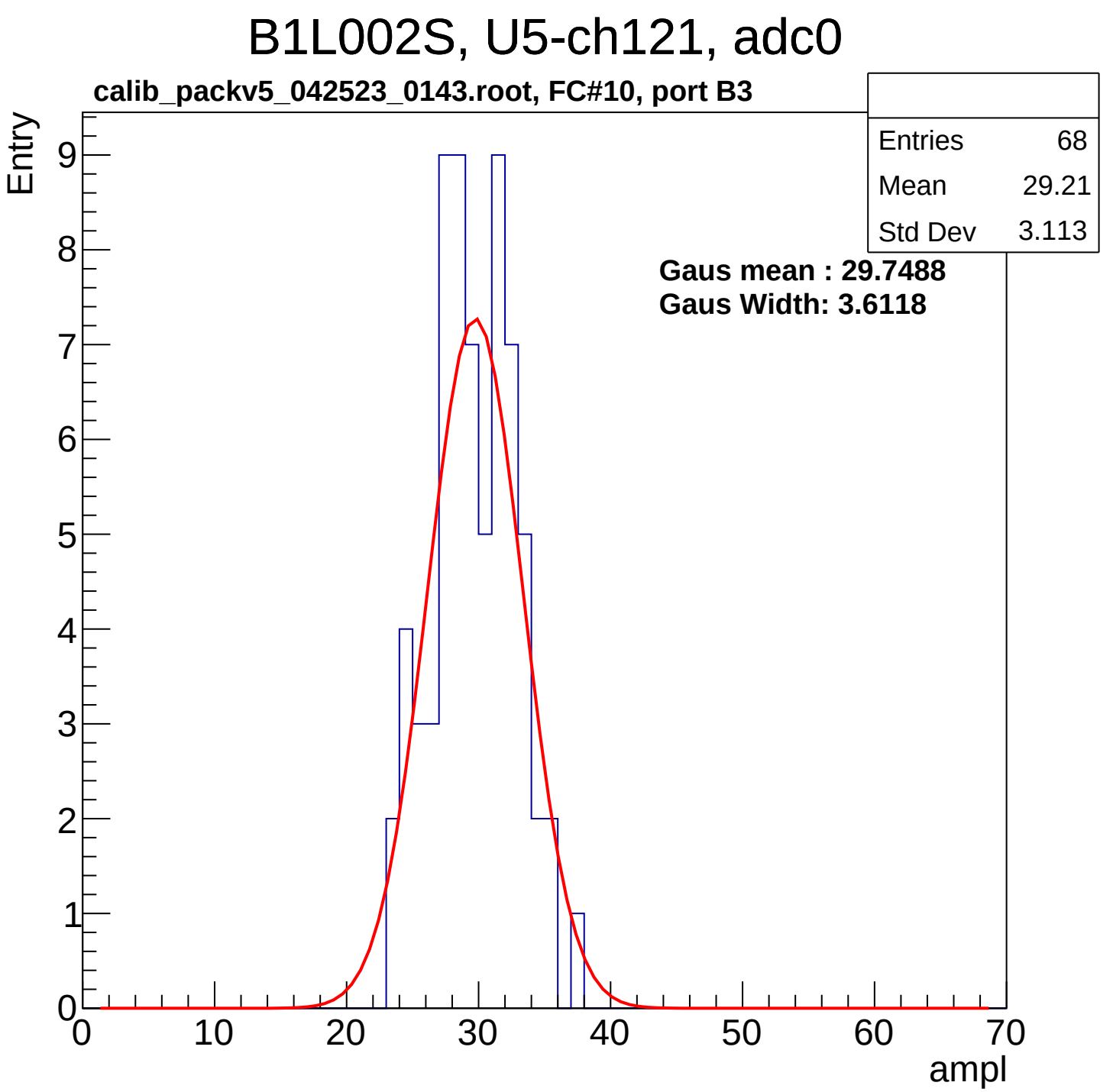

B1L002S, U5-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.77
Std Dev 3.912
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.43
Std Dev 8.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.47
Std Dev 1.5
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.21
Std Dev 3.113
Gaus mean : 29.7488
Gaus Width: 3.6118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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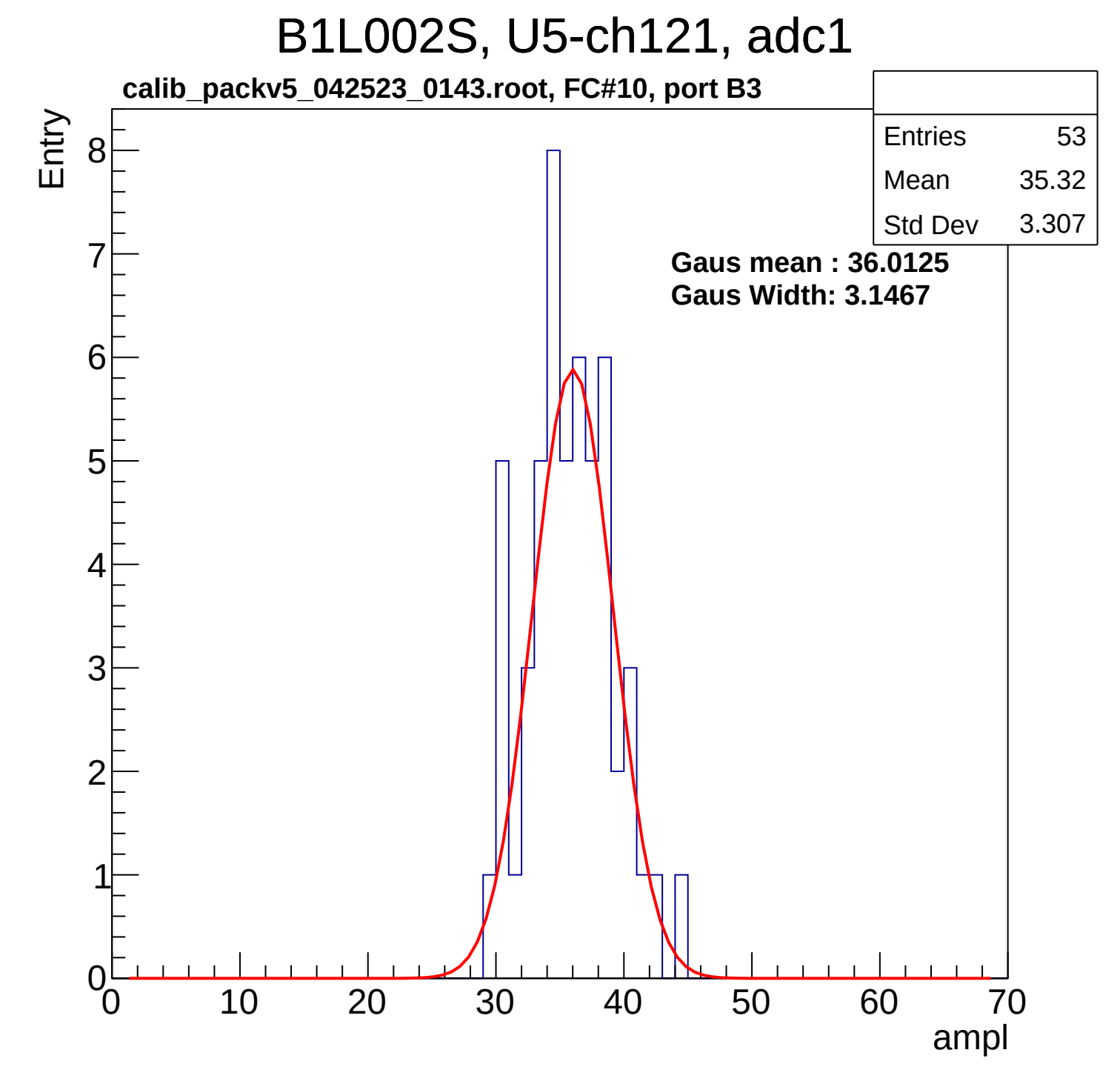
60
70


B1L002S, U5-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 35.32
Std Dev 3.307
Gaus mean : 36.0125
Gaus Width: 3.1467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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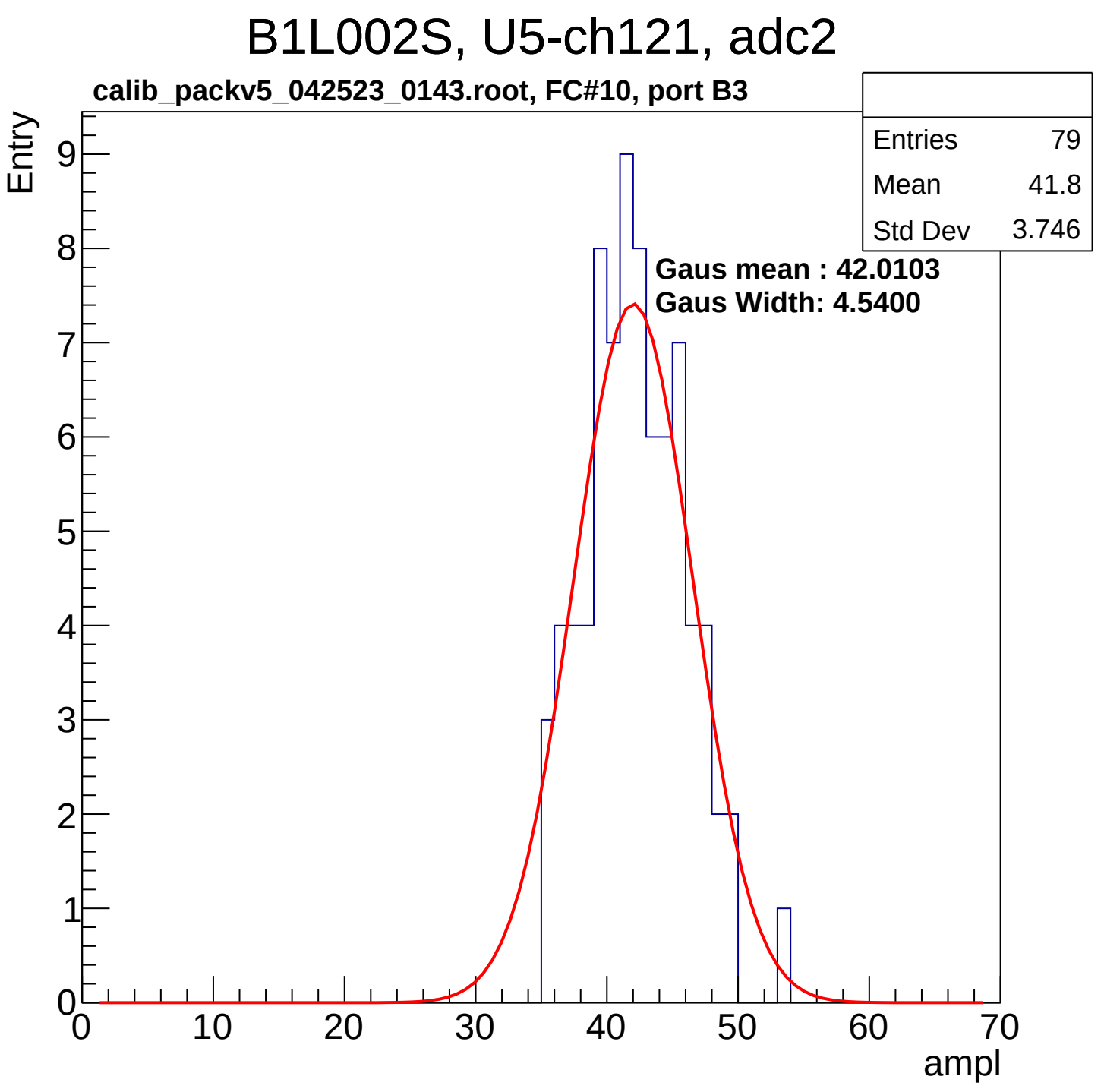
B1L002S, U5-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 41.8
Std Dev 3.746
Gaus mean : 42.0103
Gaus Width: 4.5400
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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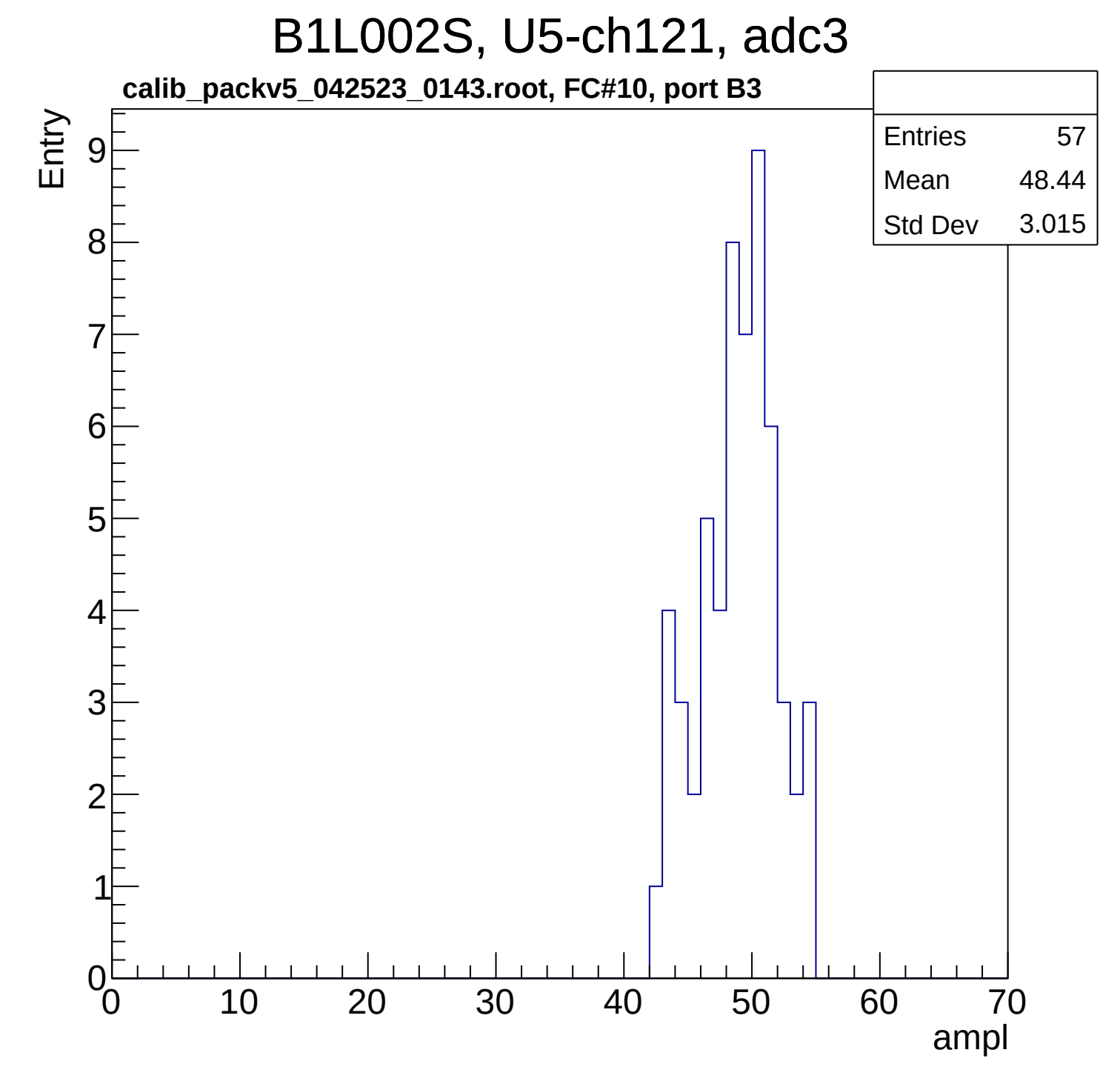
B1L002S, U5-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.44
Std Dev 3.015
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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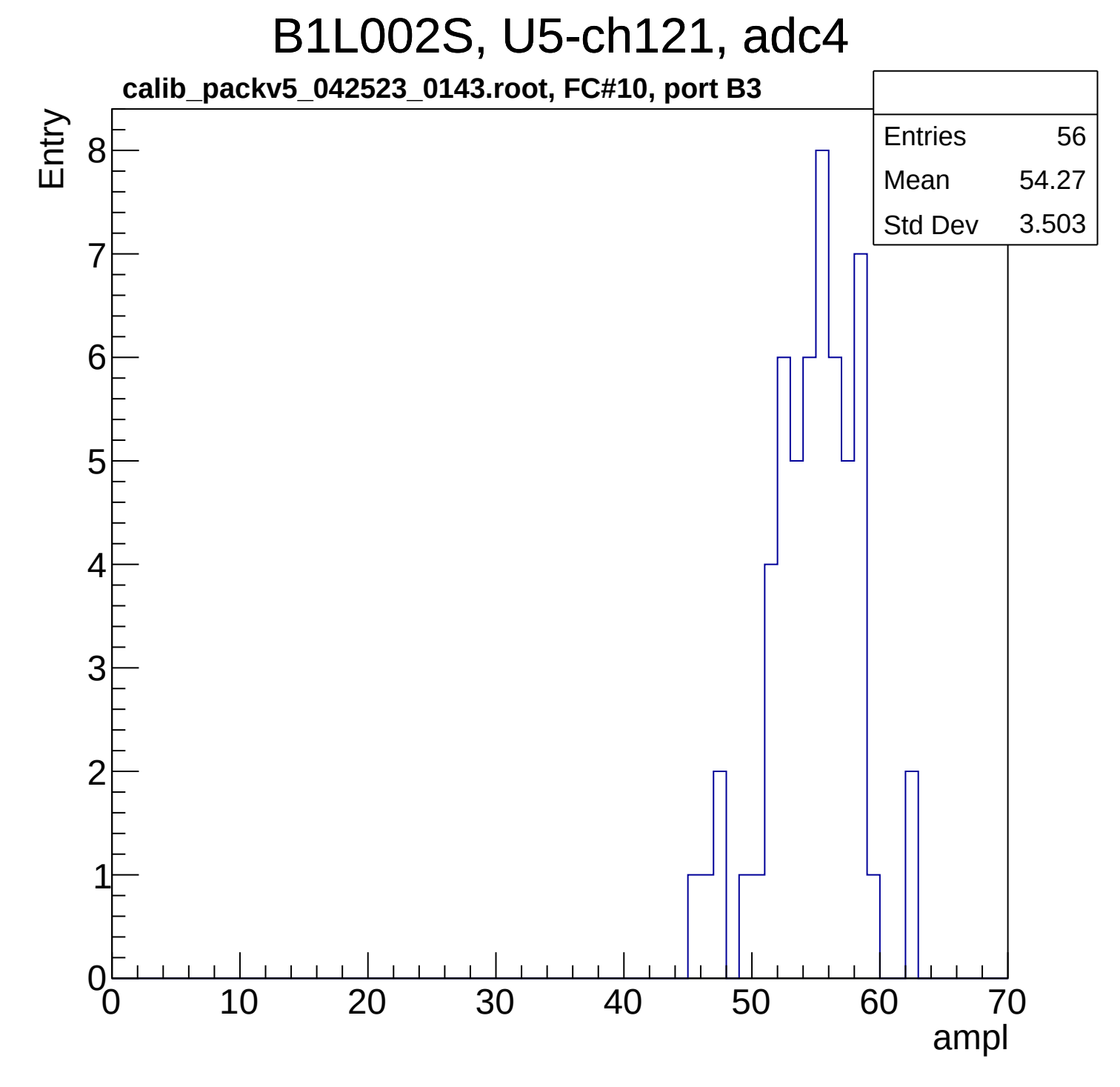
B1L002S, U5-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.27
Std Dev 3.503
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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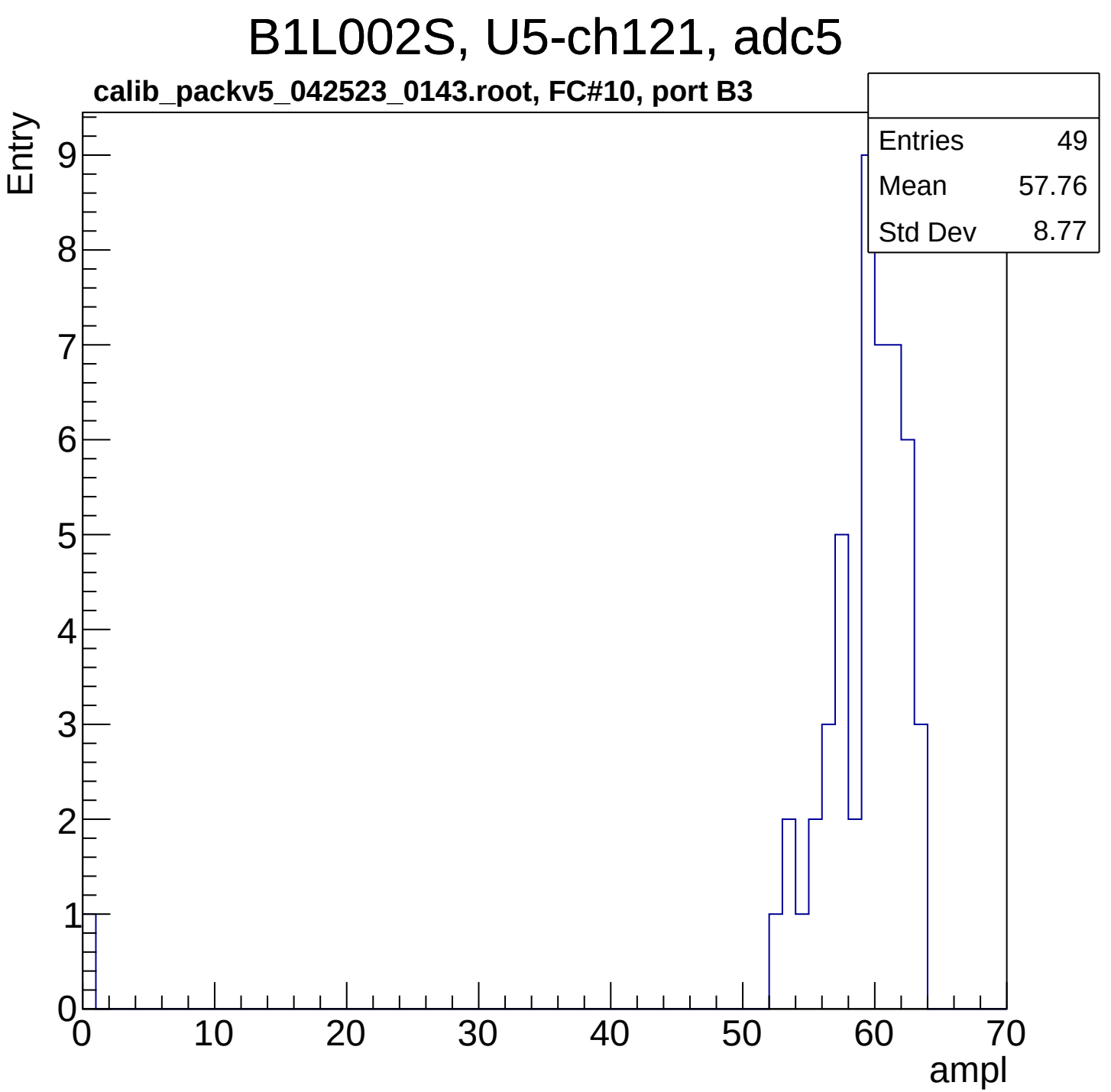
40
50
60
70


B1L002S, U5-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.76
Std Dev 8.77
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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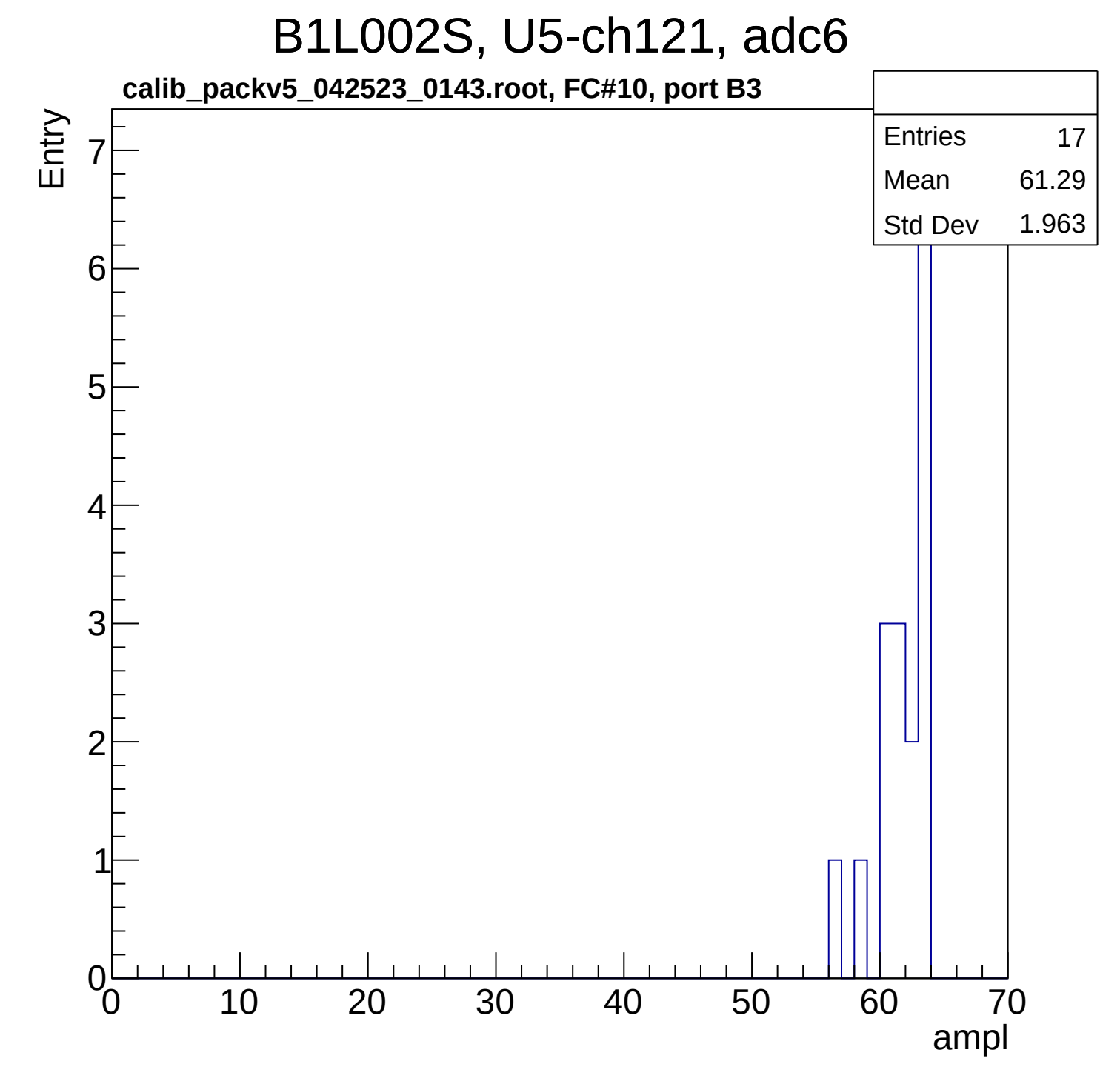
B1L002S, U5-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.29
Std Dev 1.963
Entry
ampl
0
1
2
3
4
5
6
7
0
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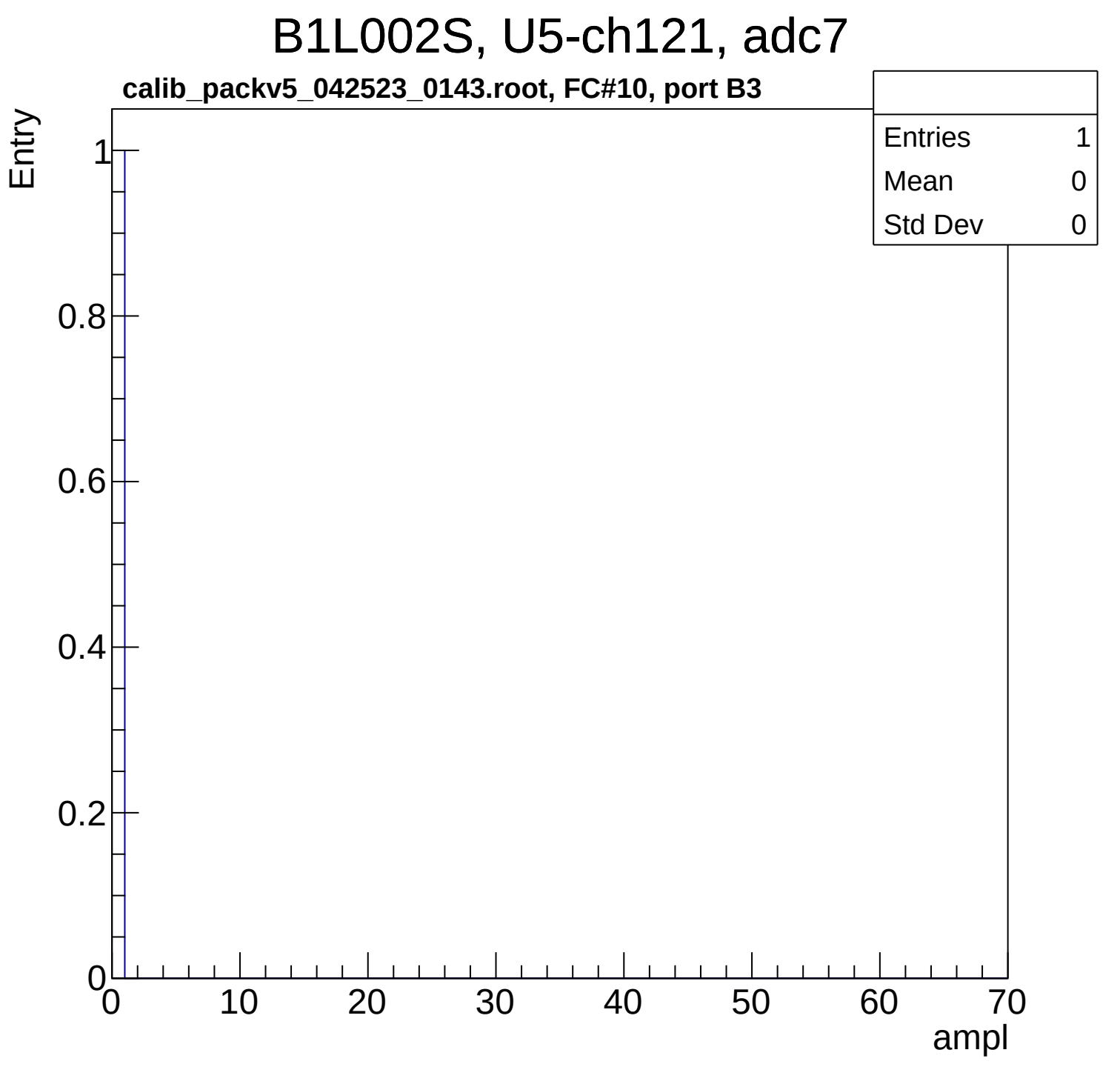
20
30
40
50
60
70


B1L002S, U5-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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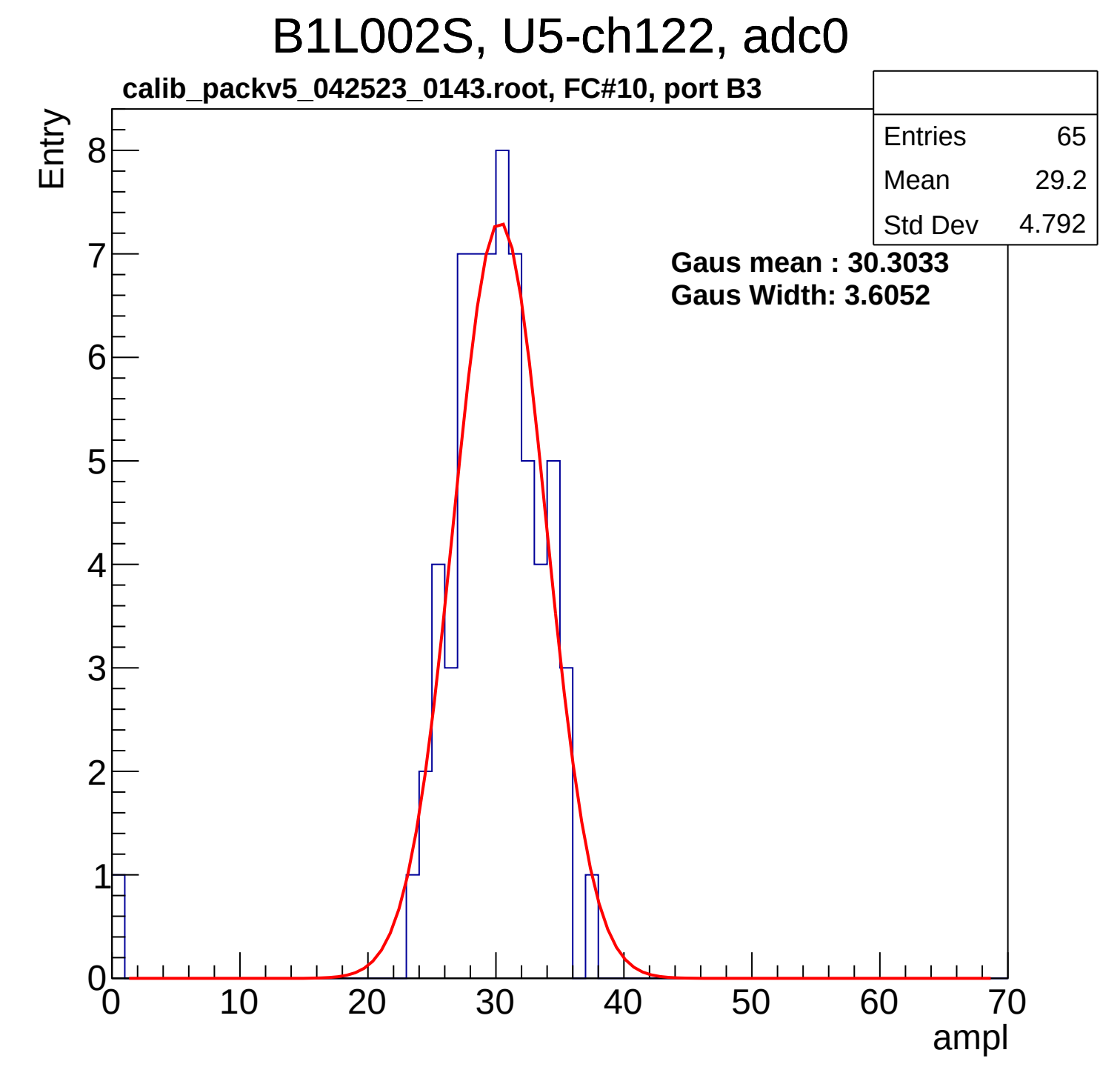
60
70
ampl


B1L002S, U5-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.2
Std Dev 4.792
Gaus mean : 30.3033
Gaus Width: 3.6052
Entry
ampl
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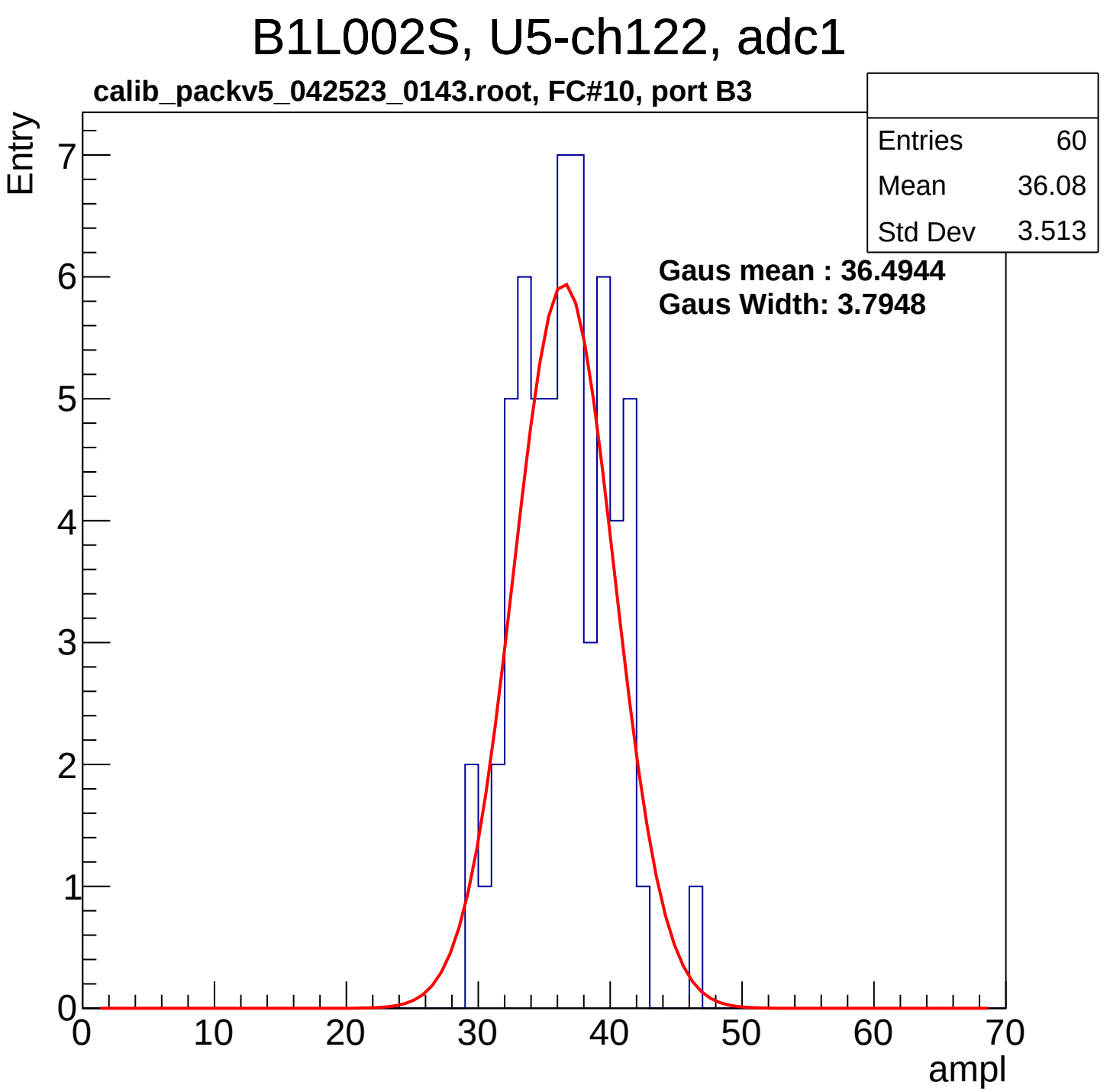
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 36.08
Std Dev 3.513
Gaus mean : 36.4944
Gaus Width: 3.7948
Entry
ampl
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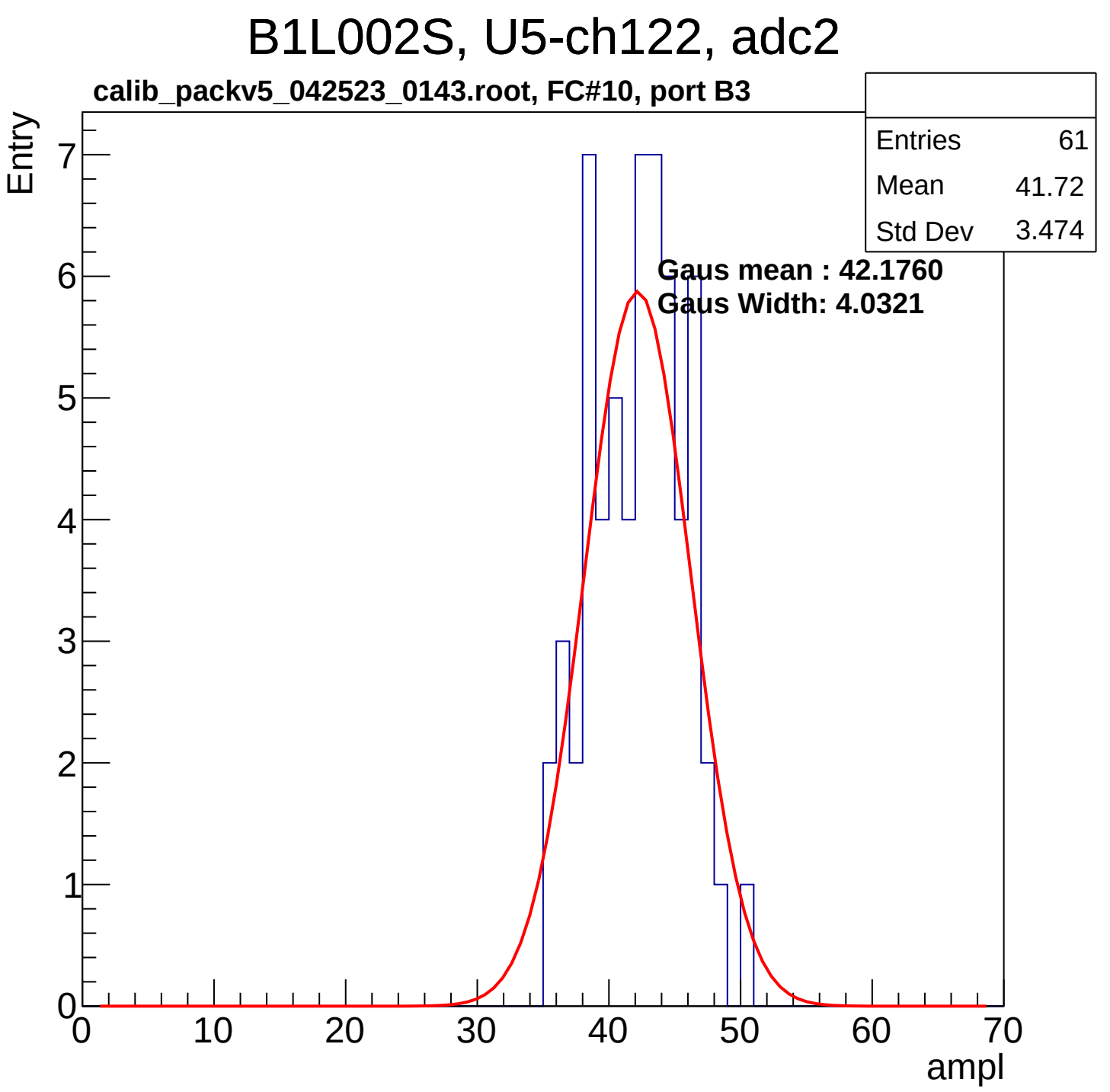
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.72
Std Dev 3.474
Gaus mean : 42.1760
Gaus Width: 4.0321
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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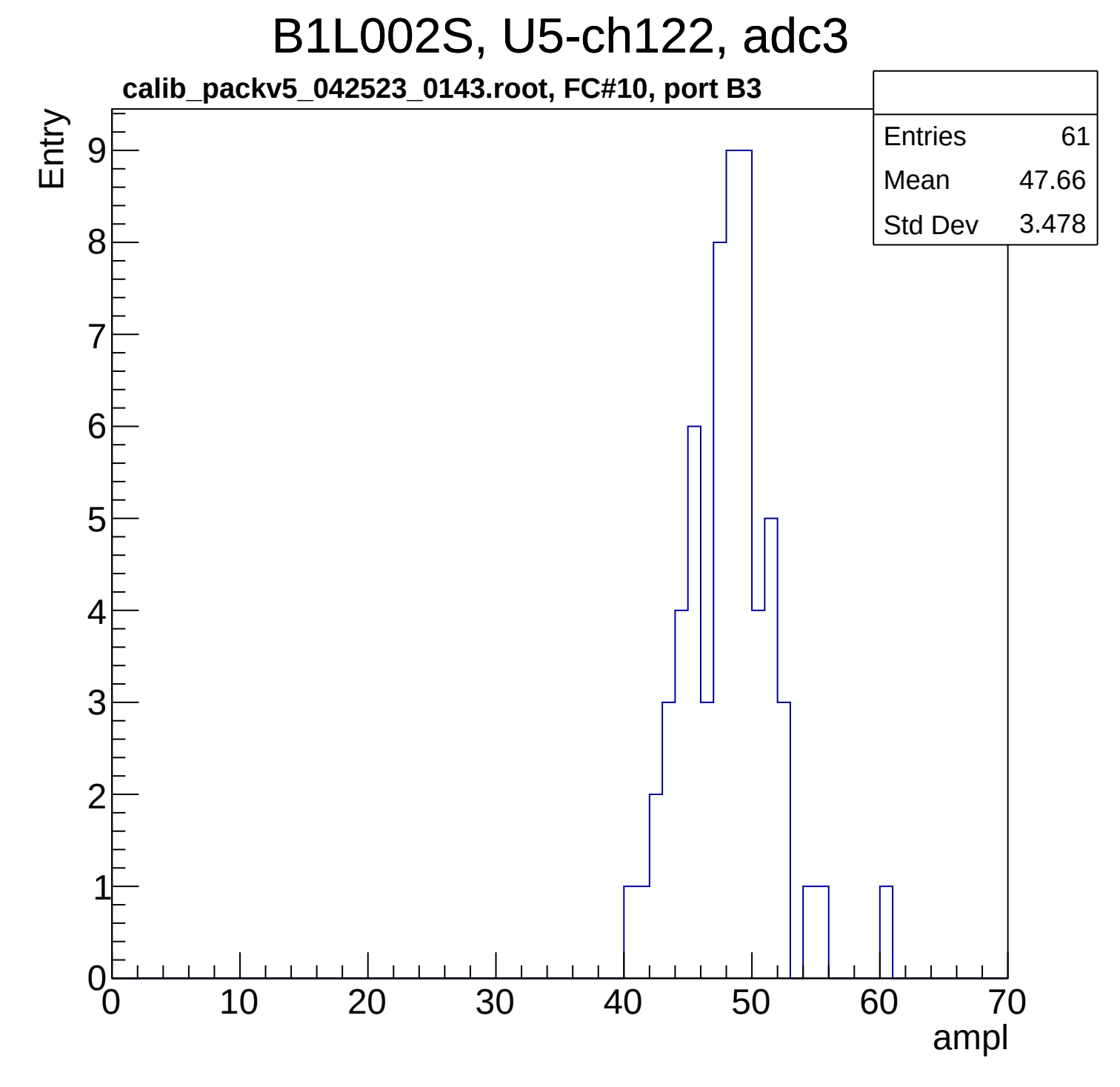
B1L002S, U5-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.66
Std Dev 3.478
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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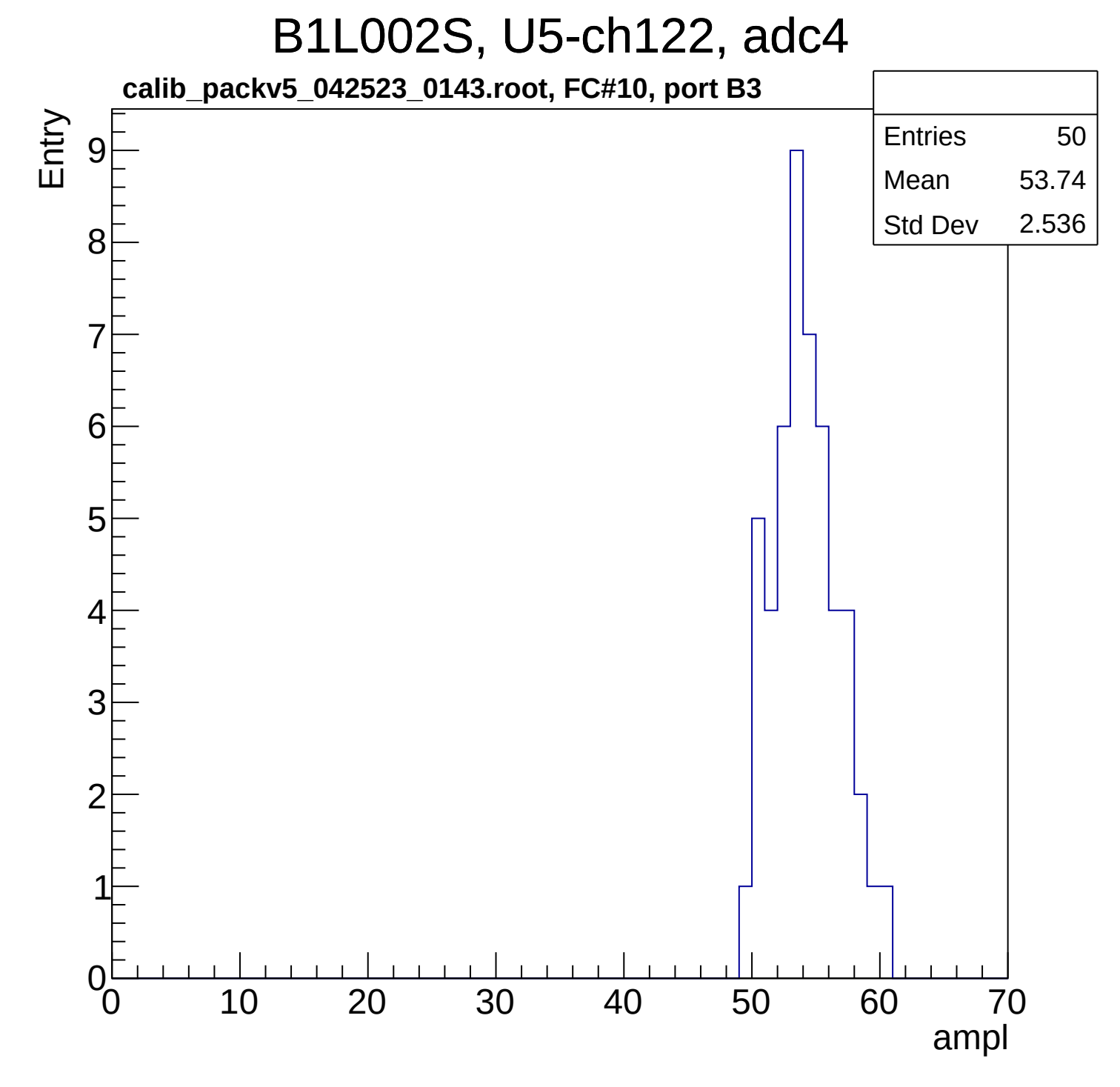
B1L002S, U5-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.74
Std Dev 2.536
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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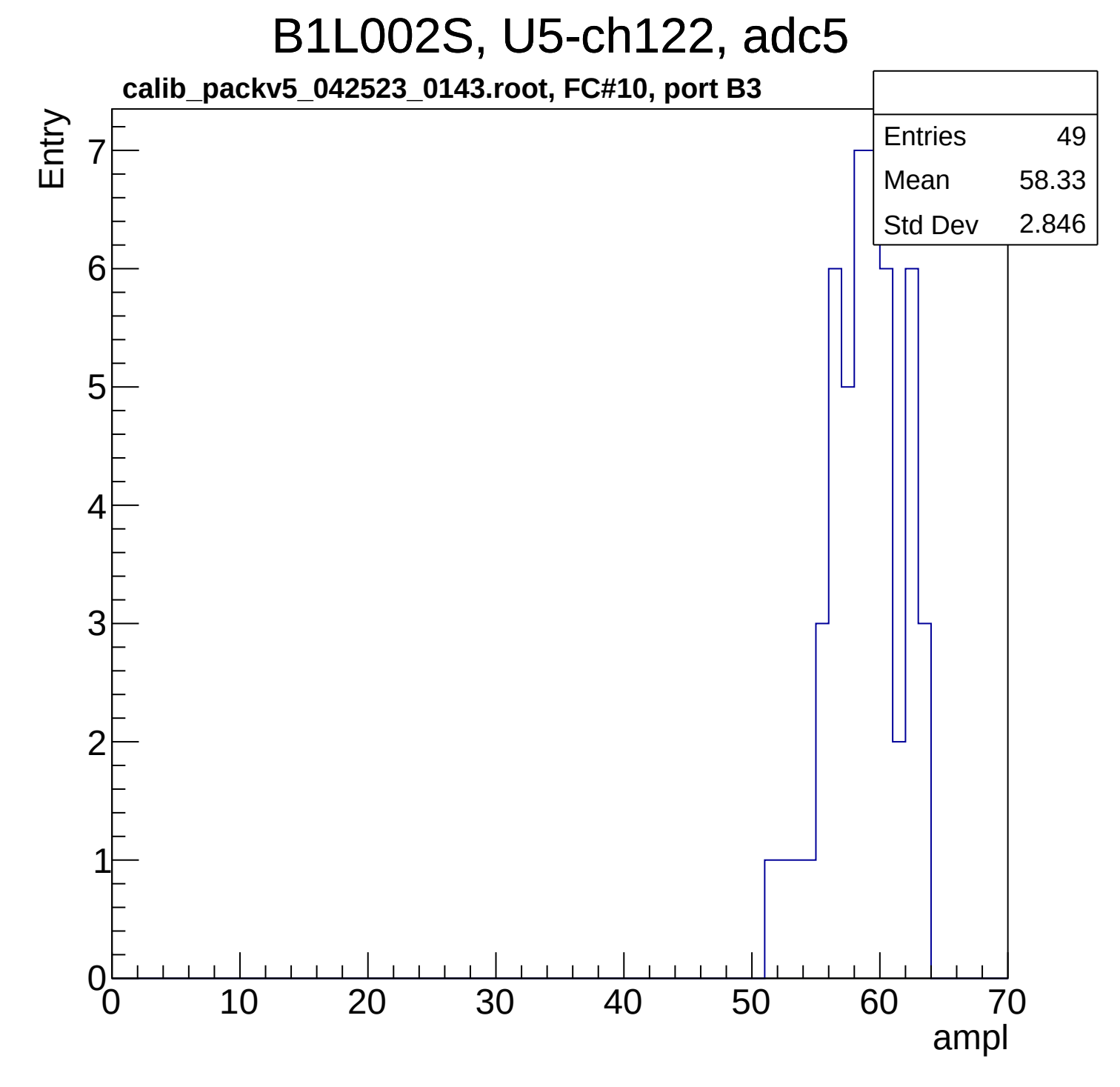
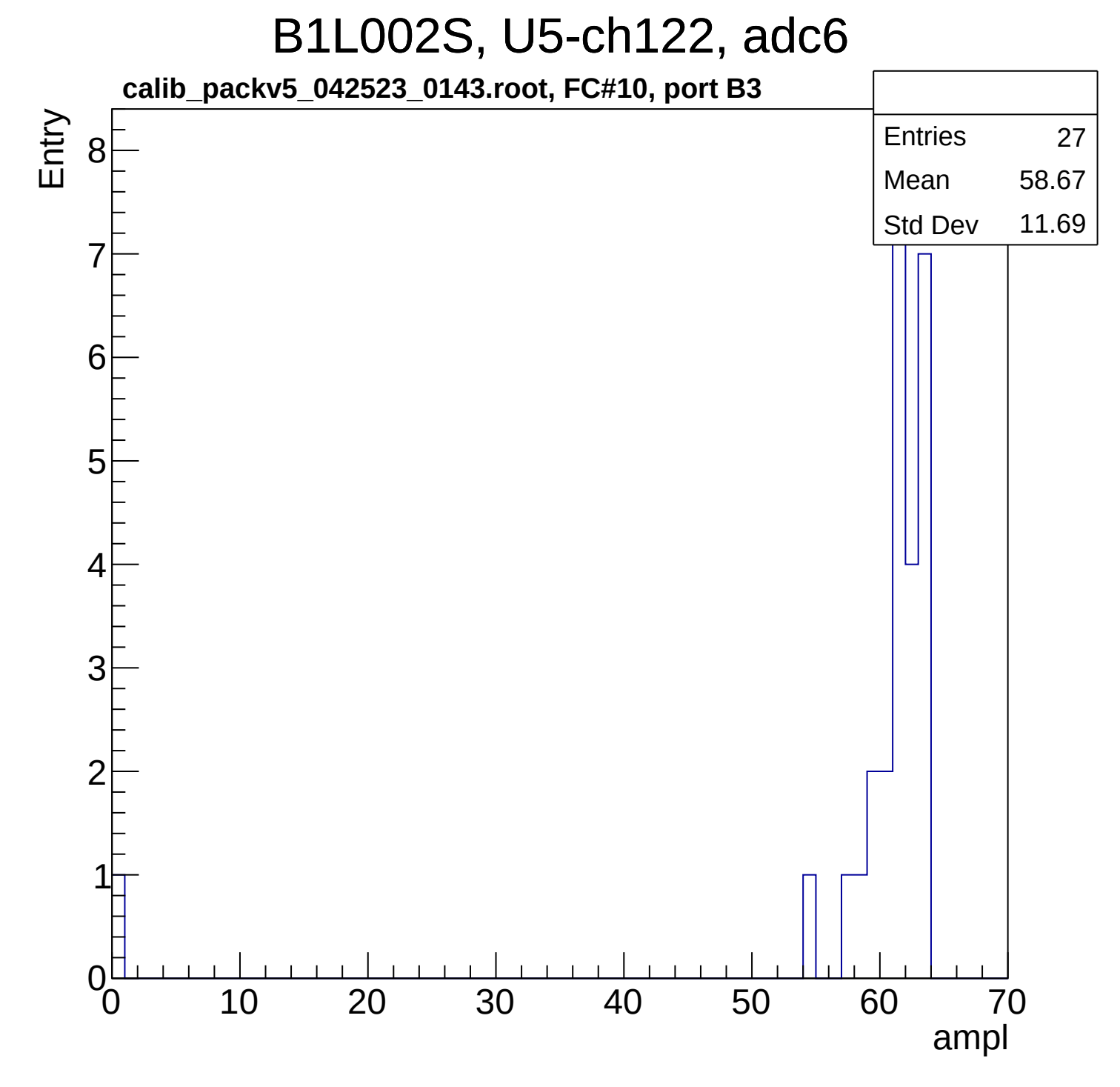

B1L002S, U5-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.33
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U5-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.67
Std Dev 11.69
Entry
ampl
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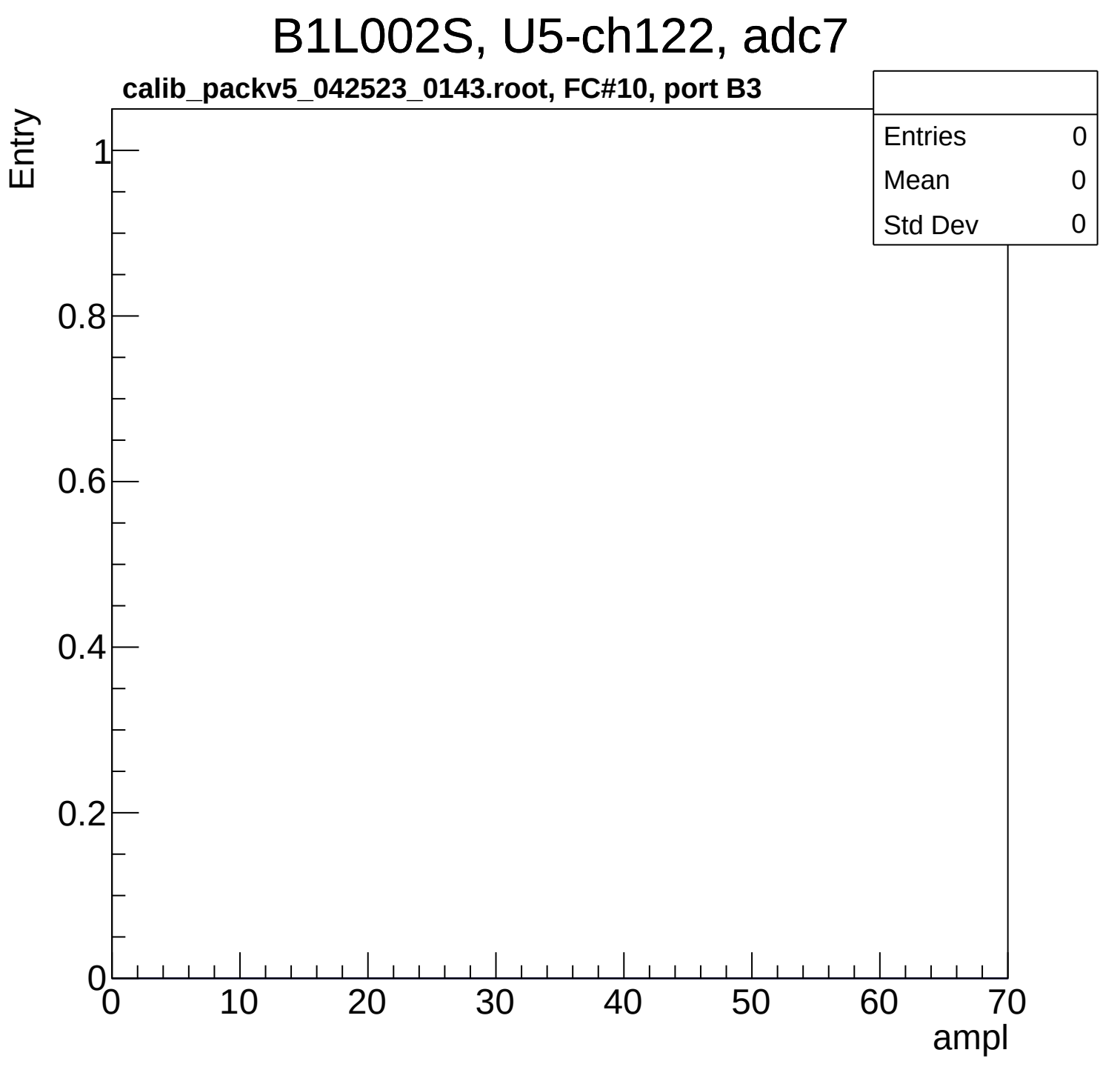
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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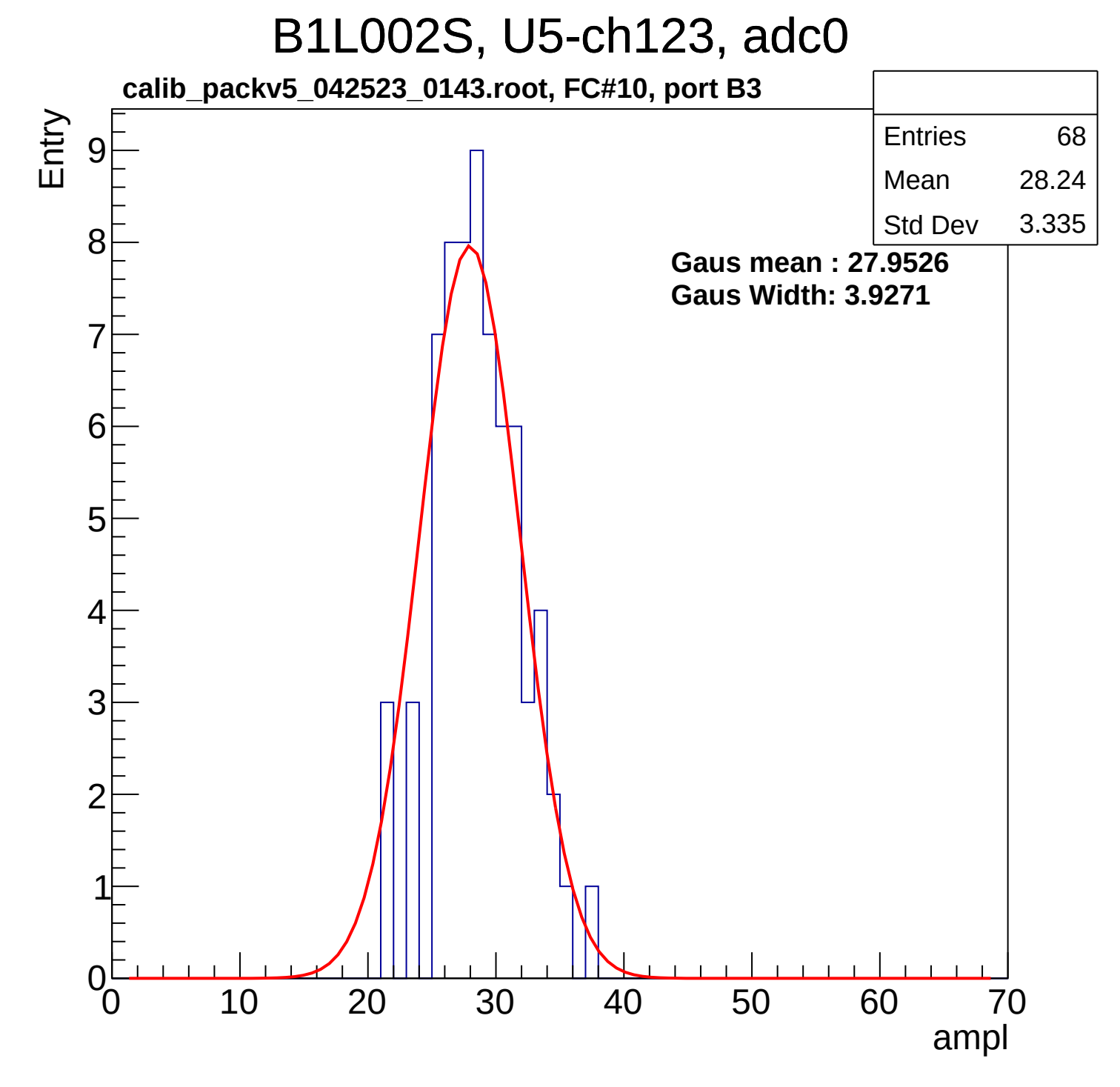
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.24
Std Dev 3.335
Gaus mean : 27.9526
Gaus Width: 3.9271
Entry
ampl
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2
3
4
5
6
7
8
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0
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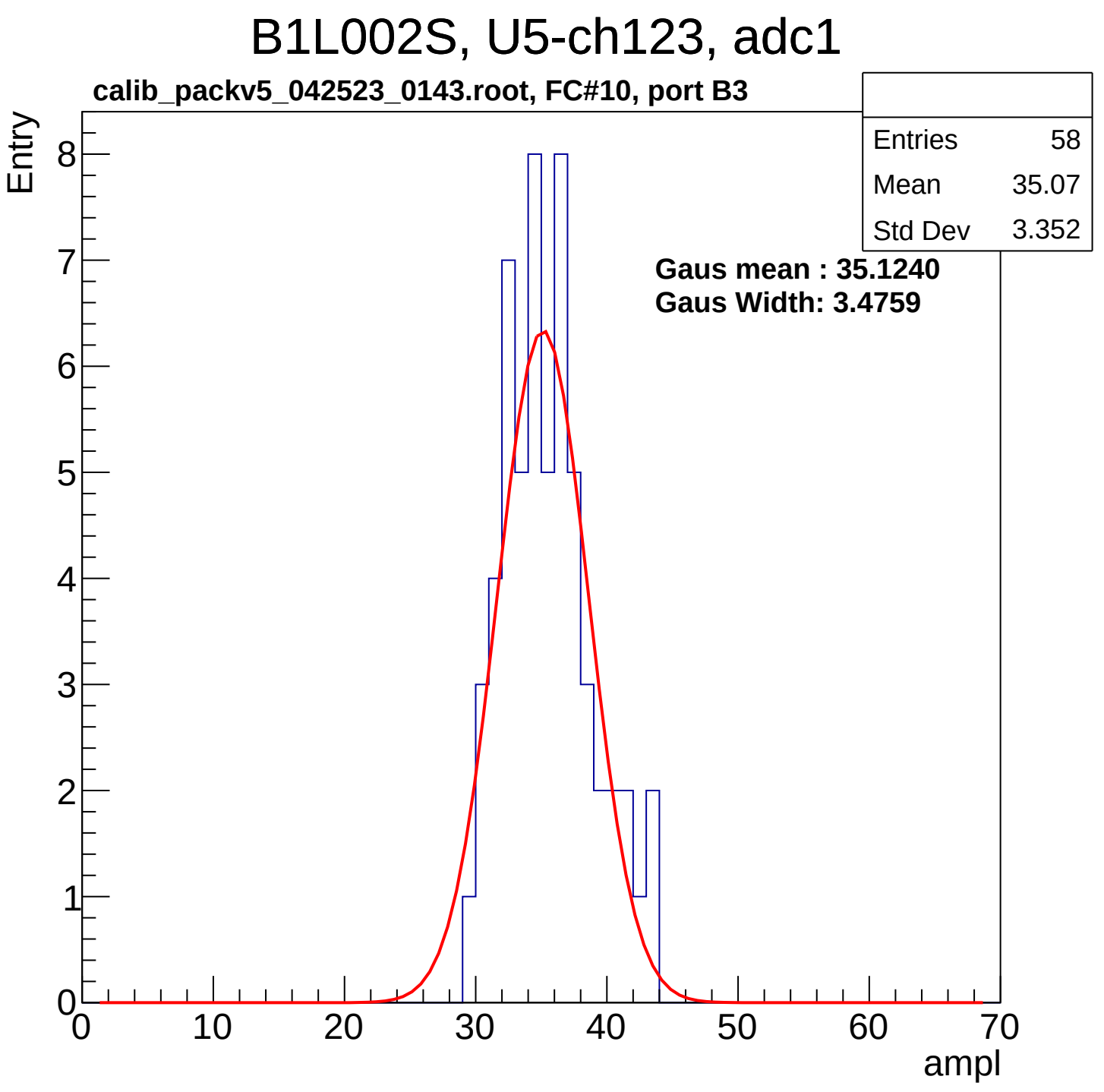
B1L002S, U5-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.07
Std Dev 3.352
Gaus mean : 35.1240
Gaus Width: 3.4759
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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50
60
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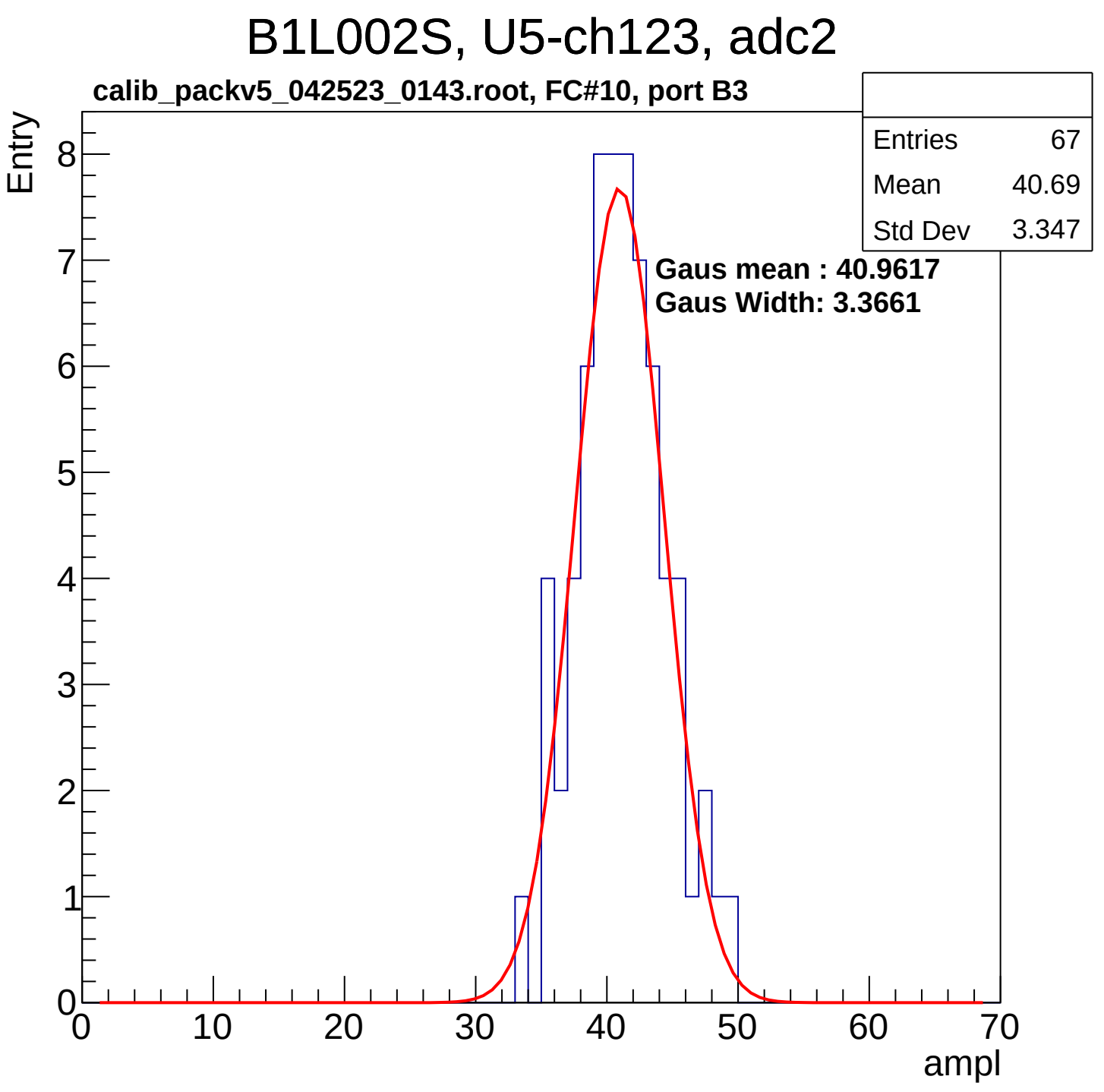
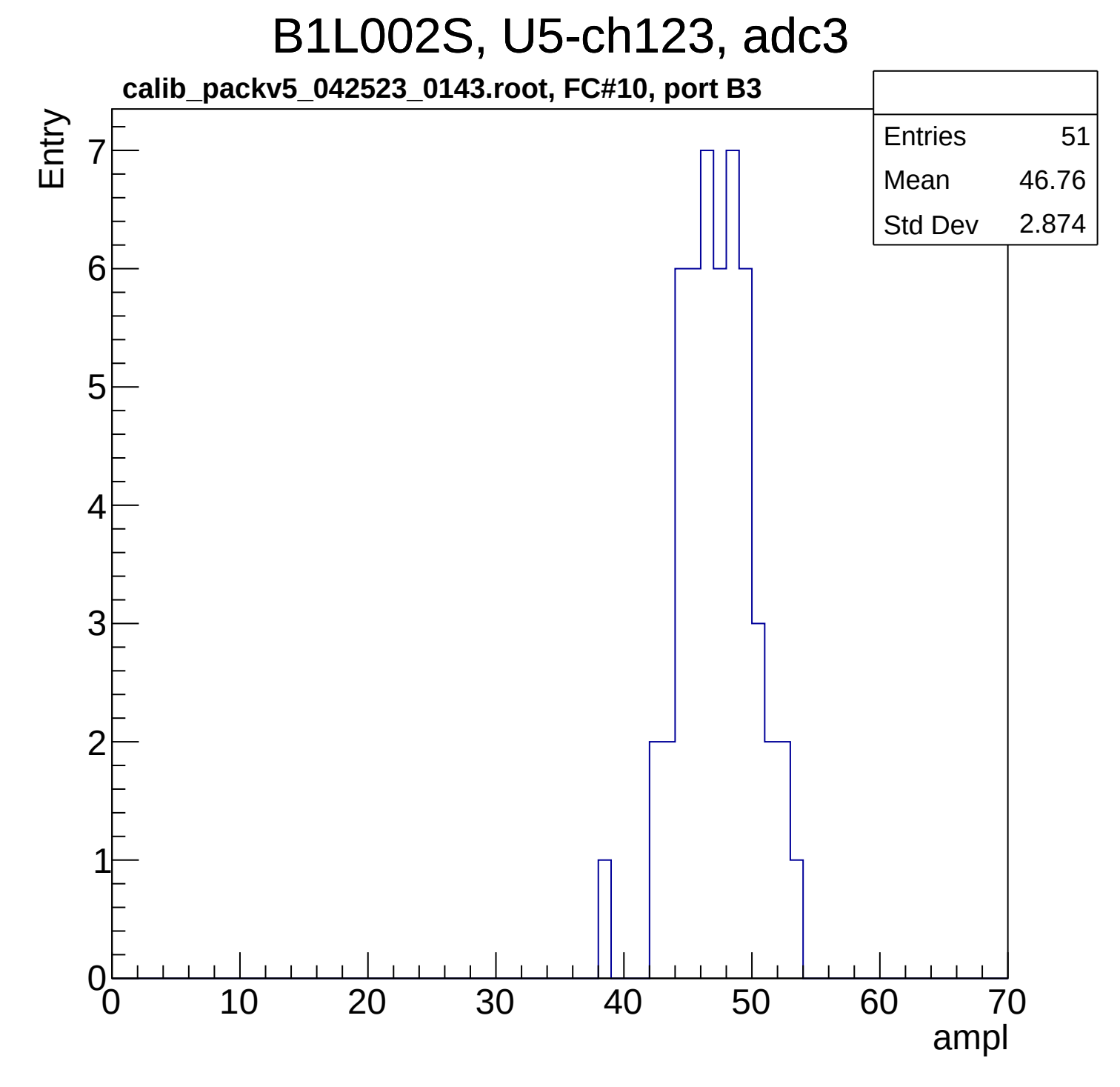

B1L002S, U5-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.69
Std Dev 3.347
Gaus mean : 40.9617
Gaus Width: 3.3661
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.76
Std Dev 2.874
Entry
ampl
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4
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0
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60
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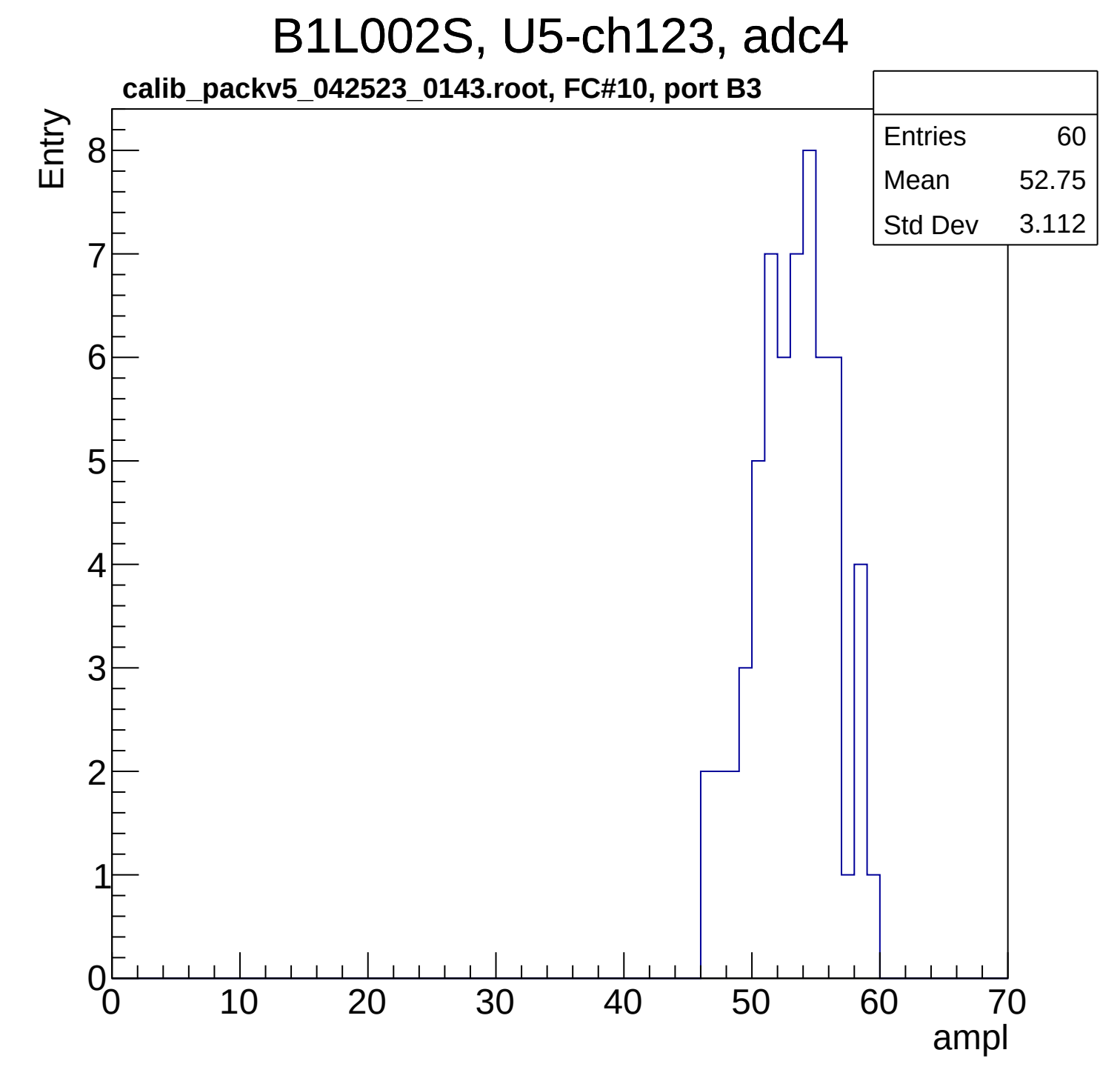
B1L002S, U5-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.75
Std Dev 3.112
Entry
0
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2
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4
5
6
7
8
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10
20
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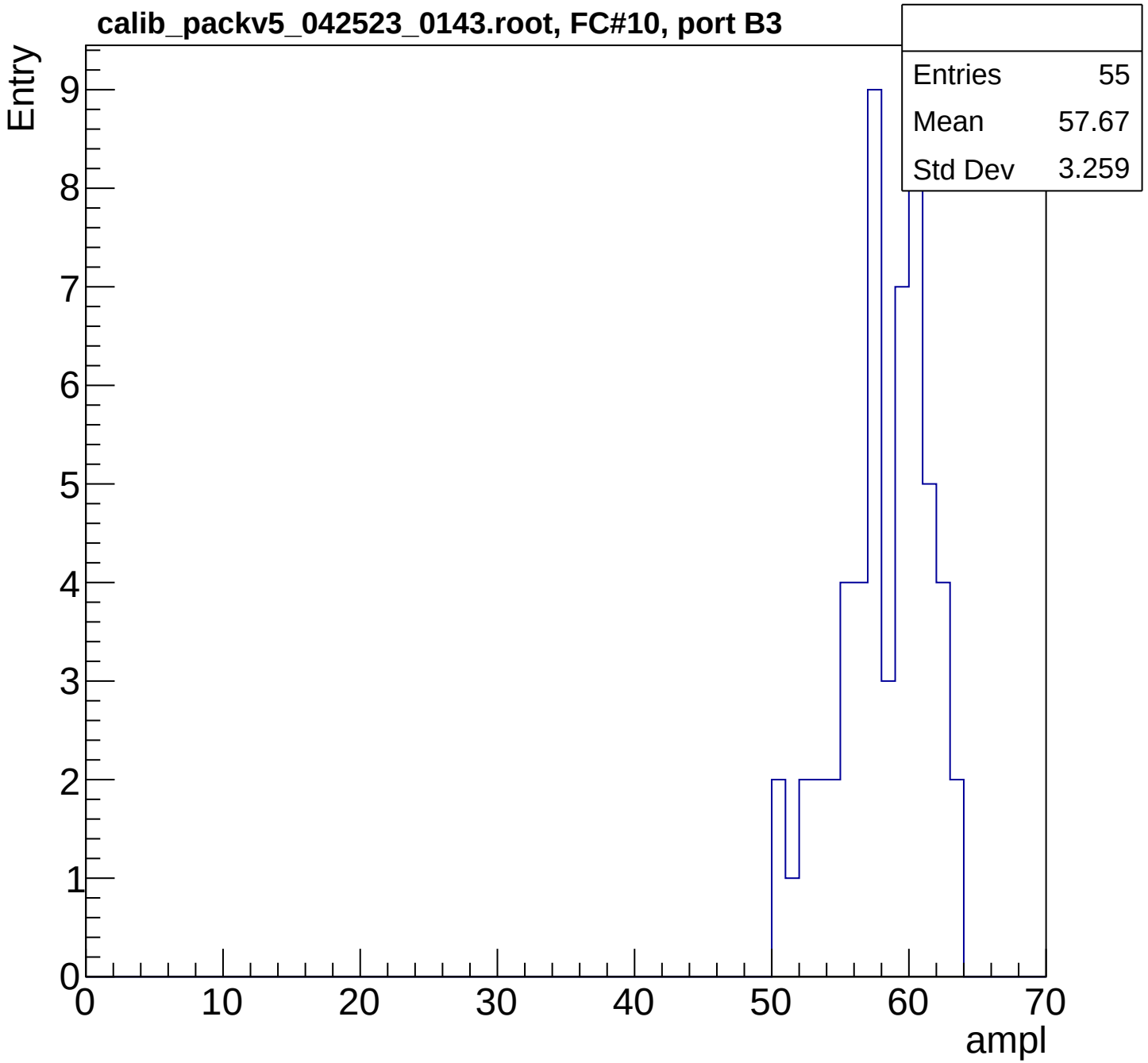
50
60
70
ampl


B1L002S, U5-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.67
Std Dev 3.259
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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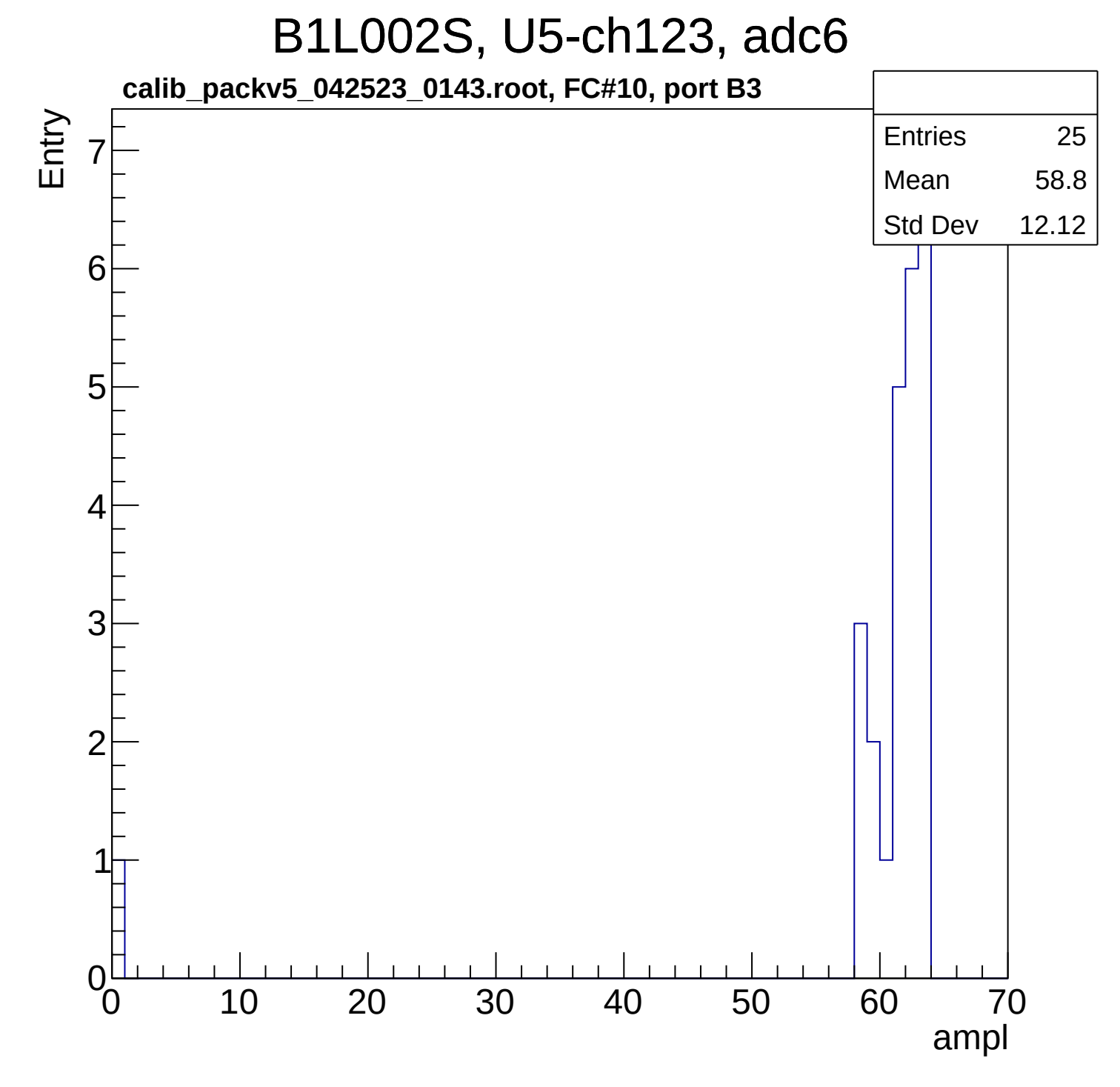
B1L002S, U5-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.8
Std Dev 12.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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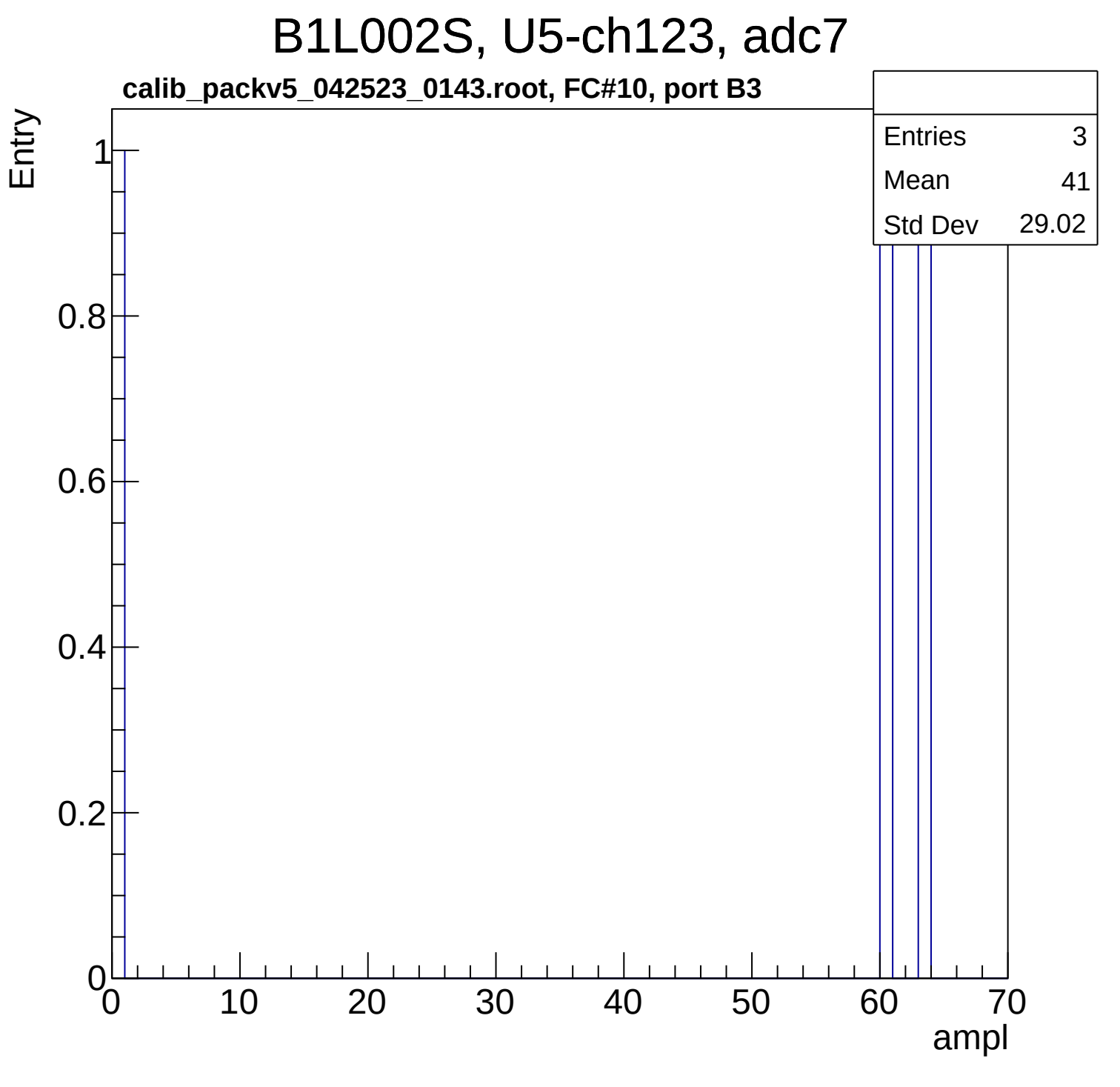
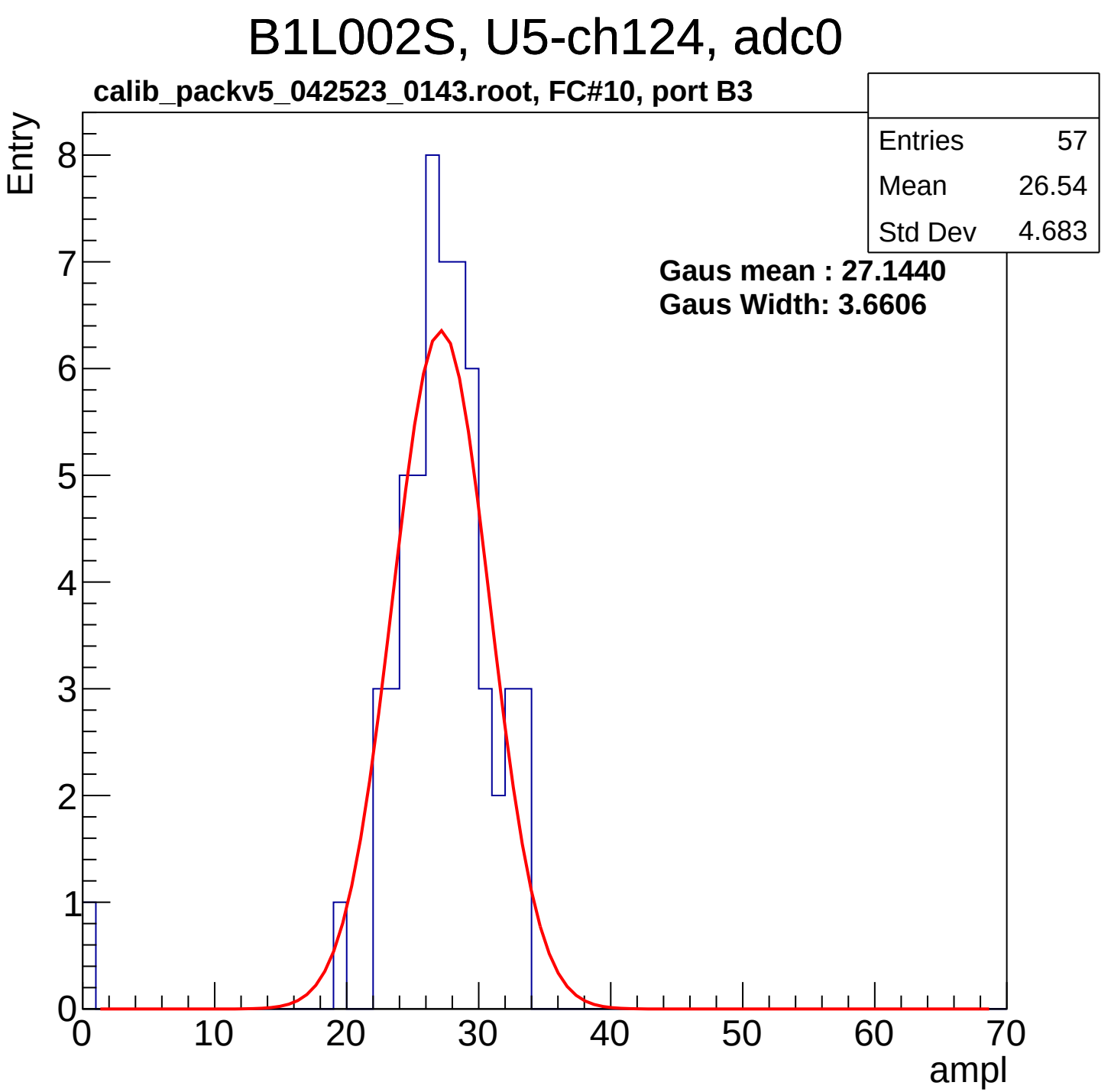

B1L002S, U5-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.54
Std Dev 4.683
Gaus mean : 27.1440
Gaus Width: 3.6606
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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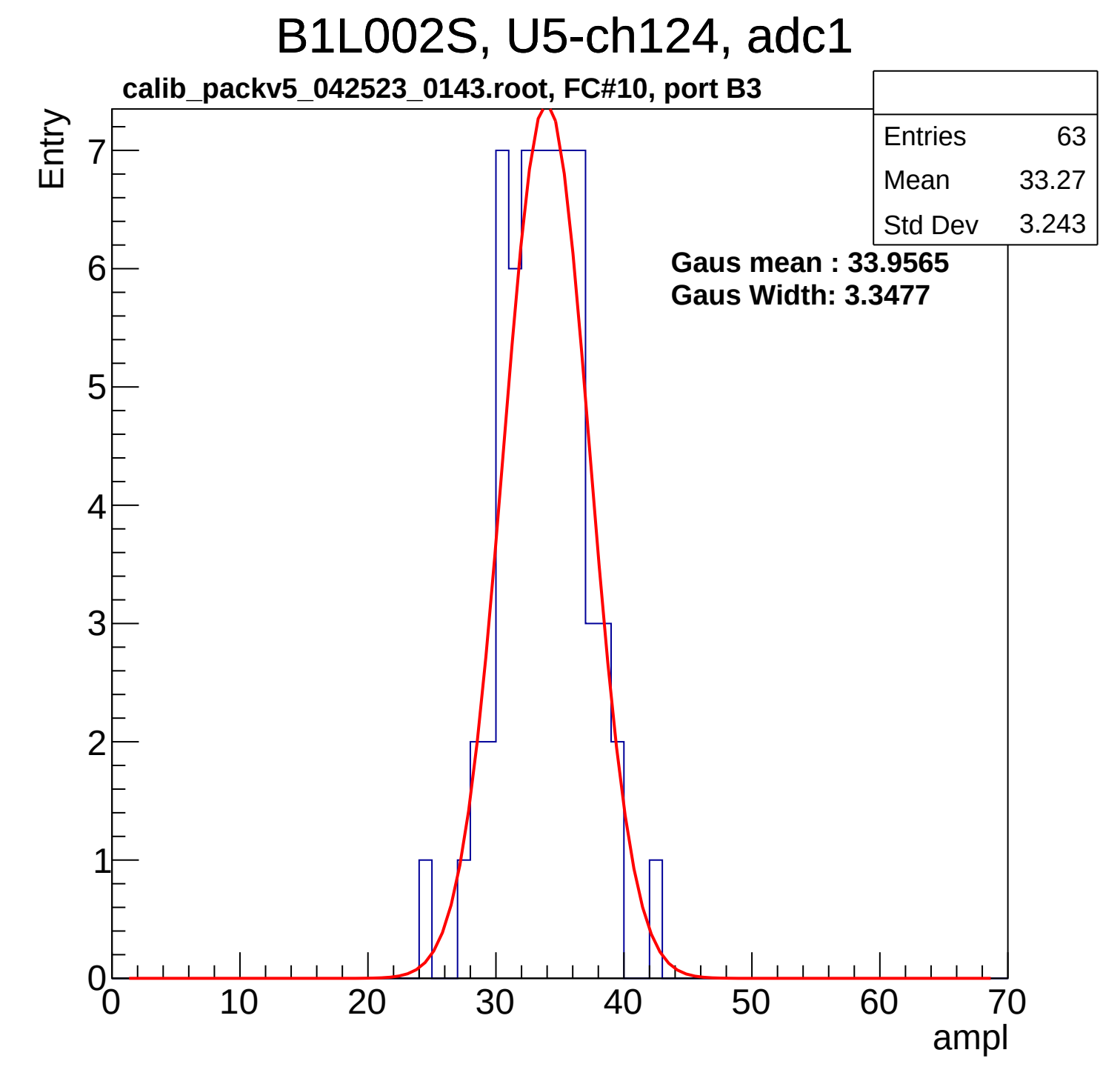
B1L002S, U5-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.27
Std Dev 3.243
Gaus mean : 33.9565
Gaus Width: 3.3477
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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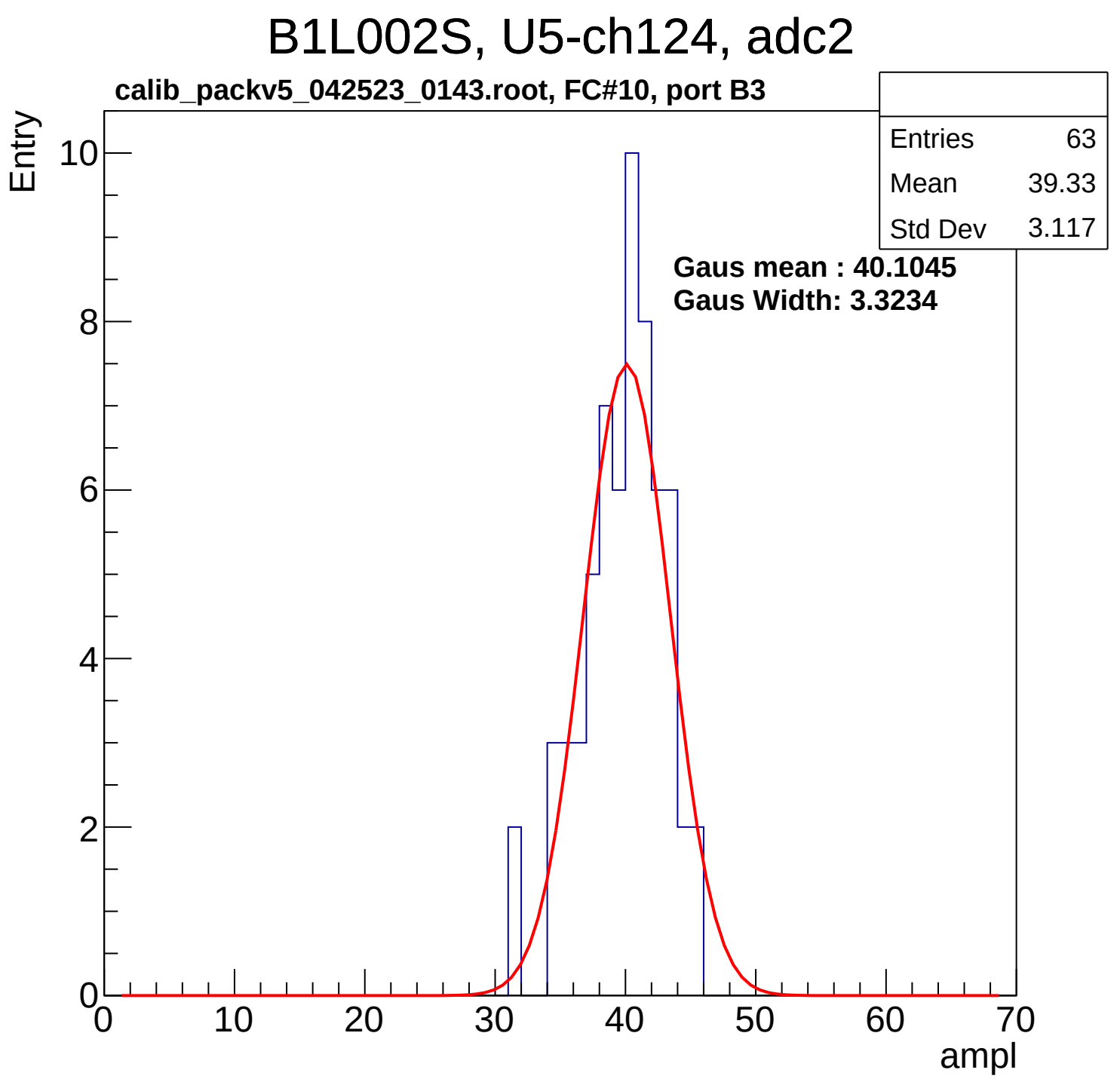
B1L002S, U5-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.33
Std Dev 3.117
Gaus mean : 40.1045
Gaus Width: 3.3234
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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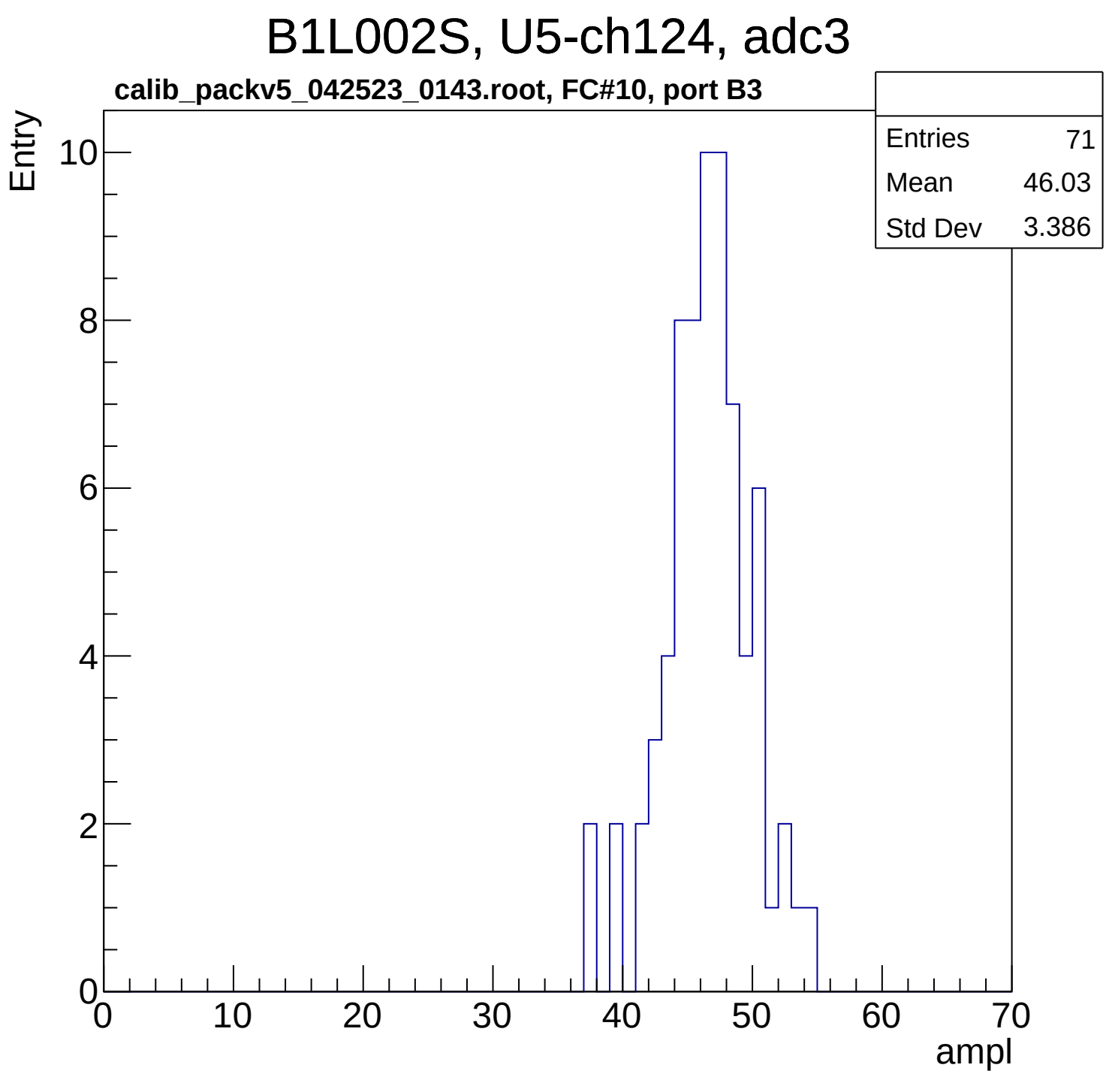
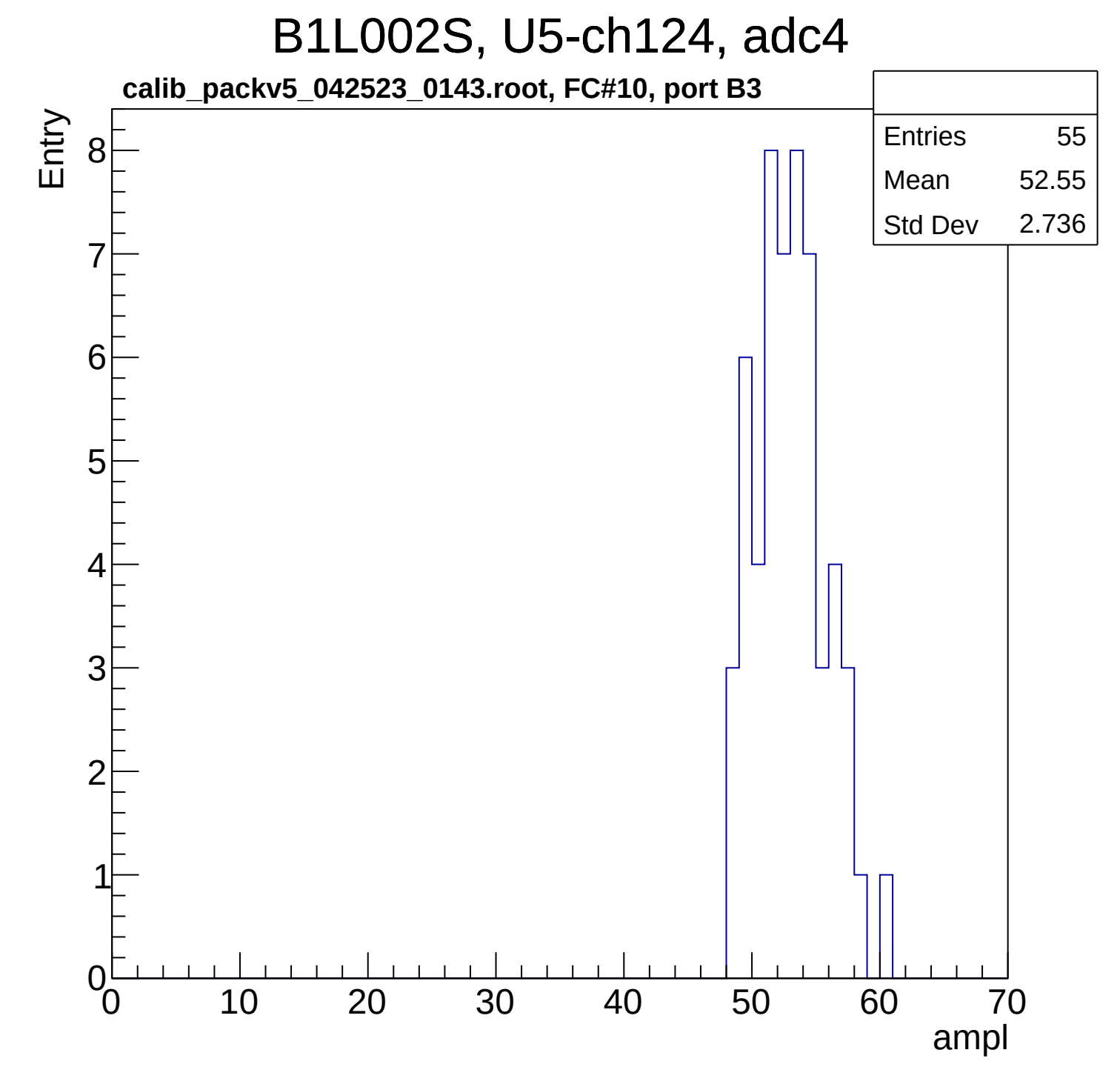

B1L002S, U5-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 46.03
Std Dev 3.386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.55
Std Dev 2.736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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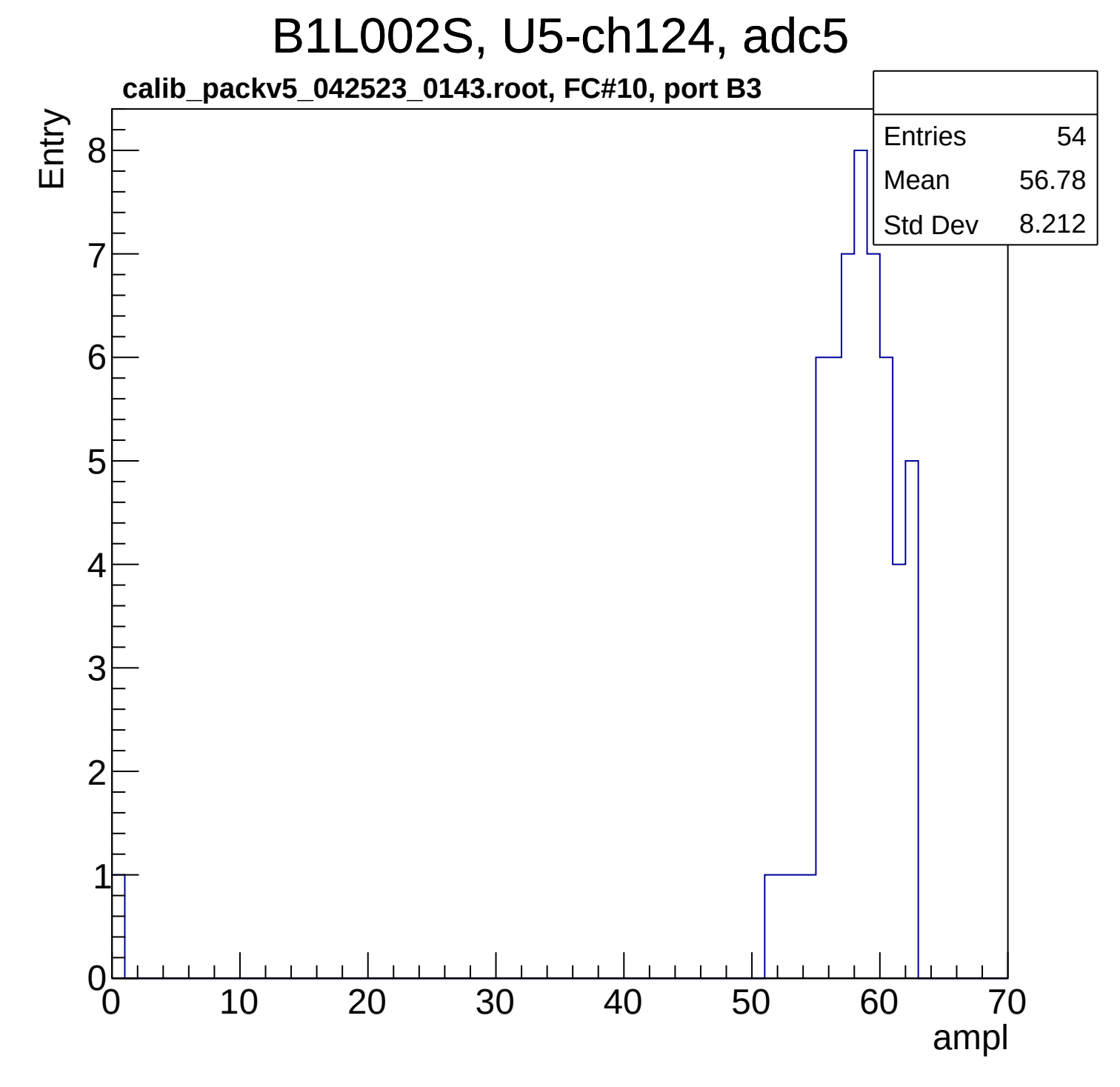
B1L002S, U5-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.78
Std Dev 8.212
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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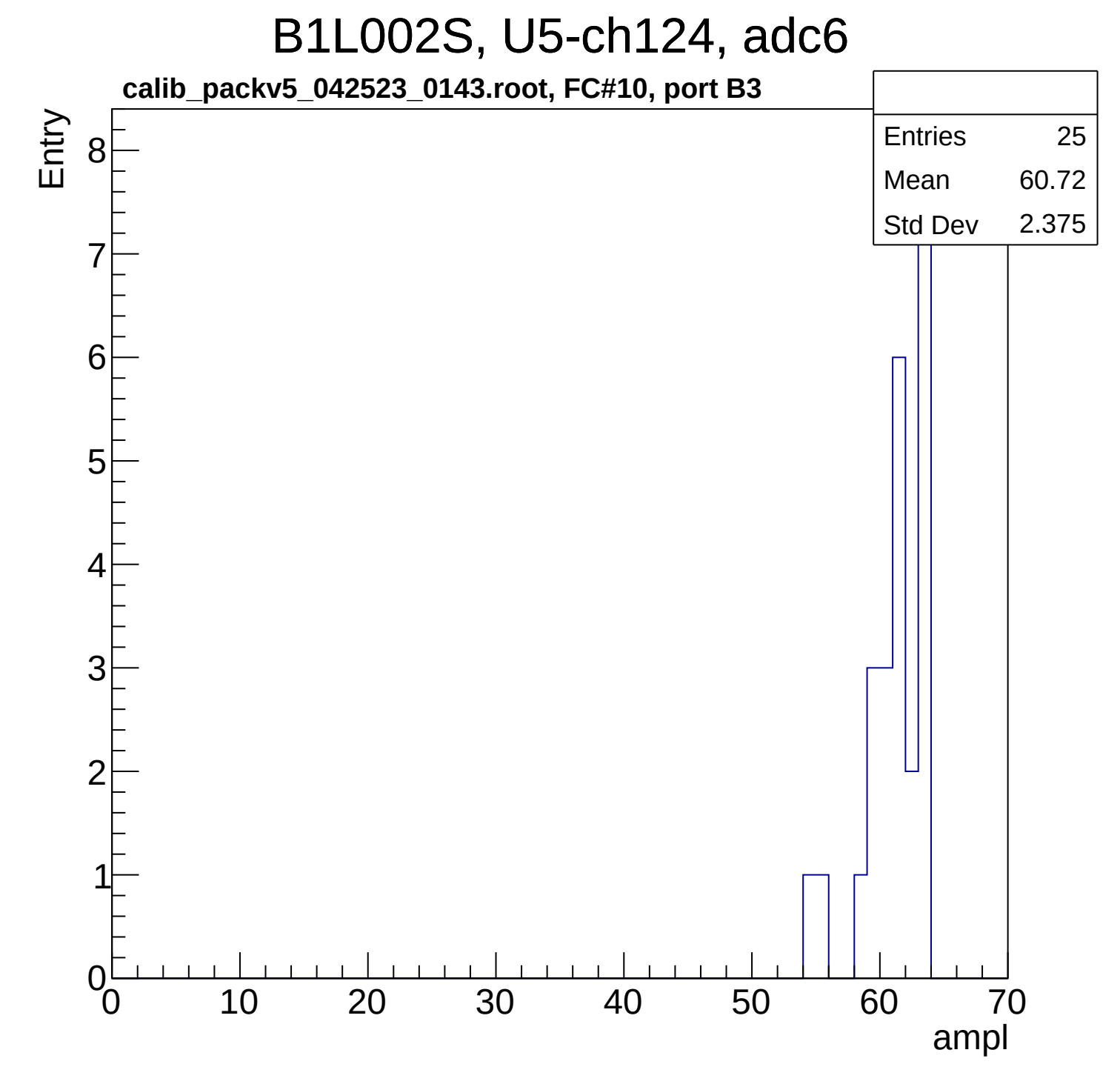
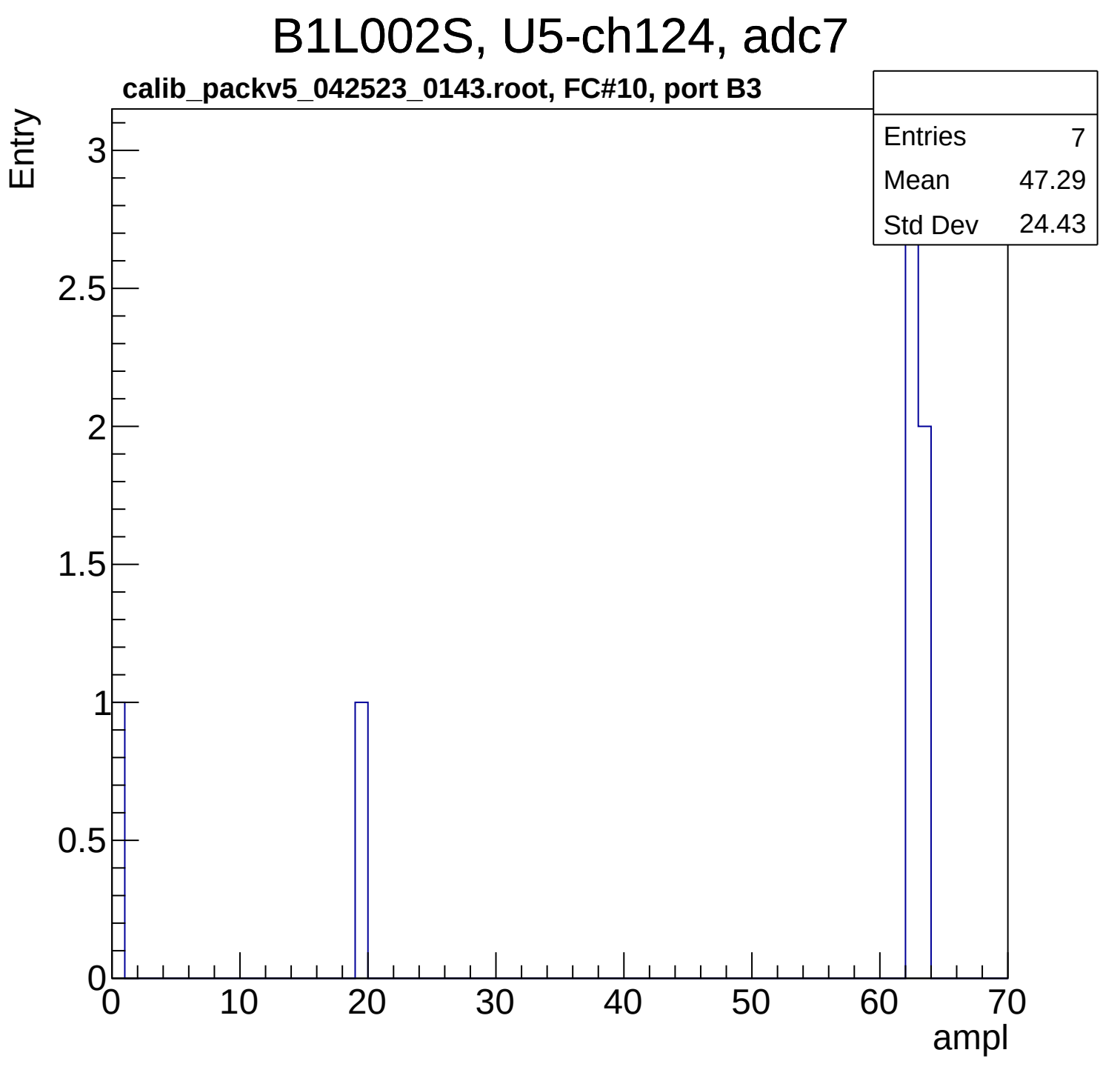
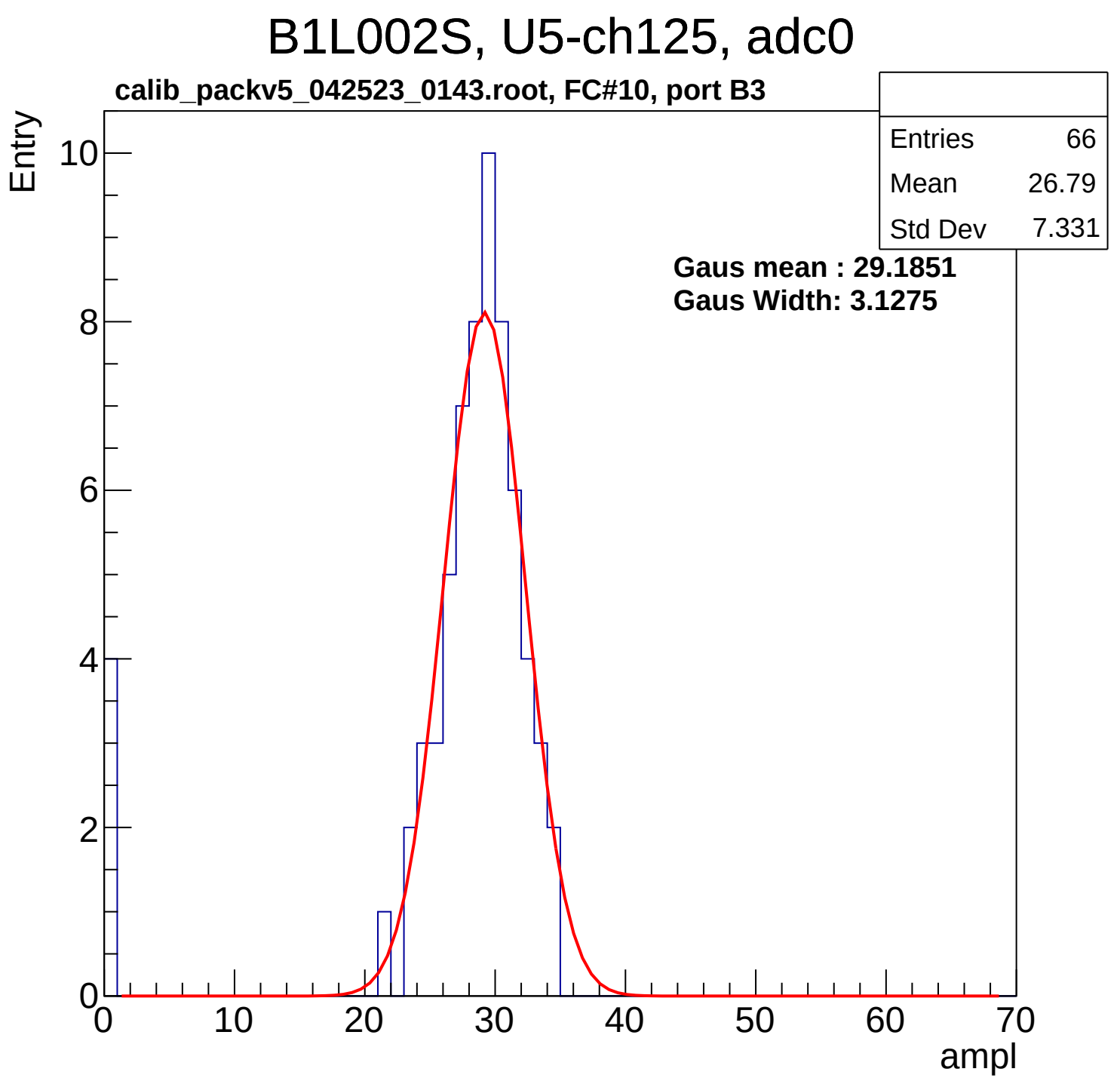
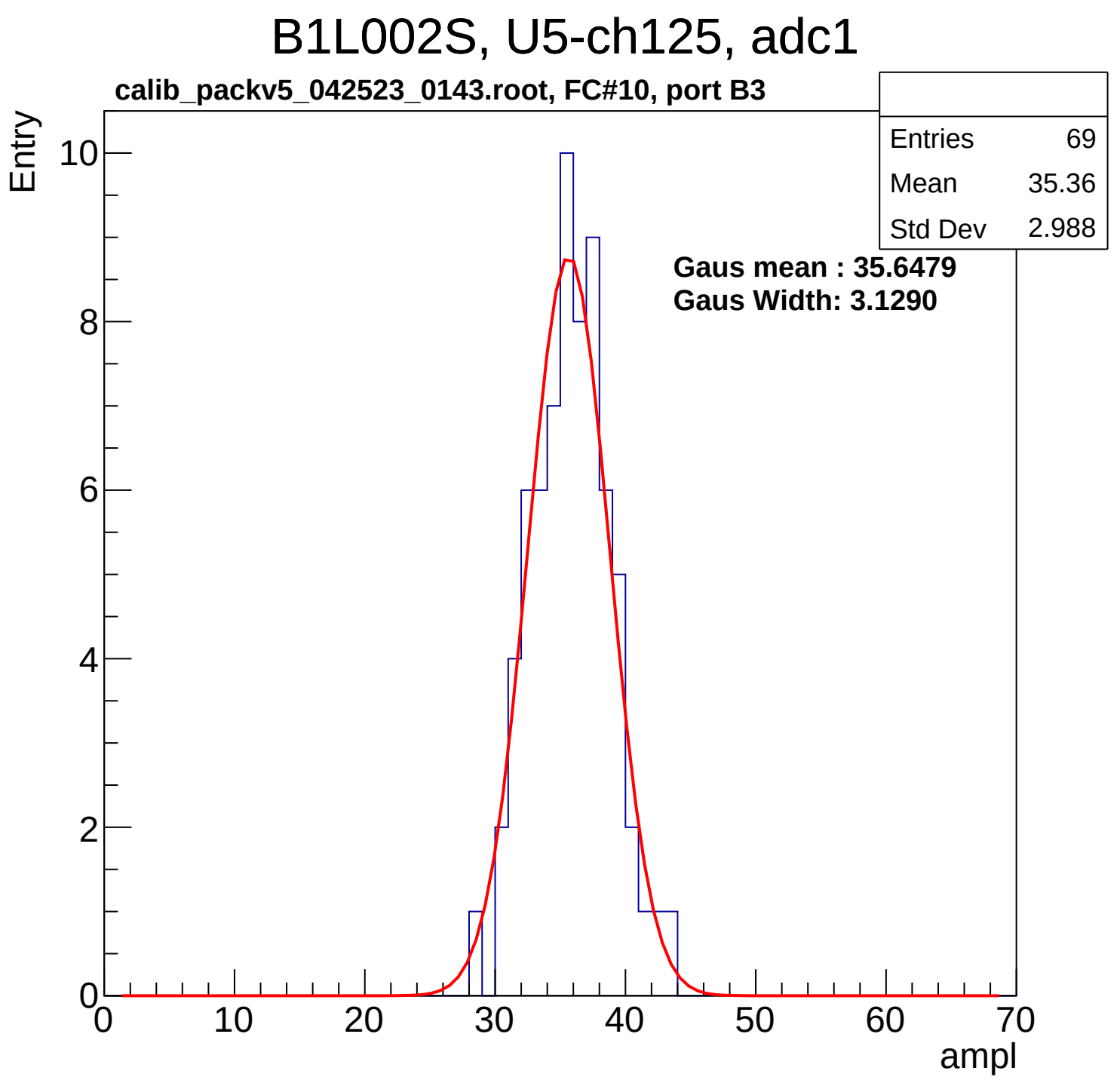
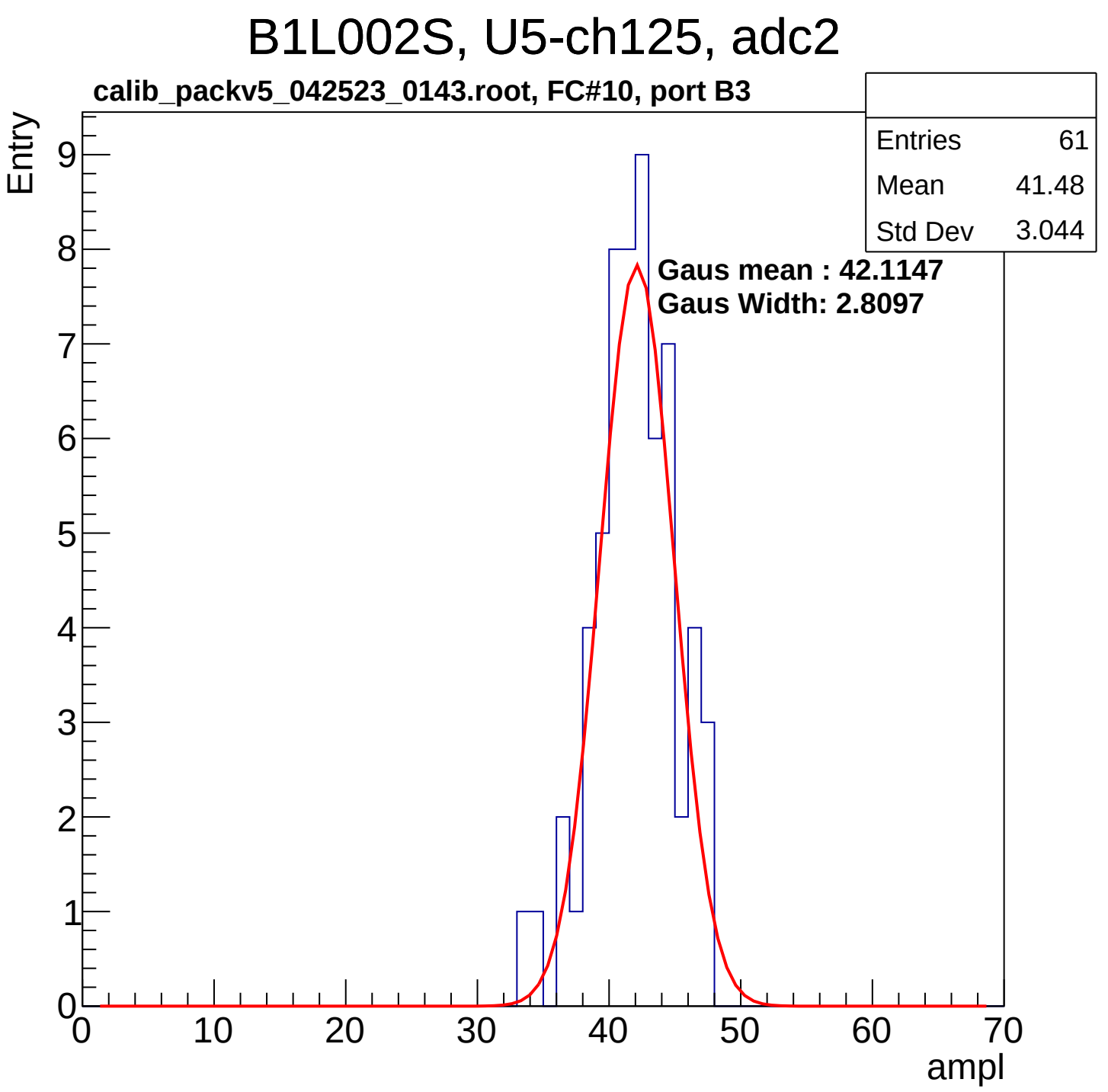
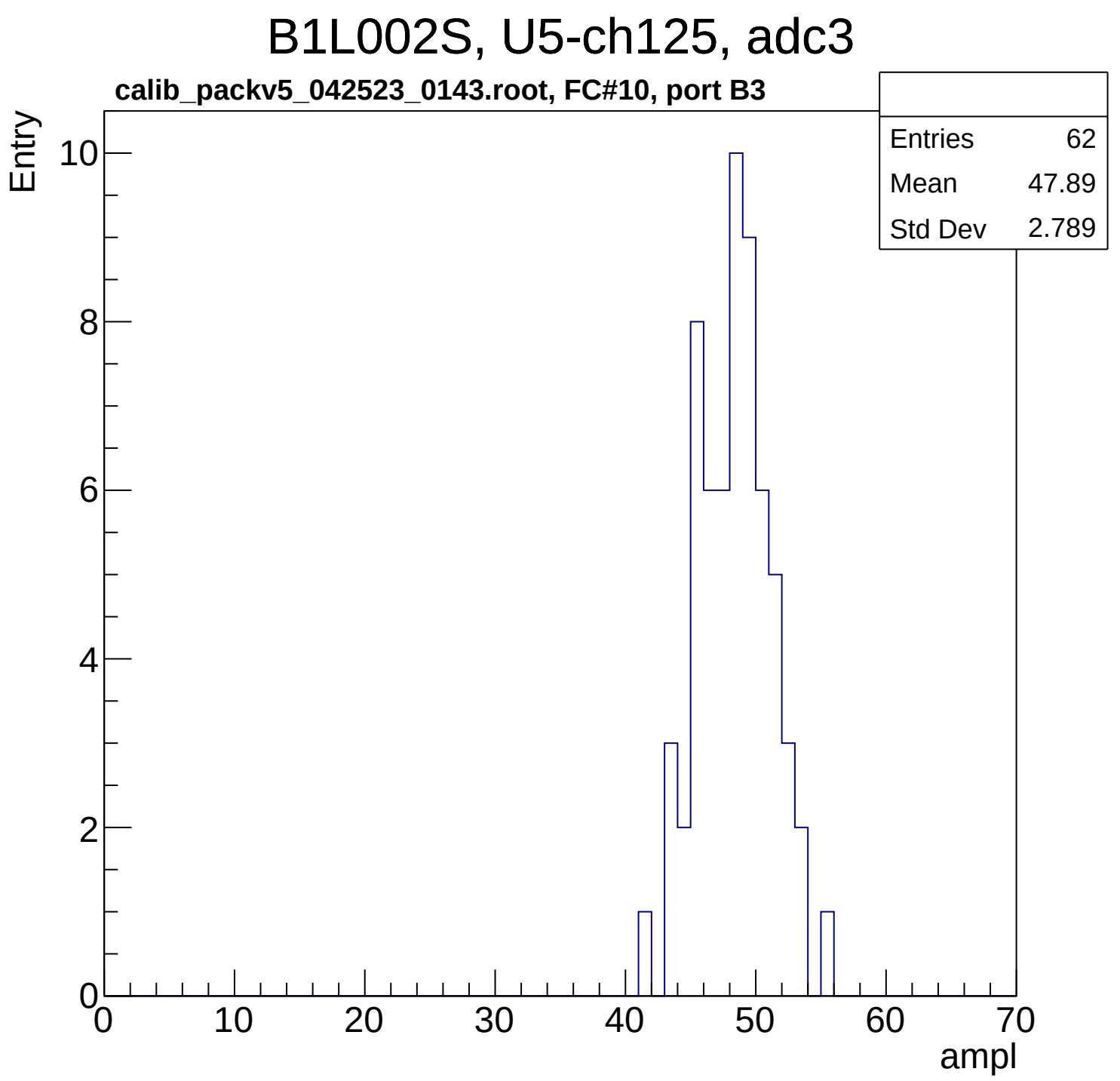

B1L002S, U5-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 60.72
Std Dev 2.375
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

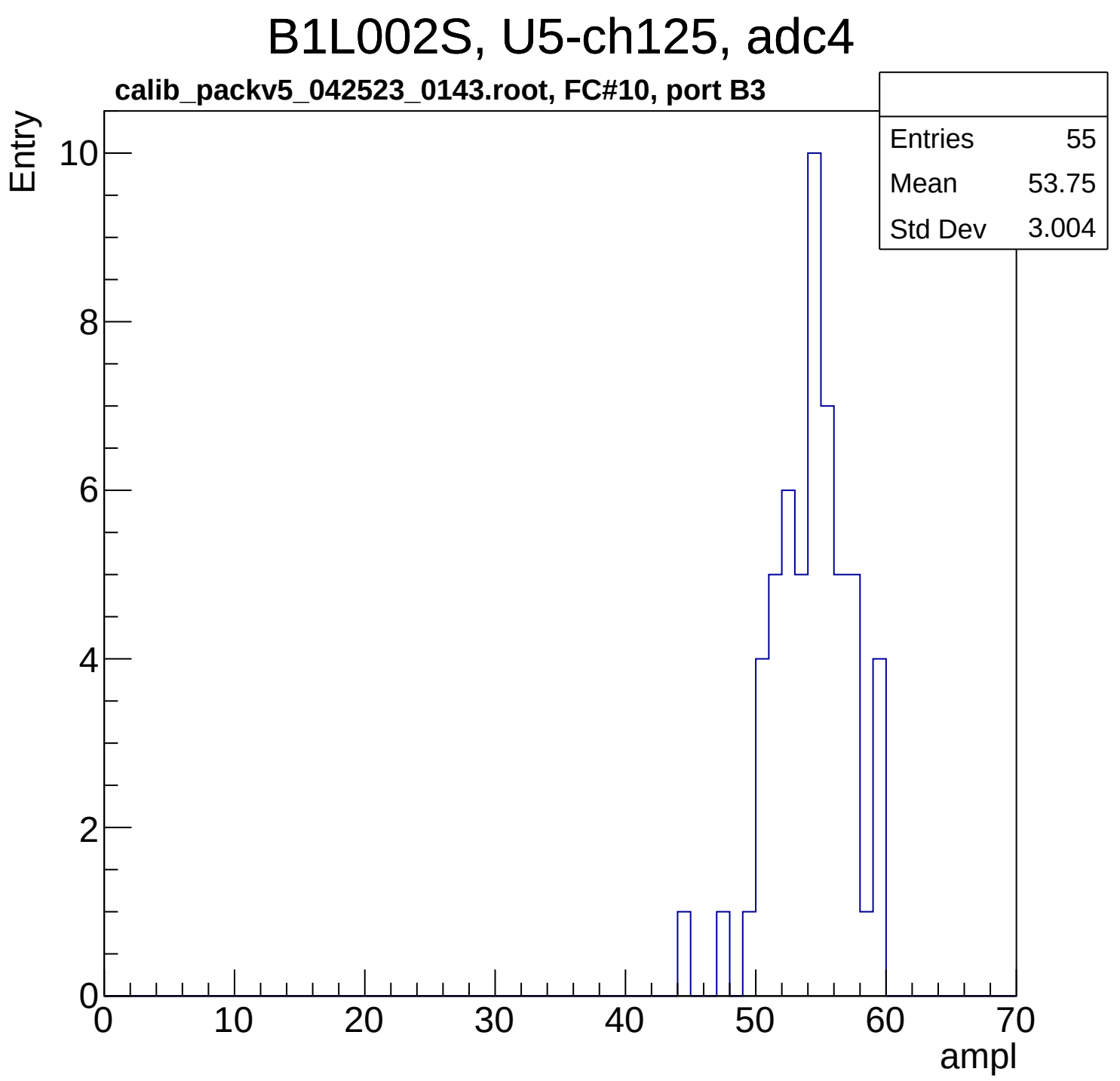

B1L002S, U5-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 47.29
Std Dev 24.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

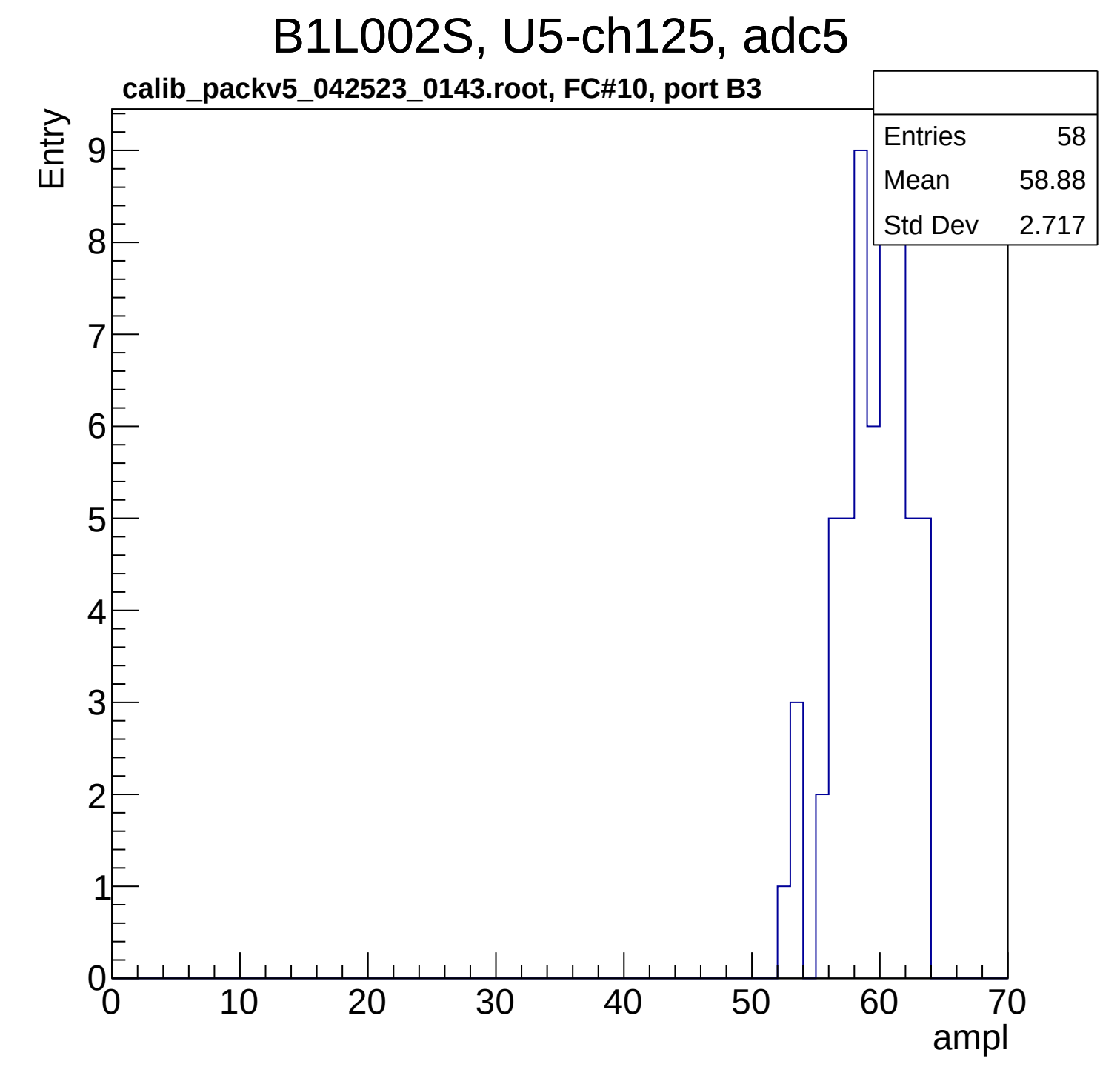

B1L002S, U5-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 26.79
Std Dev 7.331
Gaus mean : 29.1851
Gaus Width: 3.1275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.36
Std Dev 2.988
Gaus mean : 35.6479
Gaus Width: 3.1290
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.48
Std Dev 3.044
Gaus mean : 42.1147
Gaus Width: 2.8097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.89
Std Dev 2.789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U5-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.75
Std Dev 3.004
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.88
Std Dev 2.717
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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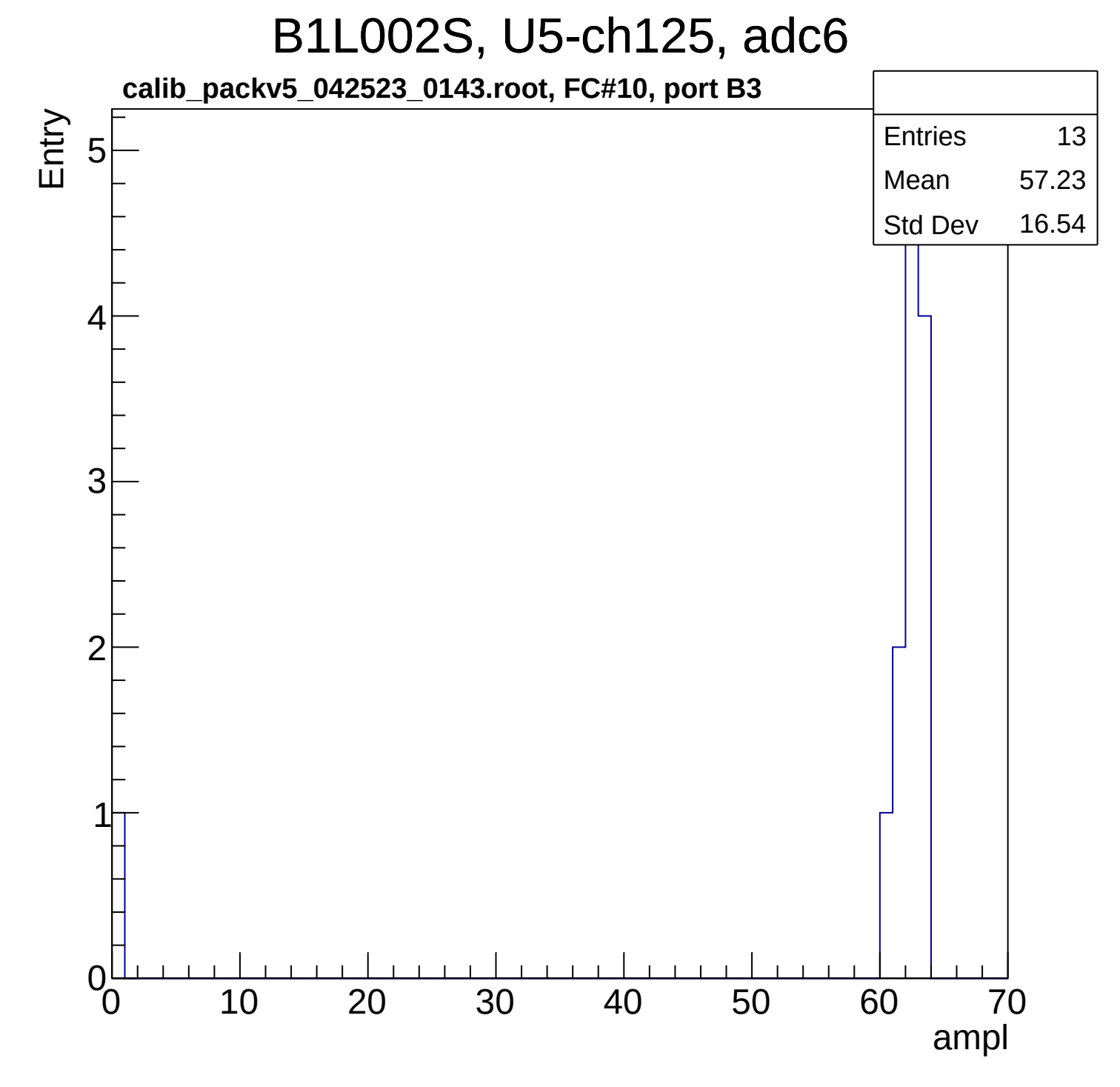
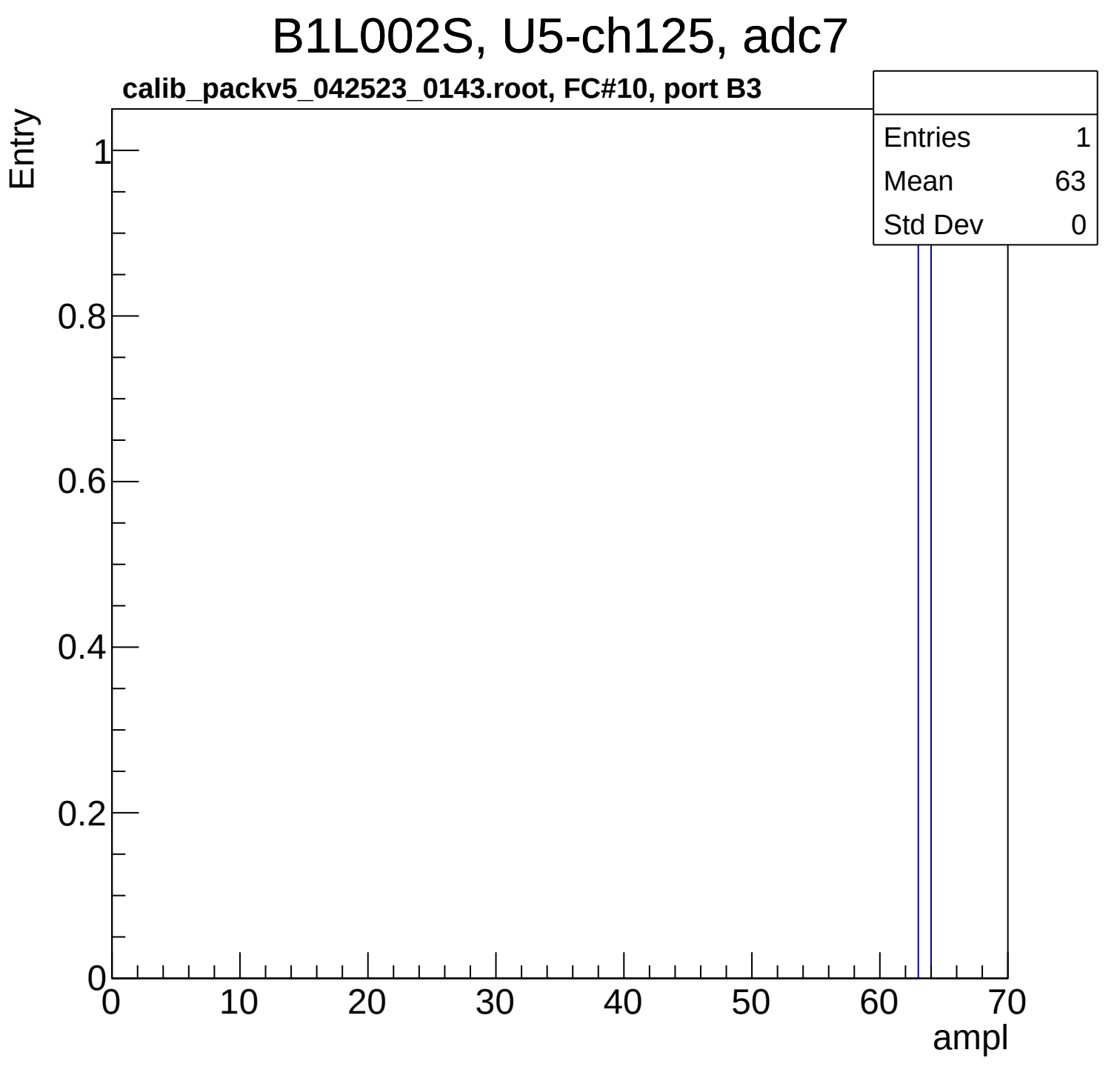
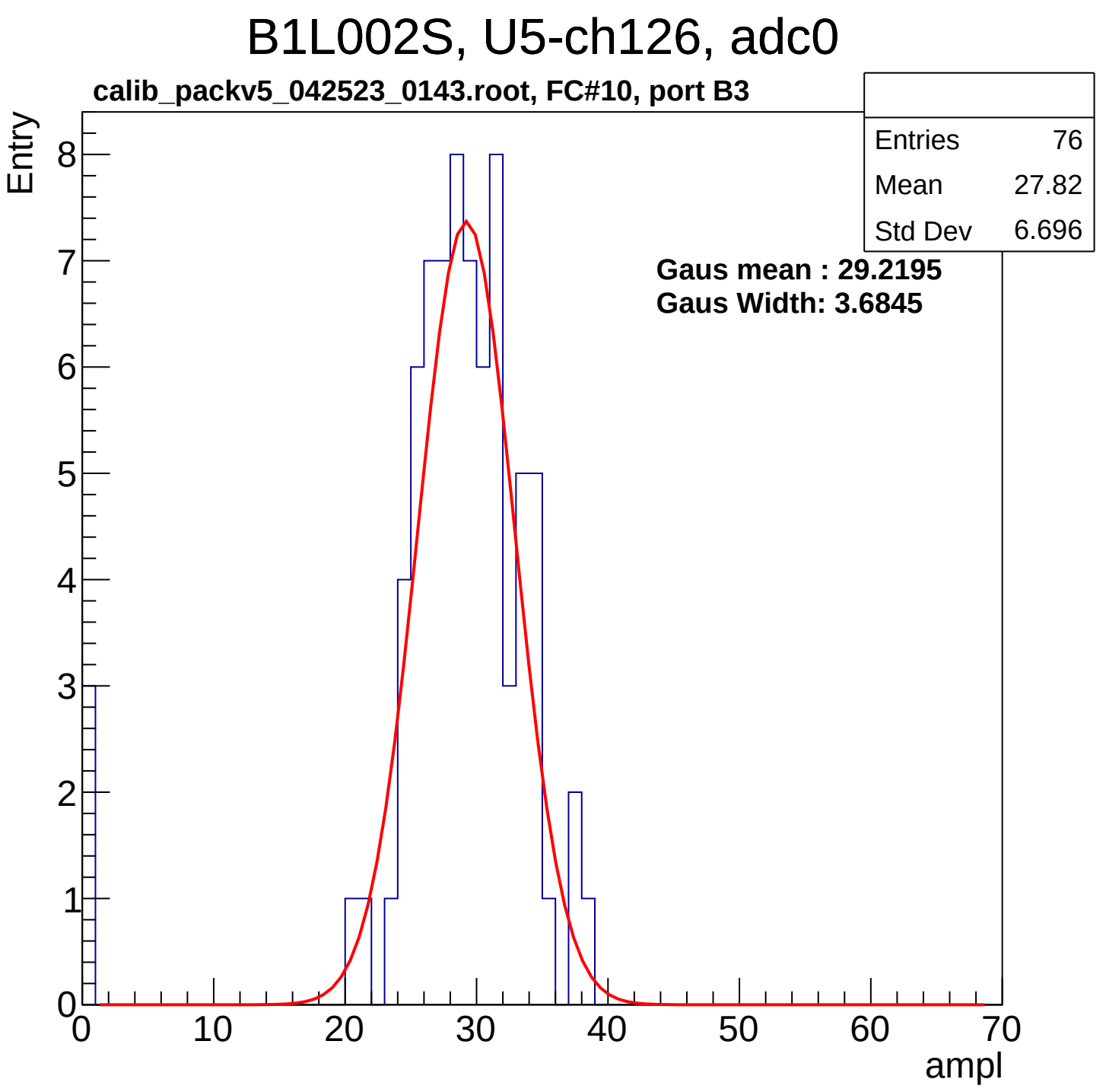
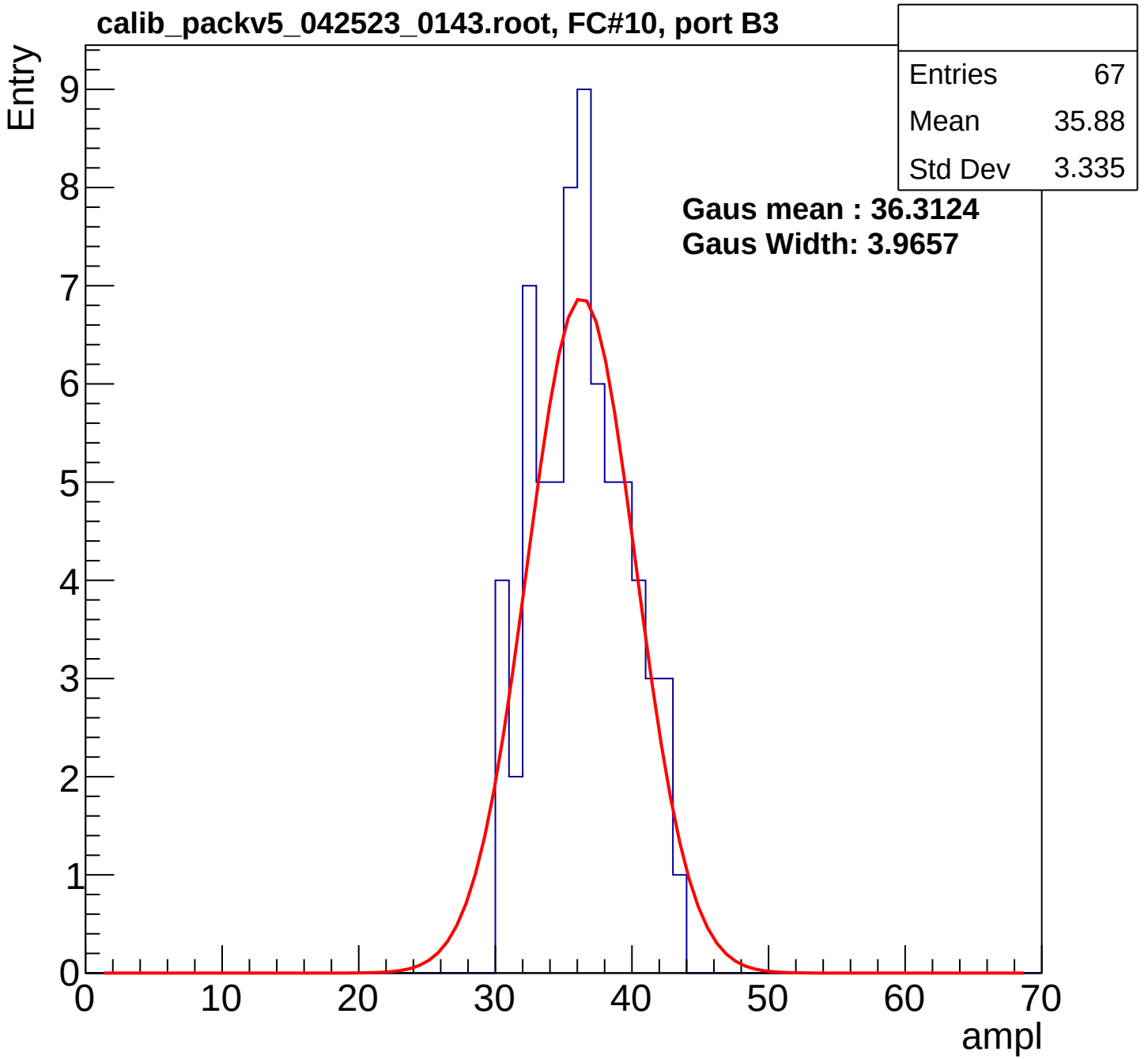
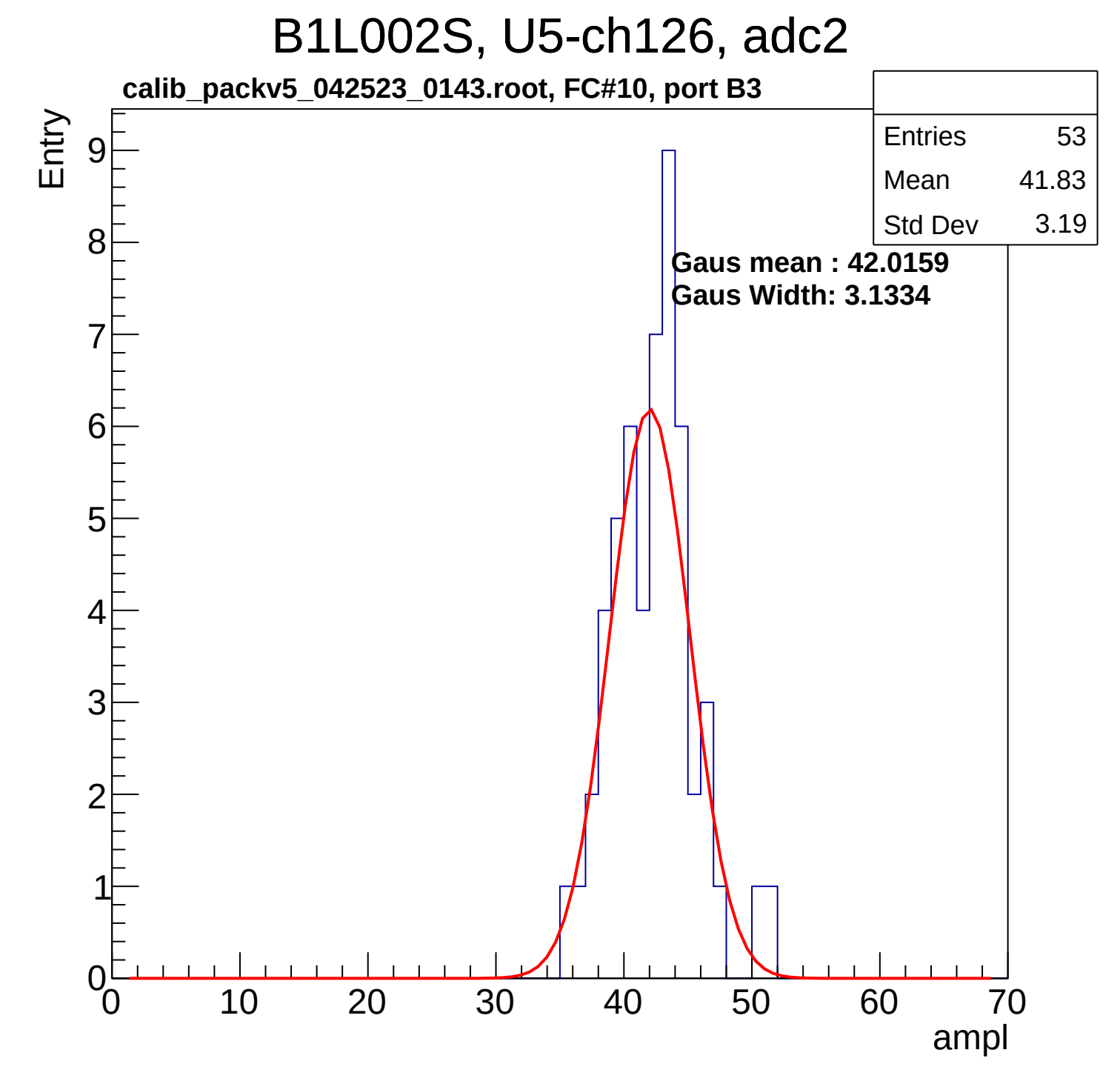
40
50
60
70

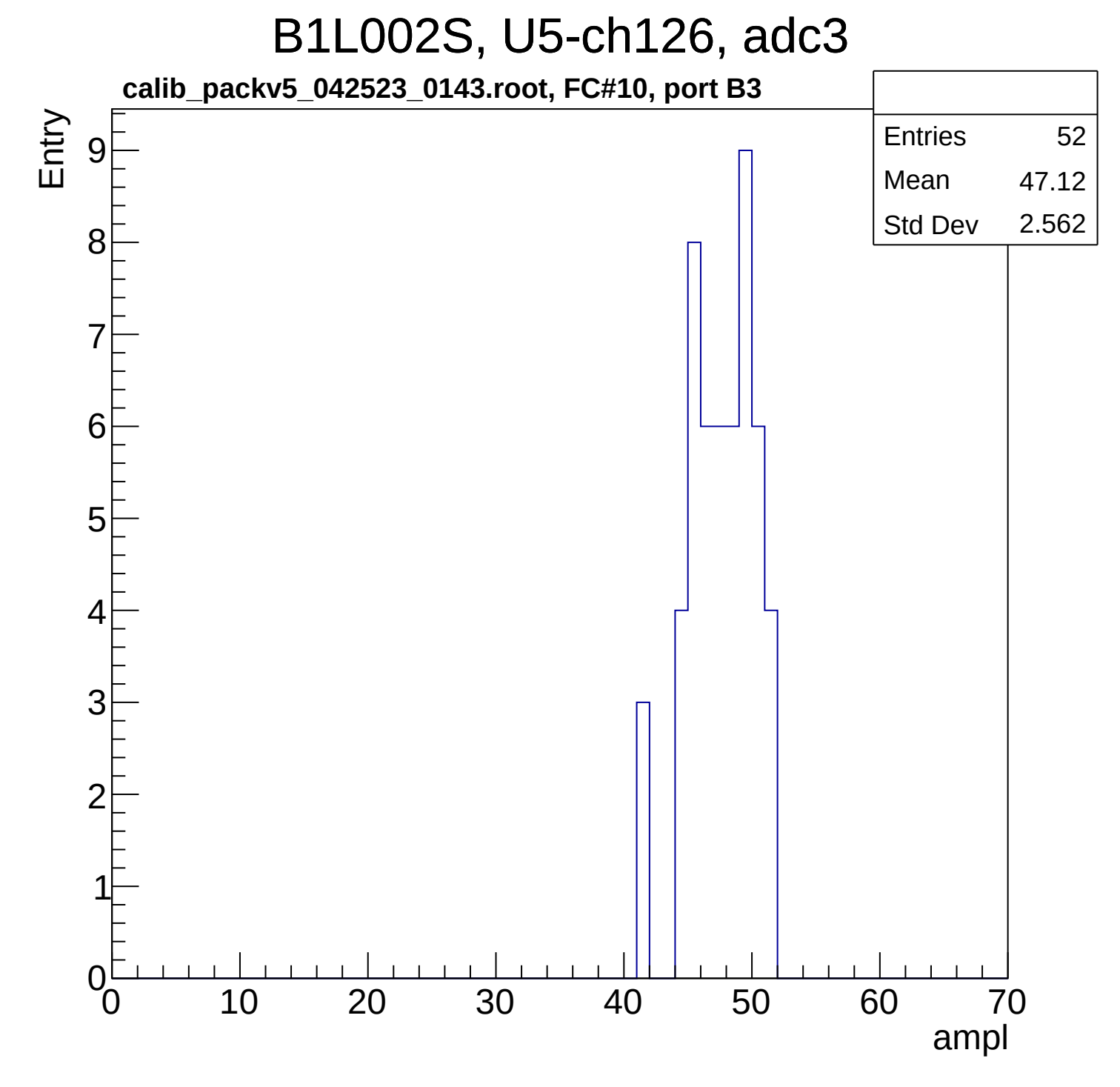

B1L002S, U5-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.23
Std Dev 16.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.82
Std Dev 6.696
Gaus mean : 29.2195
Gaus Width: 3.6845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.88
Std Dev 3.335
Gaus mean : 36.3124
Gaus Width: 3.9657
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.83
Std Dev 3.19
Gaus mean : 42.0159
Gaus Width: 3.1334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.12
Std Dev 2.562
Entry
ampl
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3
4
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6
7
8
9
0
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60
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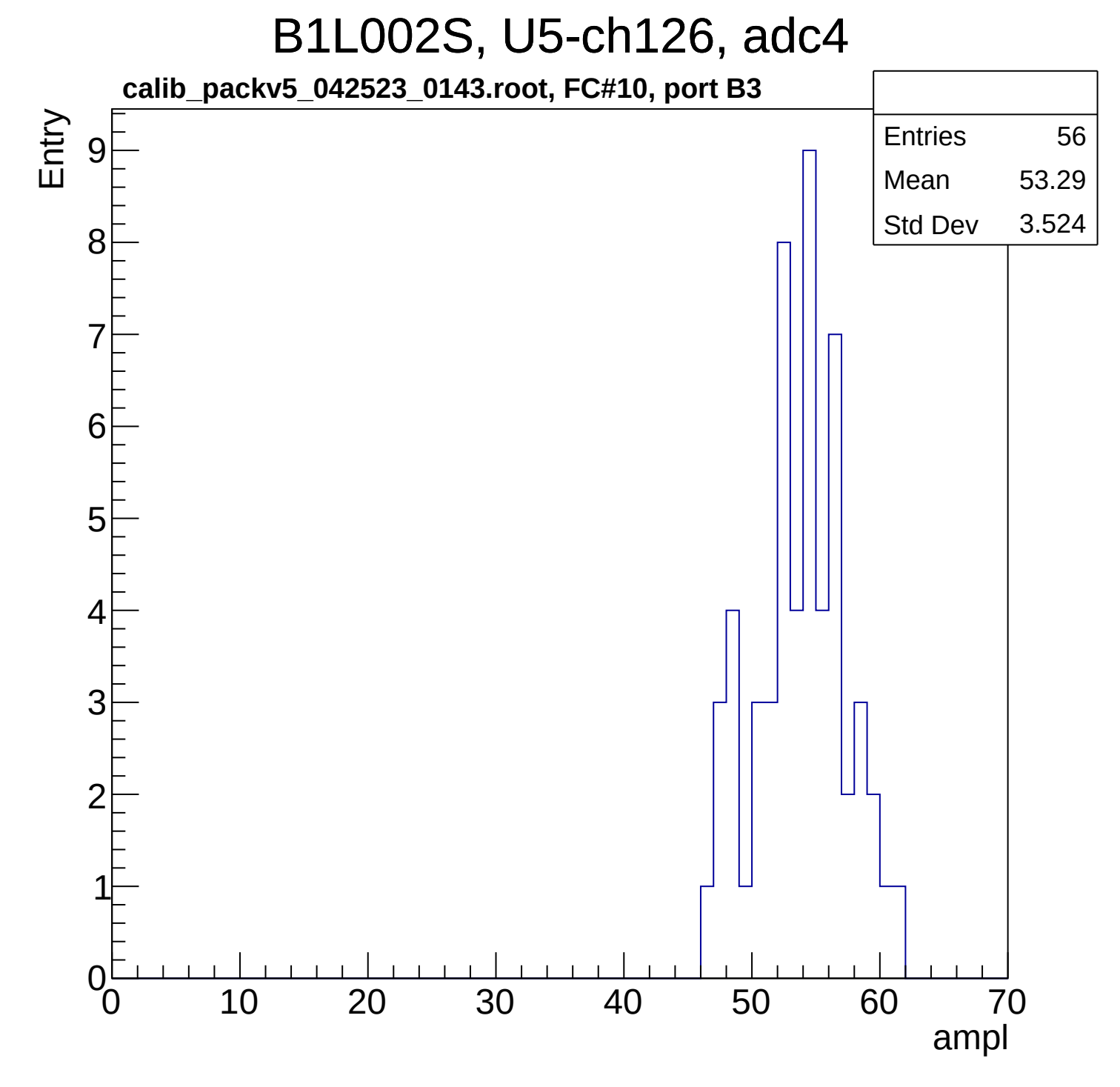
B1L002S, U5-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.29
Std Dev 3.524
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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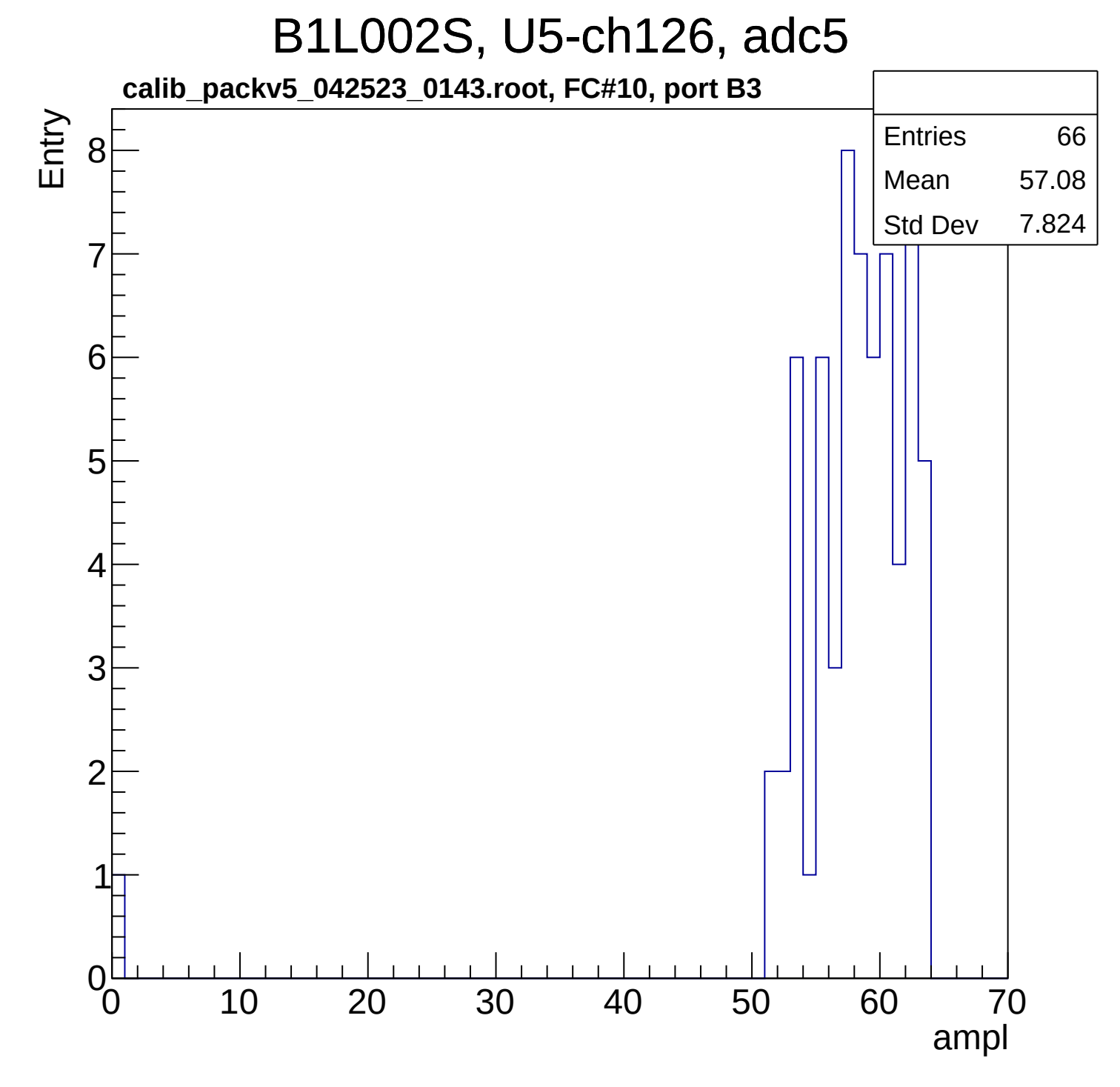
B1L002S, U5-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.08
Std Dev 7.824
Entry
ampl
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1
2
3
4
5
6
7
8
0
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30
40
50
60
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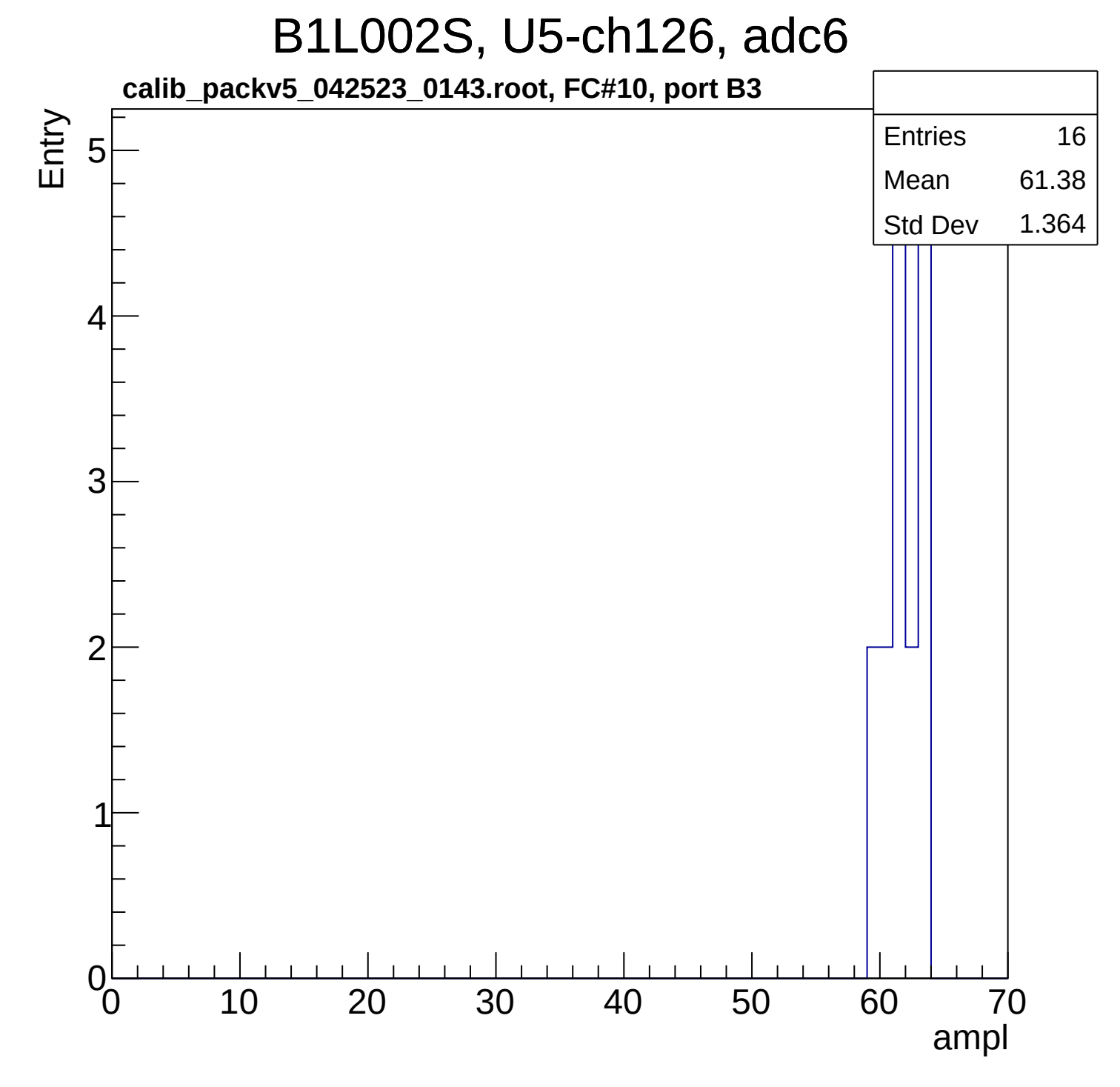
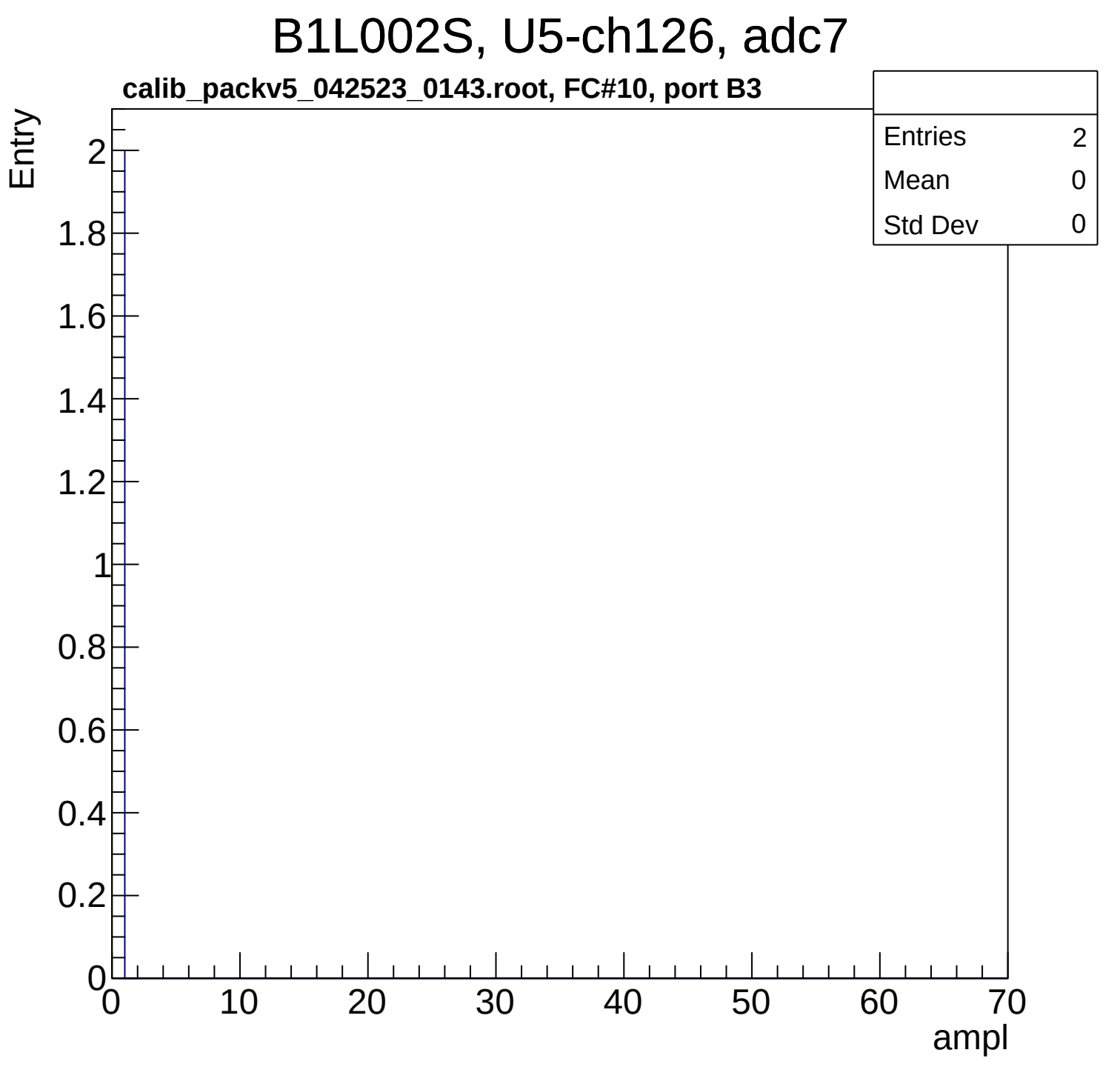

B1L002S, U5-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.38
Std Dev 1.364
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U5-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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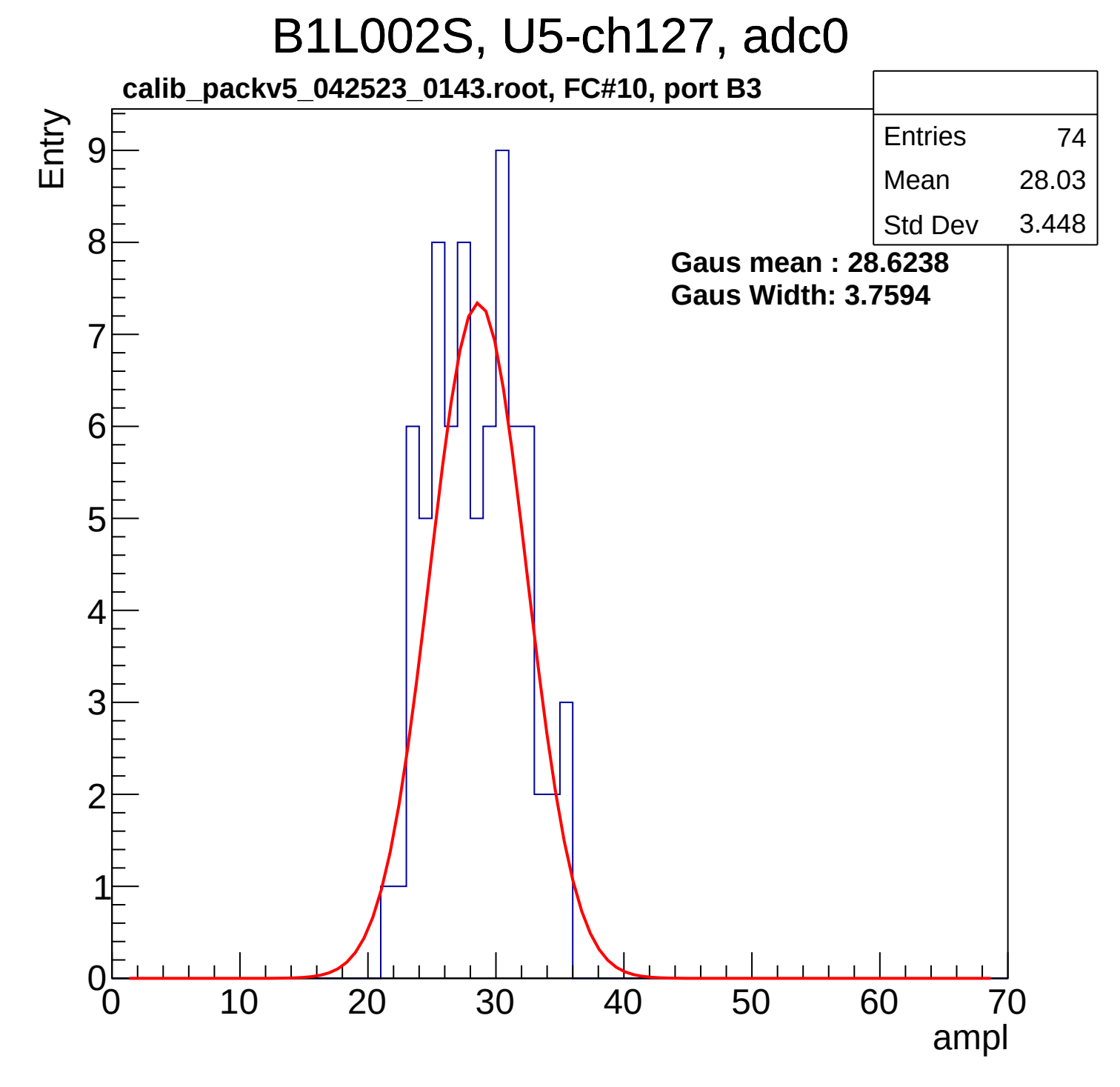
50
60
70
ampl


B1L002S, U5-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.03
Std Dev 3.448
Gaus mean : 28.6238
Gaus Width: 3.7594
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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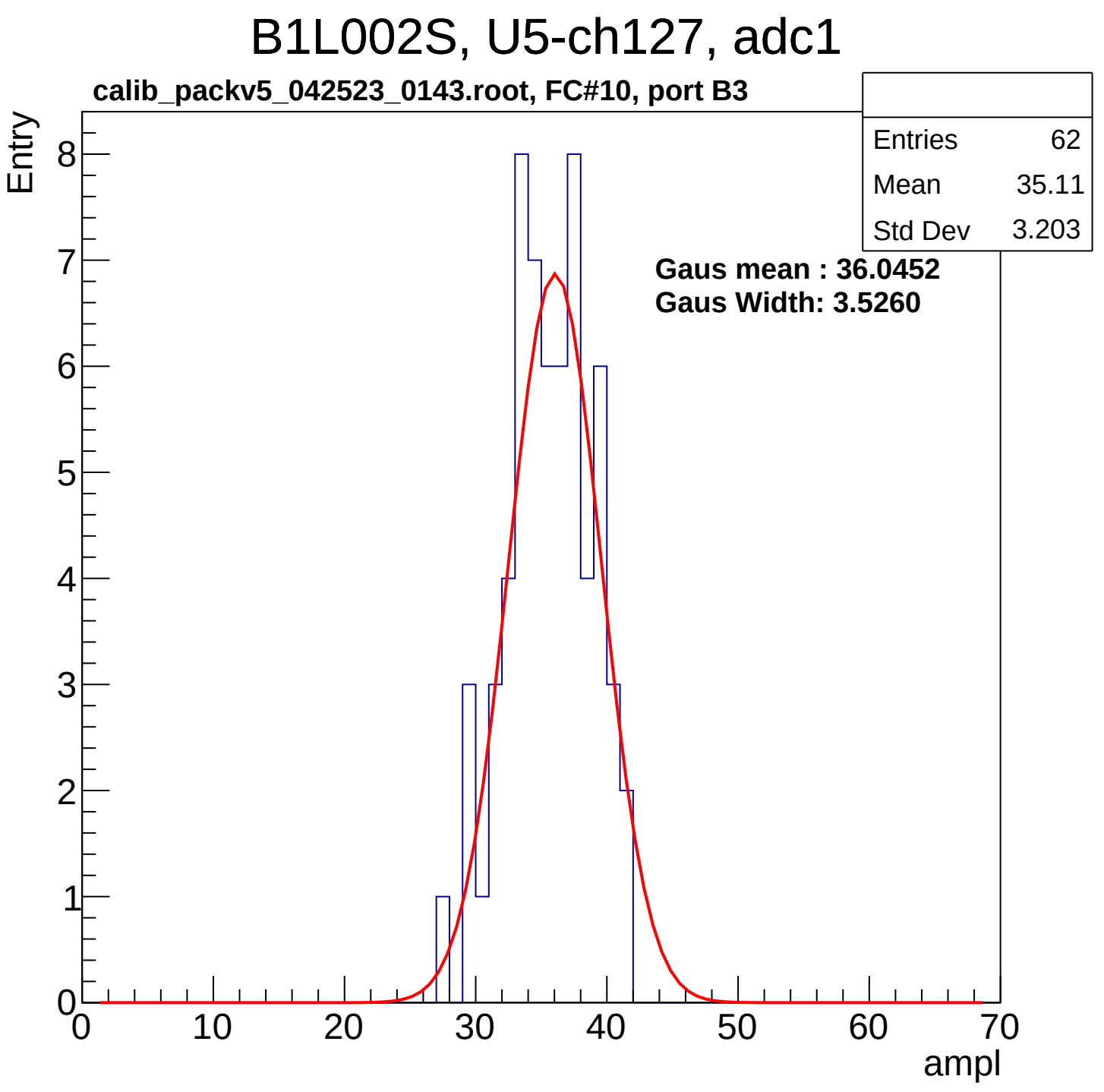
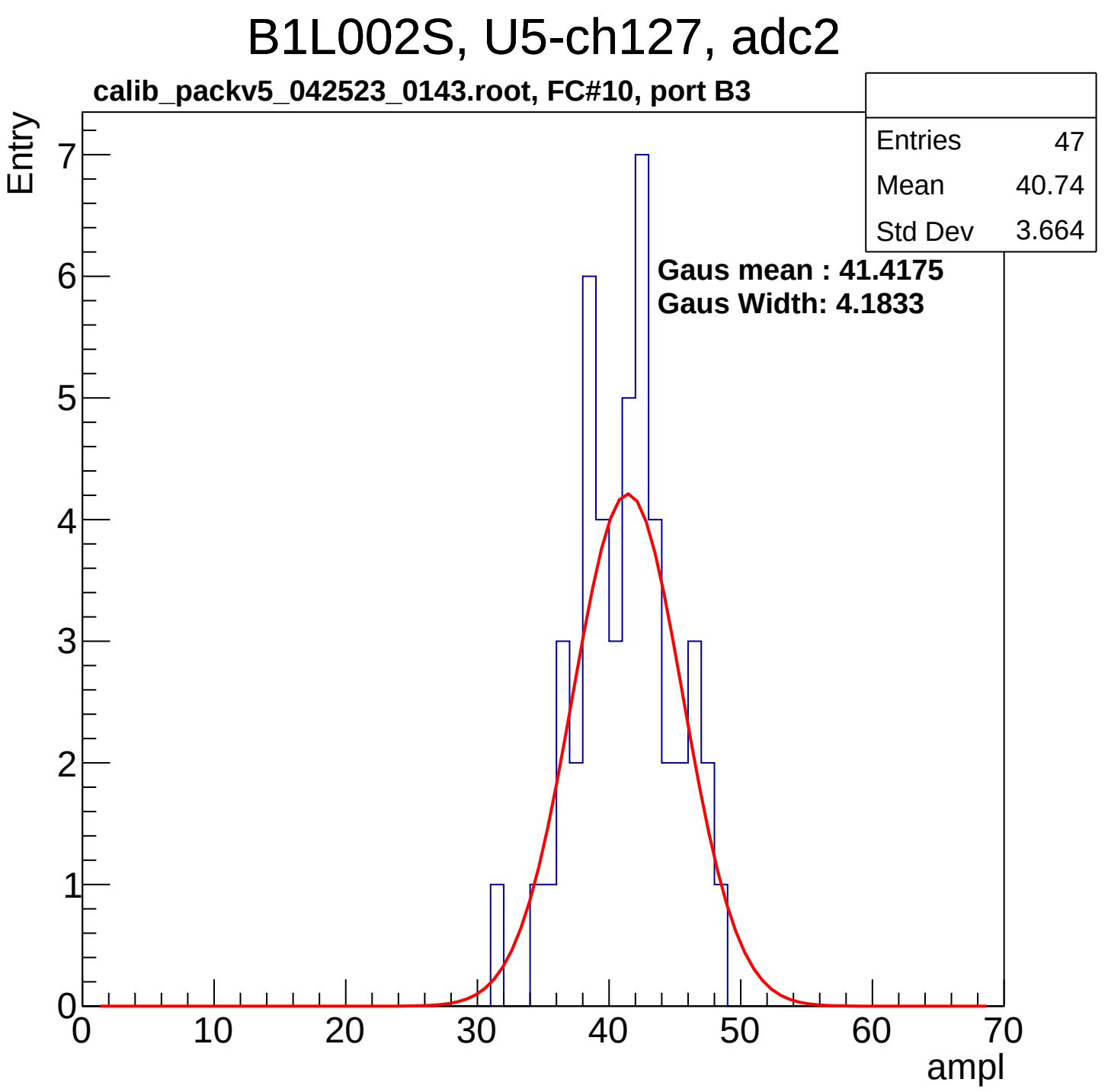
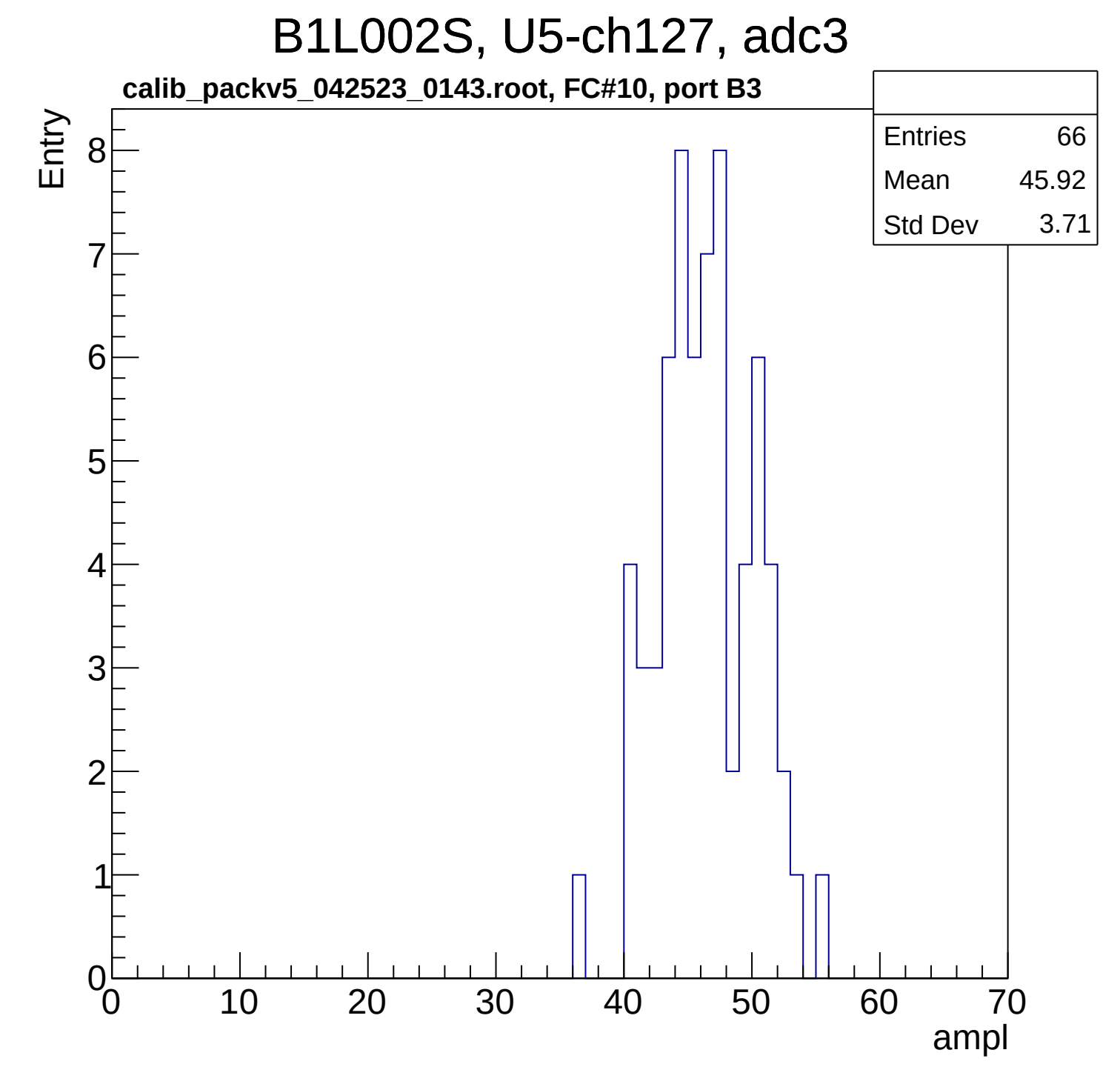
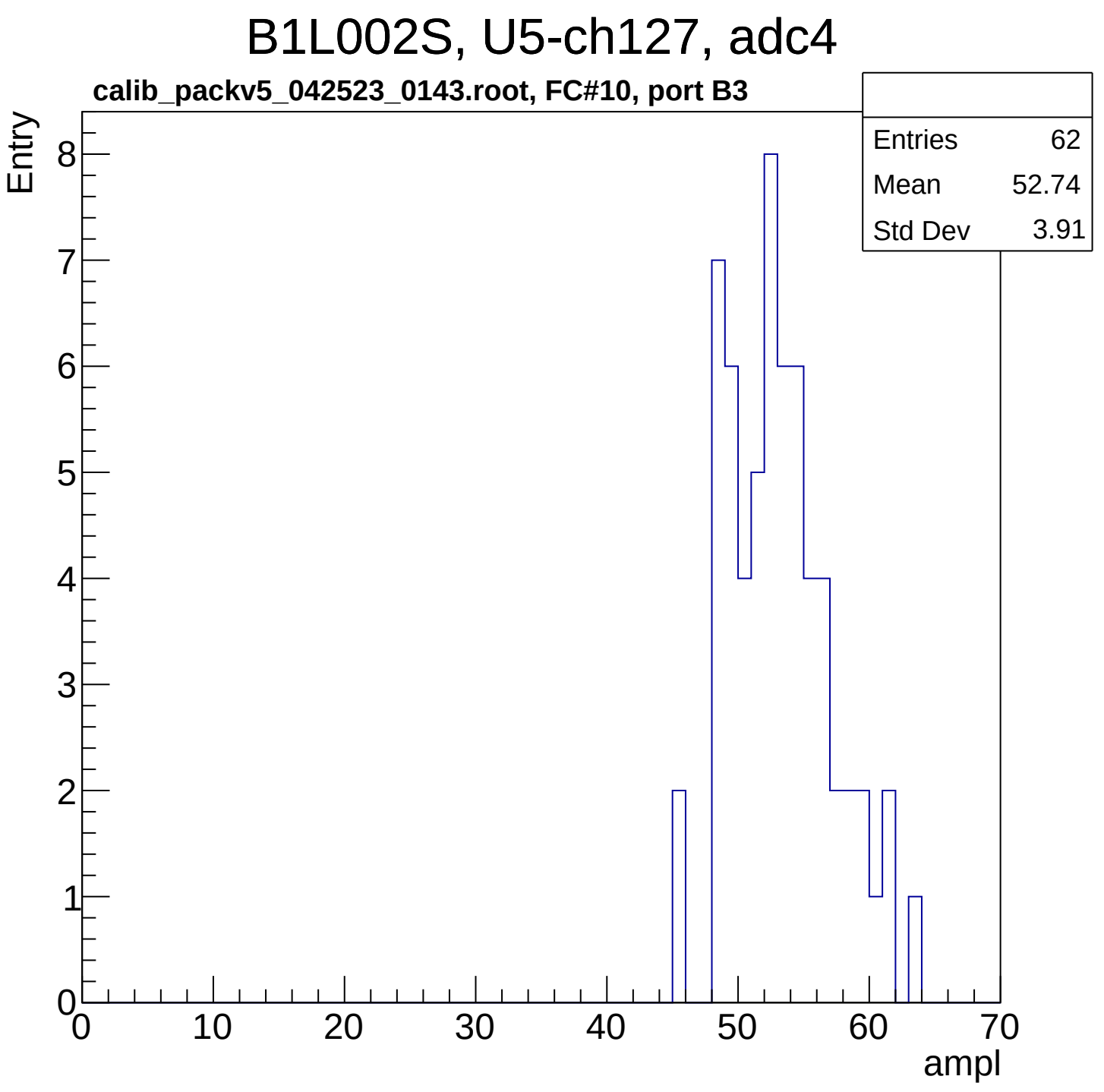
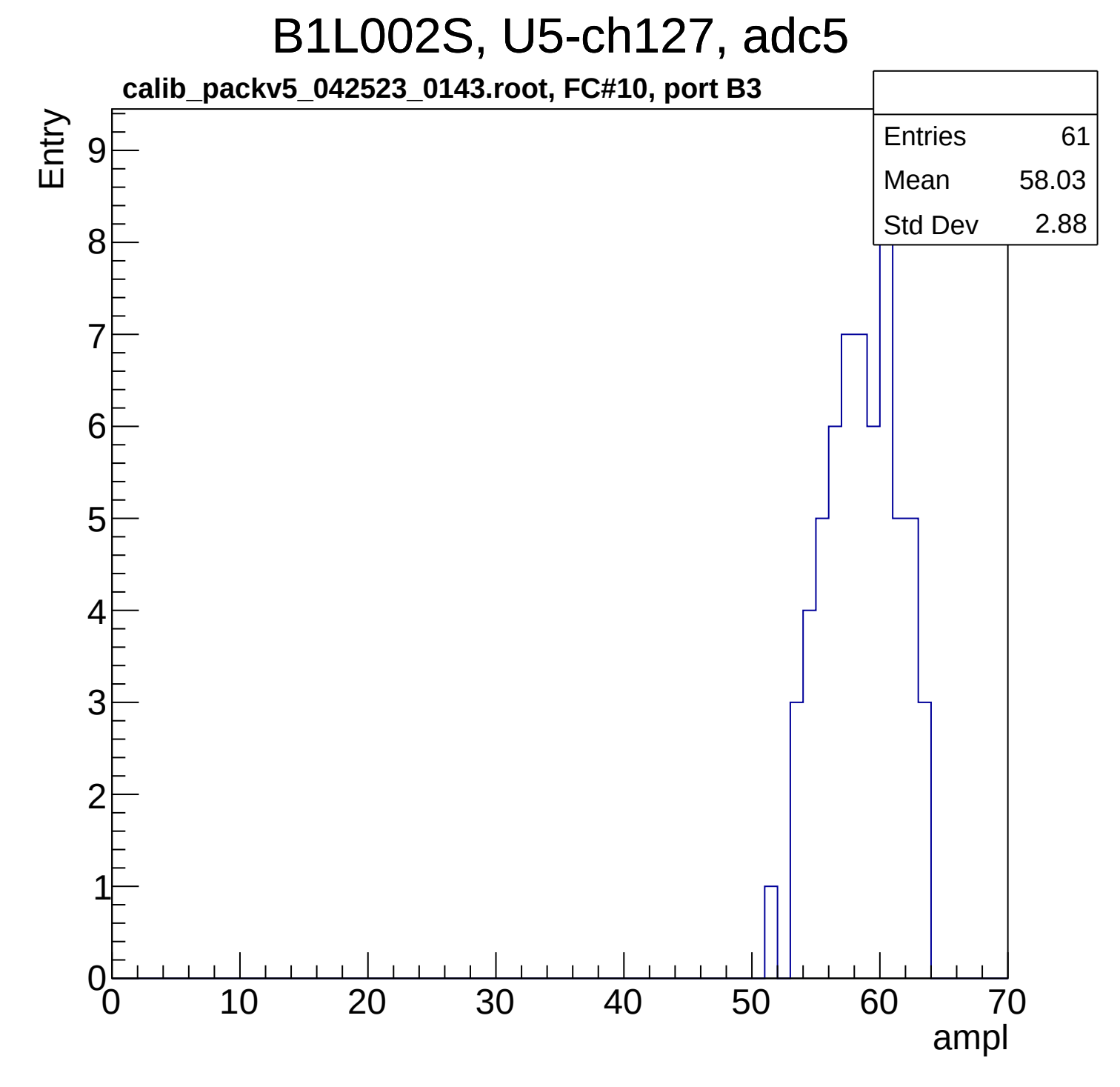
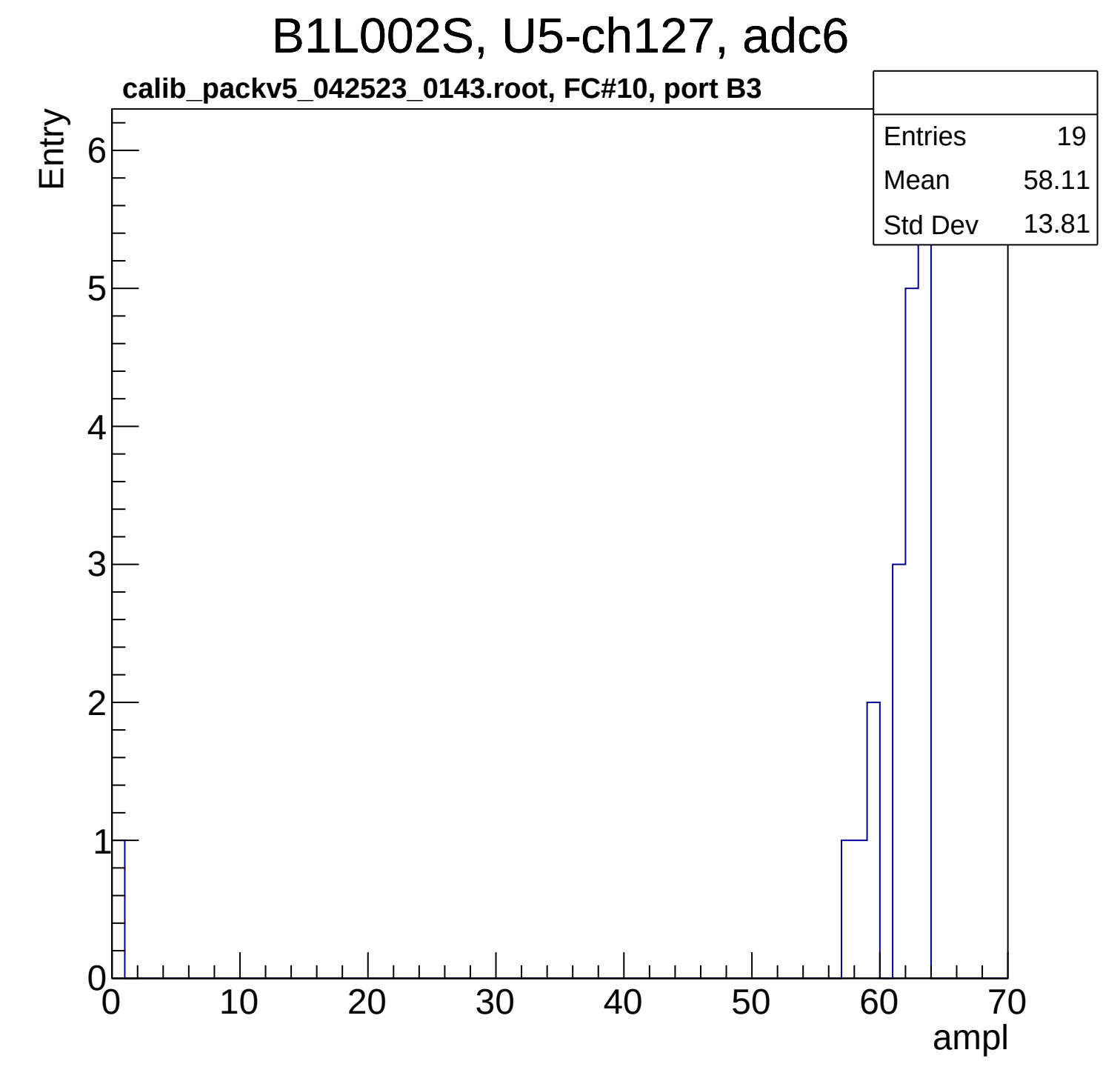

B1L002S, U5-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.11
Std Dev 3.203
Gaus mean : 36.0452
Gaus Width: 3.5260
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

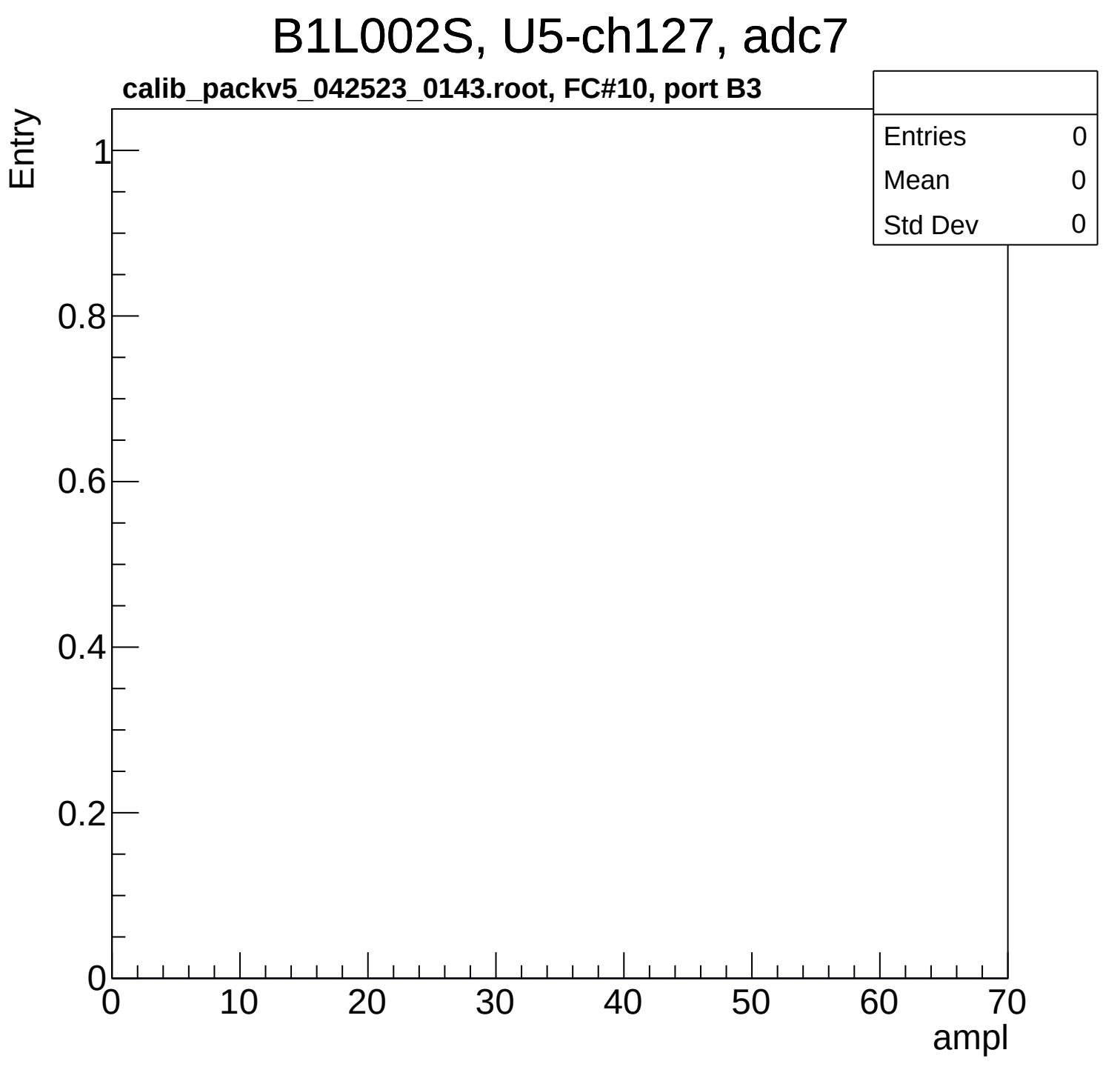

B1L002S, U5-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.74
Std Dev 3.664
Gaus mean : 41.4175
Gaus Width: 4.1833
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

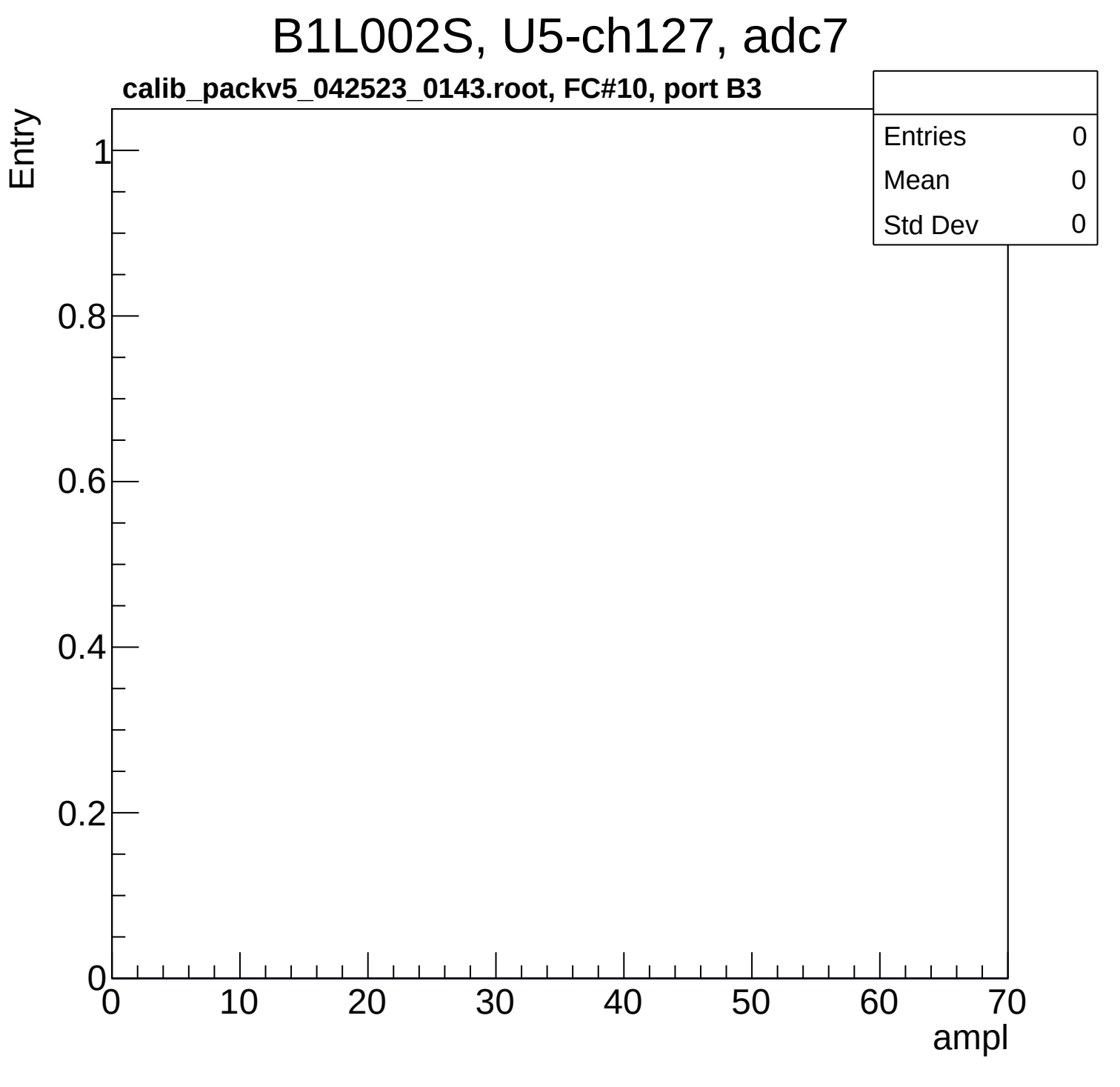

B1L002S, U5-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.92
Std Dev 3.71
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.74
Std Dev 3.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U5-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.03
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U5-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.11
Std Dev 13.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl