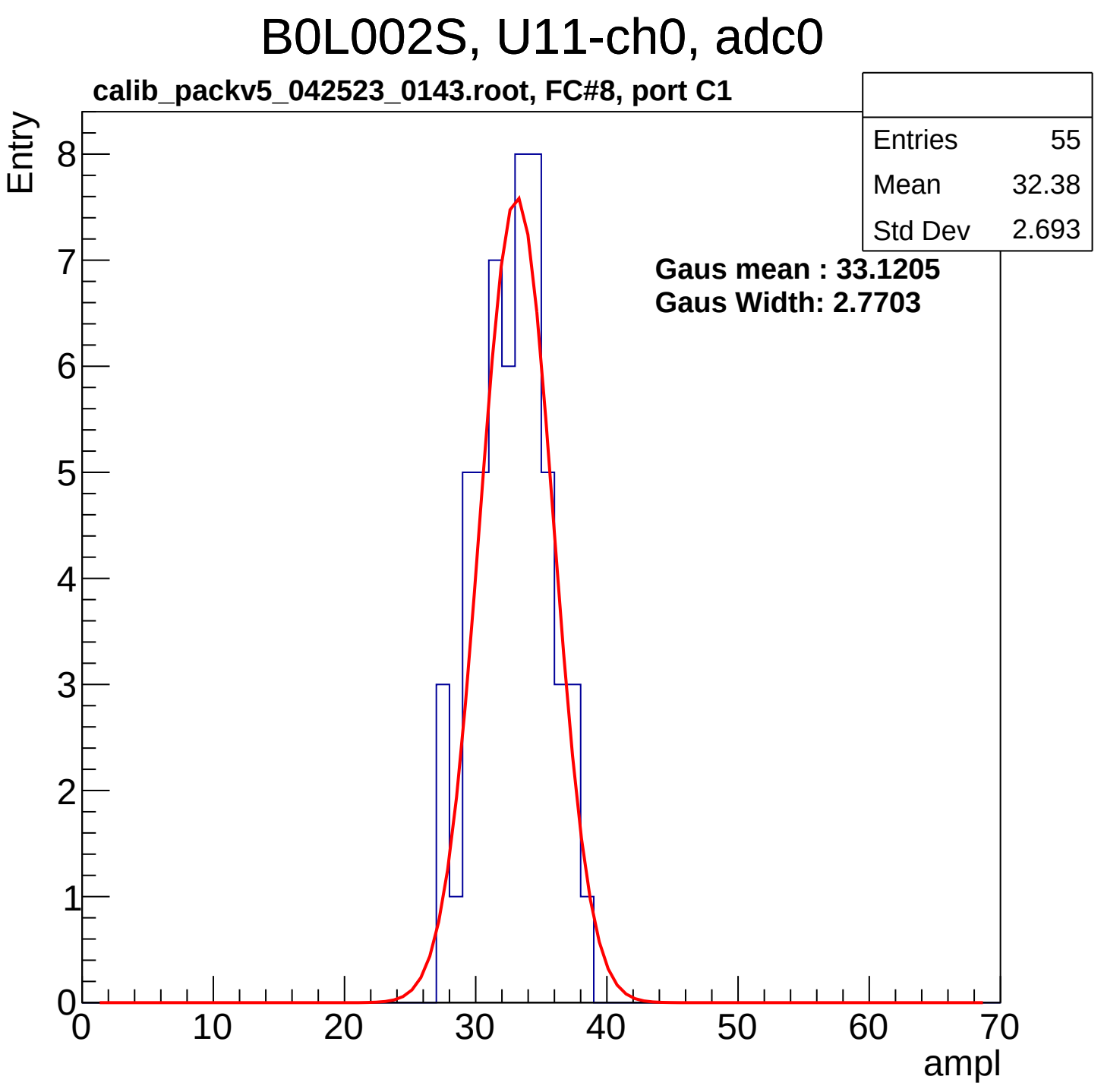
B0L002S, U11-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 32.38
Std Dev 2.693
Gaus mean : 33.1205
Gaus Width: 2.7703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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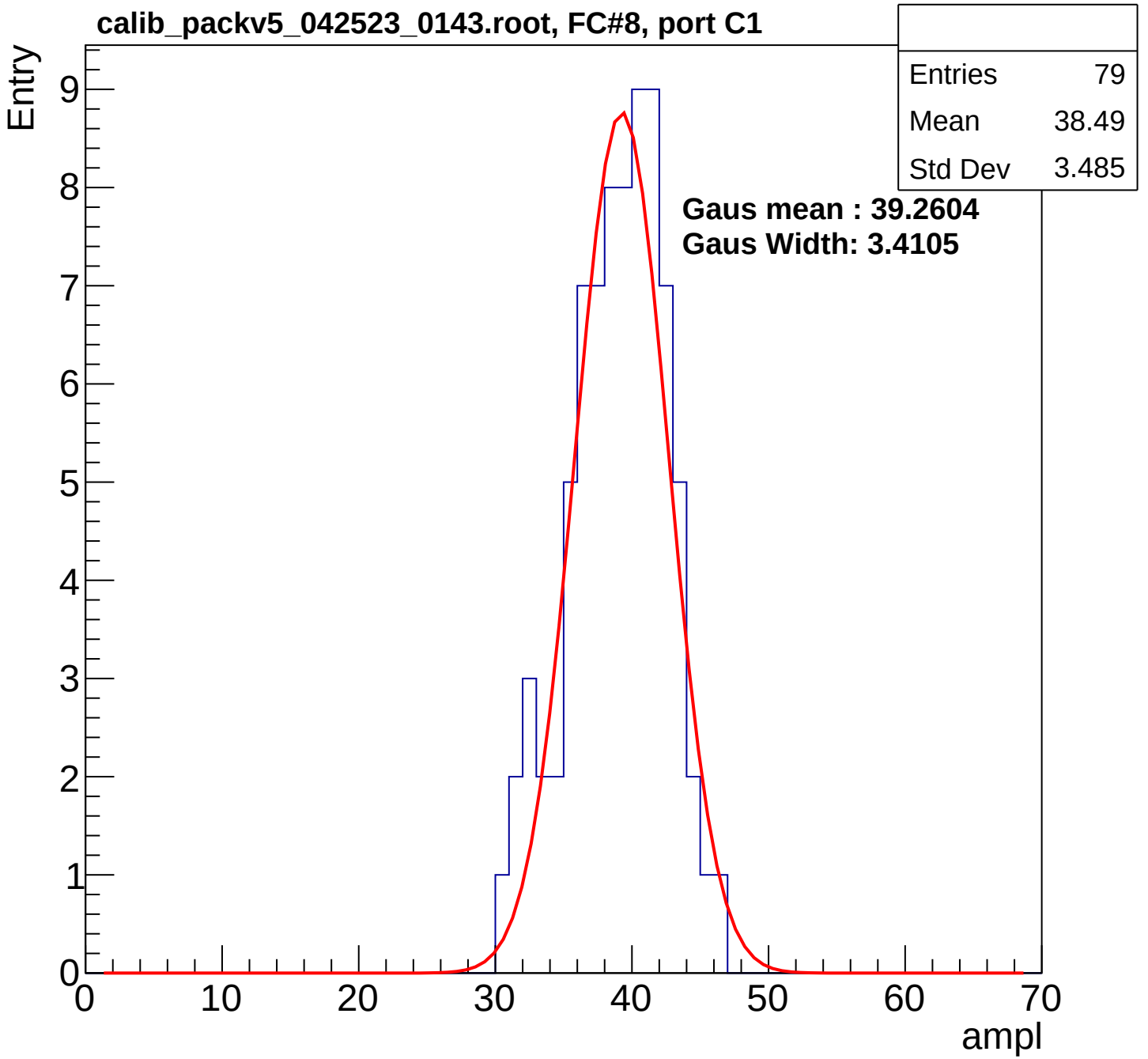
B0L002S, U11-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 38.49
Std Dev 3.485
Gaus mean : 39.2604
Gaus Width: 3.4105
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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40
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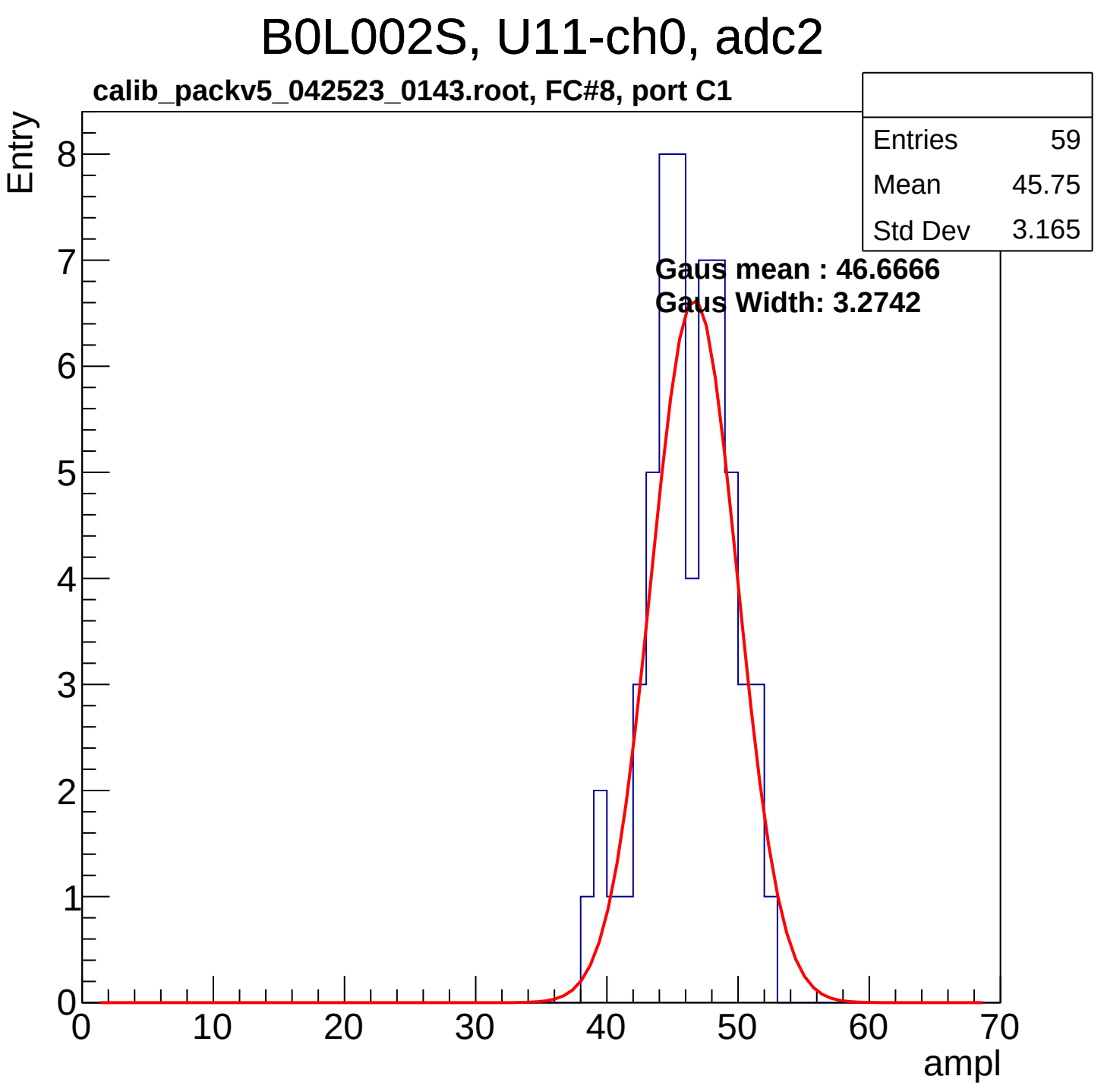
B0L002S, U11-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 45.75
Std Dev 3.165
Gaus mean : 46.6666
Gaus Width: 3.2742
Entry
ampl
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1
2
3
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5
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0
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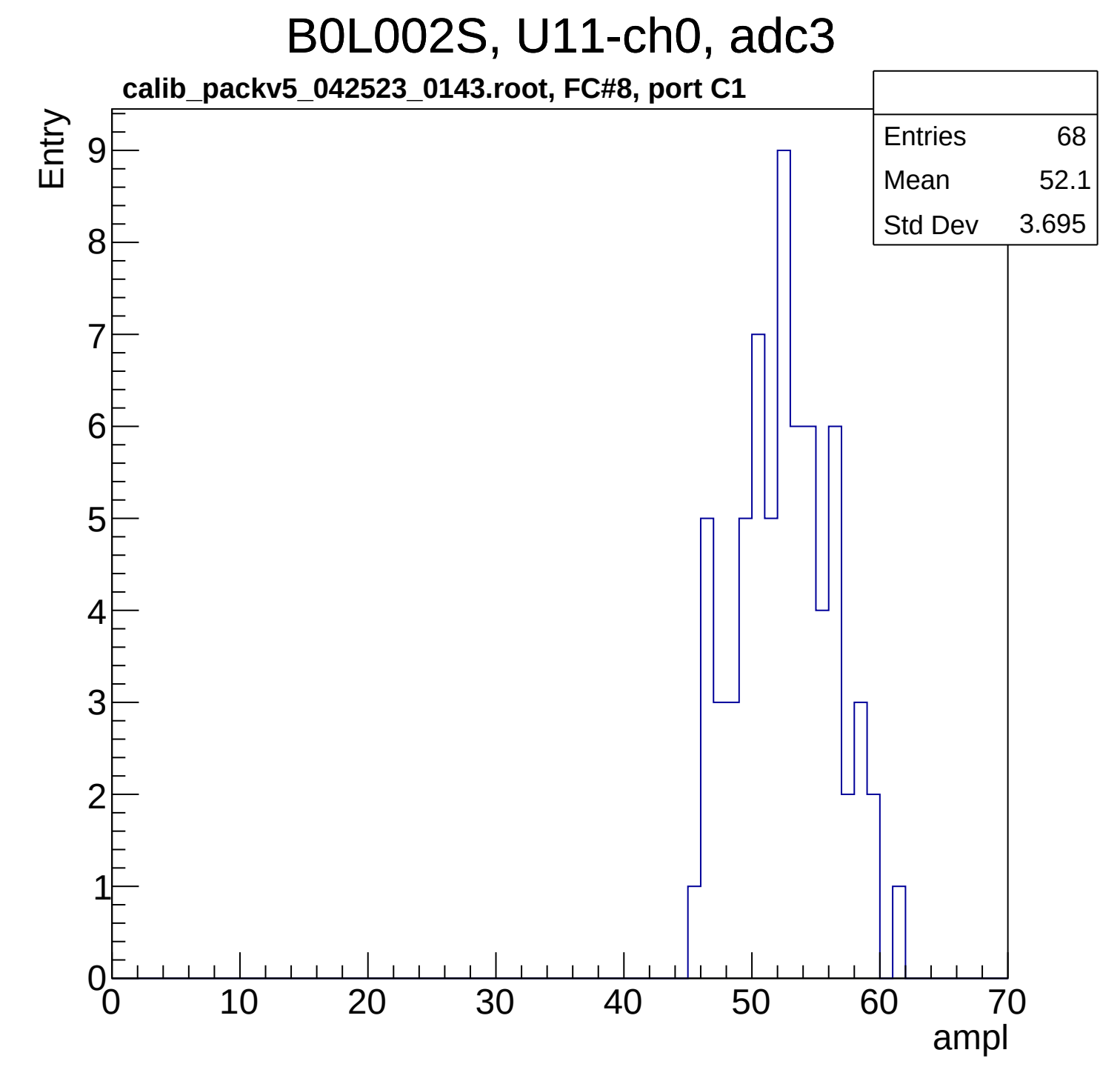
B0L002S, U11-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 52.1
Std Dev 3.695
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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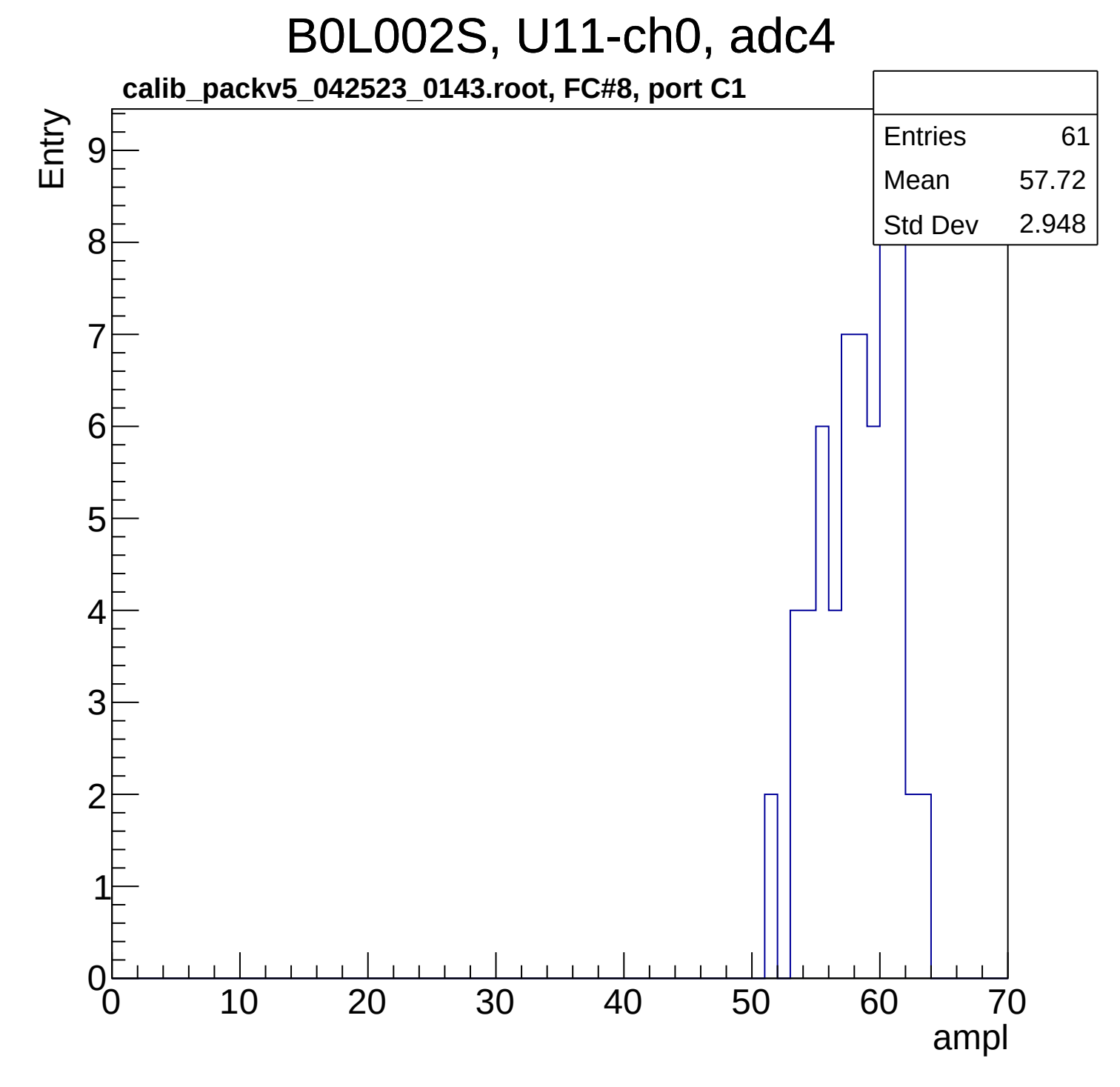
B0L002S, U11-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.72
Std Dev 2.948
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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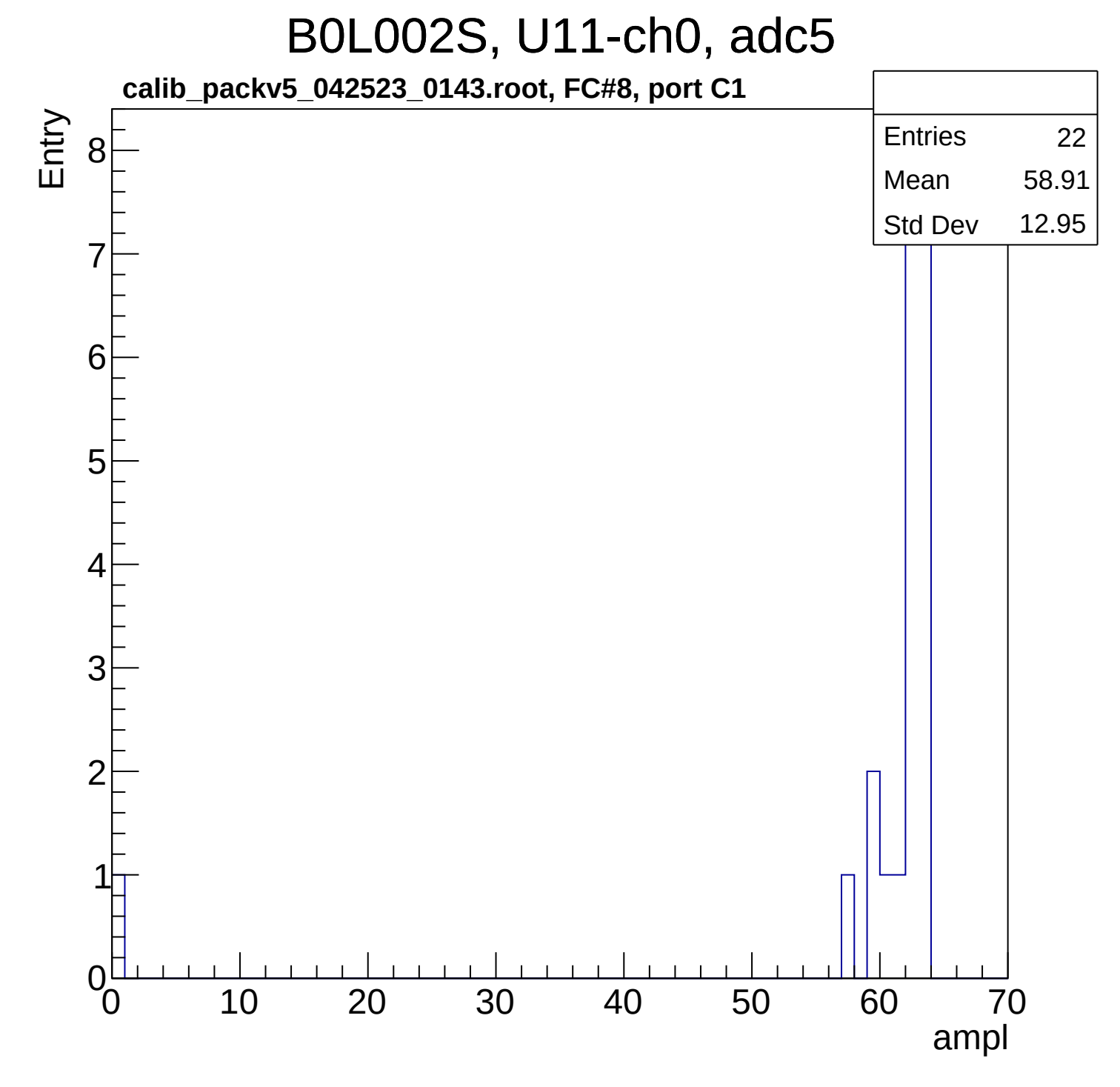
B0L002S, U11-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 58.91
Std Dev 12.95
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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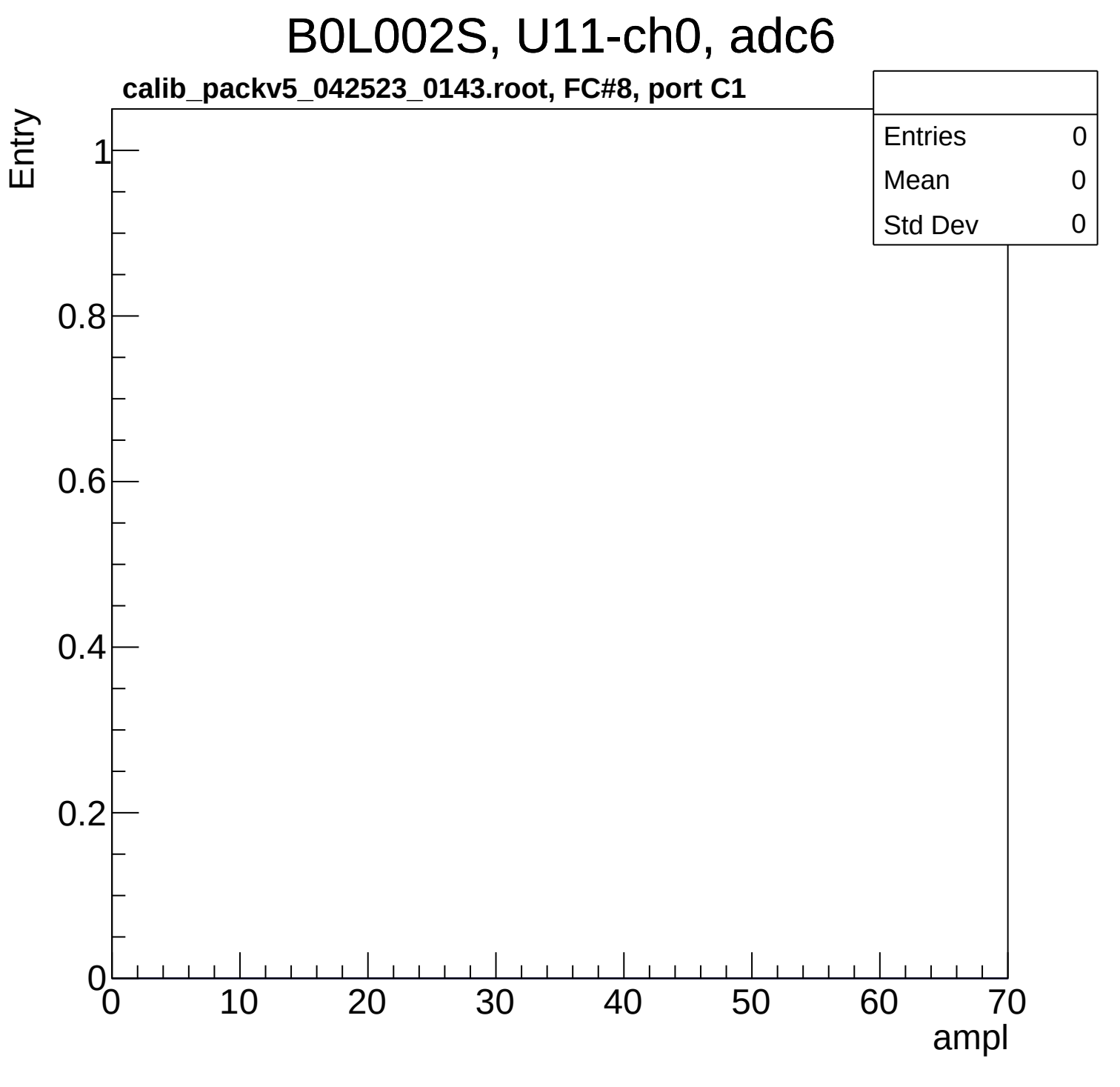
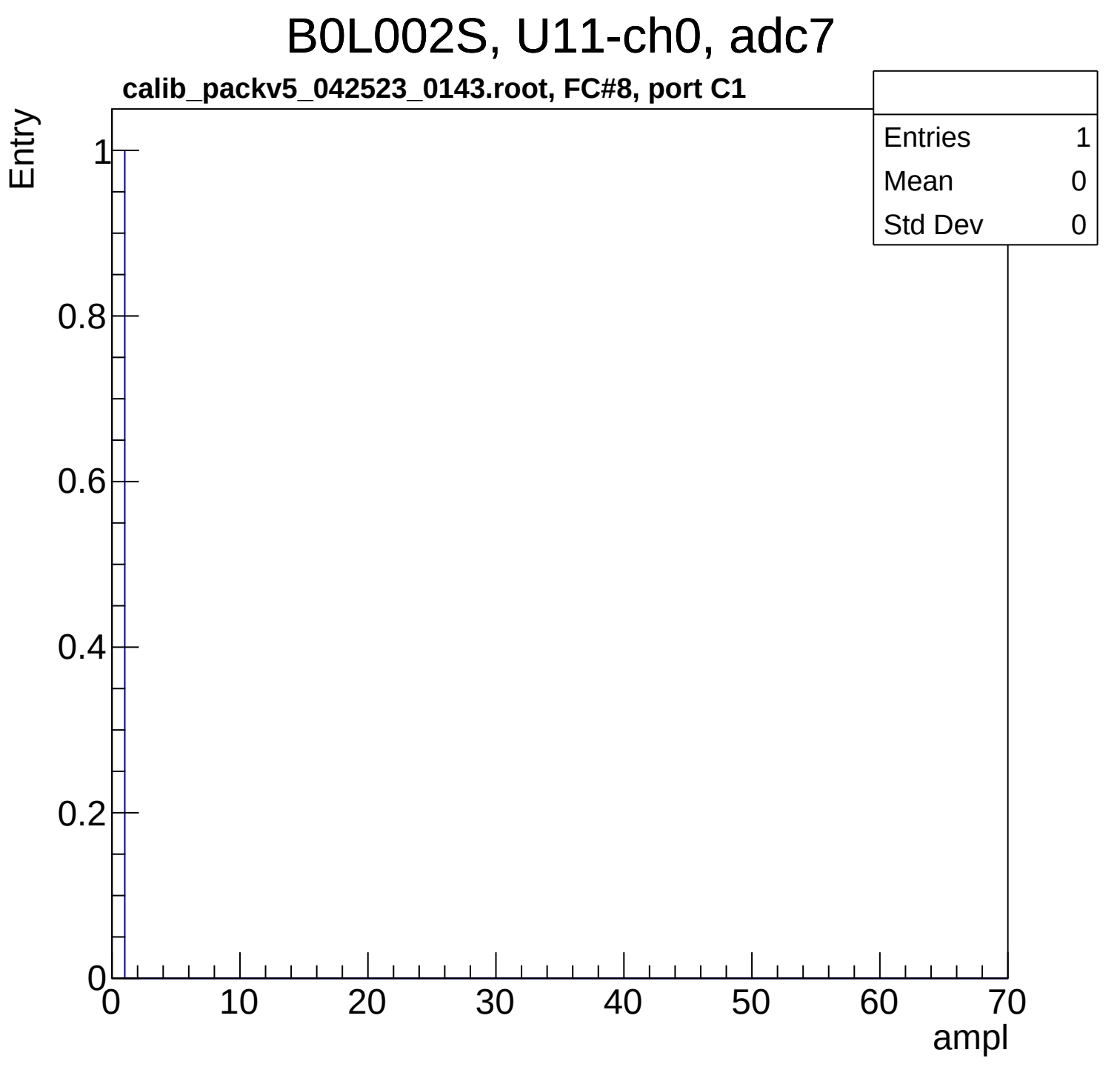

B0L002S, U11-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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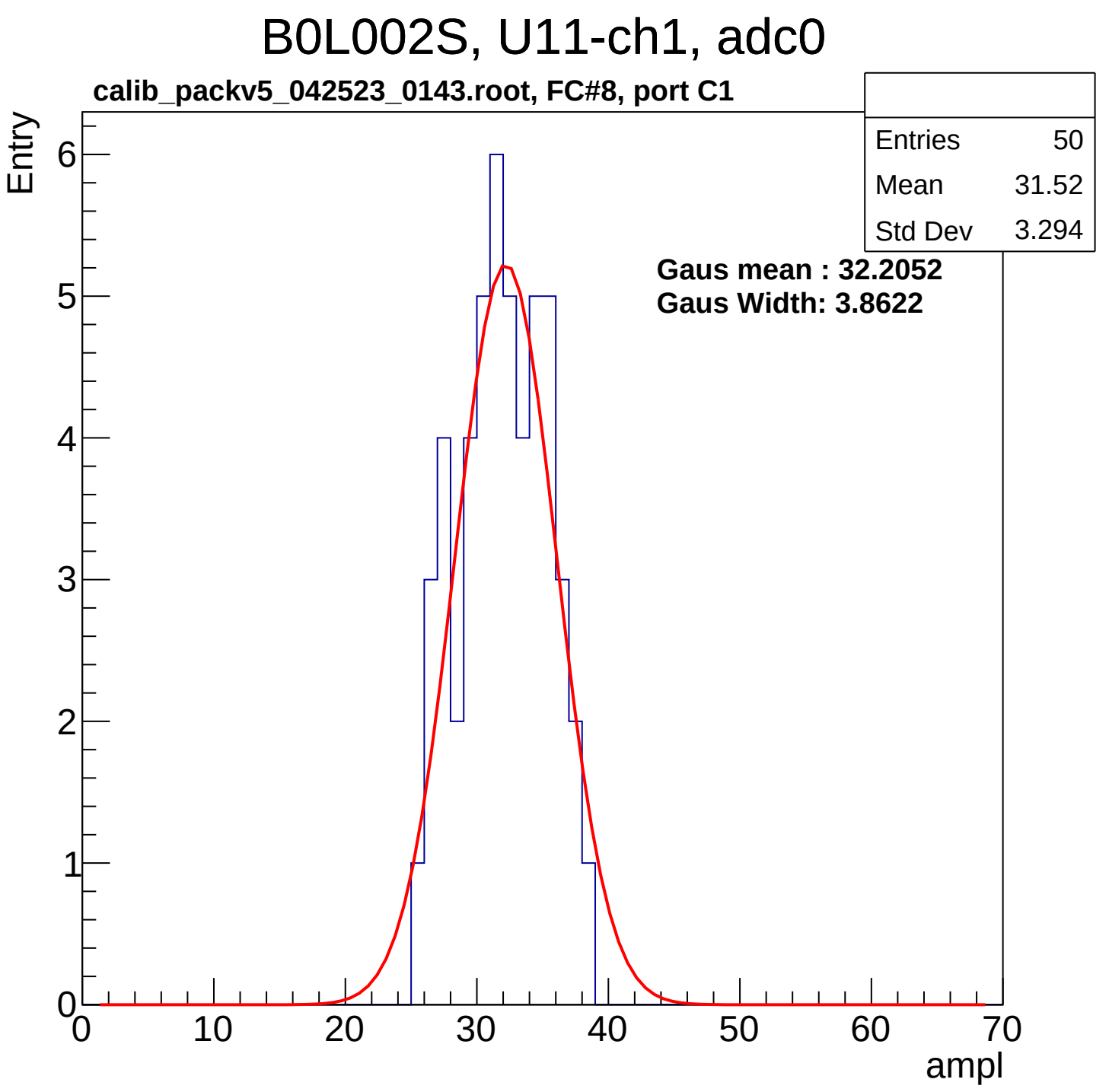
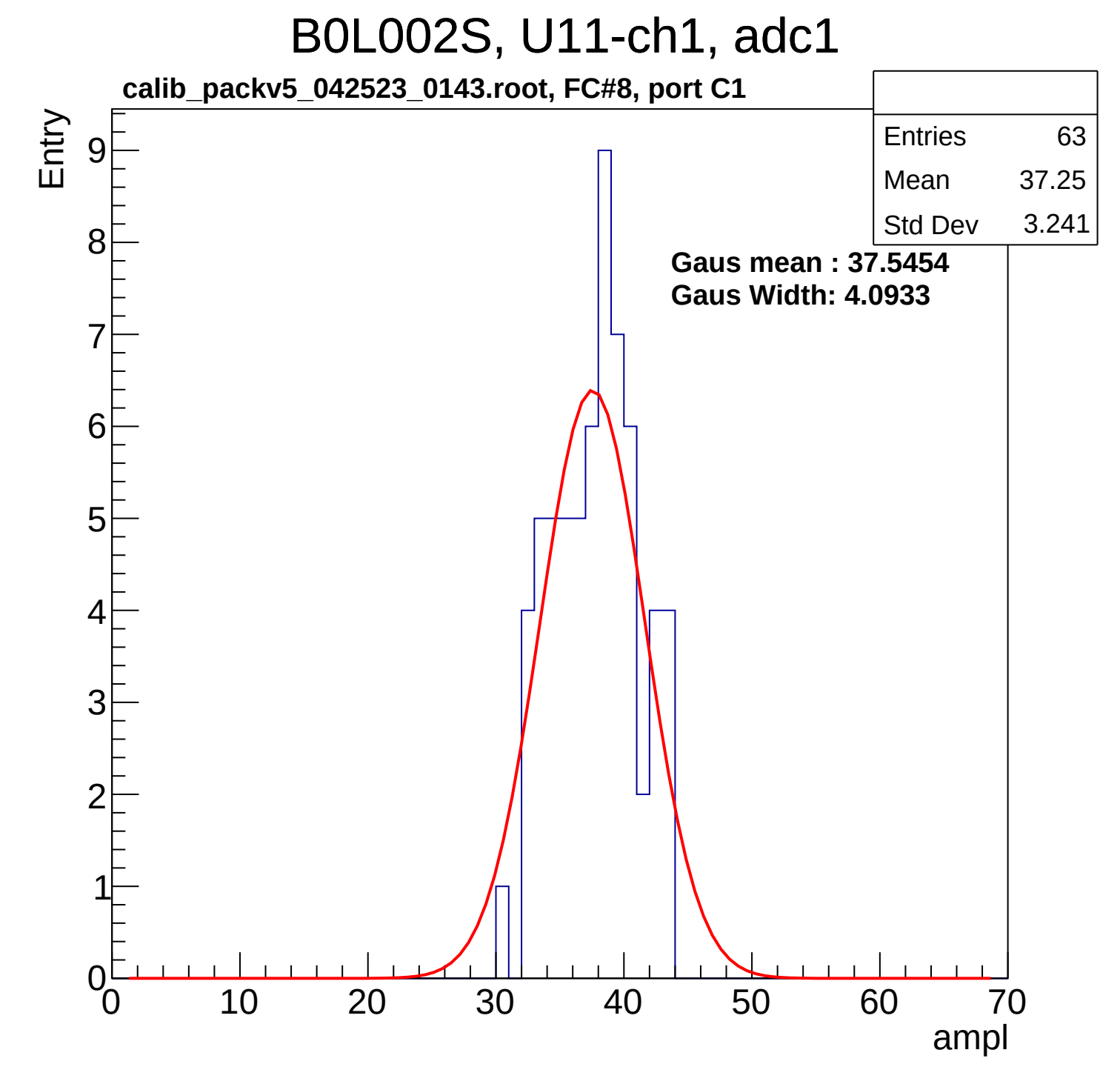
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 31.52
Std Dev 3.294
Gaus mean : 32.2052
Gaus Width: 3.8622
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U11-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 37.25
Std Dev 3.241
Gaus mean : 37.5454
Gaus Width: 4.0933
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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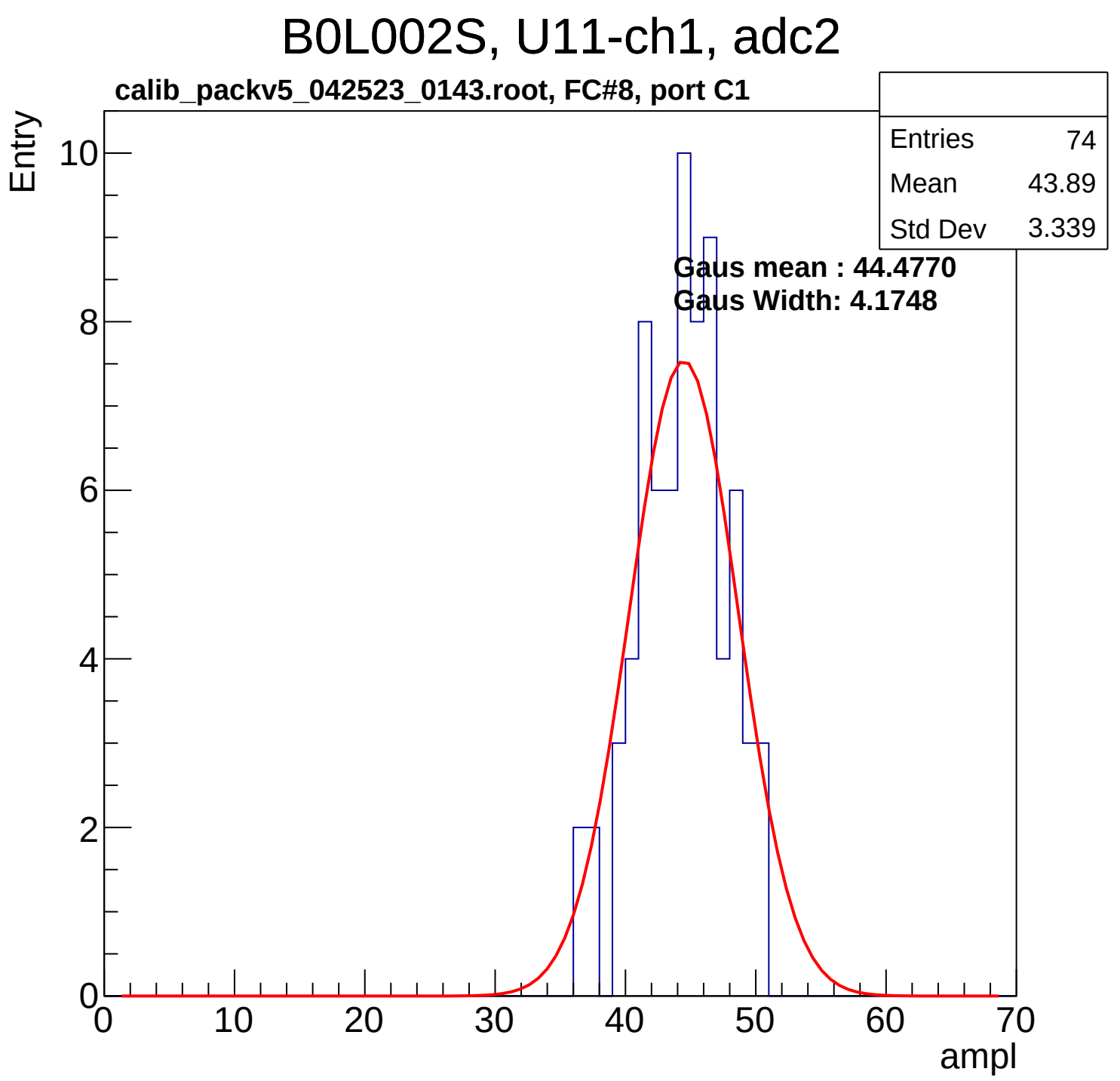
B0L002S, U11-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.89
Std Dev 3.339
Gaus mean : 44.4770
Gaus Width: 4.1748
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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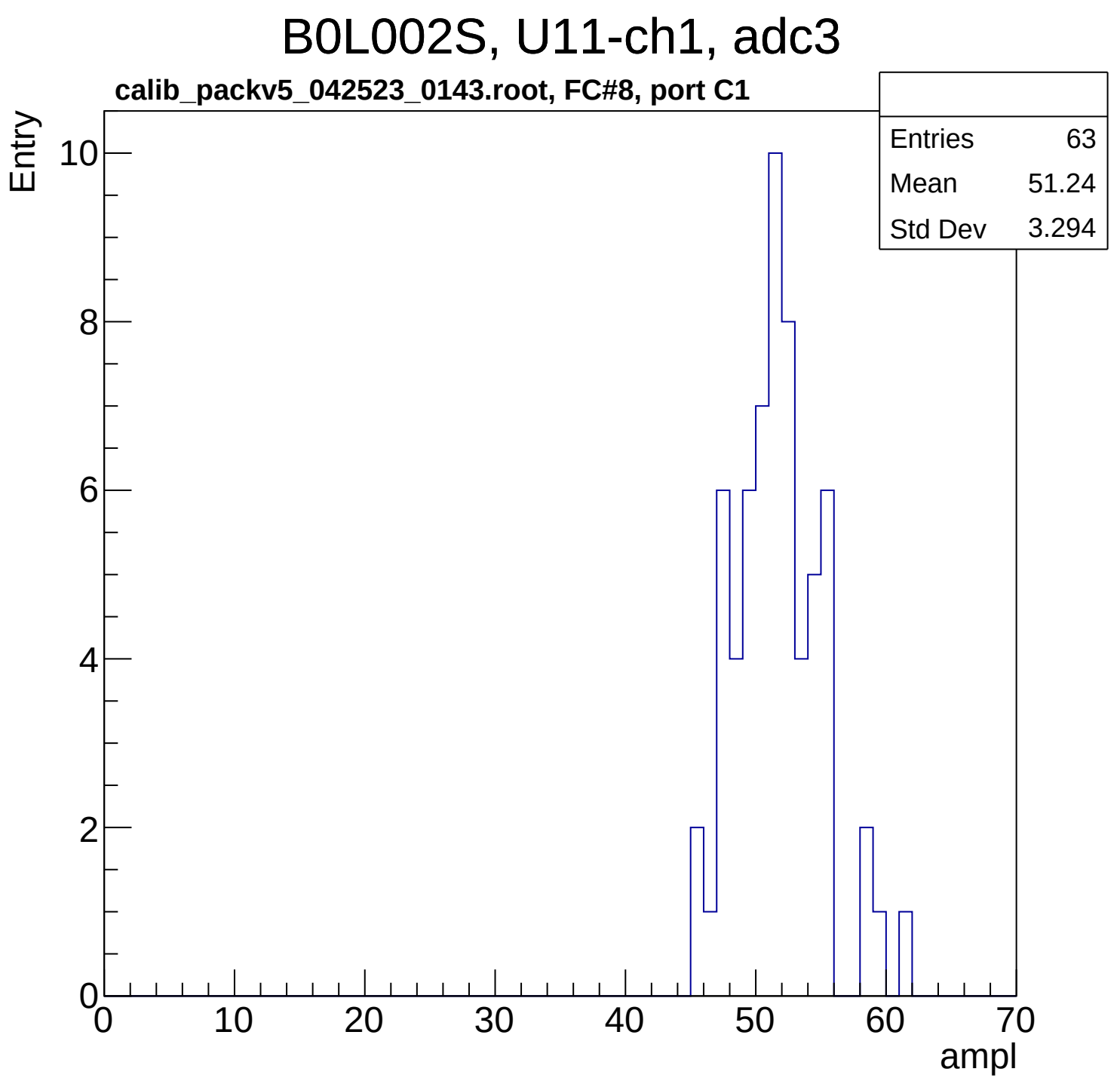
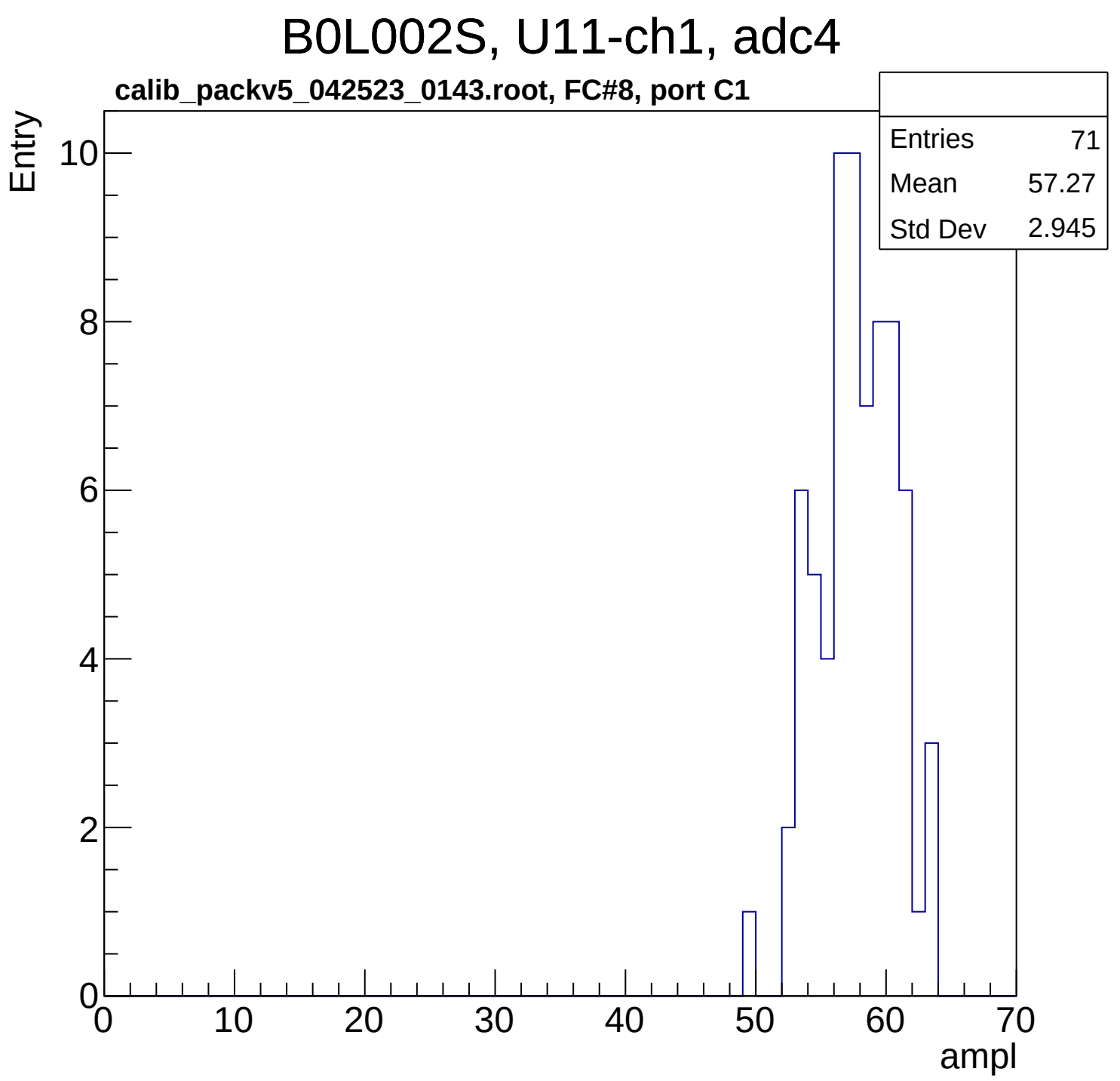
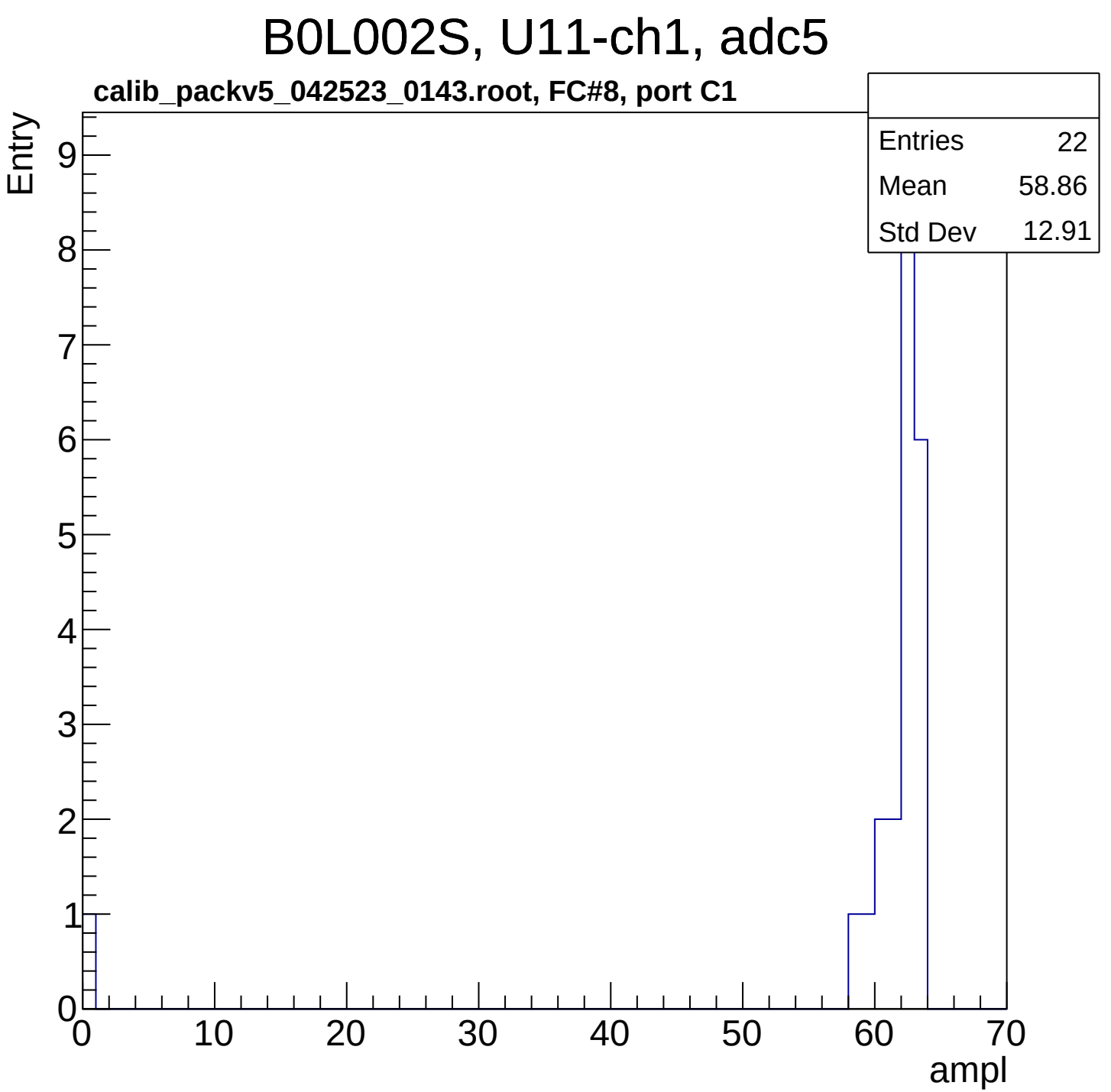
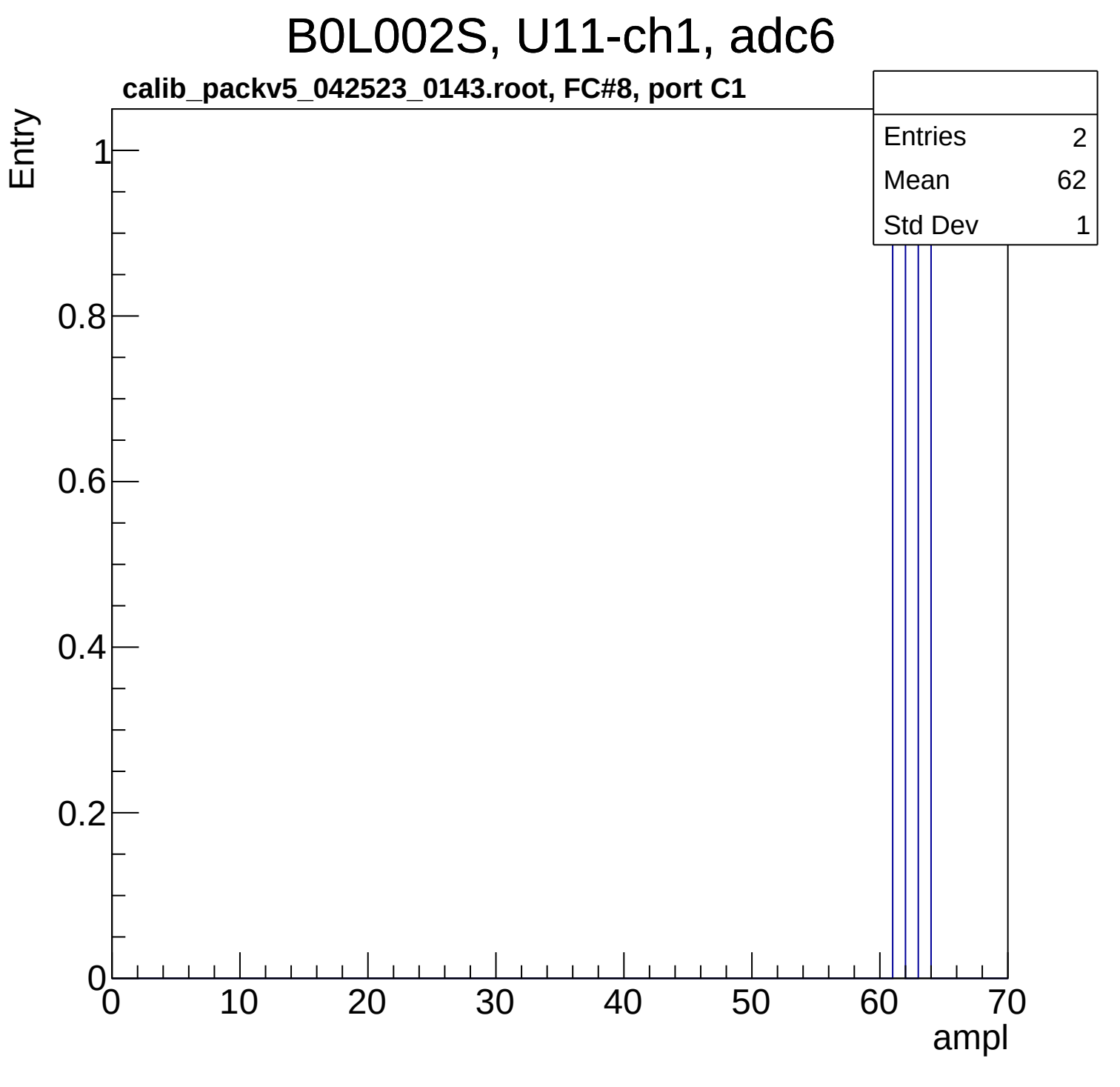
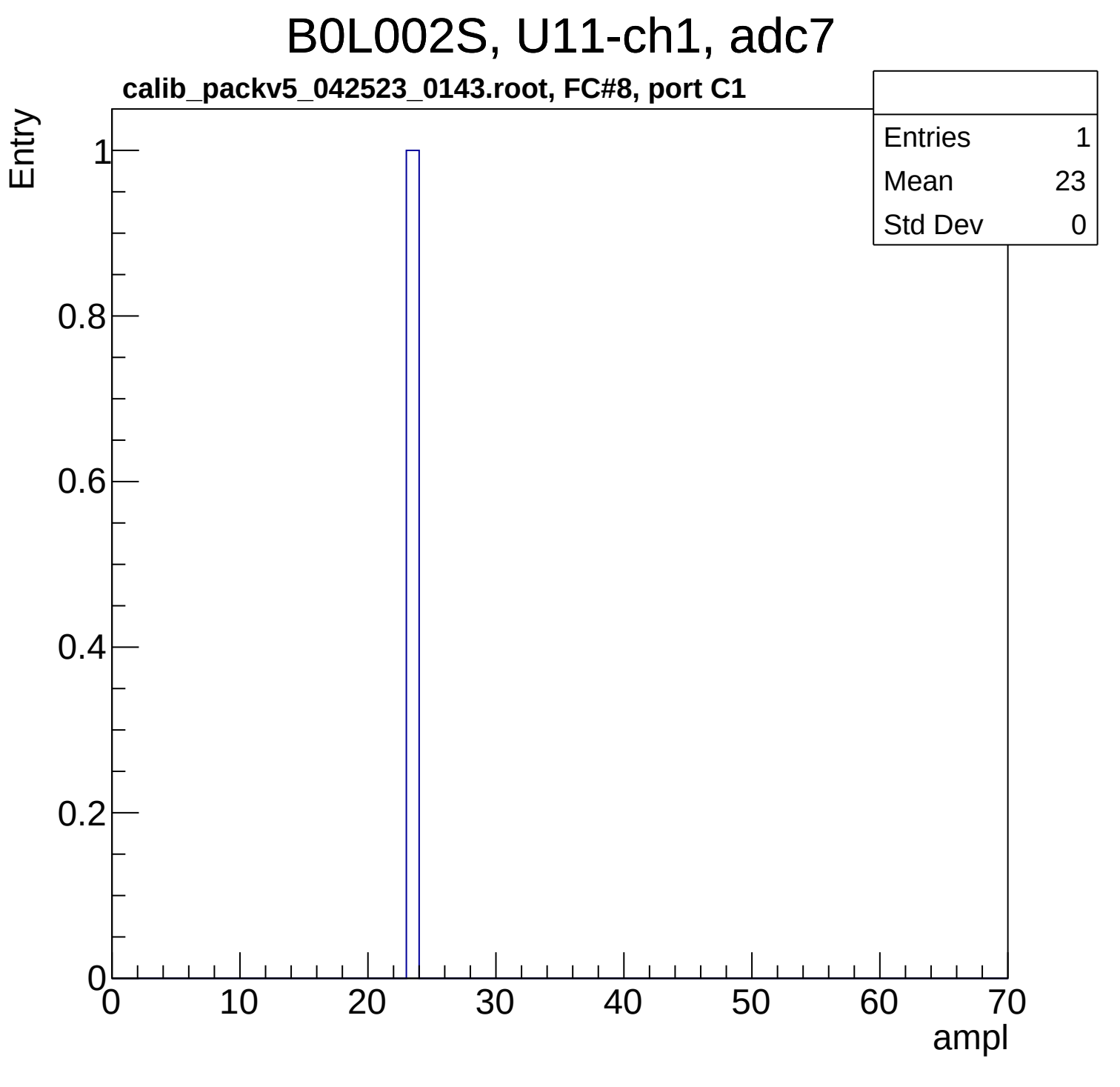

B0L002S, U11-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.24
Std Dev 3.294
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 57.27
Std Dev 2.945
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 58.86
Std Dev 12.91
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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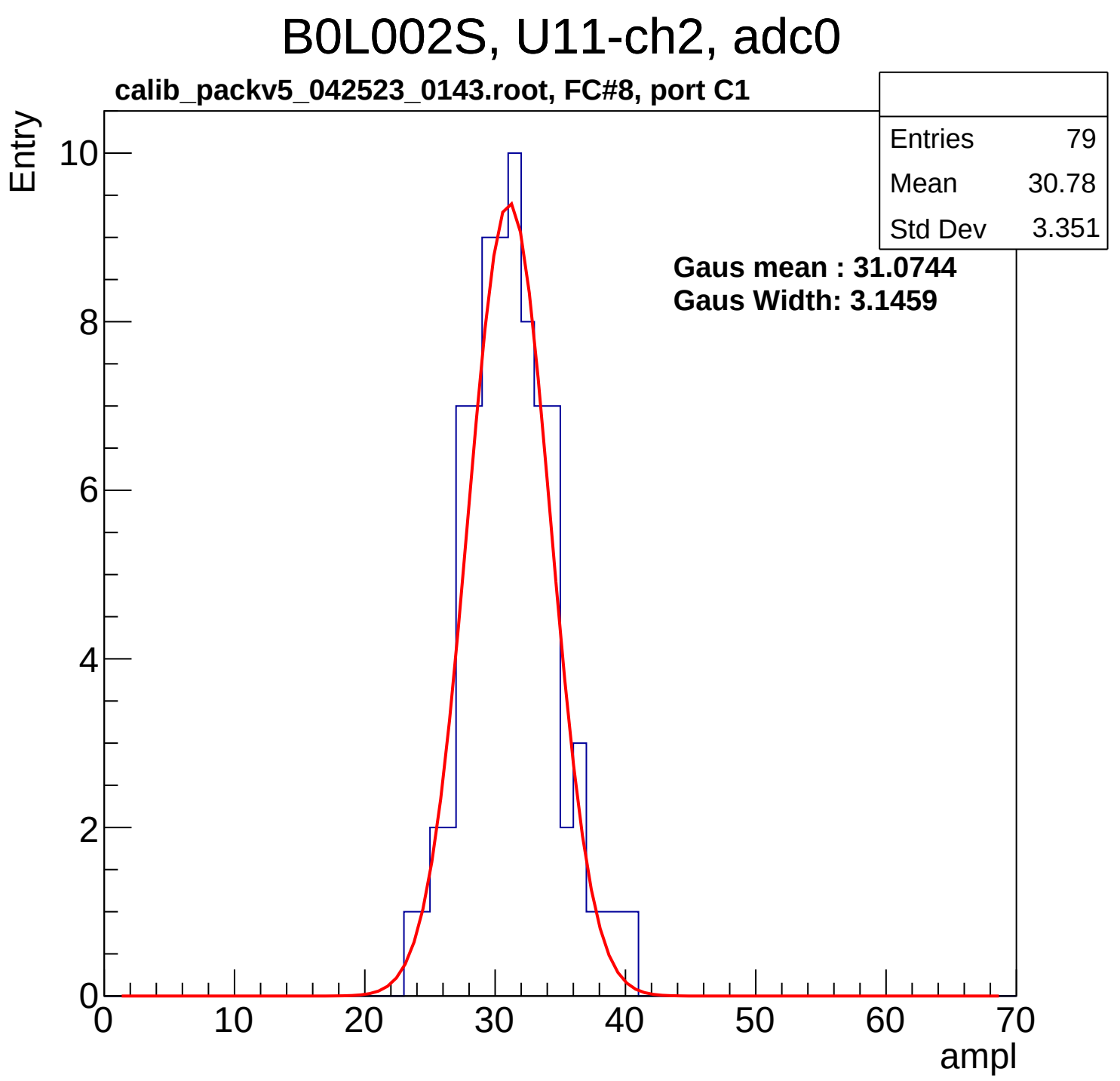
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.78
Std Dev 3.351
Gaus mean : 31.0744
Gaus Width: 3.1459
Entry
ampl
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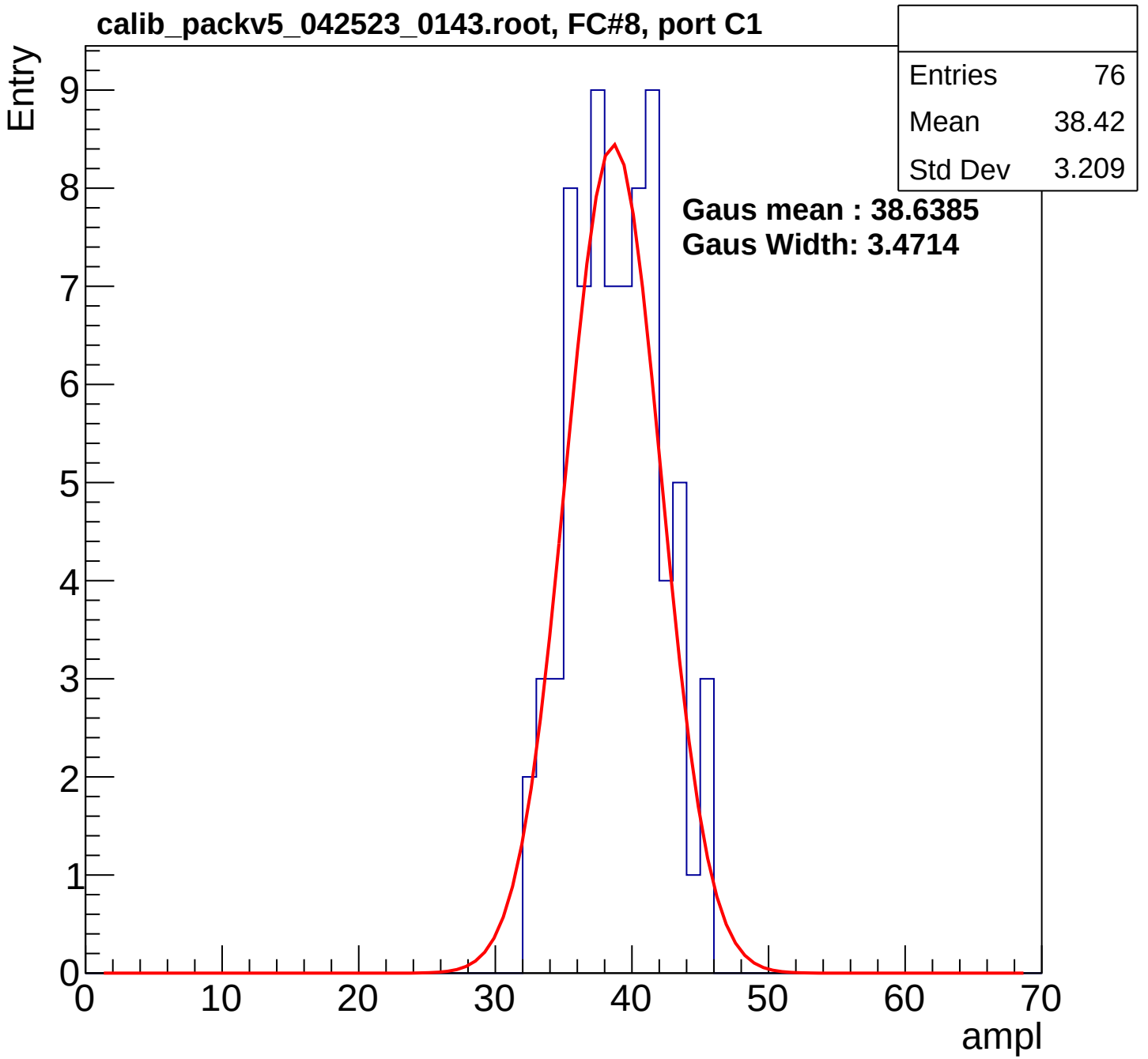
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 38.42
Std Dev 3.209
Gaus mean : 38.6385
Gaus Width: 3.4714
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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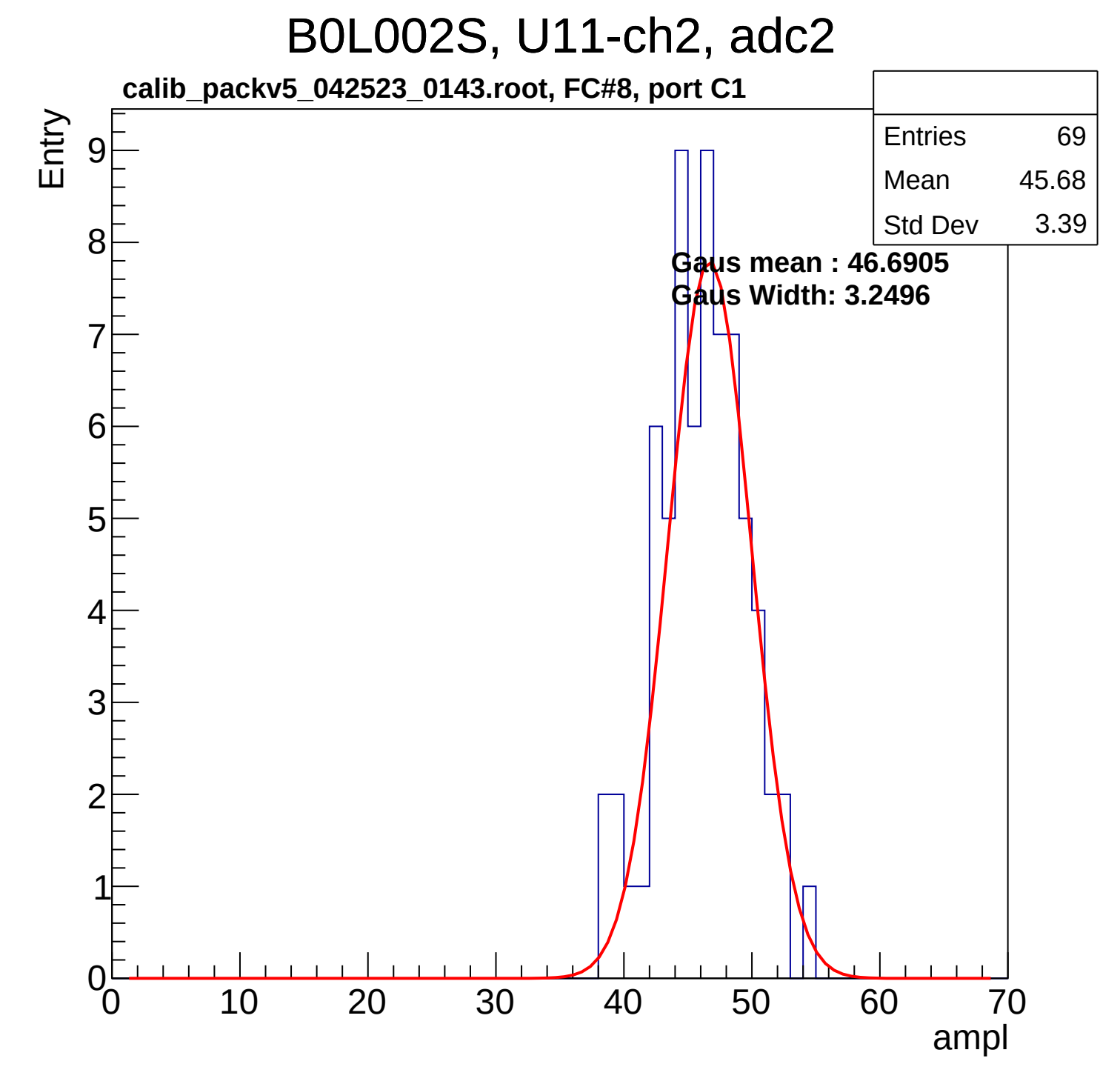
60
70


B0L002S, U11-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.68
Std Dev 3.39
Gaus mean : 46.6905
Gaus Width: 3.2496
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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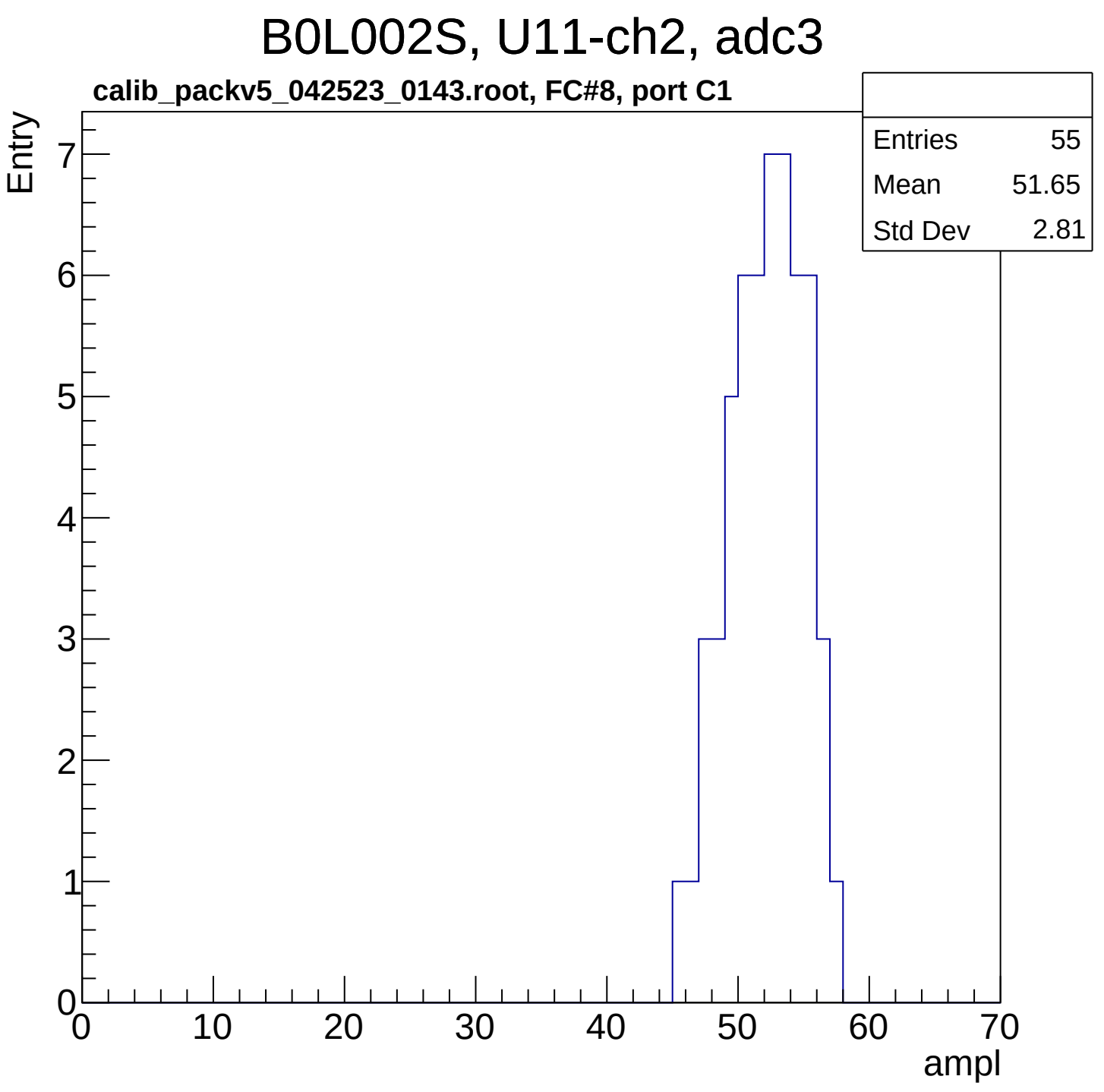
B0L002S, U11-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 51.65
Std Dev 2.81
Entry
ampl
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2
3
4
5
6
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0
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20
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40
50
60
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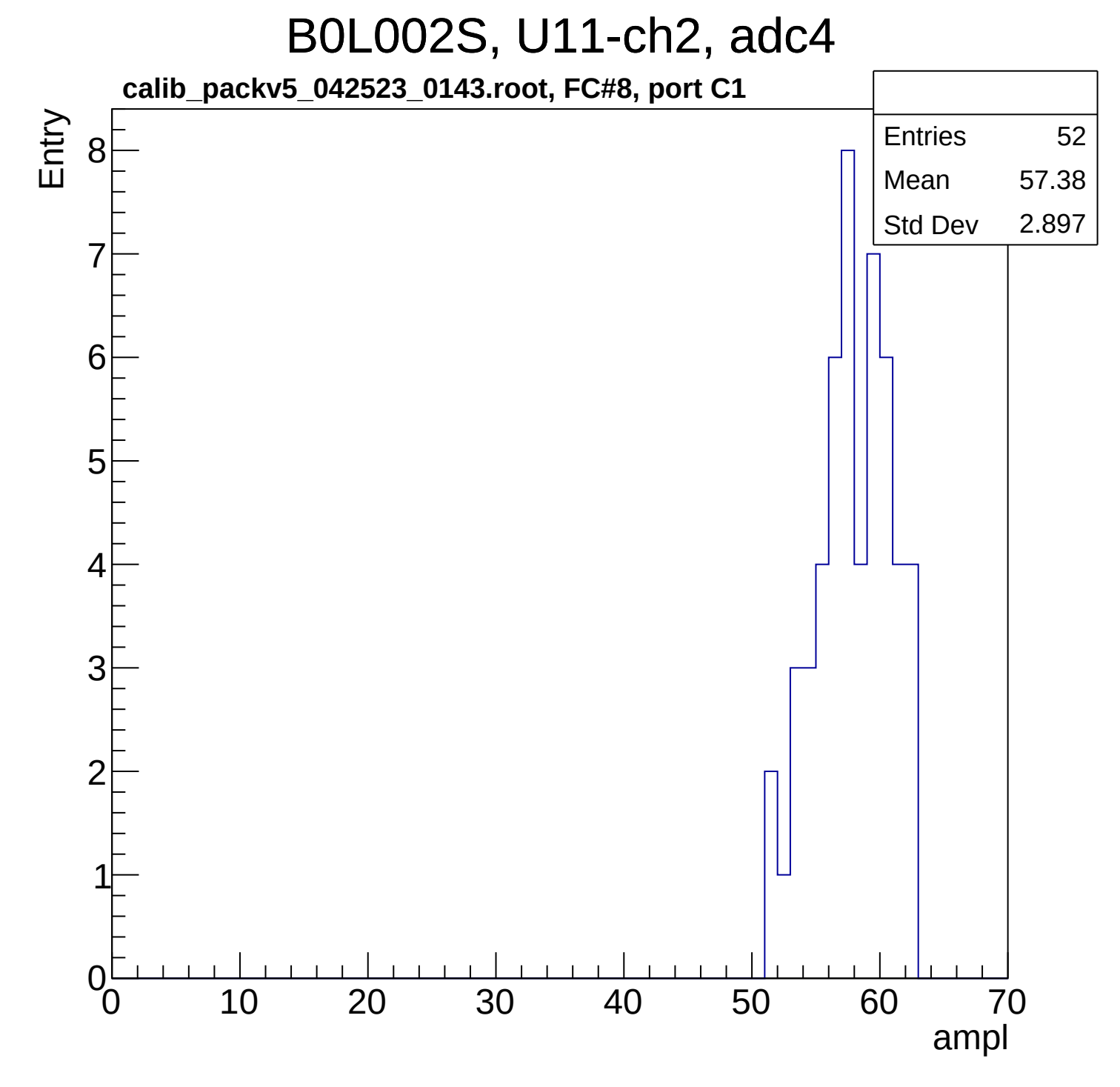
B0L002S, U11-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.38
Std Dev 2.897
Entry
ampl
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2
3
4
5
6
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0
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60
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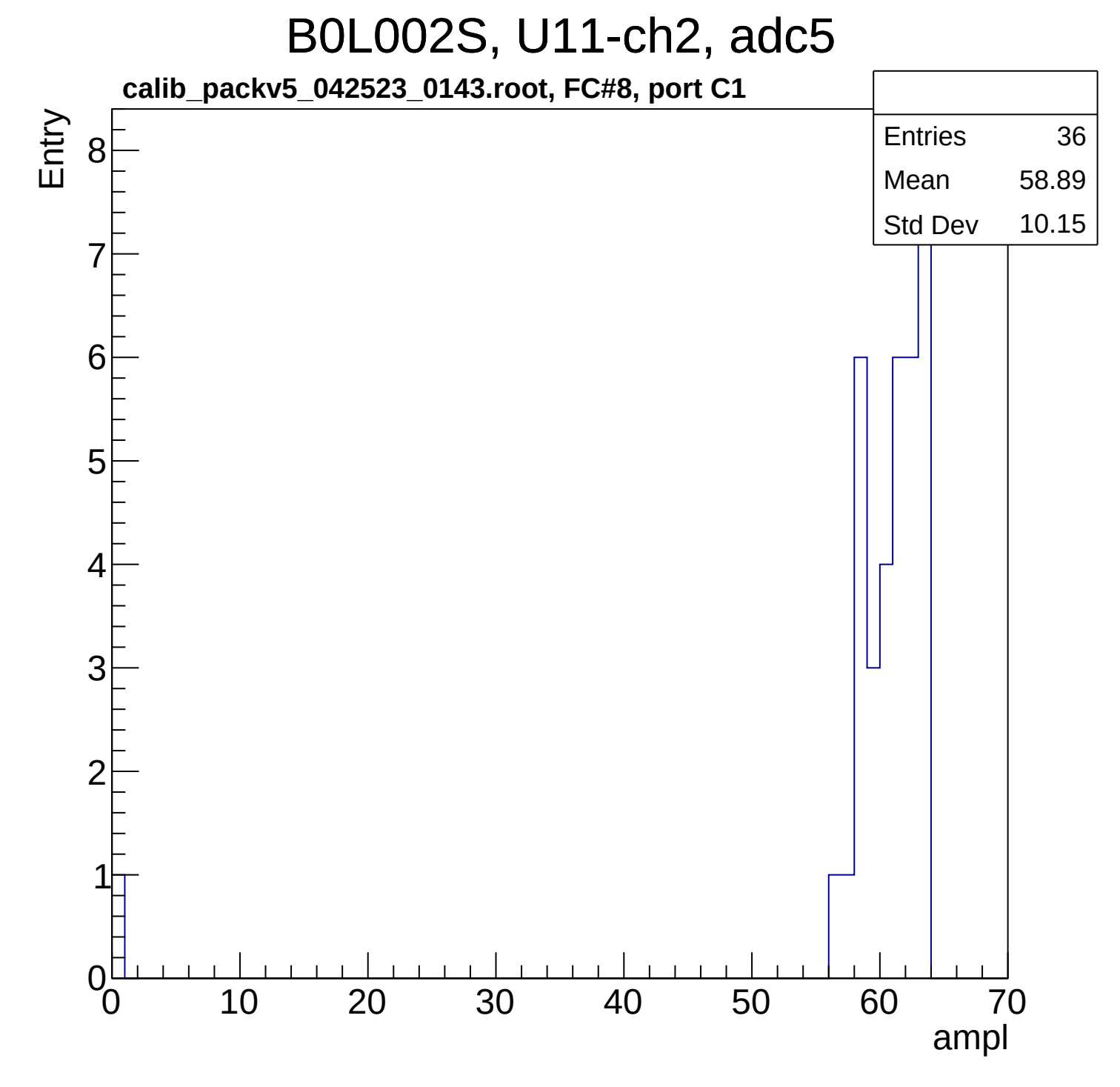
B0L002S, U11-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.89
Std Dev 10.15
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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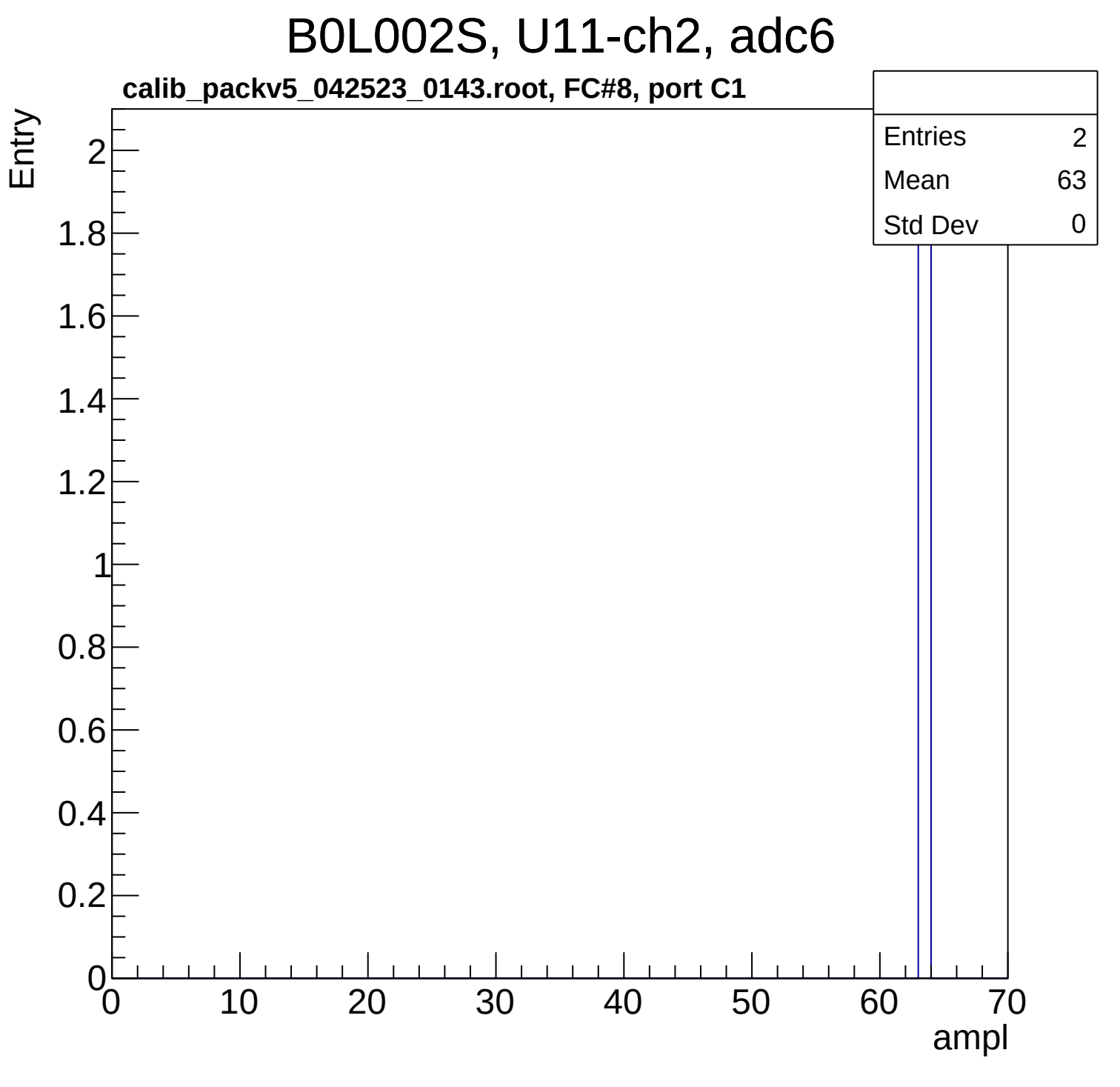
B0L002S, U11-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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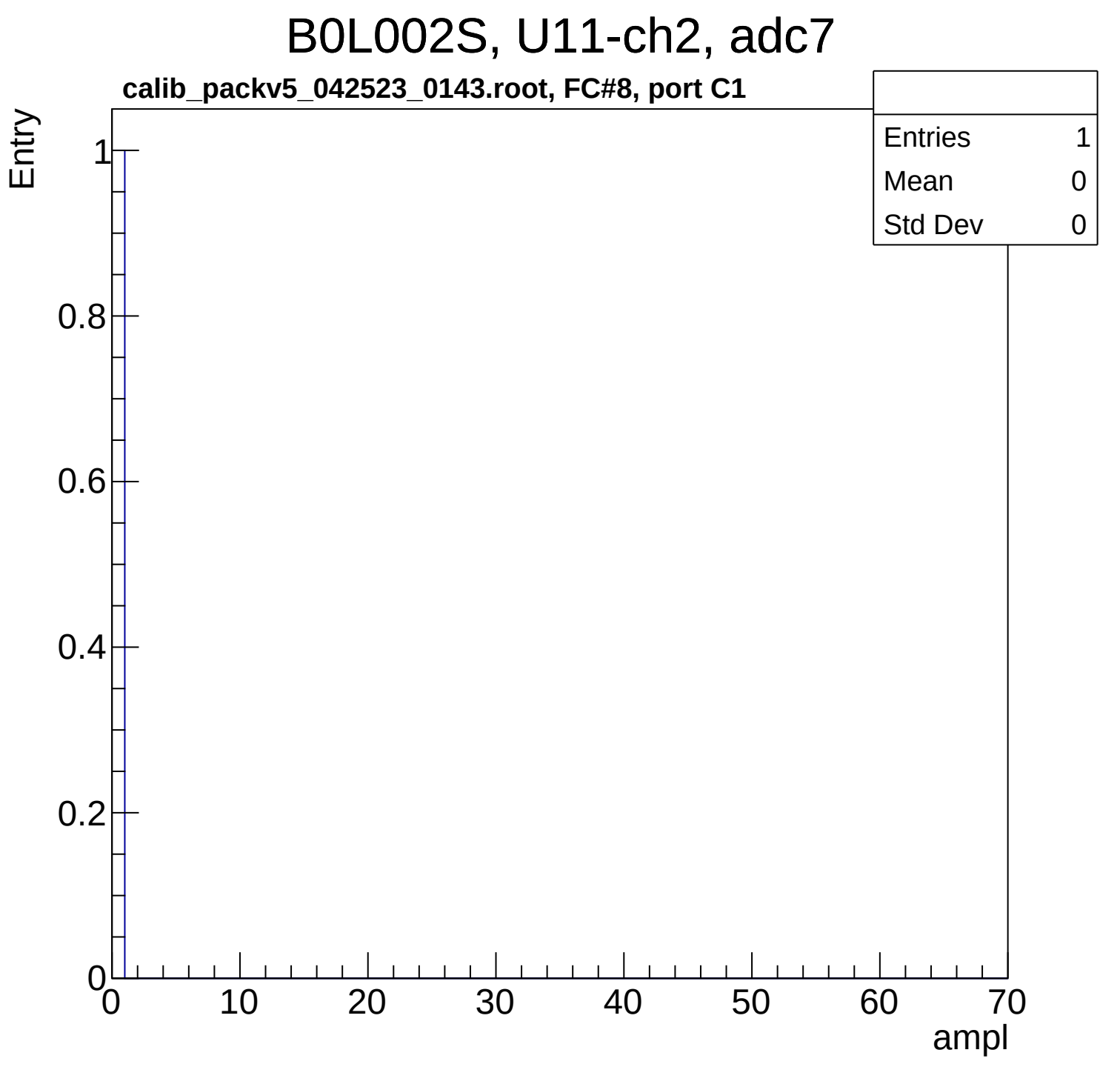
B0L002S, U11-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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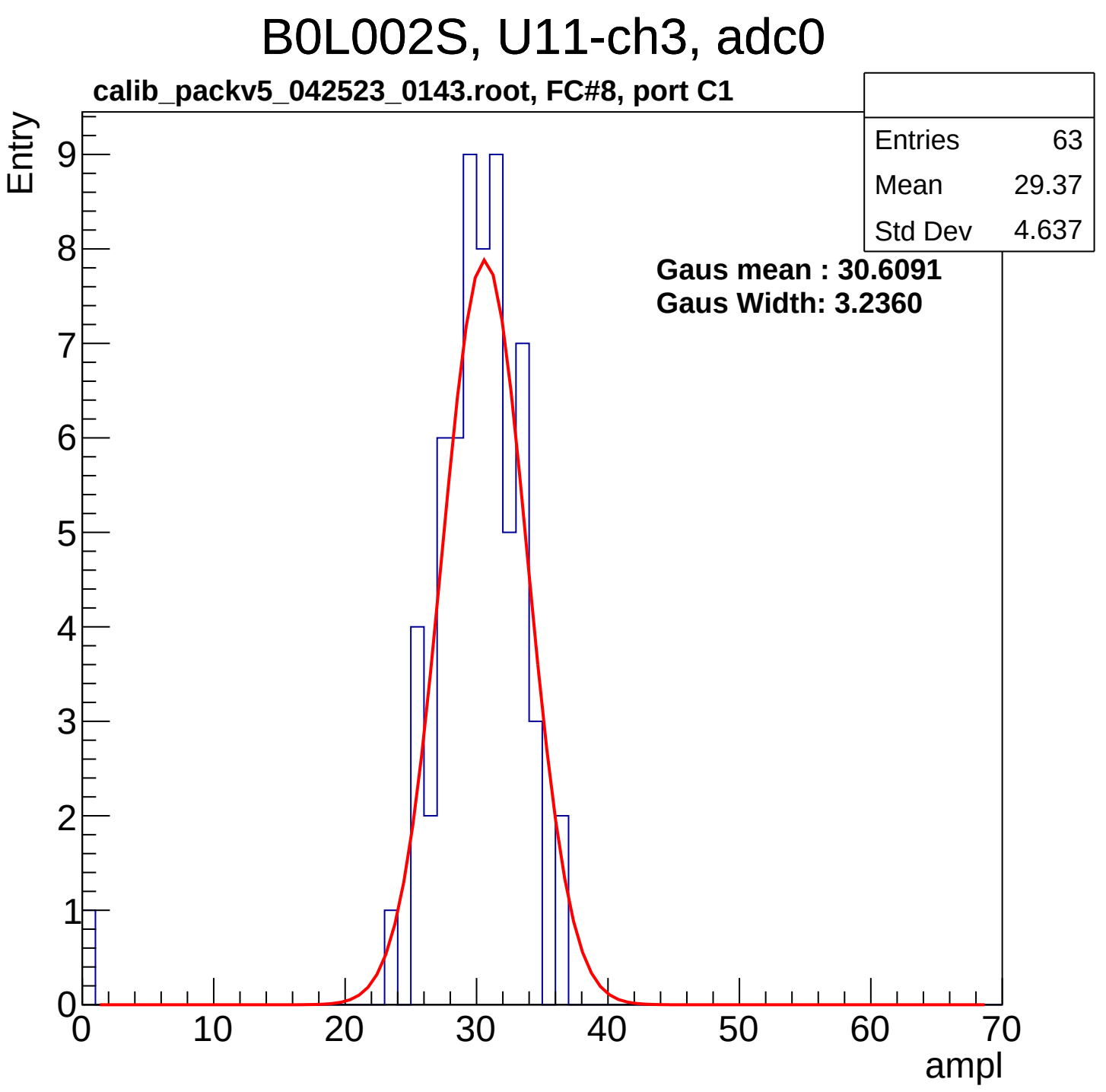
40
50
60
70
ampl


B0L002S, U11-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.37
Std Dev 4.637
Gaus mean : 30.6091
Gaus Width: 3.2360
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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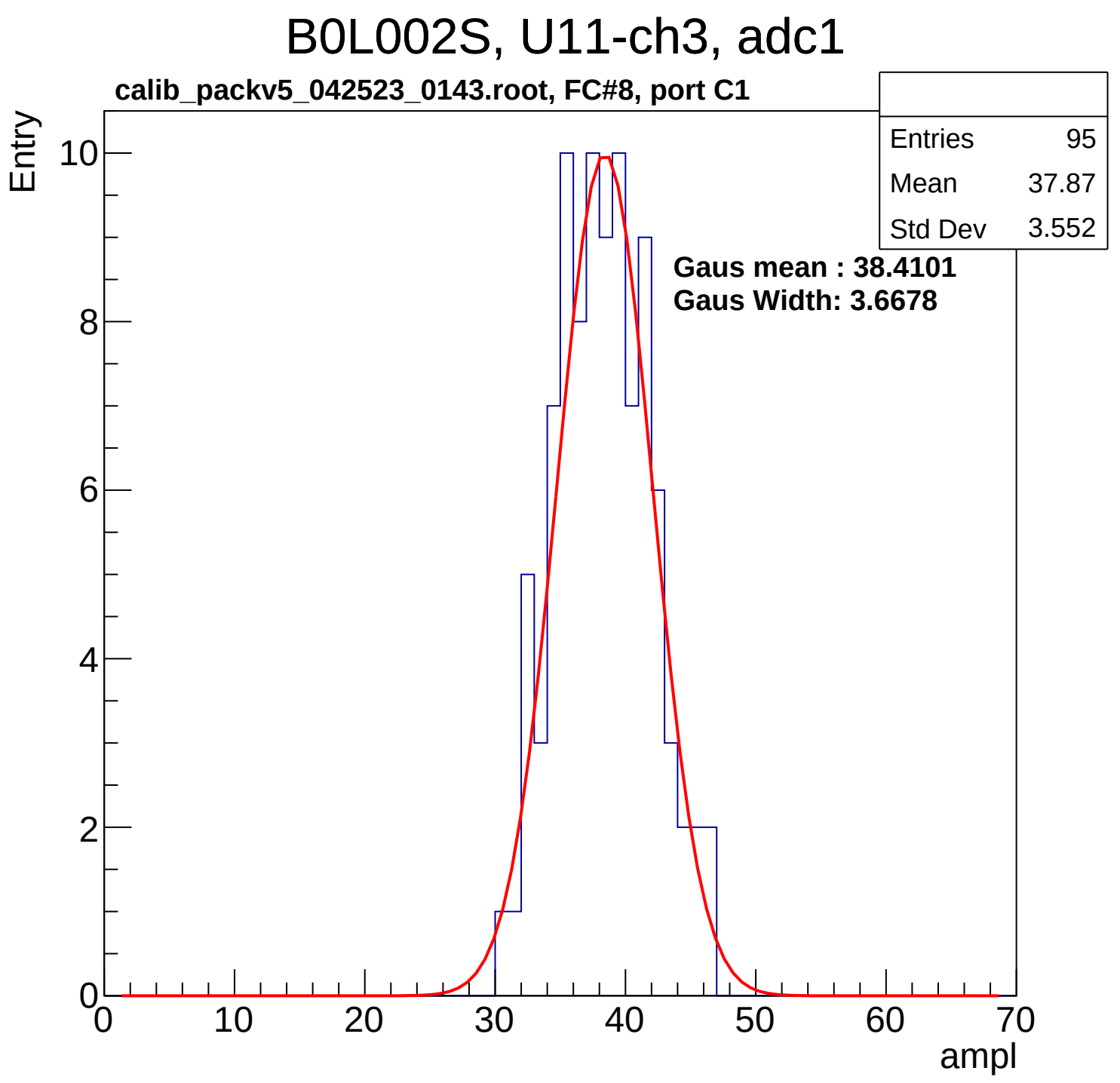
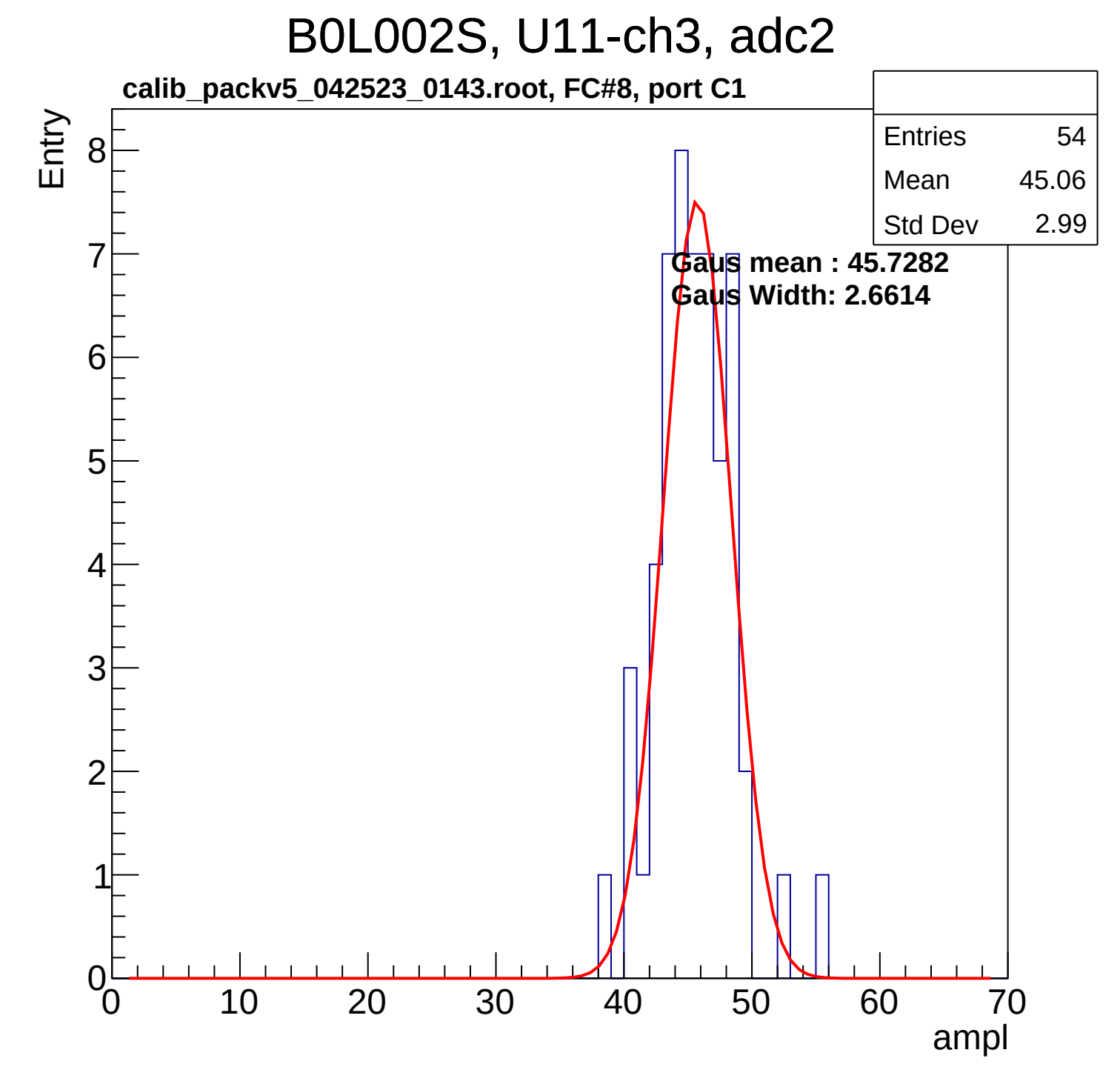

B0L002S, U11-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 95
Mean 37.87
Std Dev 3.552
Gaus mean : 38.4101
Gaus Width: 3.6678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 45.06
Std Dev 2.99
Gaus mean : 45.7282
Gaus Width: 2.6614
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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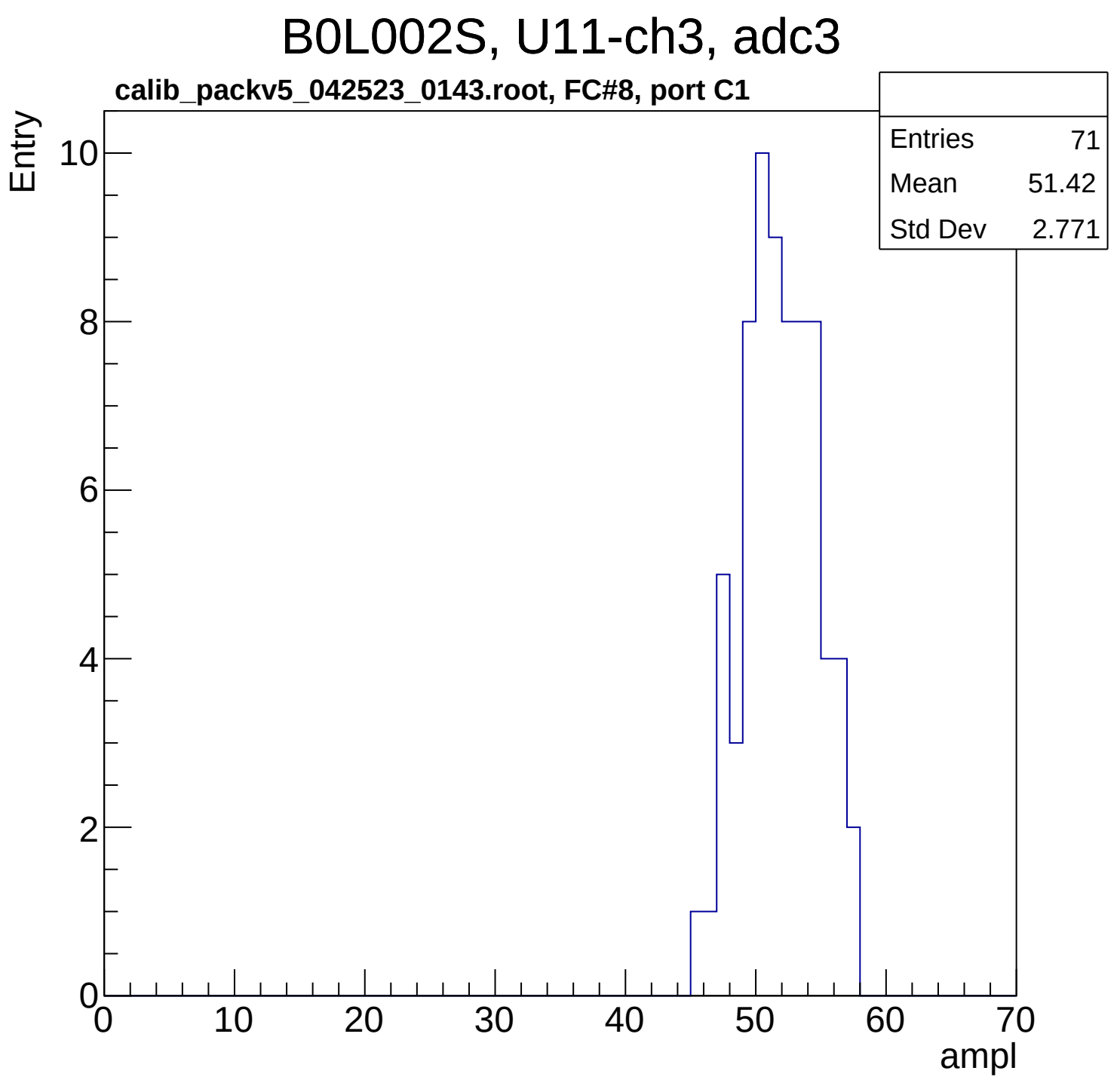
B0L002S, U11-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.42
Std Dev 2.771
Entry
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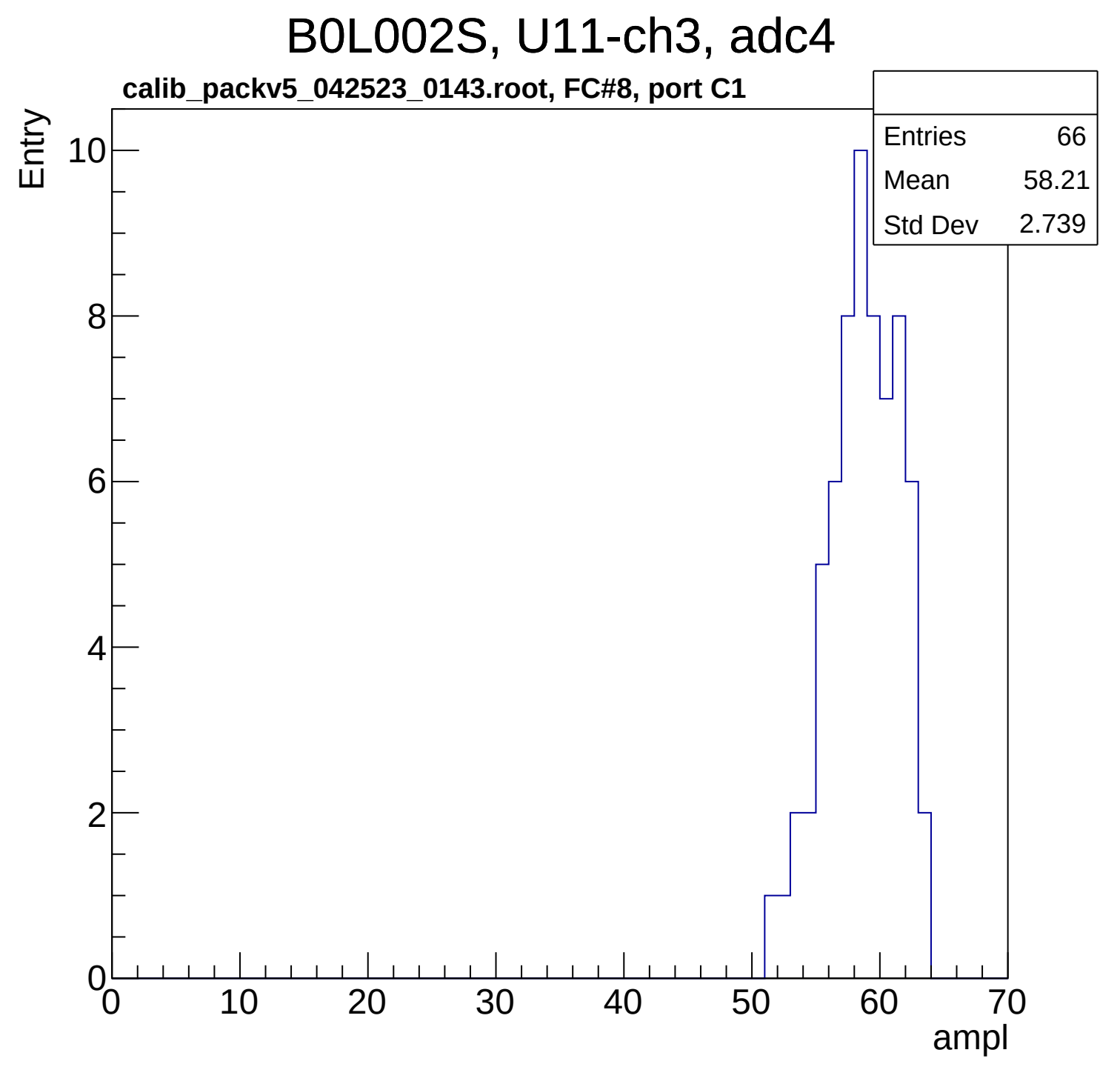
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 58.21
Std Dev 2.739
Entry
ampl
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4
6
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40
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60
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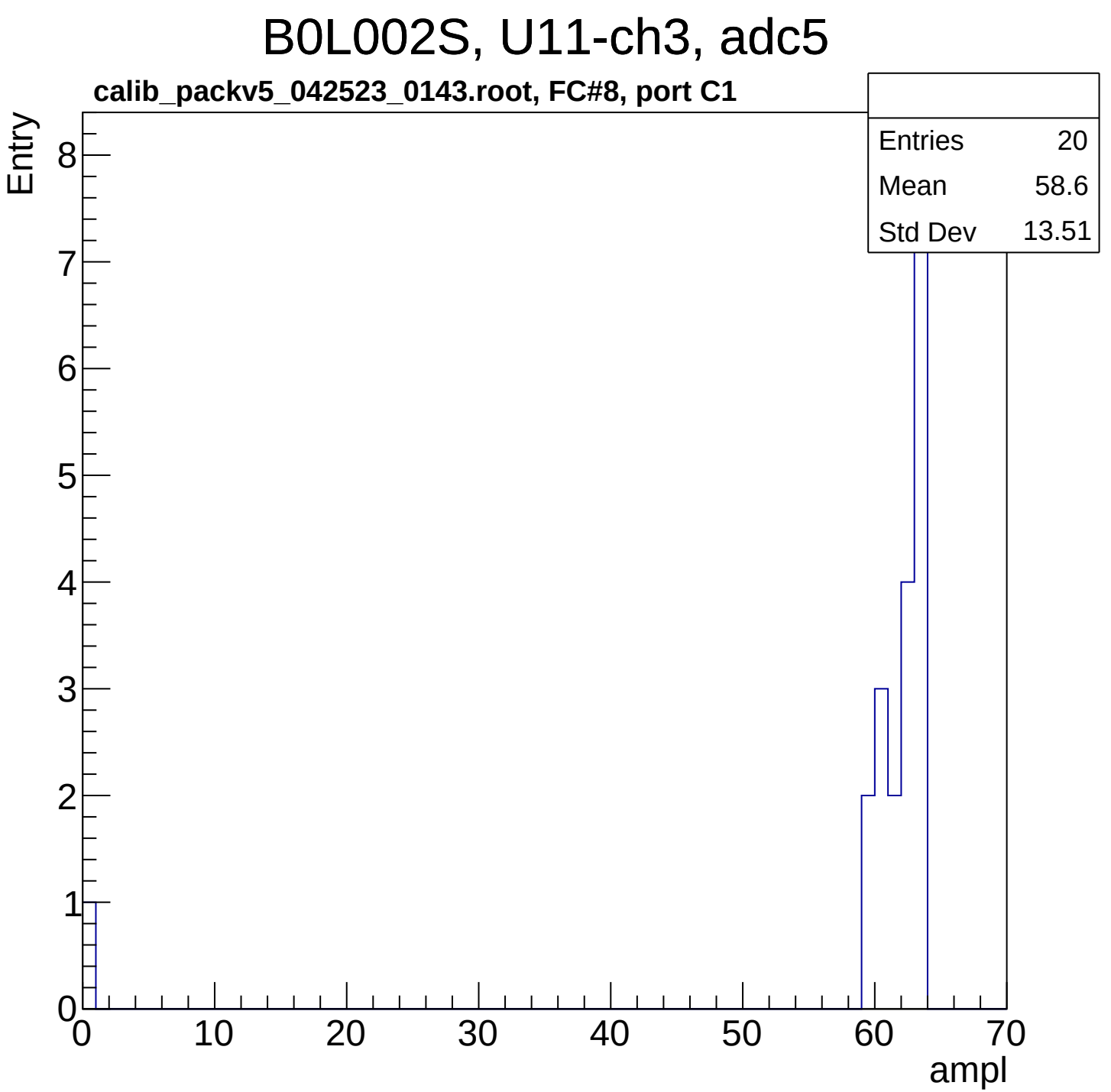
B0L002S, U11-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 58.6
Std Dev 13.51
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
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60
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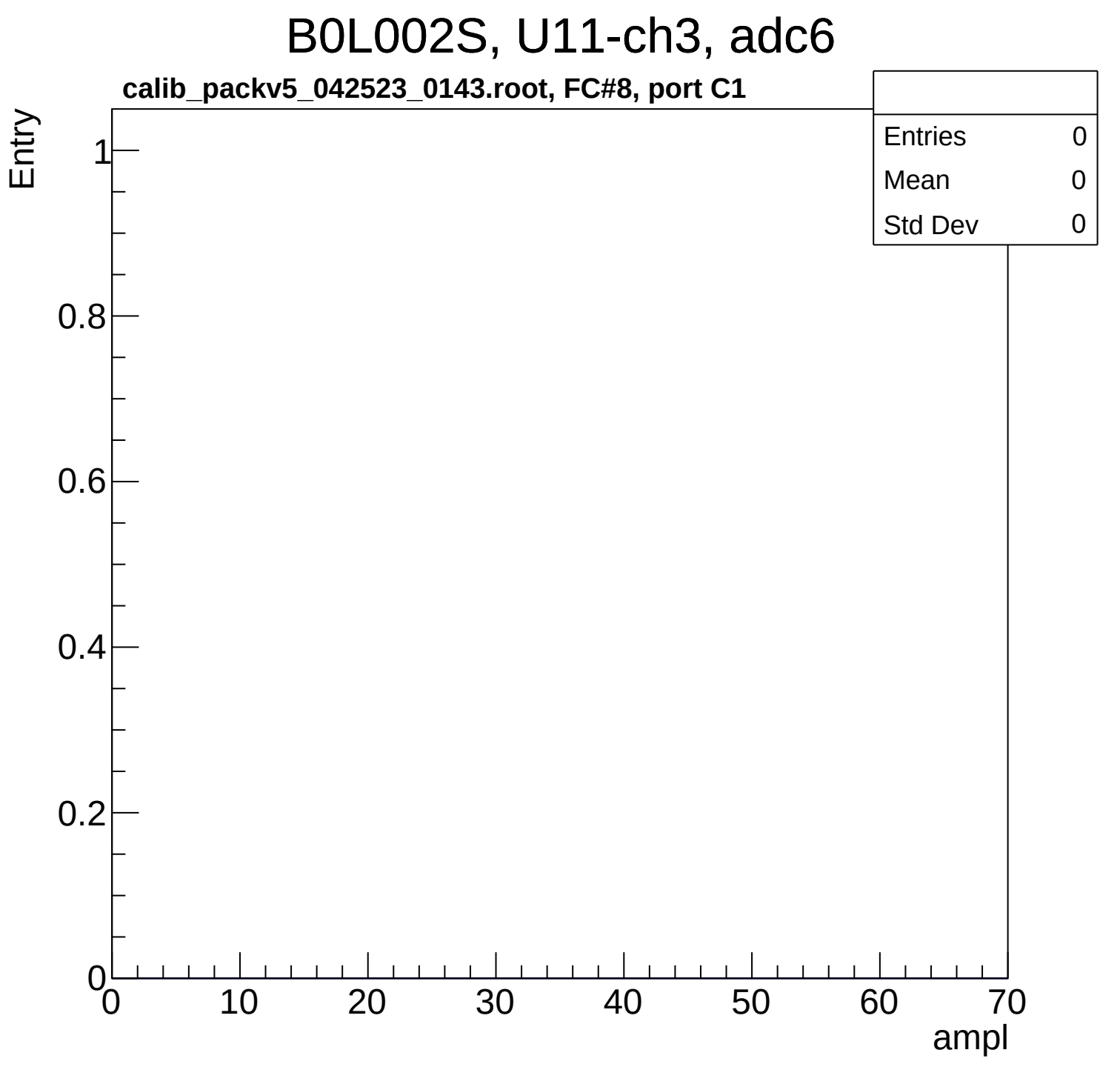
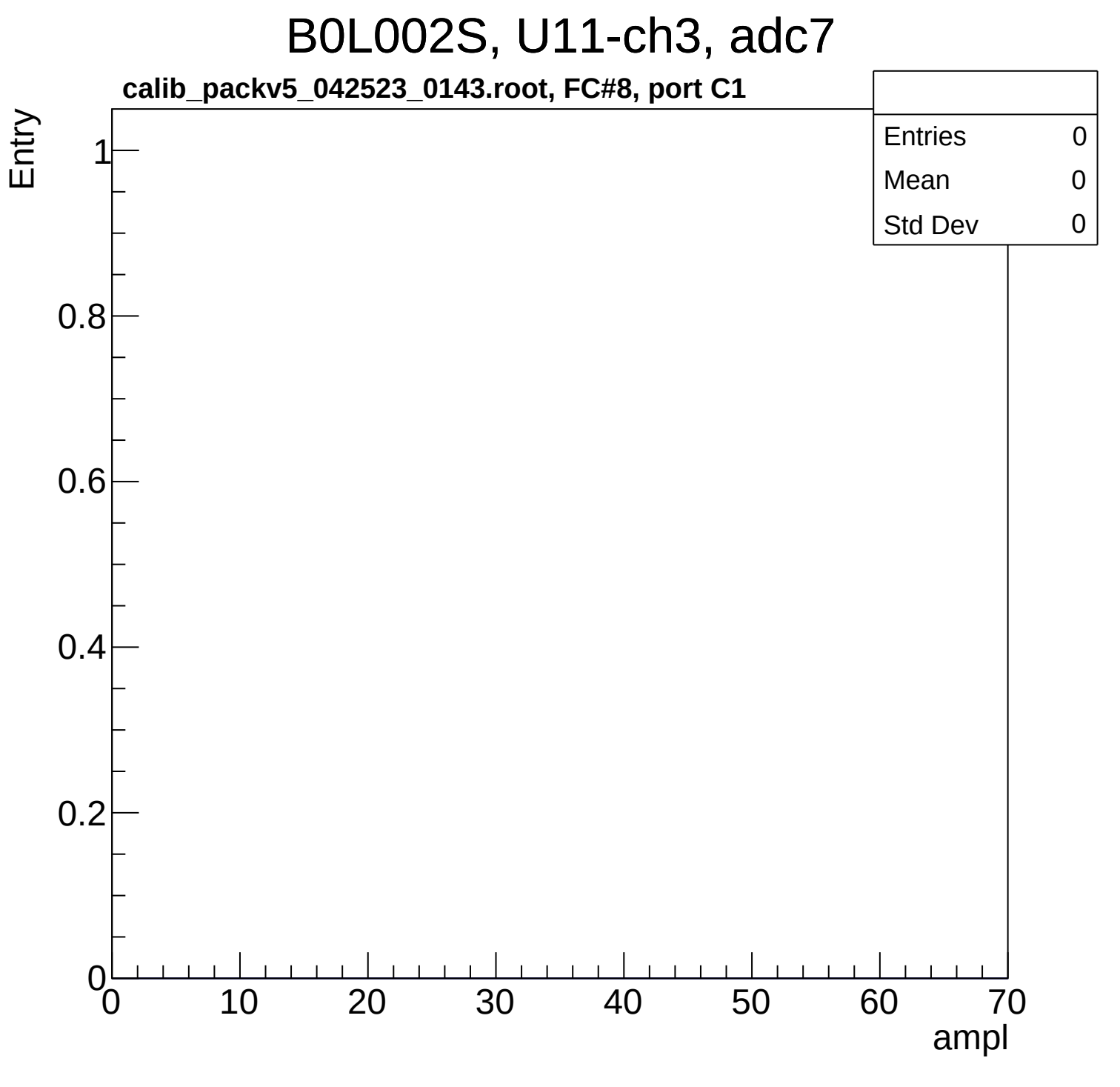

B0L002S, U11-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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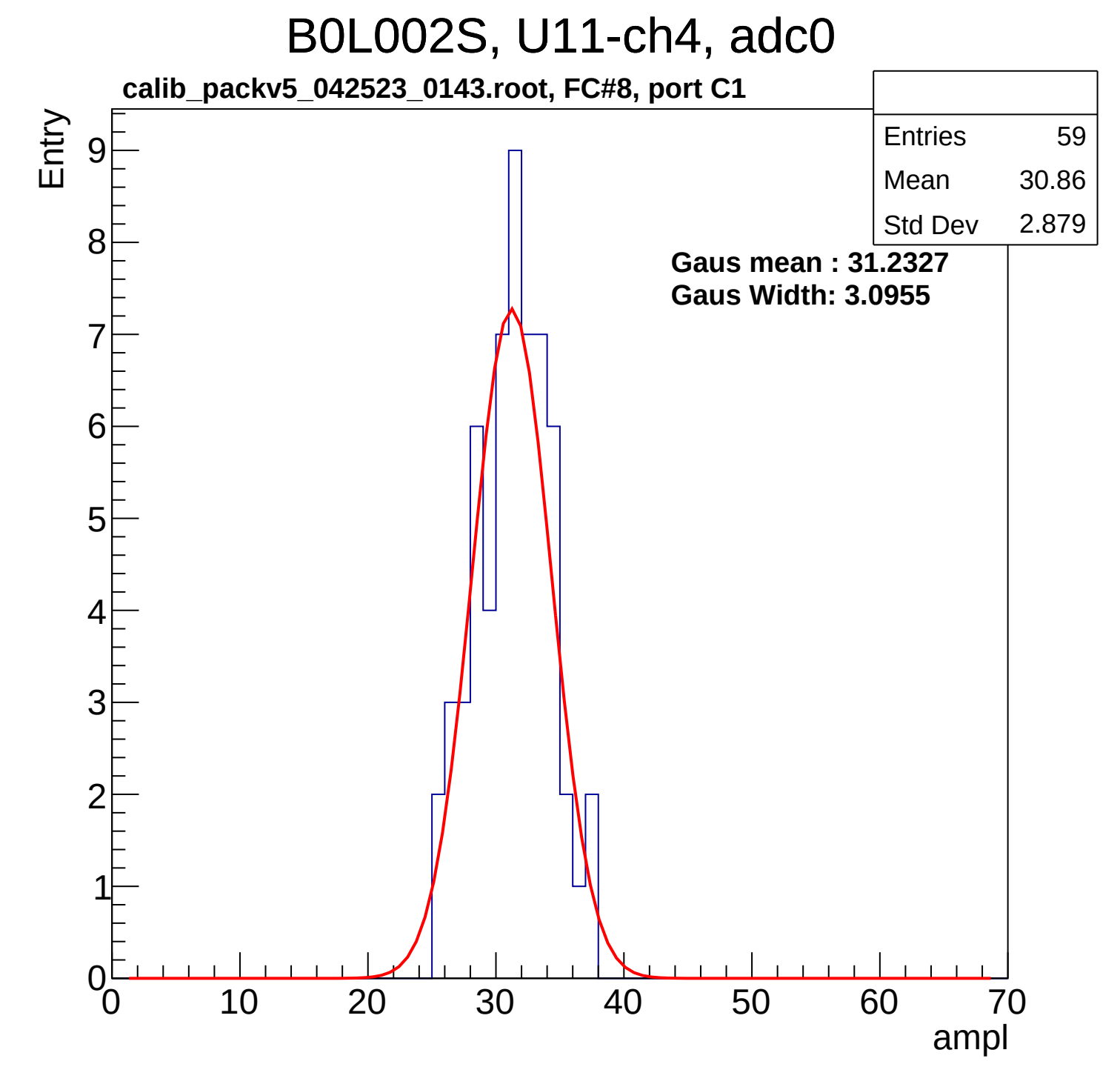
ampl


B0L002S, U11-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.86
Std Dev 2.879
Gaus mean : 31.2327
Gaus Width: 3.0955
Entry
ampl
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3
4
5
6
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8
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0
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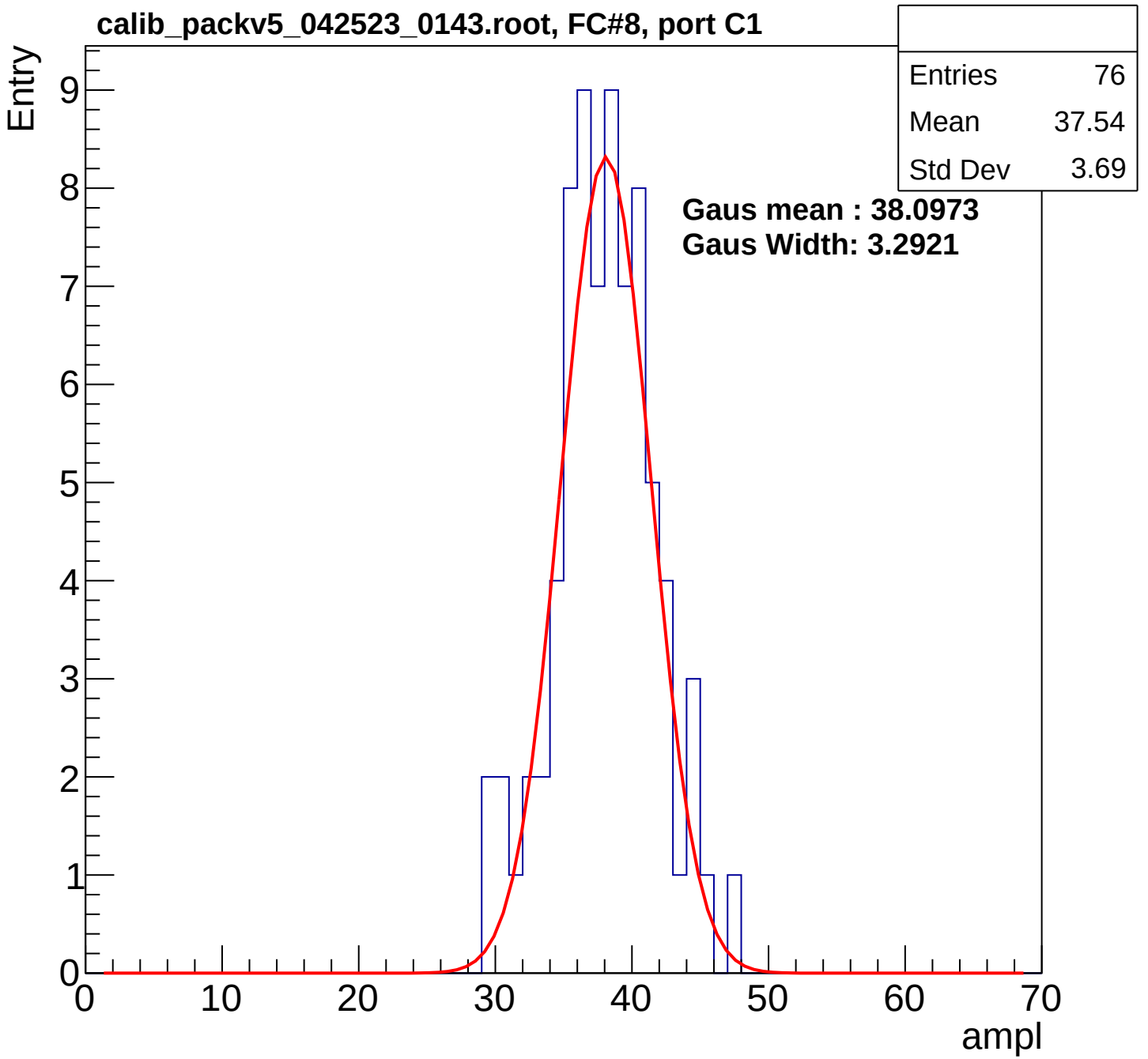
B0L002S, U11-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.54
Std Dev 3.69
Gaus mean : 38.0973
Gaus Width: 3.2921
Entry
ampl
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2
3
4
5
6
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8
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0
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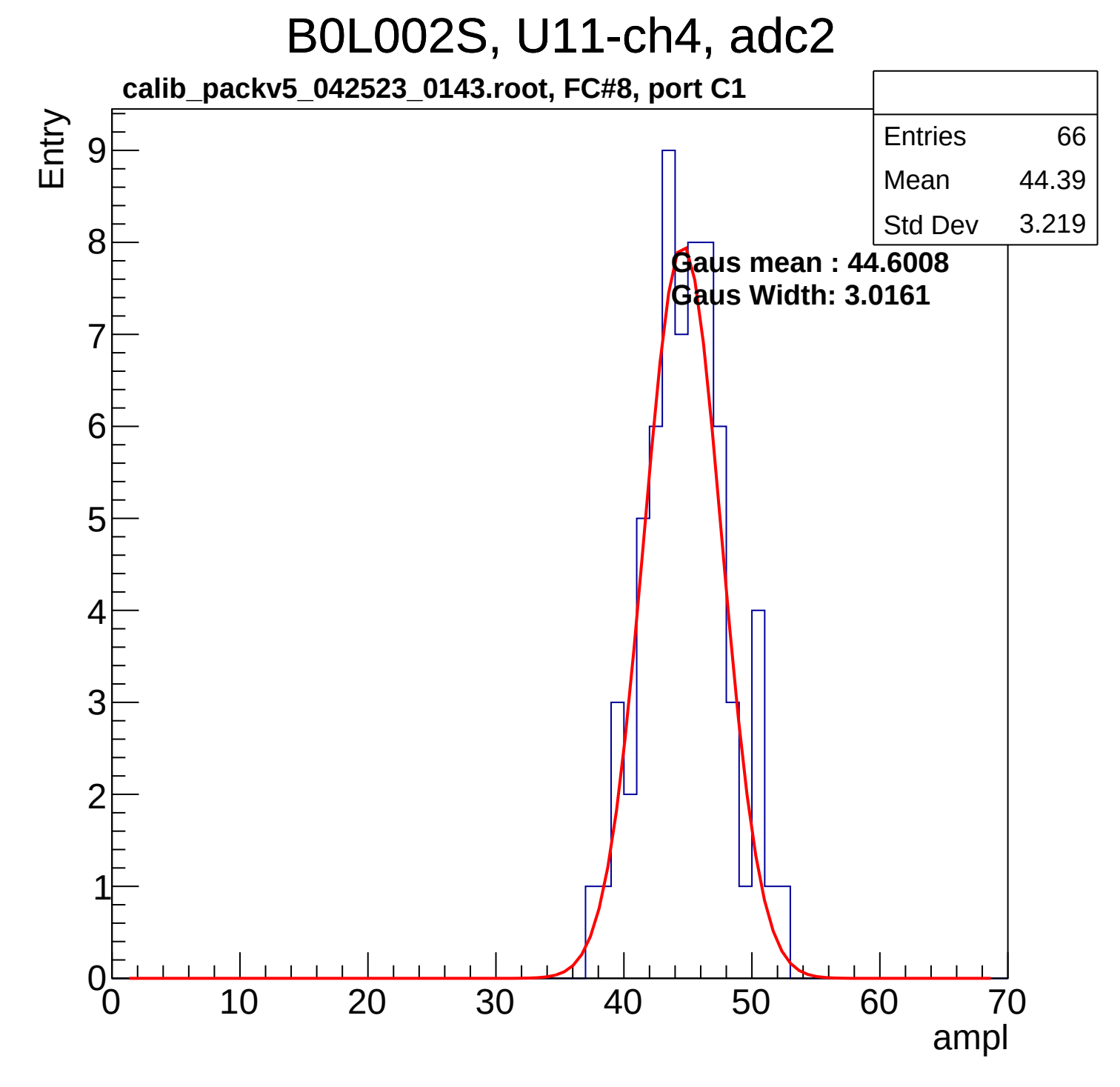
B0L002S, U11-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.39
Std Dev 3.219
Gaus mean : 44.6008
Gaus Width: 3.0161
Entry
ampl
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2
3
4
5
6
7
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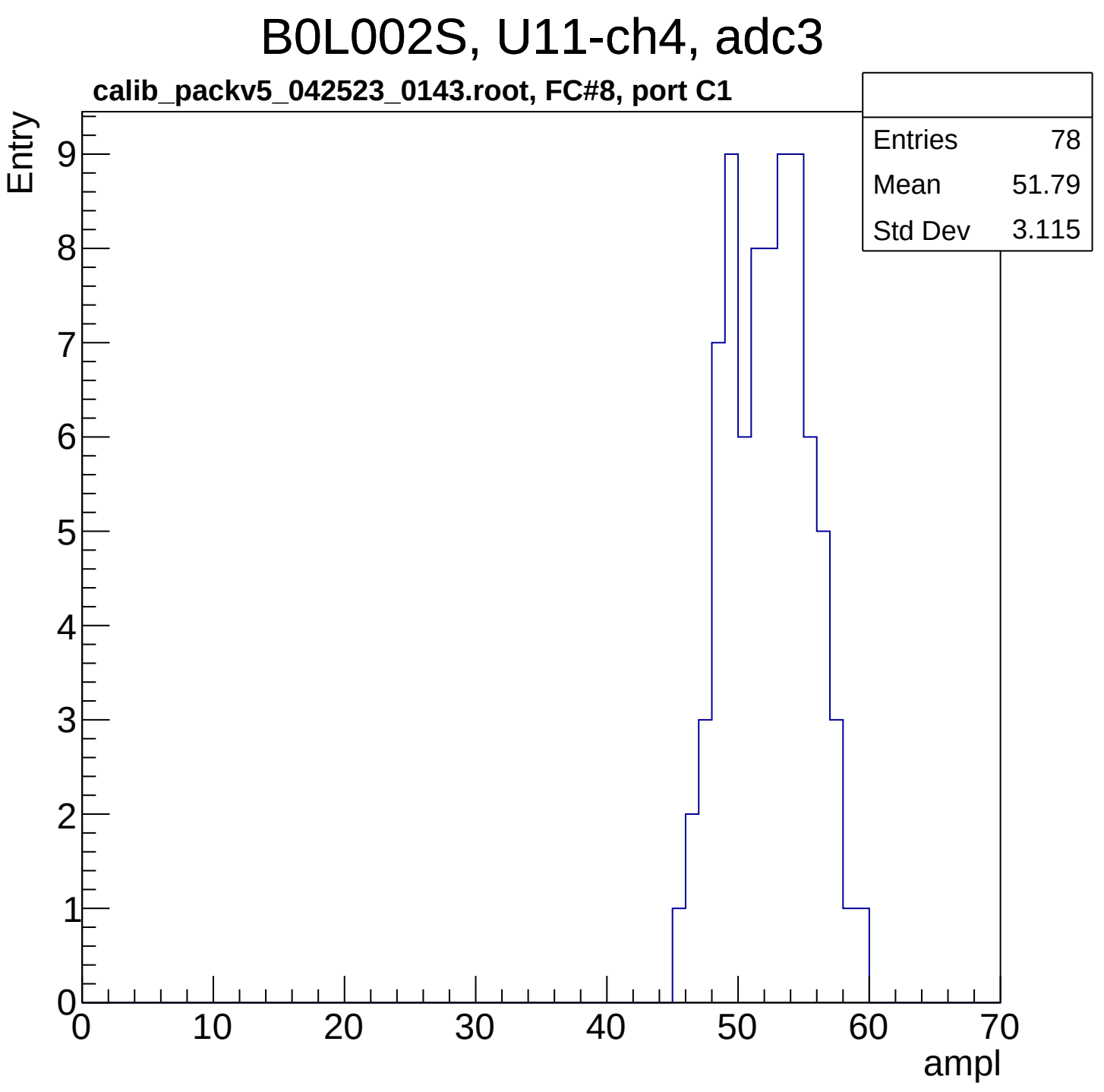
B0L002S, U11-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 51.79
Std Dev 3.115
Entry
ampl
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3
4
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6
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0
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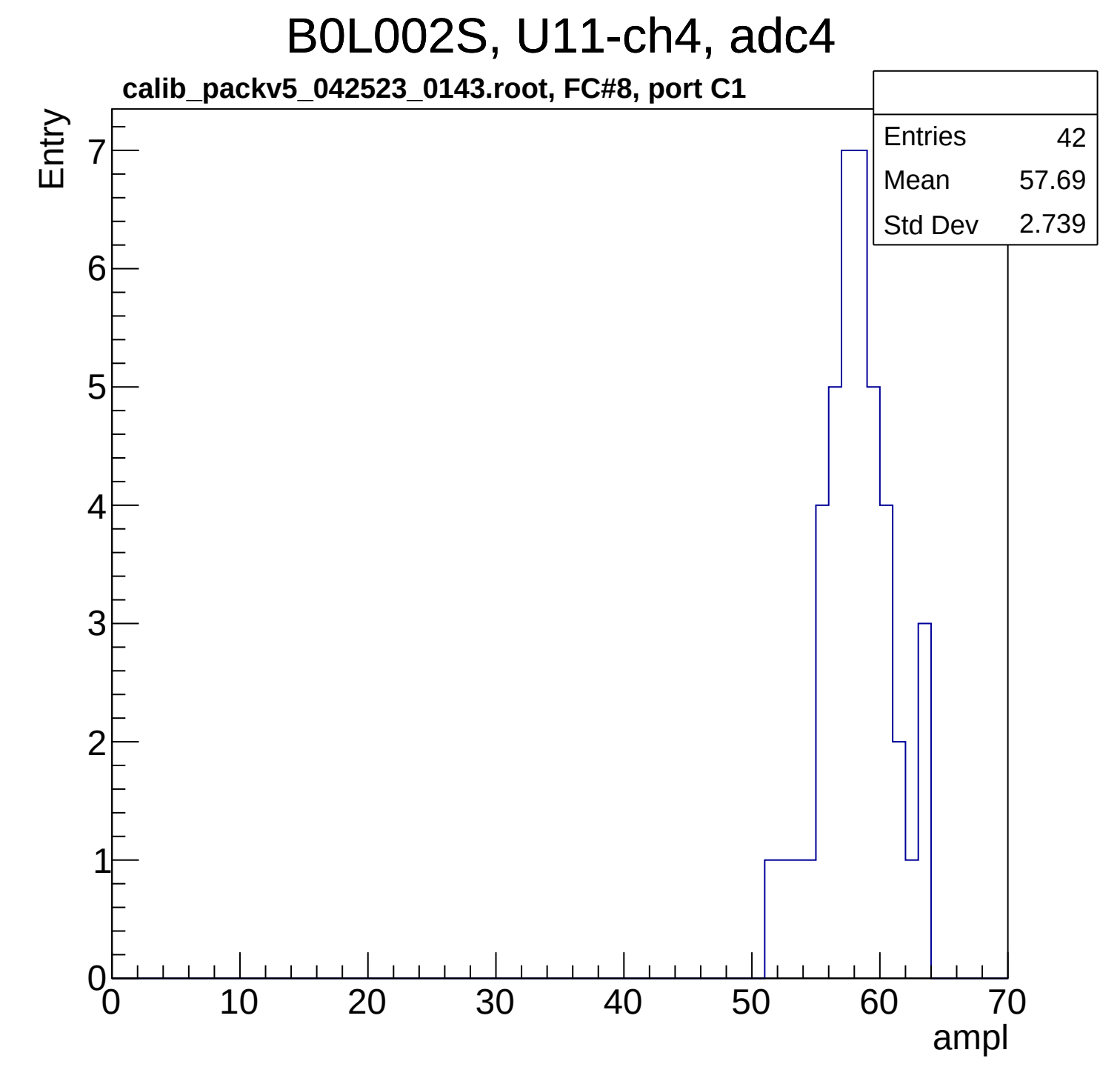
B0L002S, U11-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 57.69
Std Dev 2.739
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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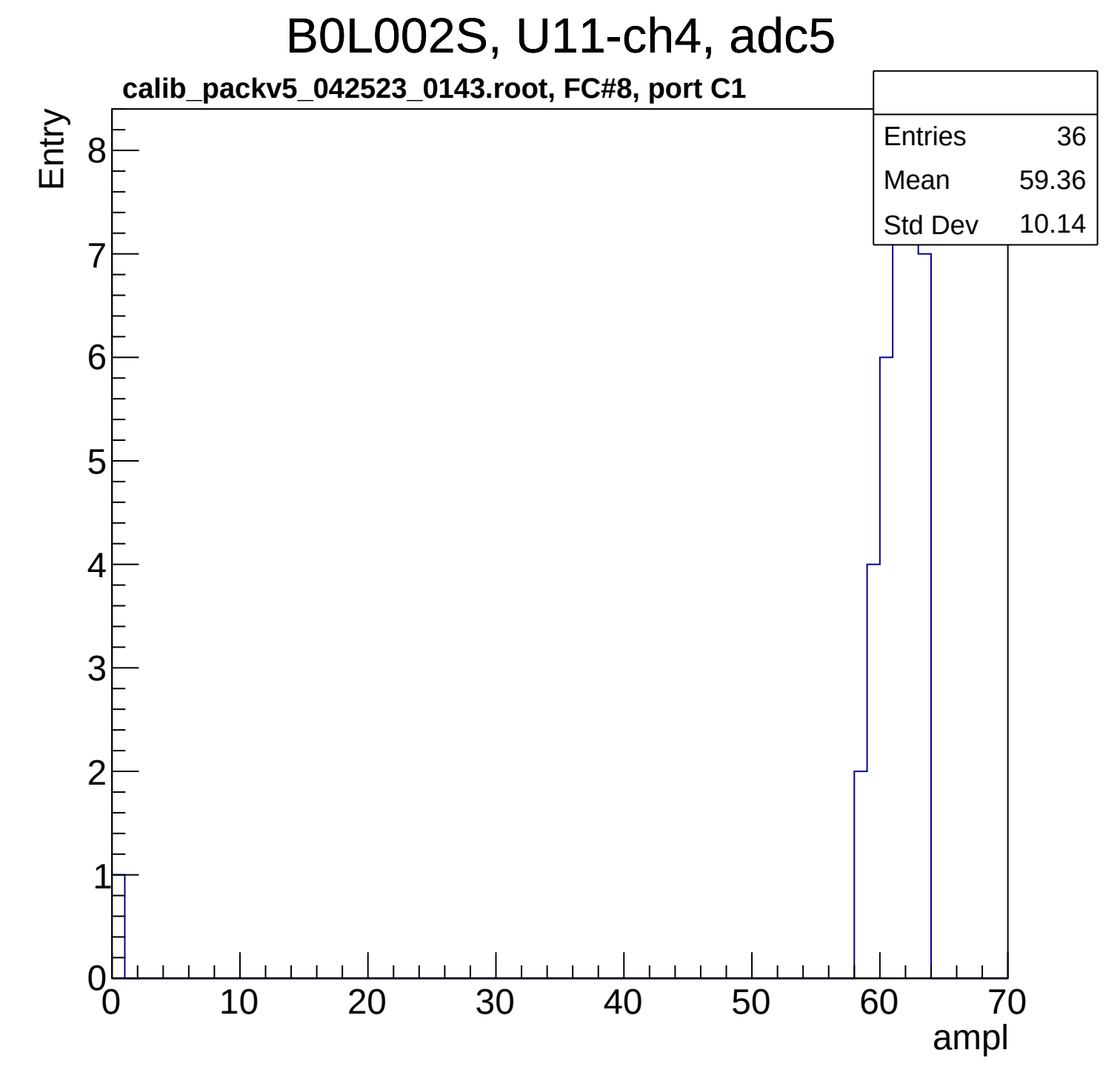
B0L002S, U11-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.36
Std Dev 10.14
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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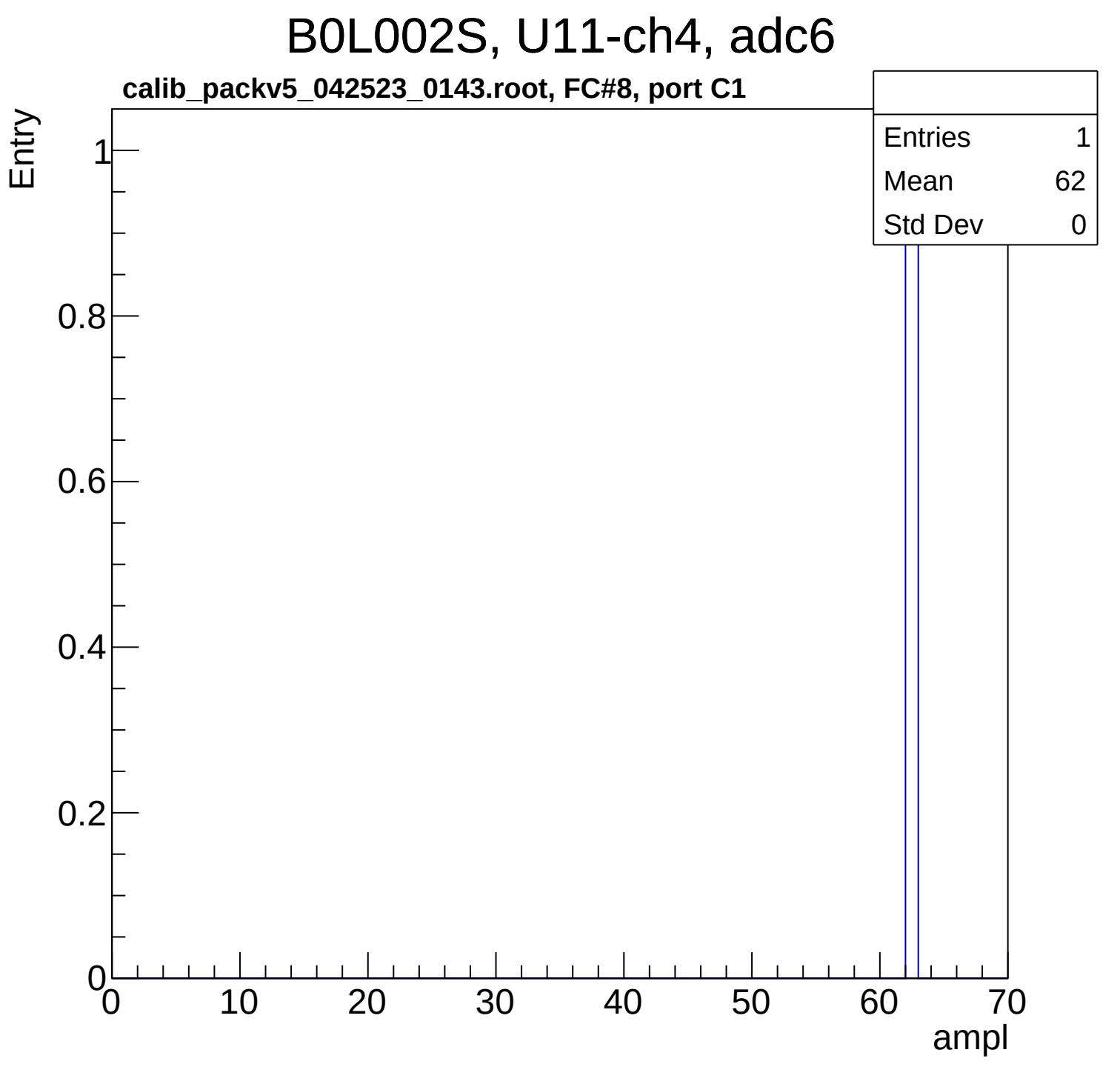
B0L002S, U11-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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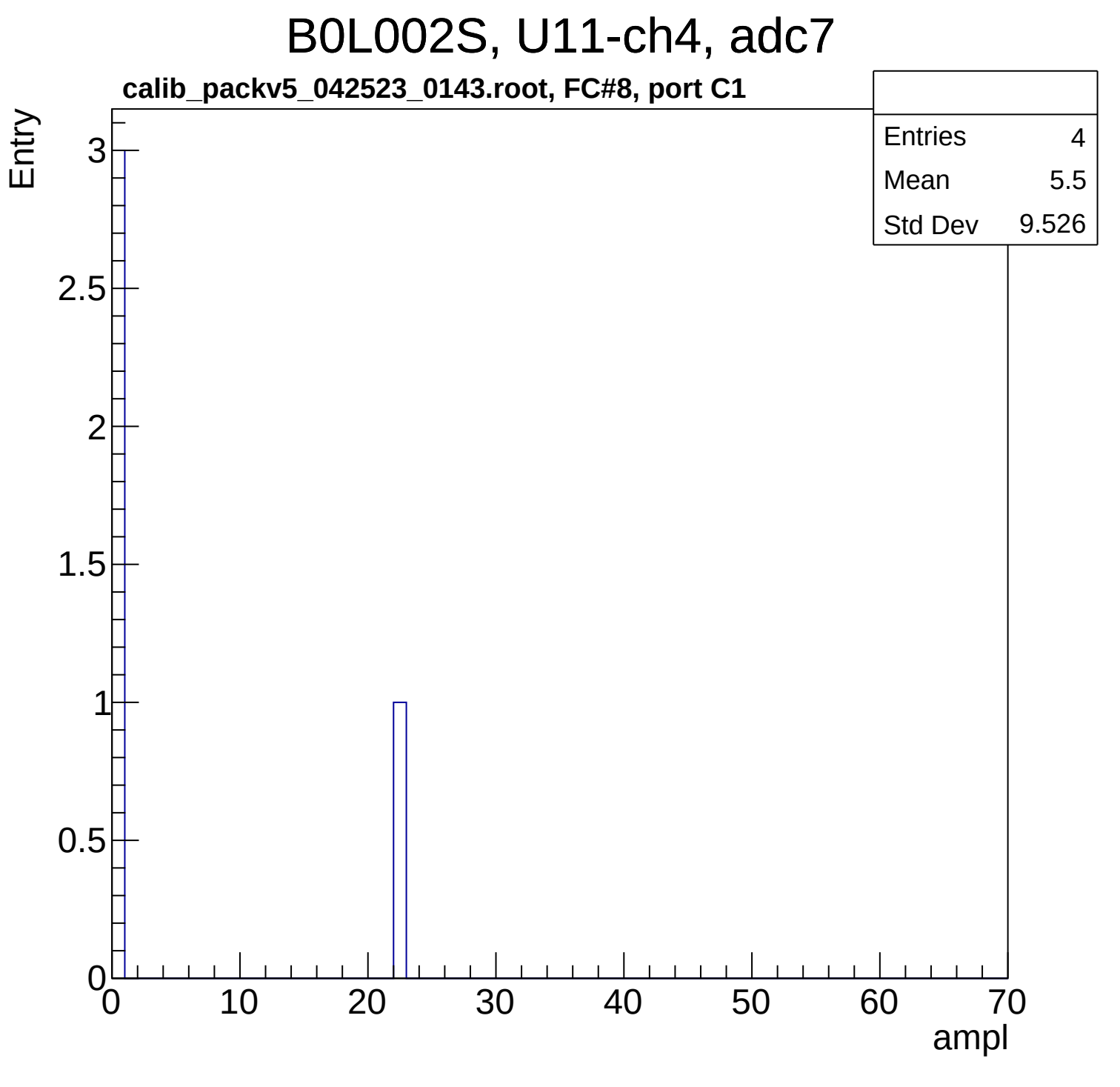
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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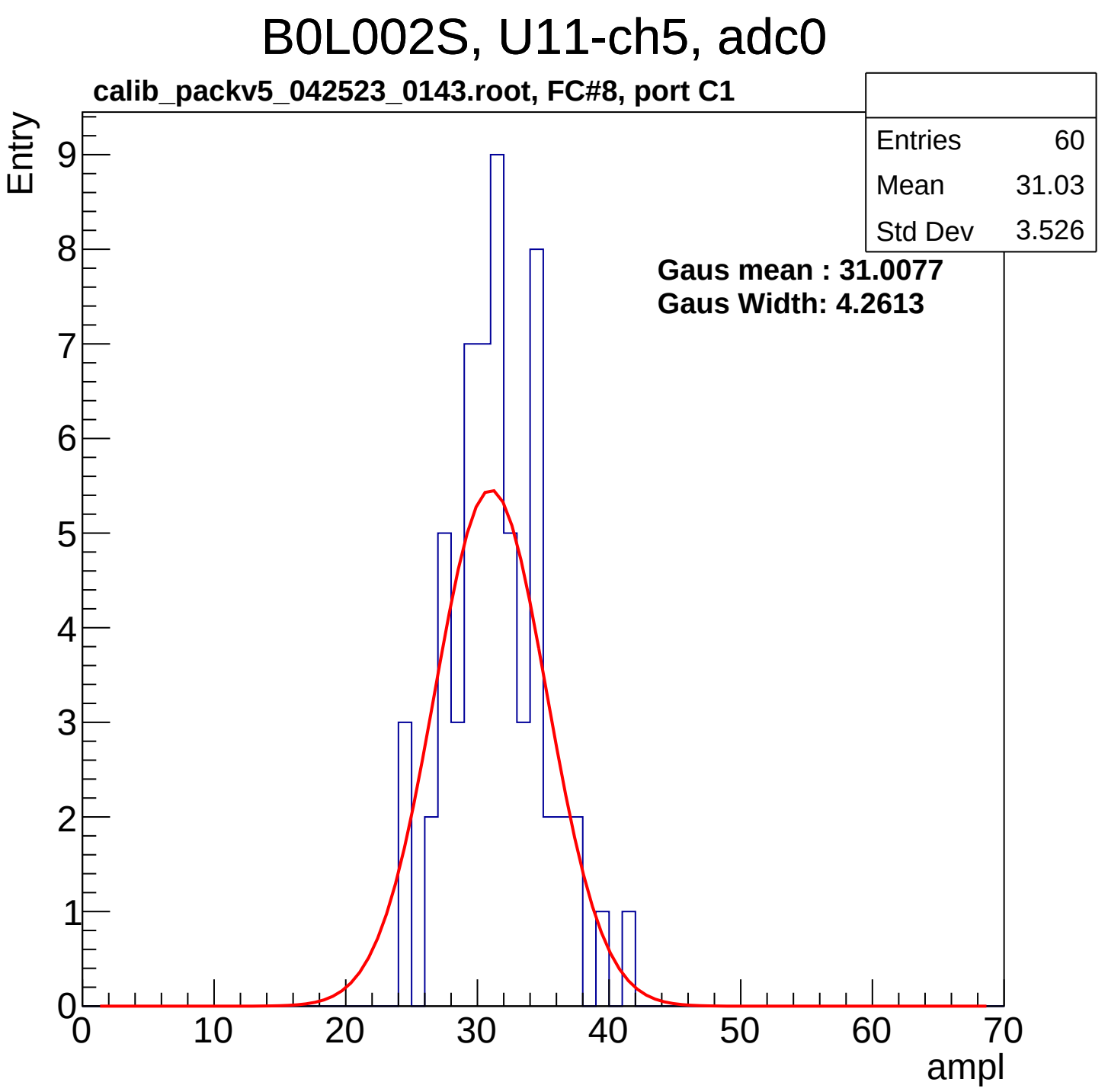
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U11-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.03
Std Dev 3.526
Gaus mean : 31.0077
Gaus Width: 4.2613
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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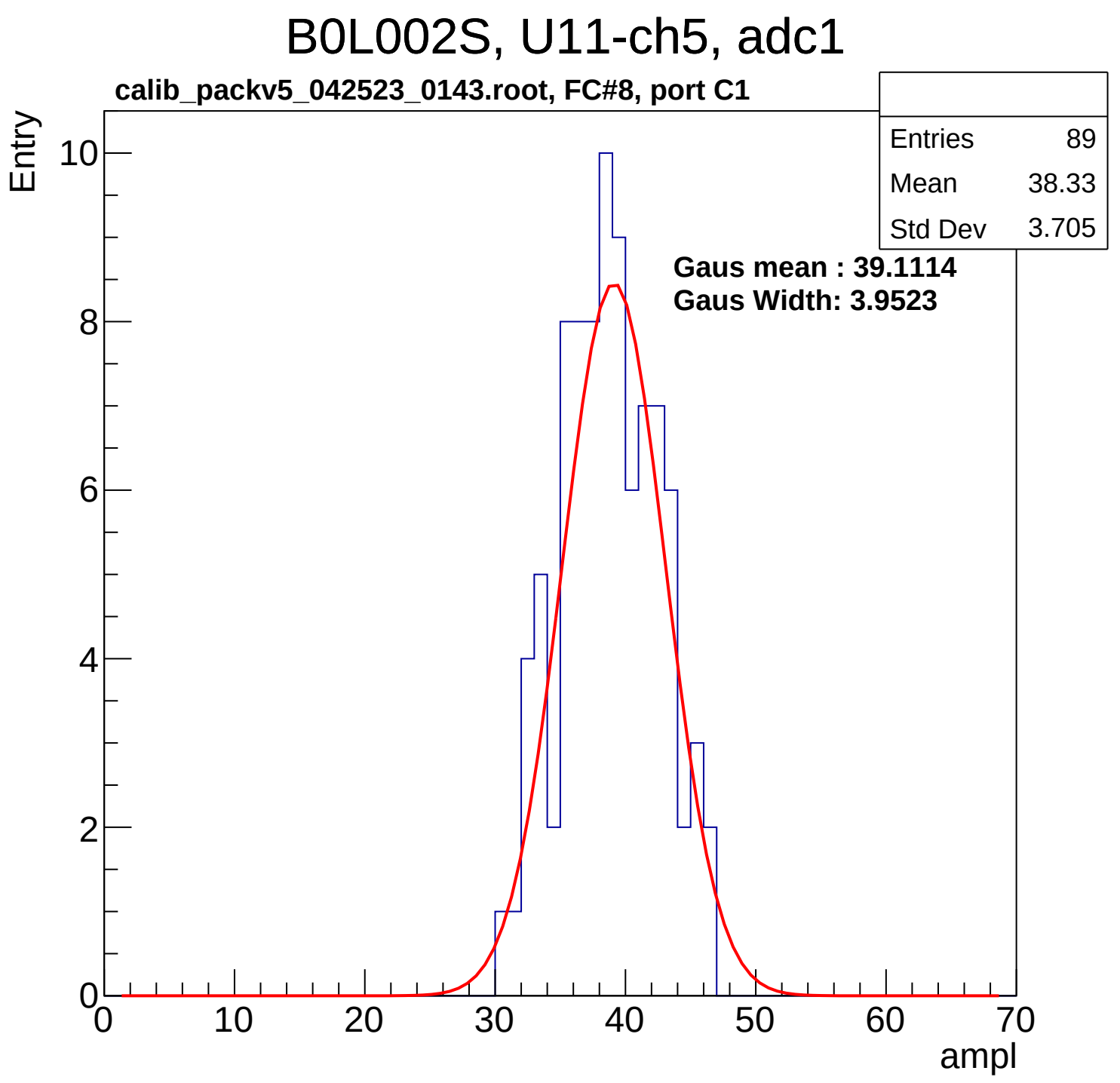
40
50
60
70


B0L002S, U11-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 38.33
Std Dev 3.705
Gaus mean : 39.1114
Gaus Width: 3.9523
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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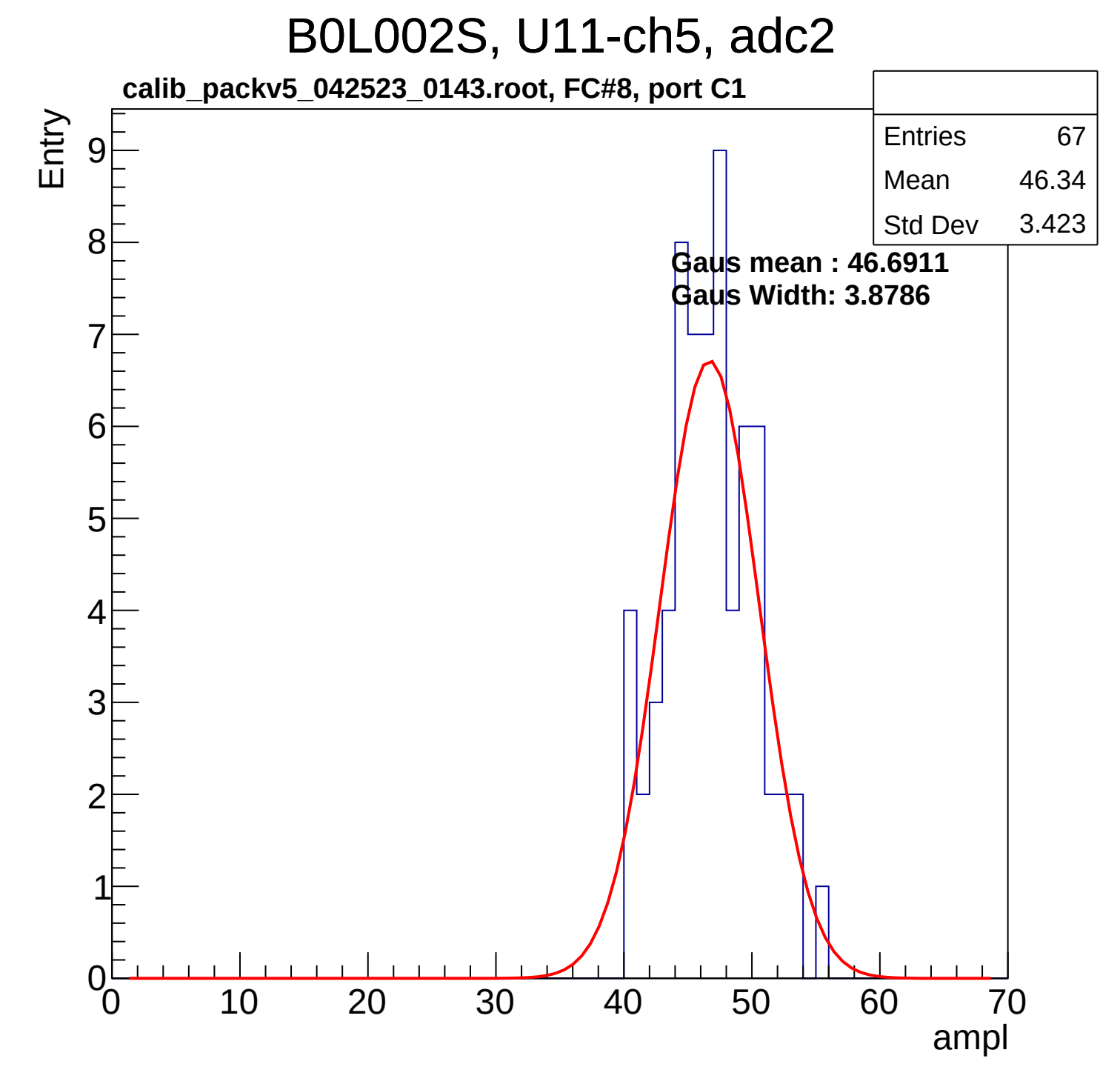
B0L002S, U11-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 46.34
Std Dev 3.423
Gaus mean : 46.6911
Gaus Width: 3.8786
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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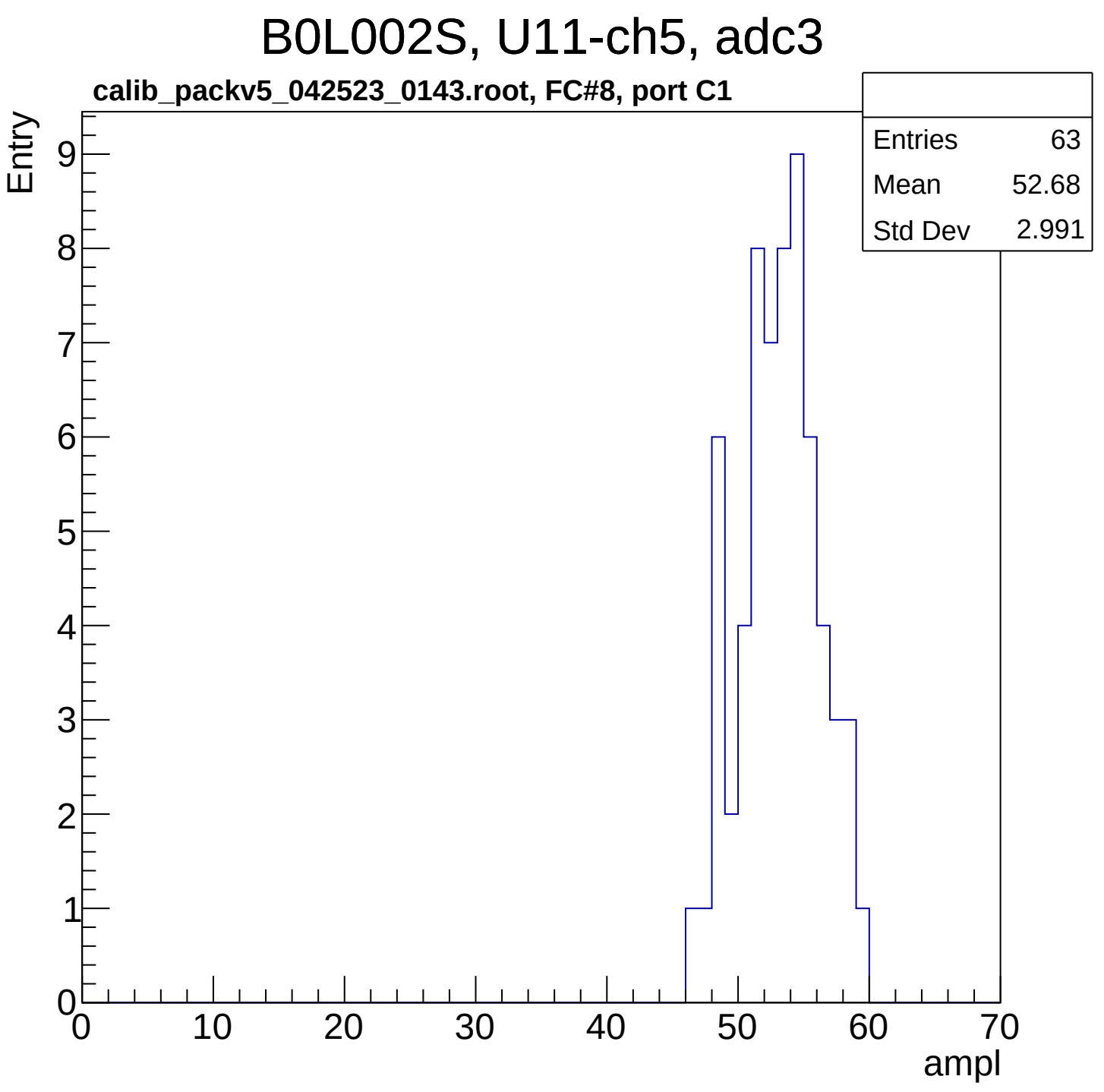
B0L002S, U11-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52.68
Std Dev 2.991
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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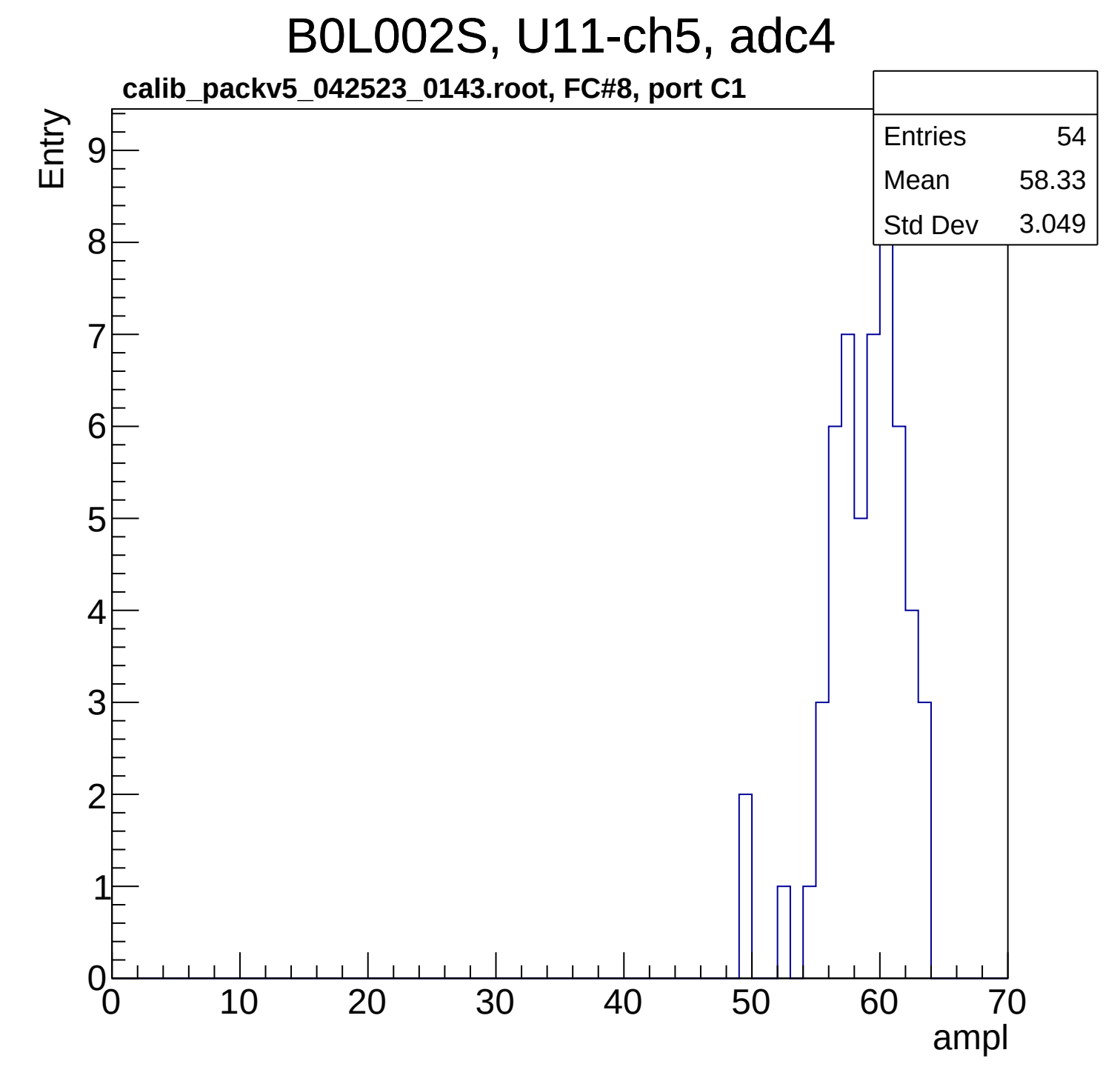
B0L002S, U11-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 58.33
Std Dev 3.049
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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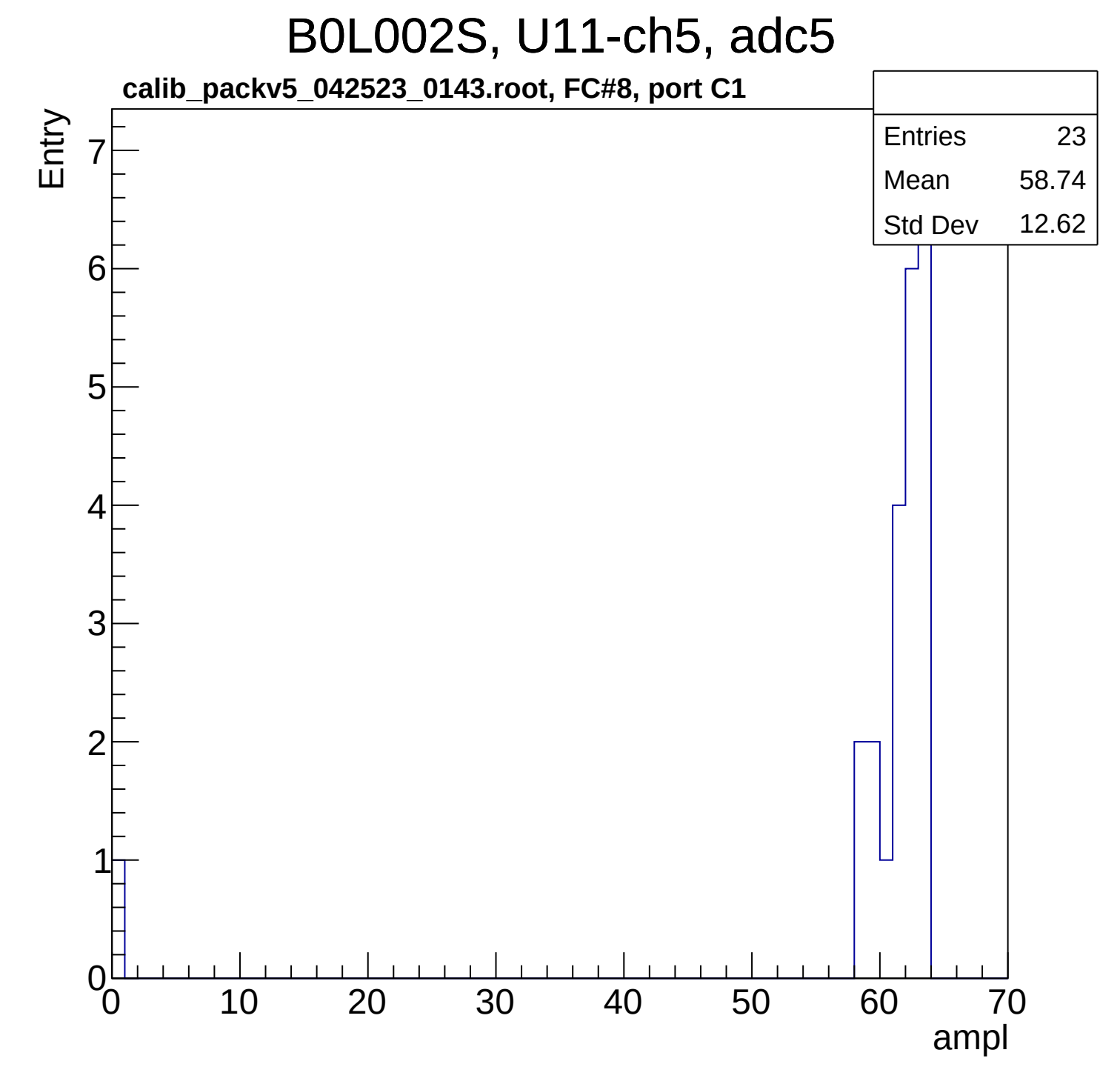
B0L002S, U11-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.74
Std Dev 12.62
Entry
ampl
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1
2
3
4
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6
7
0
10
20
30
40
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60
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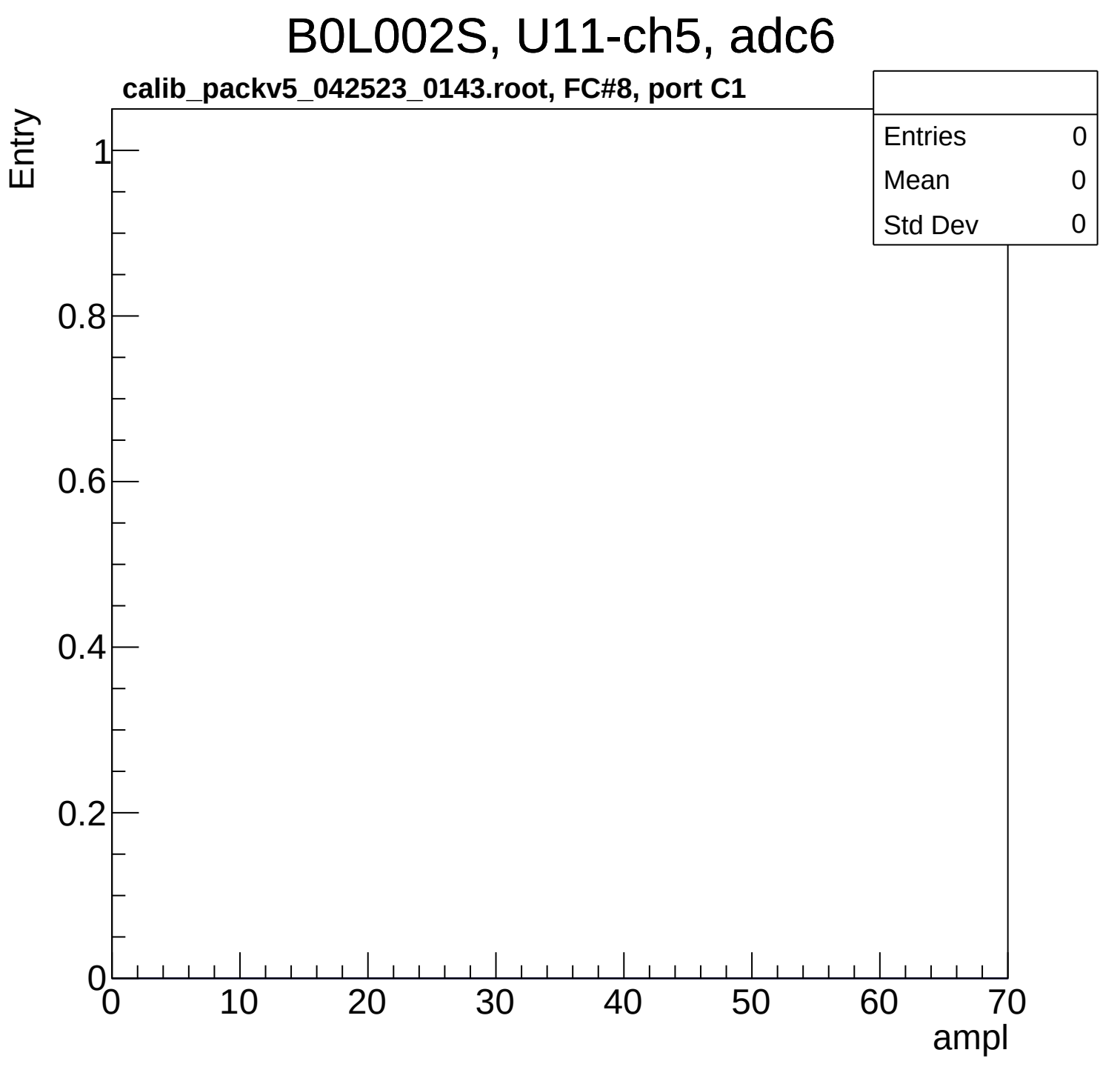
B0L002S, U11-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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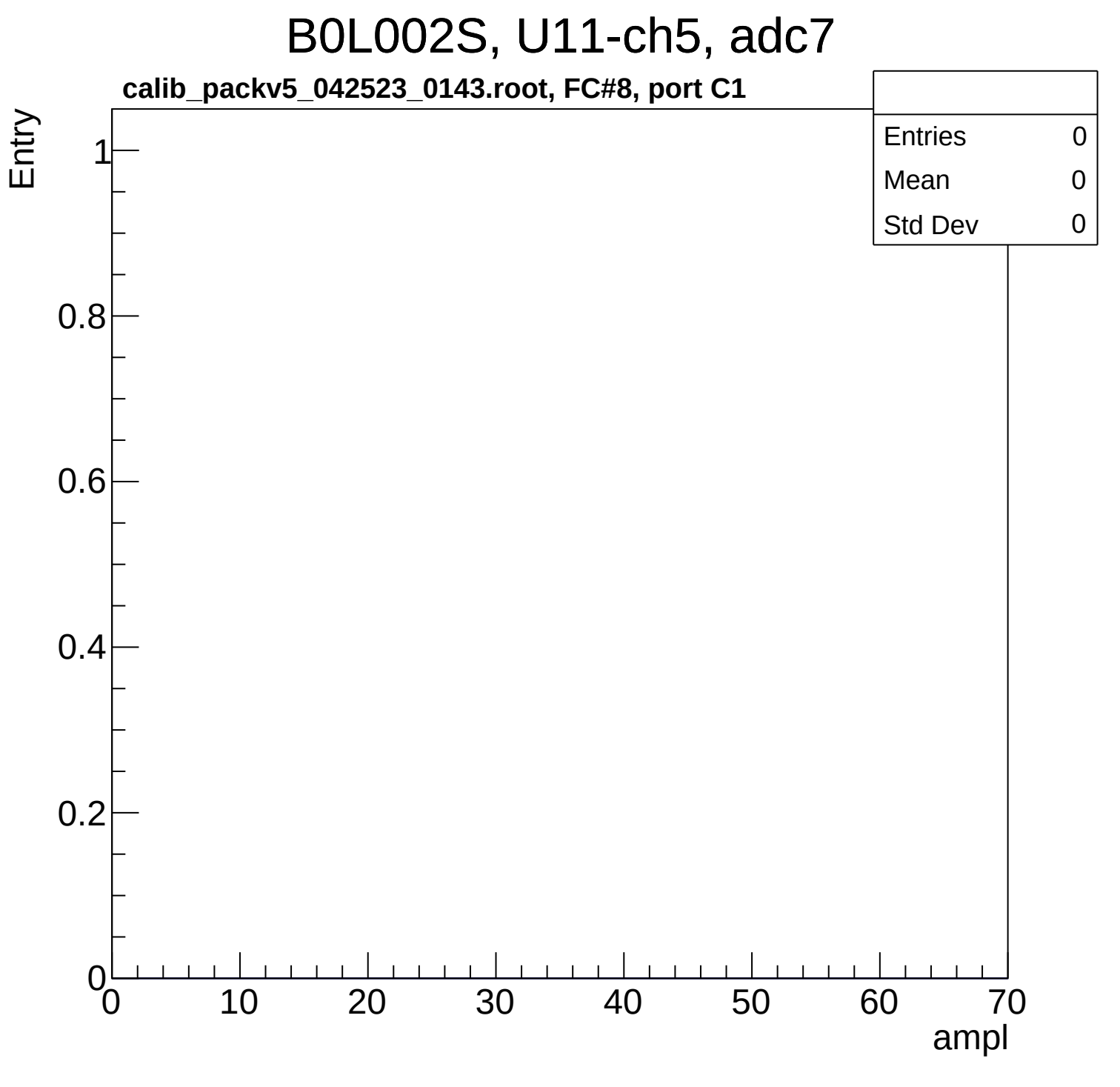
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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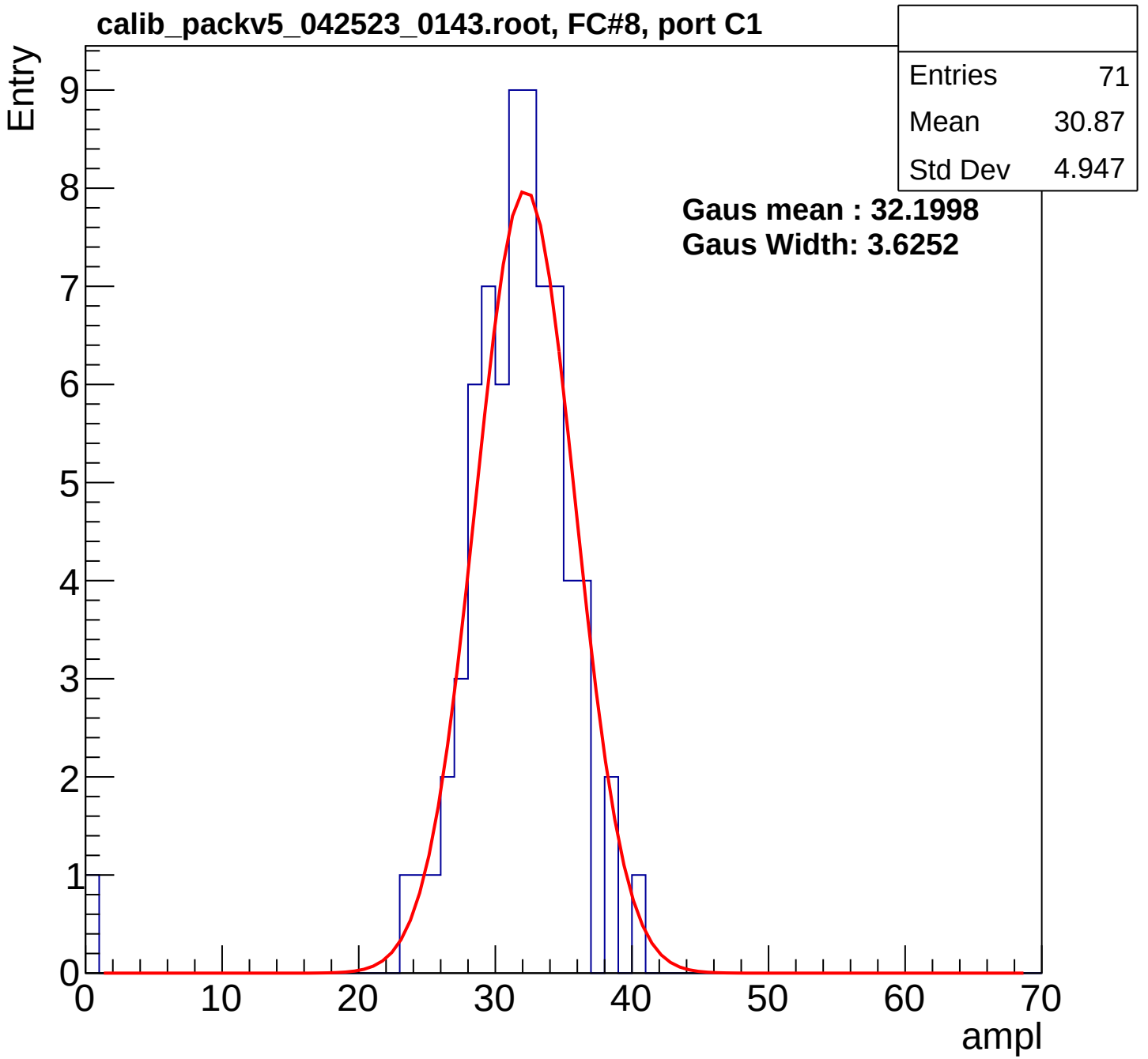
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.87
Std Dev 4.947
Gaus mean : 32.1998
Gaus Width: 3.6252
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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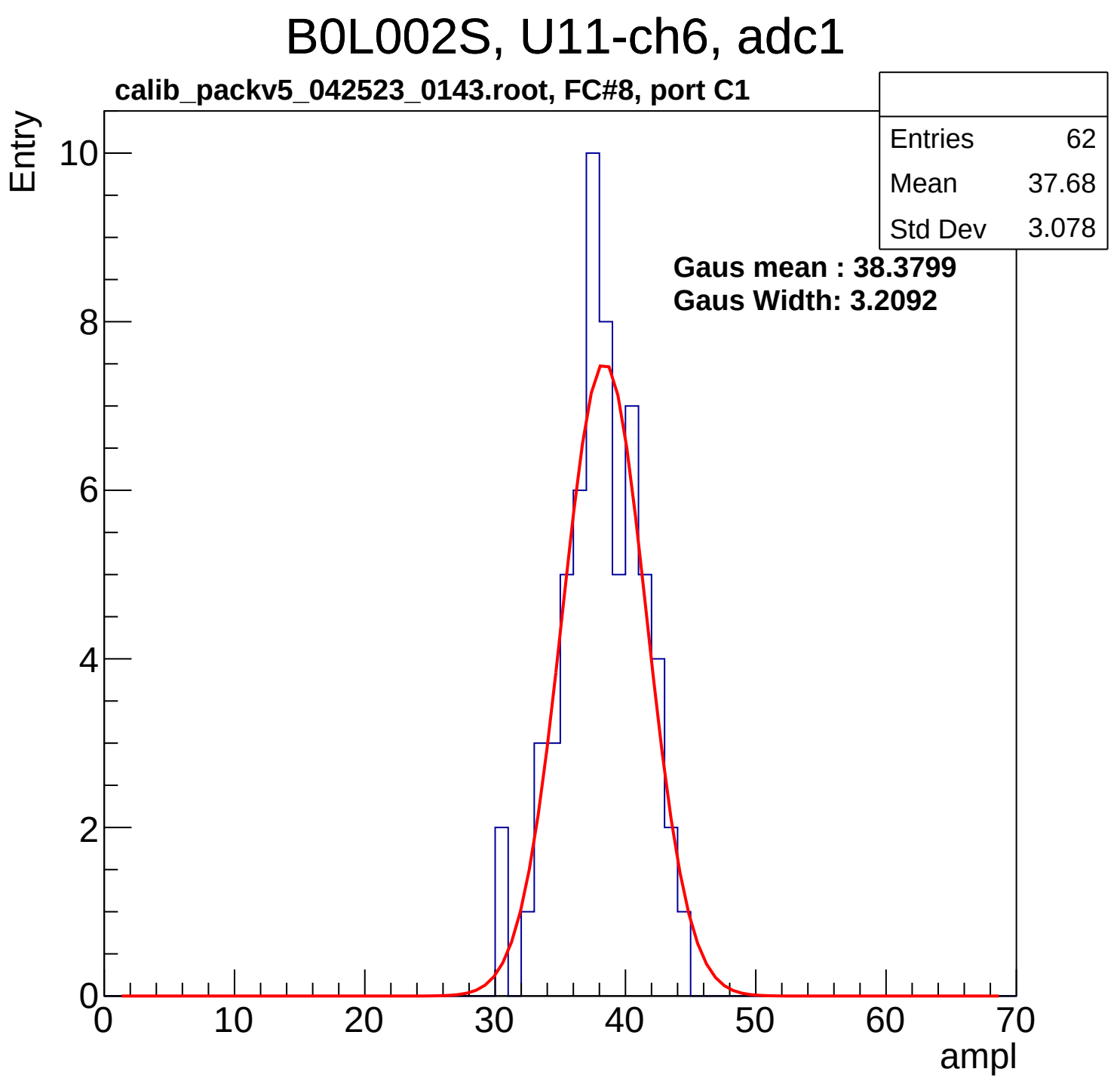
B0L002S, U11-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.68
Std Dev 3.078
Gaus mean : 38.3799
Gaus Width: 3.2092
Entry
ampl
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6
8
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0
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20
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40
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60
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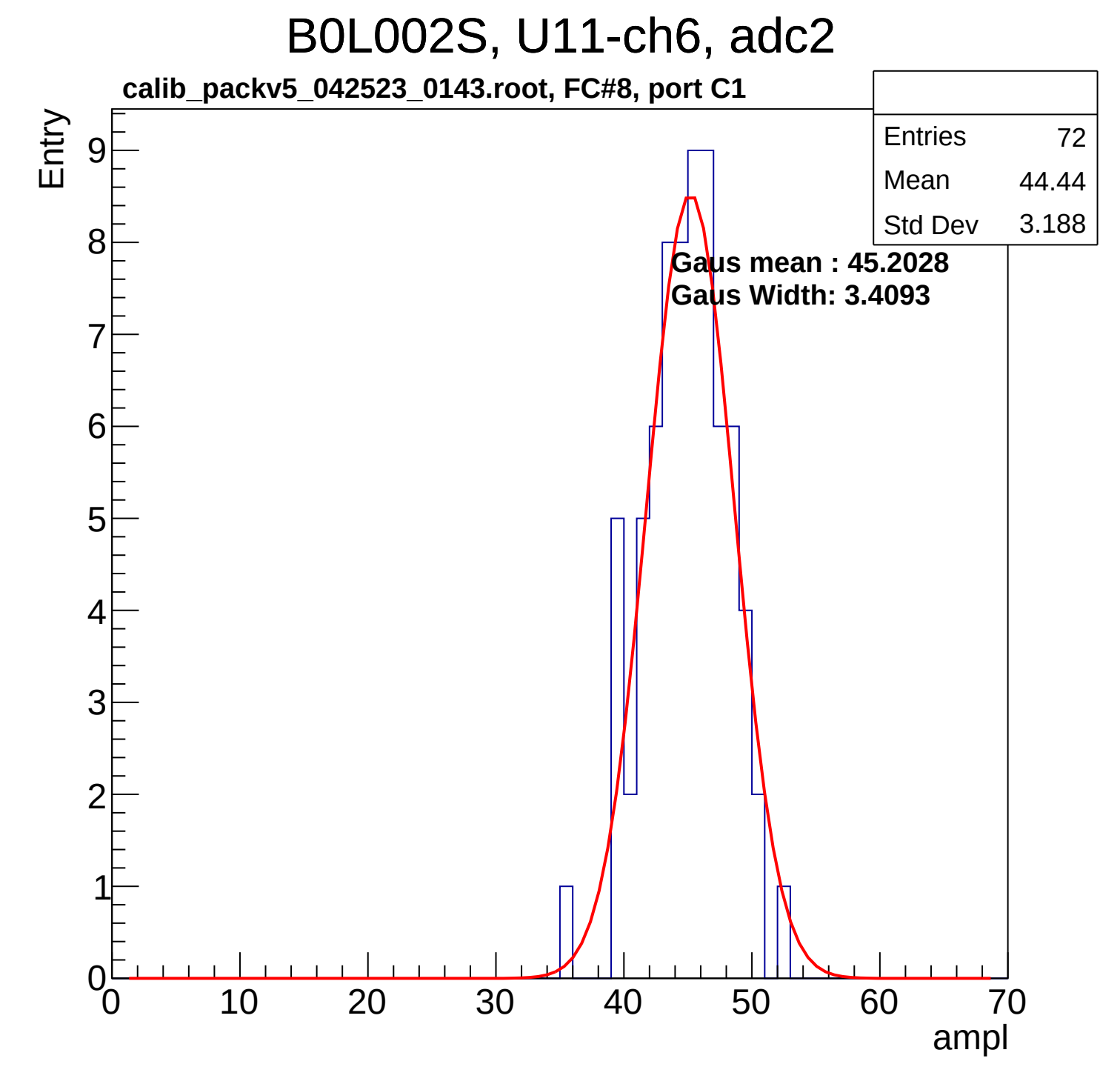
B0L002S, U11-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.44
Std Dev 3.188
Gaus mean : 45.2028
Gaus Width: 3.4093
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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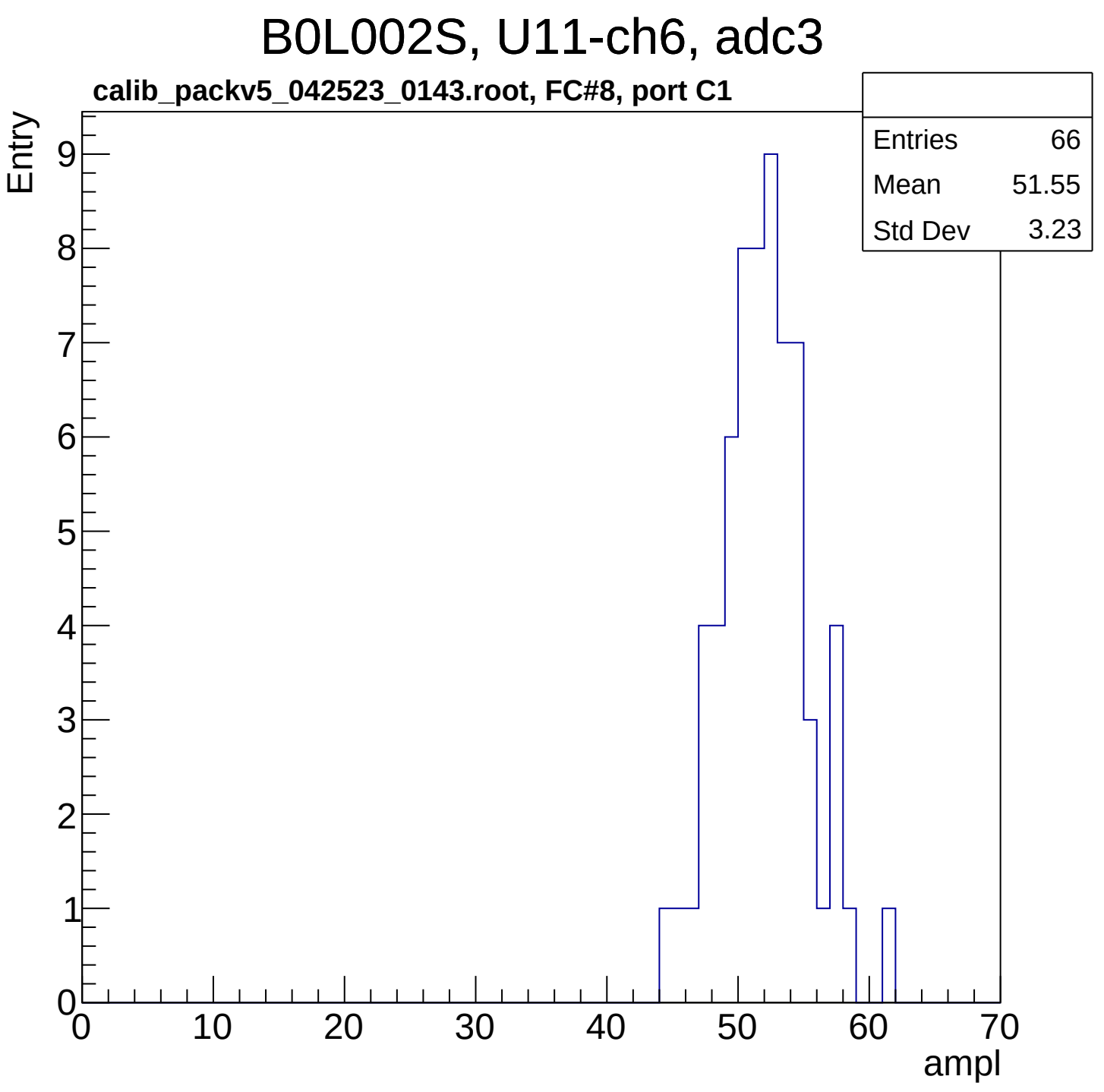
B0L002S, U11-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.55
Std Dev 3.23
Entry
ampl
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5
6
7
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0
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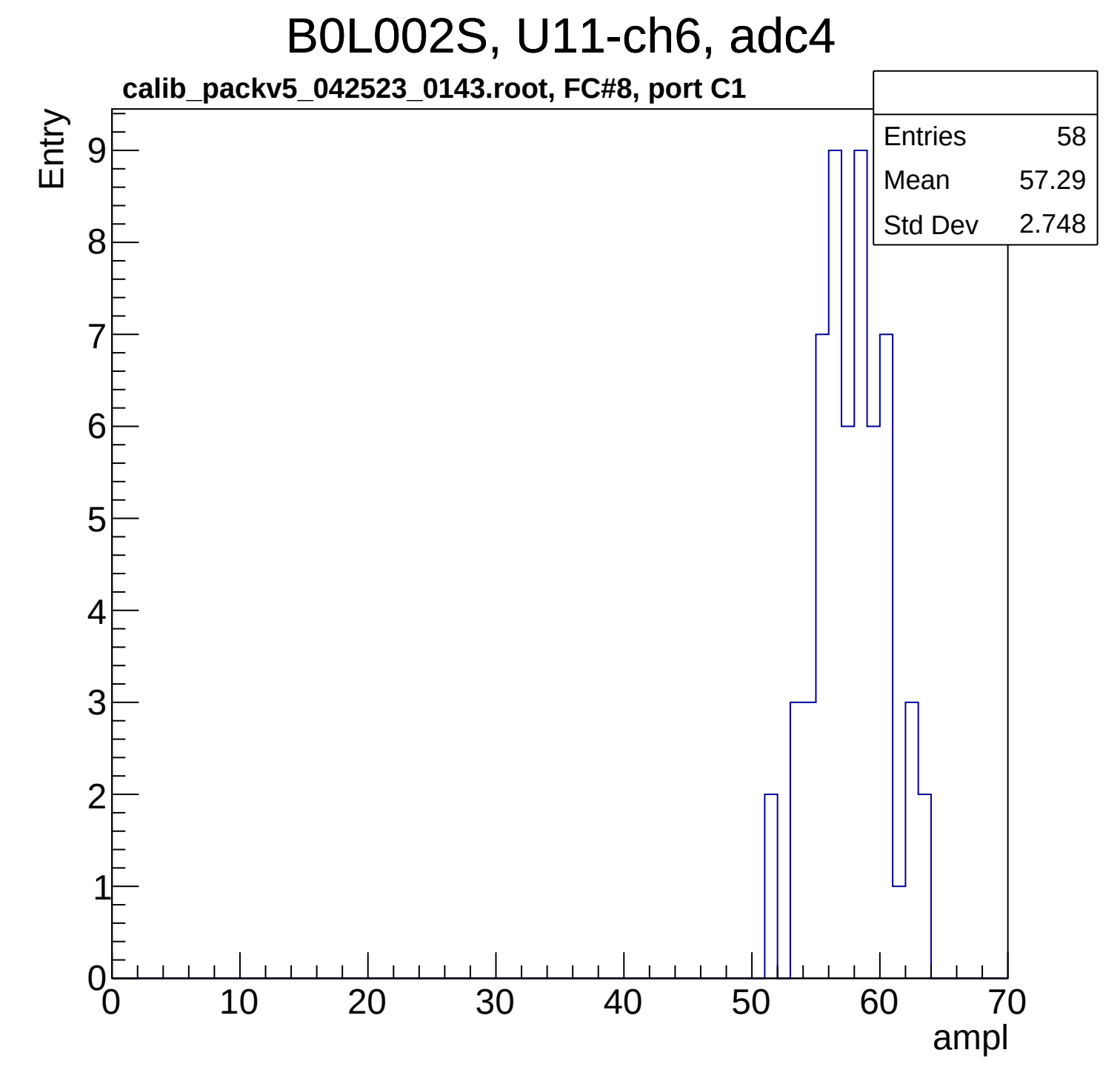
B0L002S, U11-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.29
Std Dev 2.748
Entry
ampl
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7
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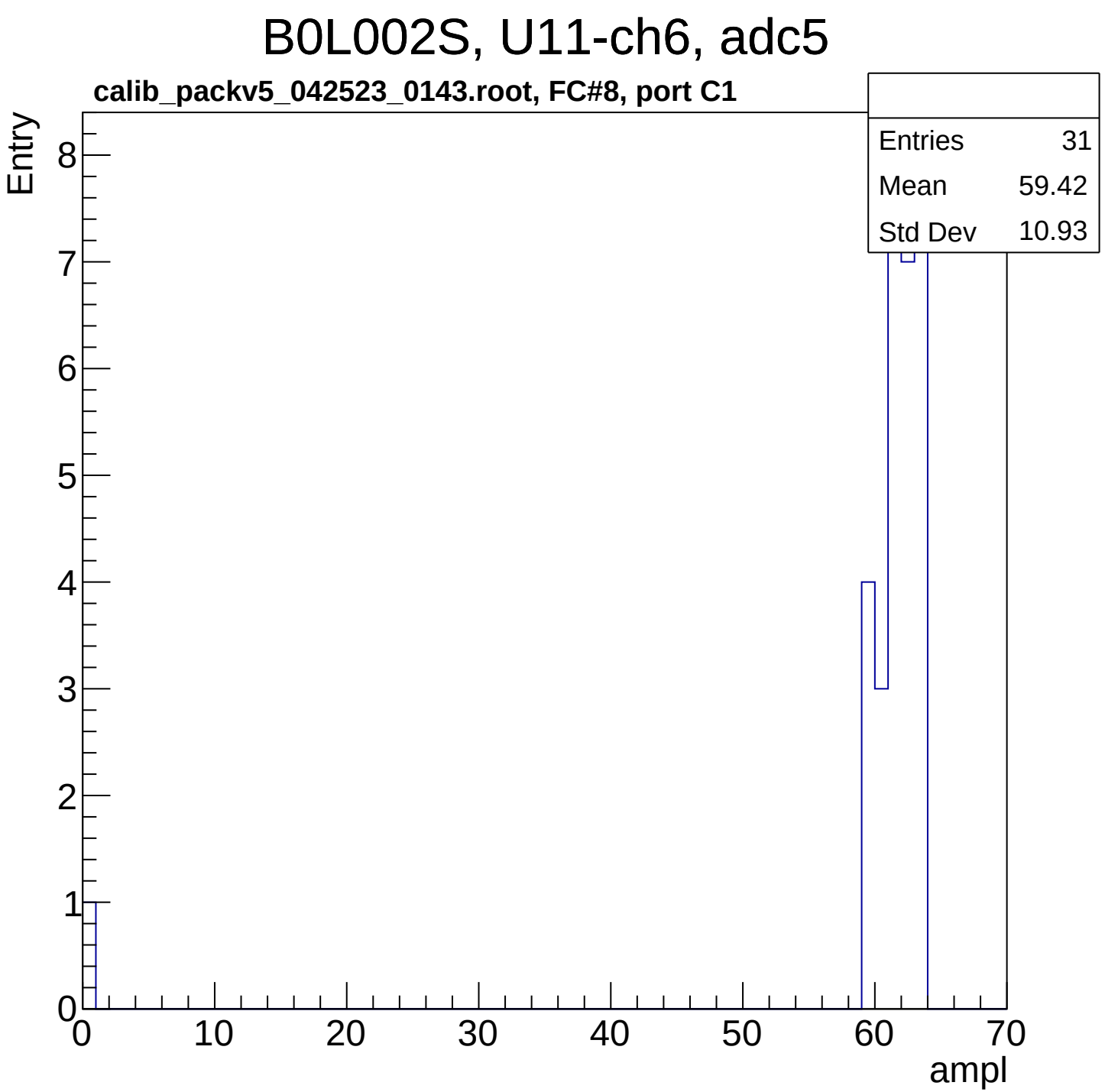
B0L002S, U11-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.42
Std Dev 10.93
Entry
ampl
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2
3
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7
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0
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60
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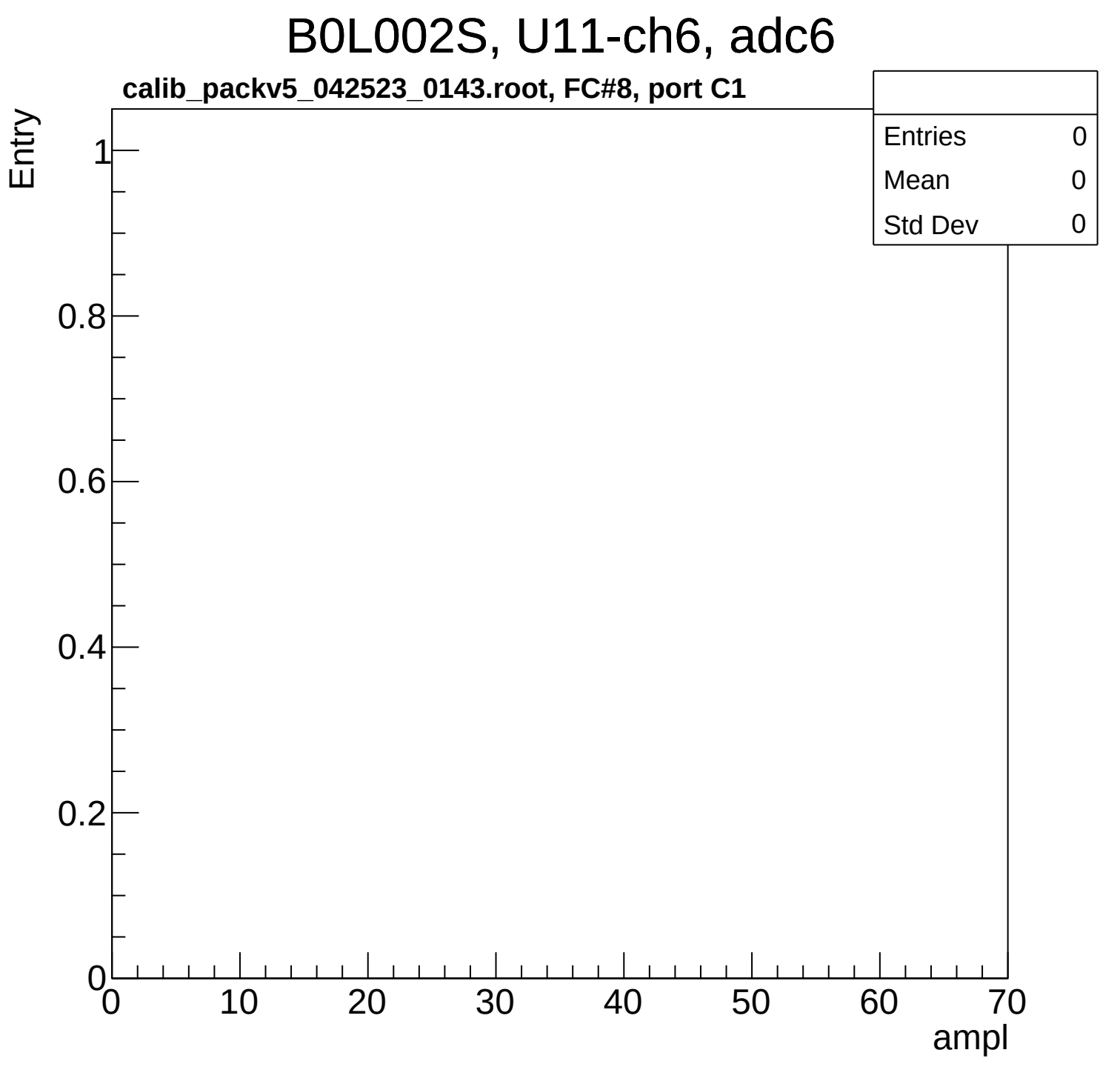
B0L002S, U11-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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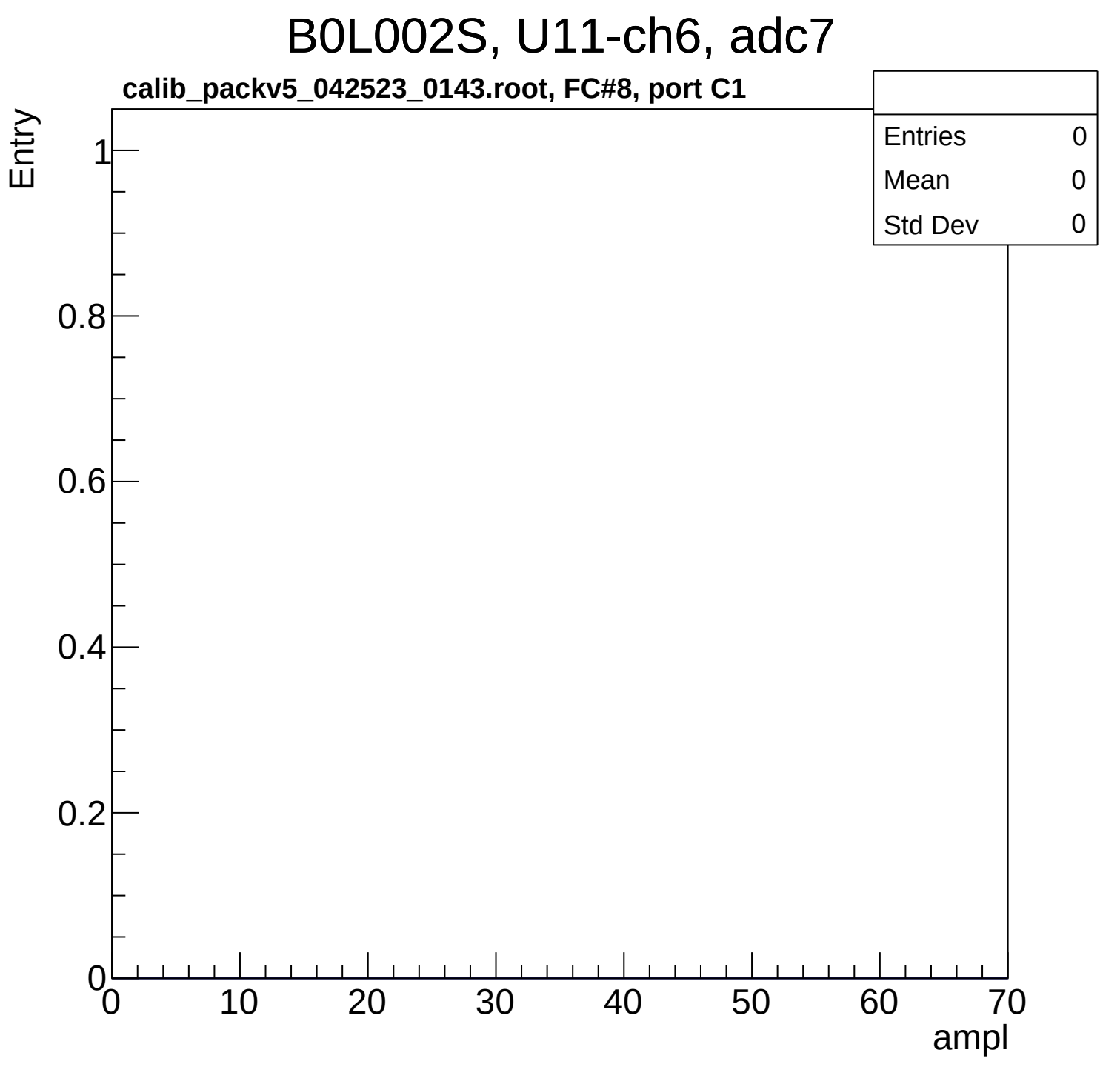
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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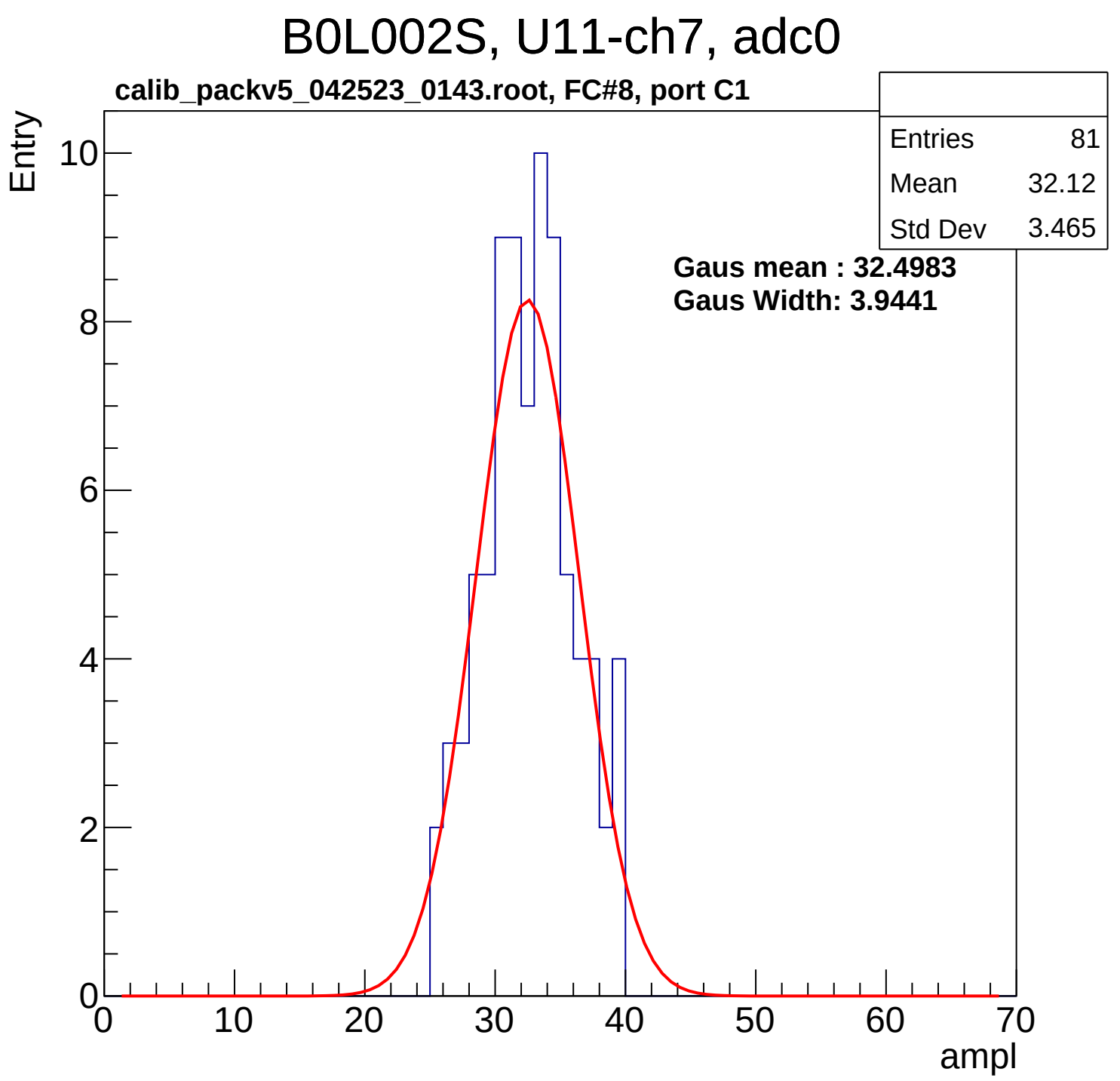
50
60
70
ampl


B0L002S, U11-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 32.12
Std Dev 3.465
Gaus mean : 32.4983
Gaus Width: 3.9441
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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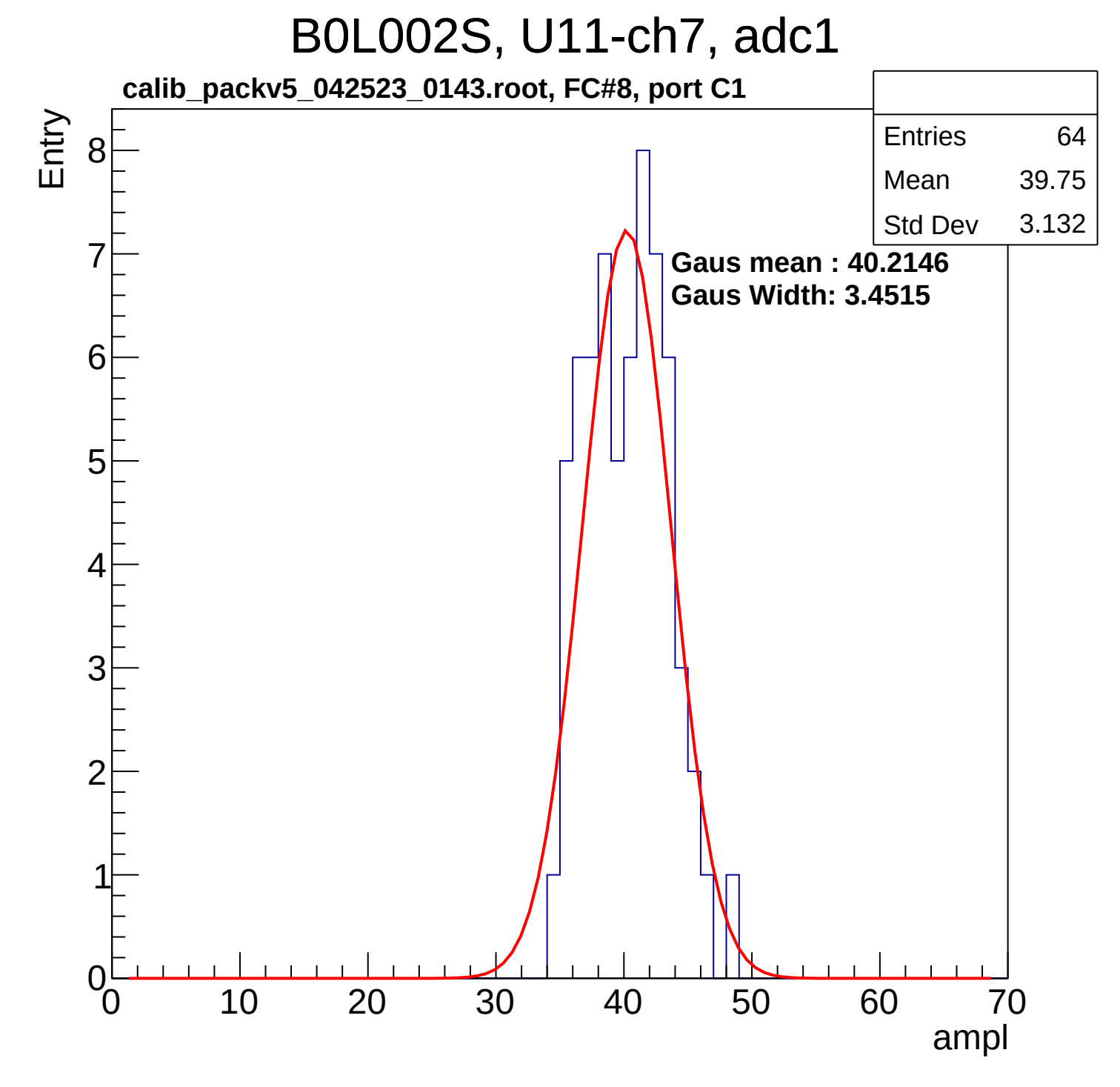
B0L002S, U11-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 39.75
Std Dev 3.132
Gaus mean : 40.2146
Gaus Width: 3.4515
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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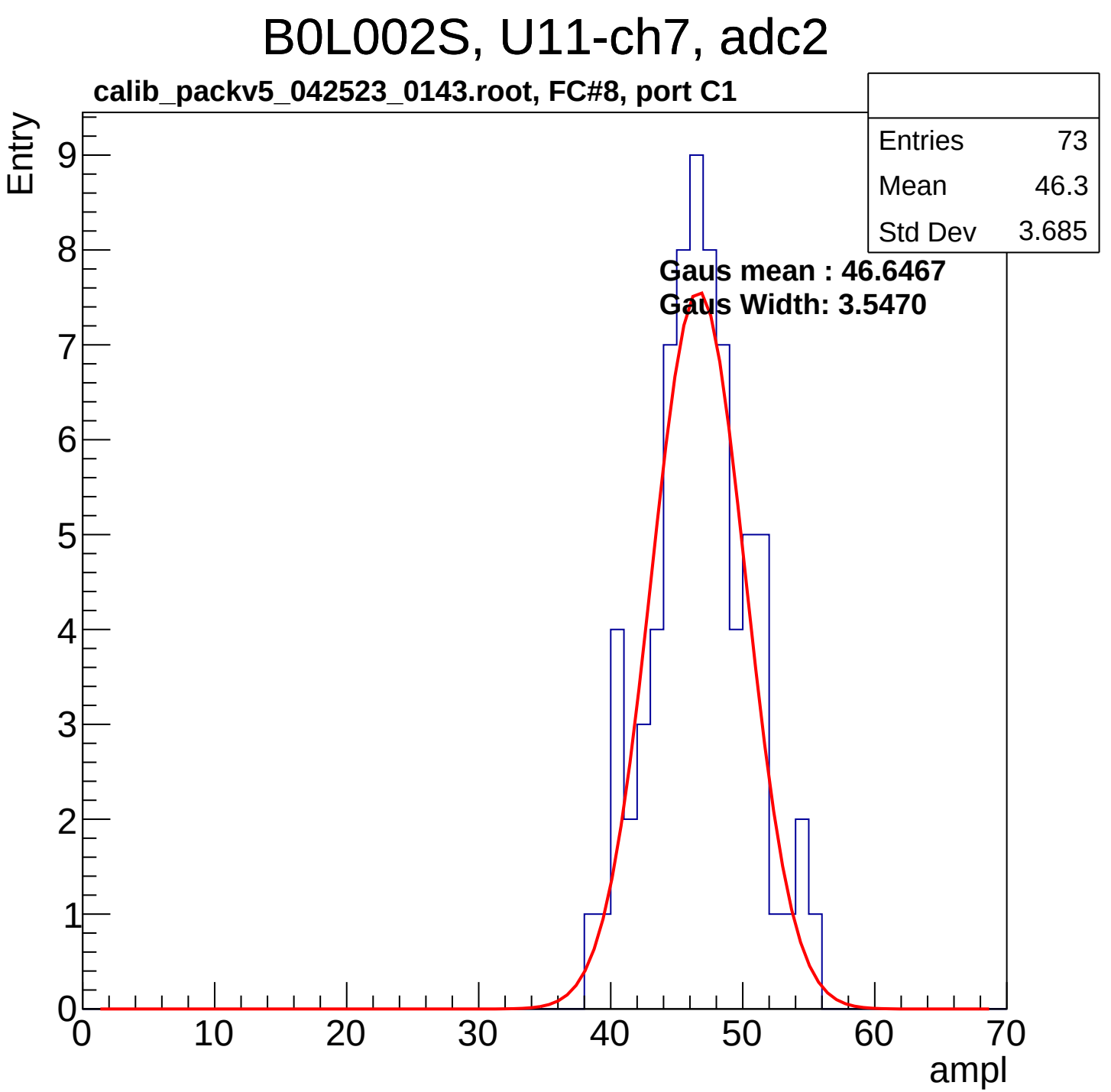
B0L002S, U11-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 46.3
Std Dev 3.685
Gaus mean : 46.6467
Gaus Width: 3.5470
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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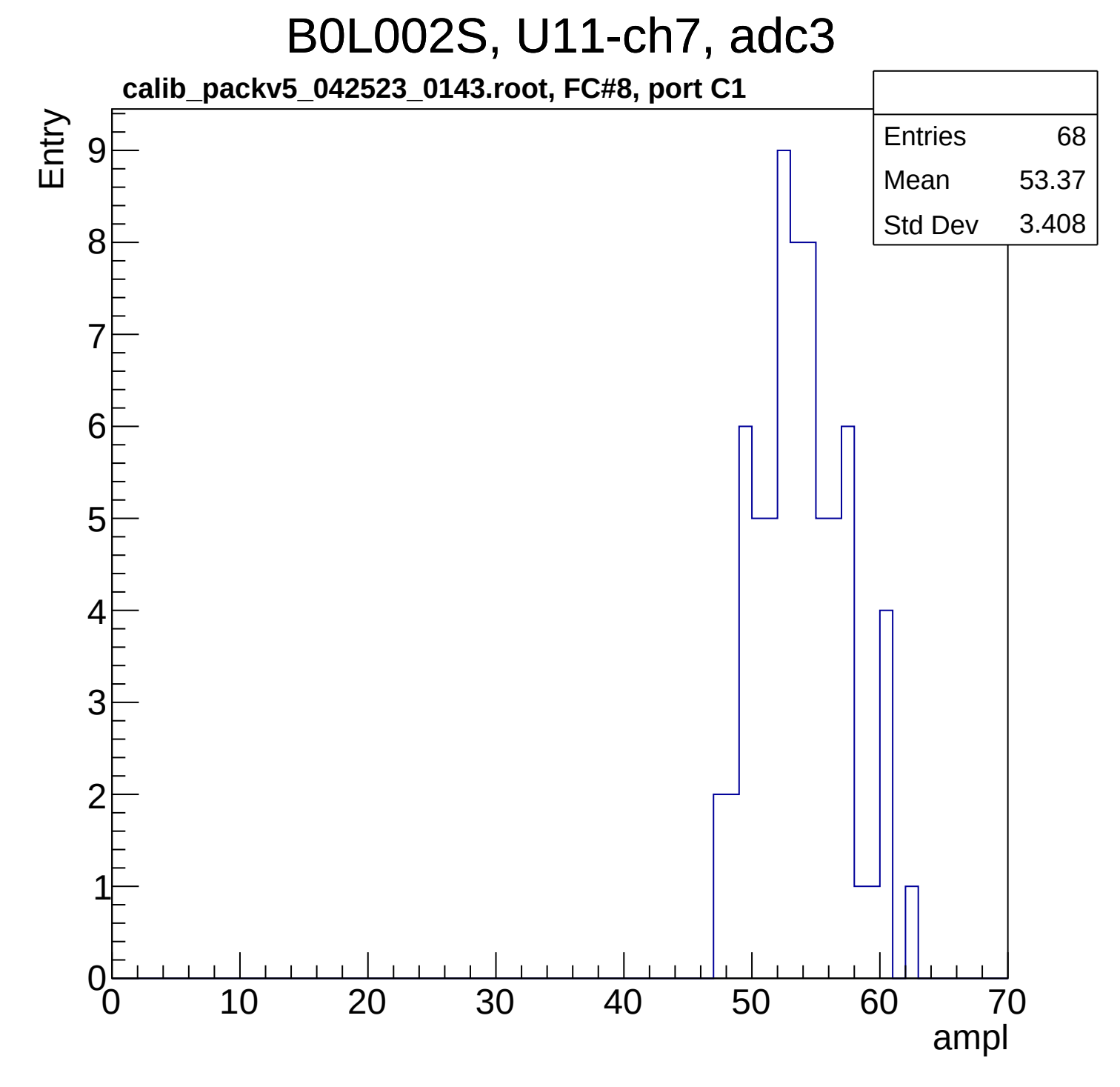
B0L002S, U11-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 53.37
Std Dev 3.408
Entry
ampl
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3
4
5
6
7
8
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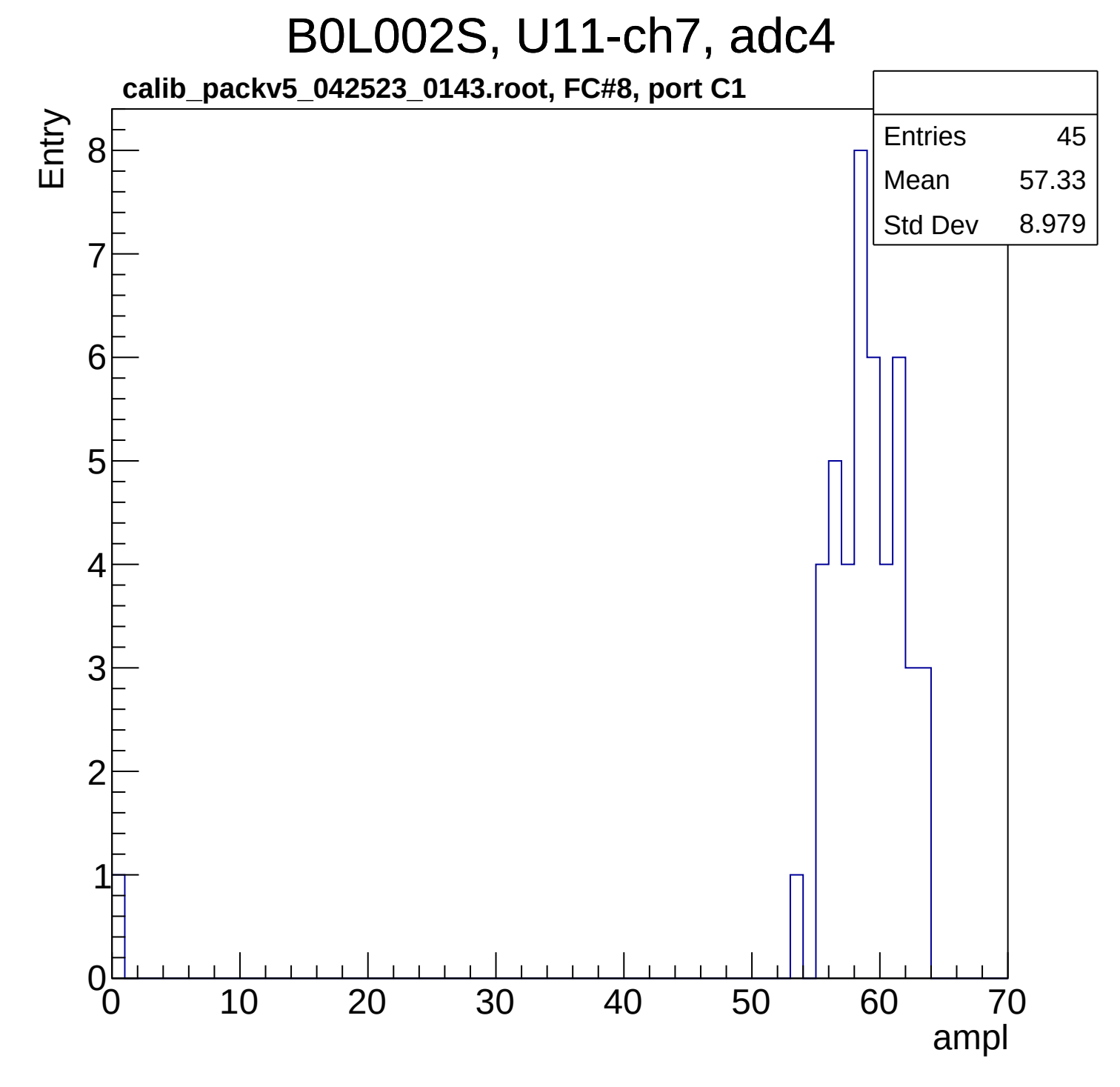
B0L002S, U11-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57.33
Std Dev 8.979
Entry
ampl
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40
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60
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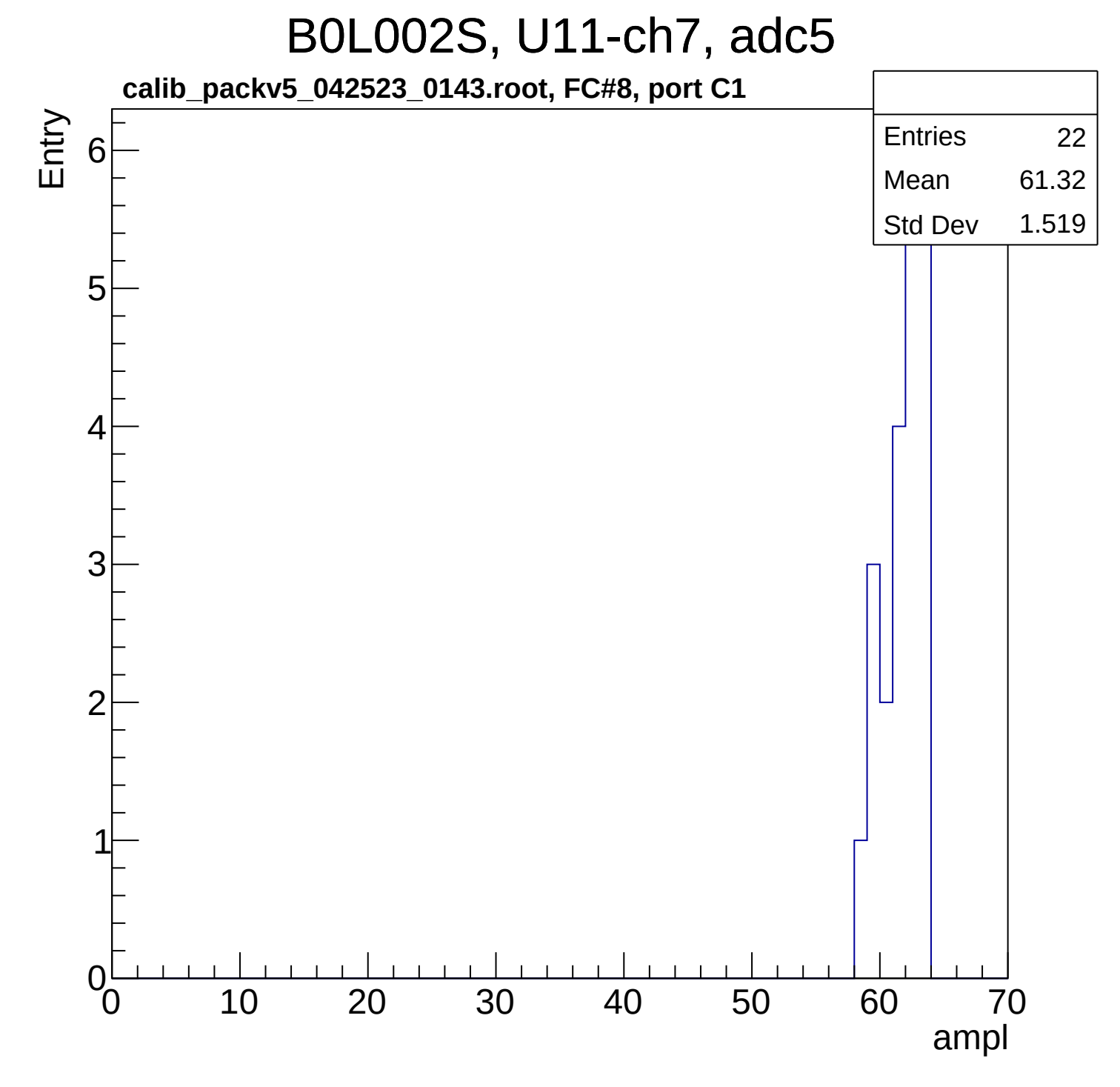
B0L002S, U11-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.32
Std Dev 1.519
Entry
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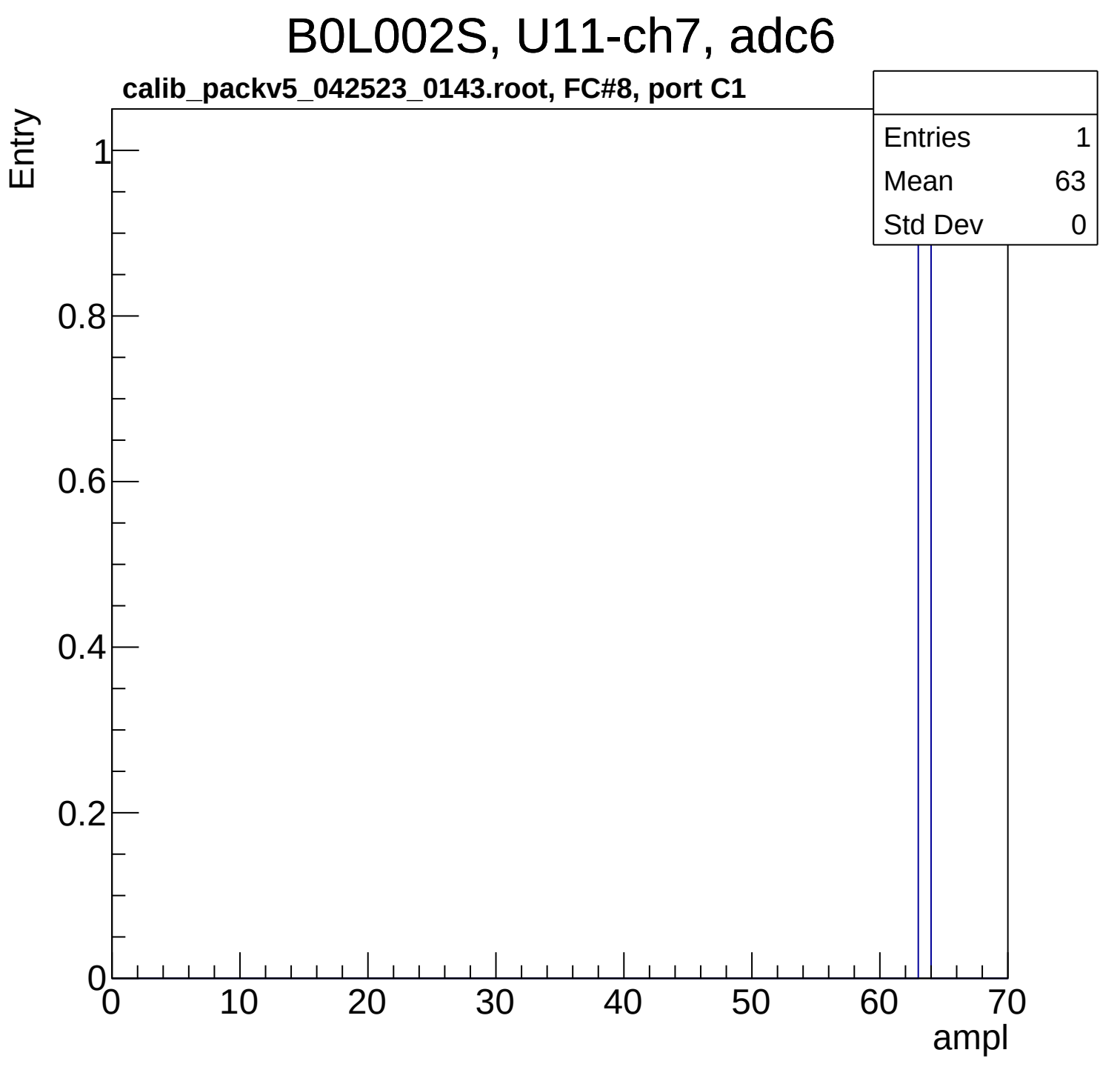
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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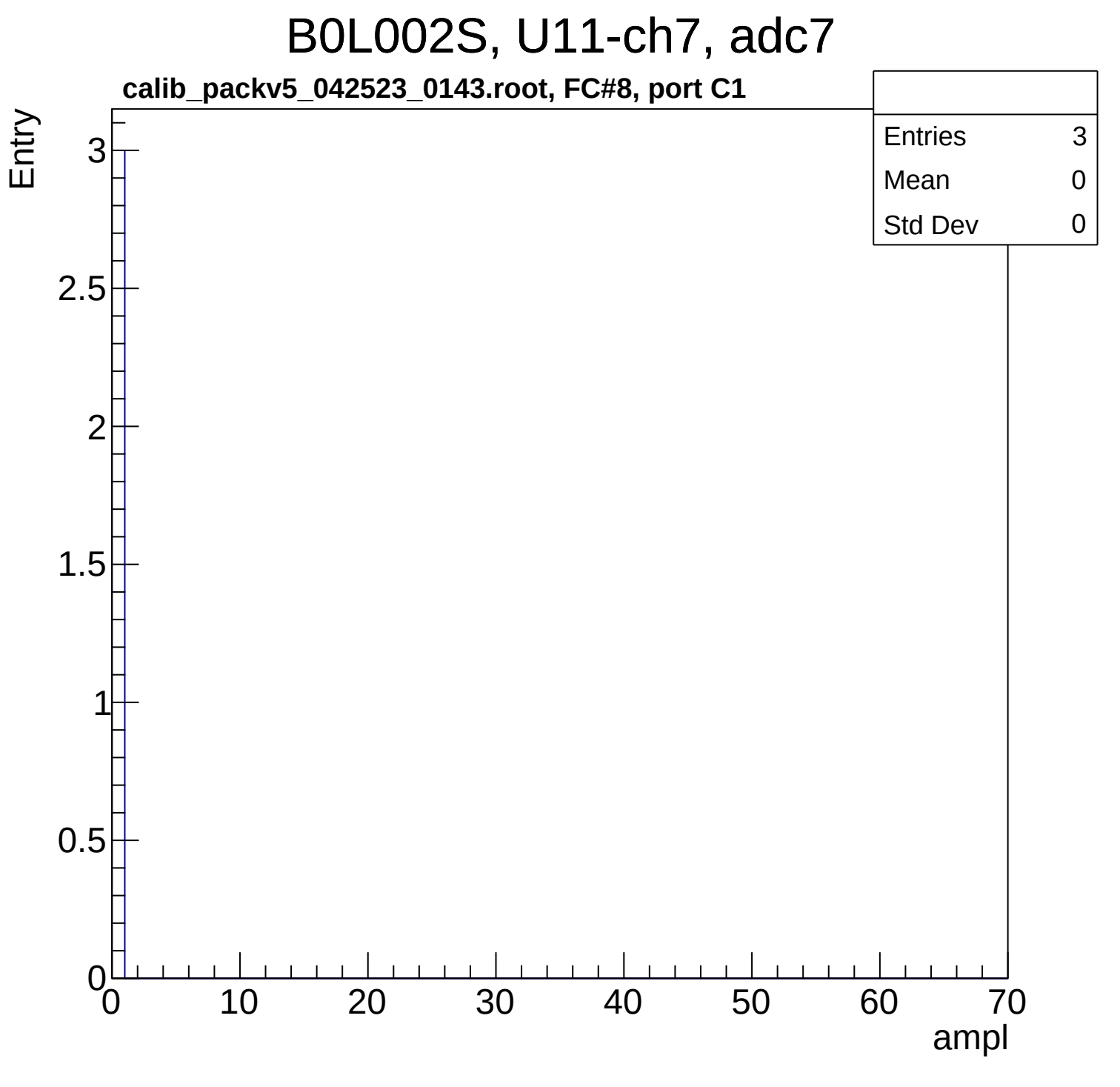
50
60
70
ampl


B0L002S, U11-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
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10
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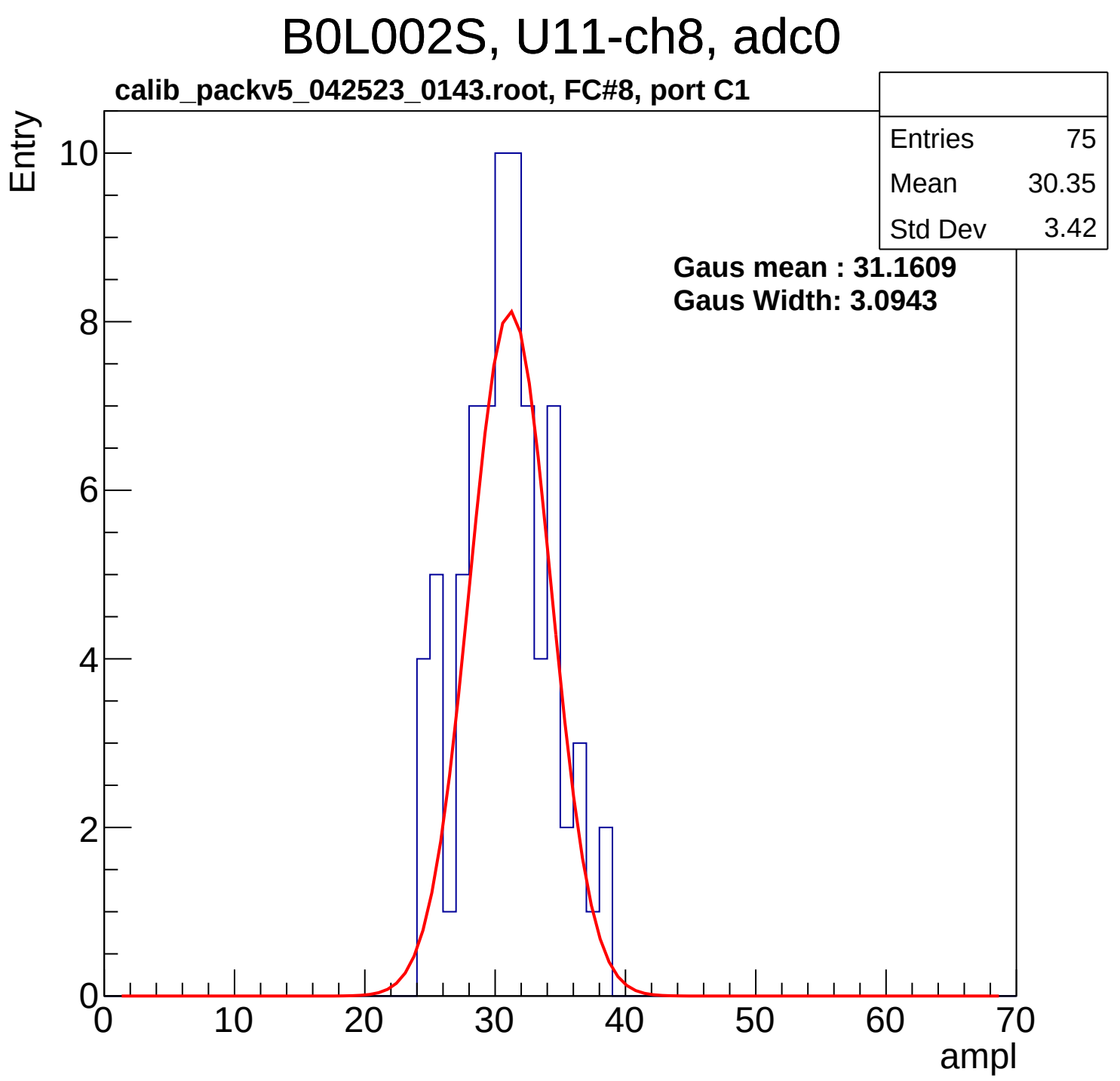
50
60
70
ampl


B0L002S, U11-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30.35
Std Dev 3.42
Gaus mean : 31.1609
Gaus Width: 3.0943
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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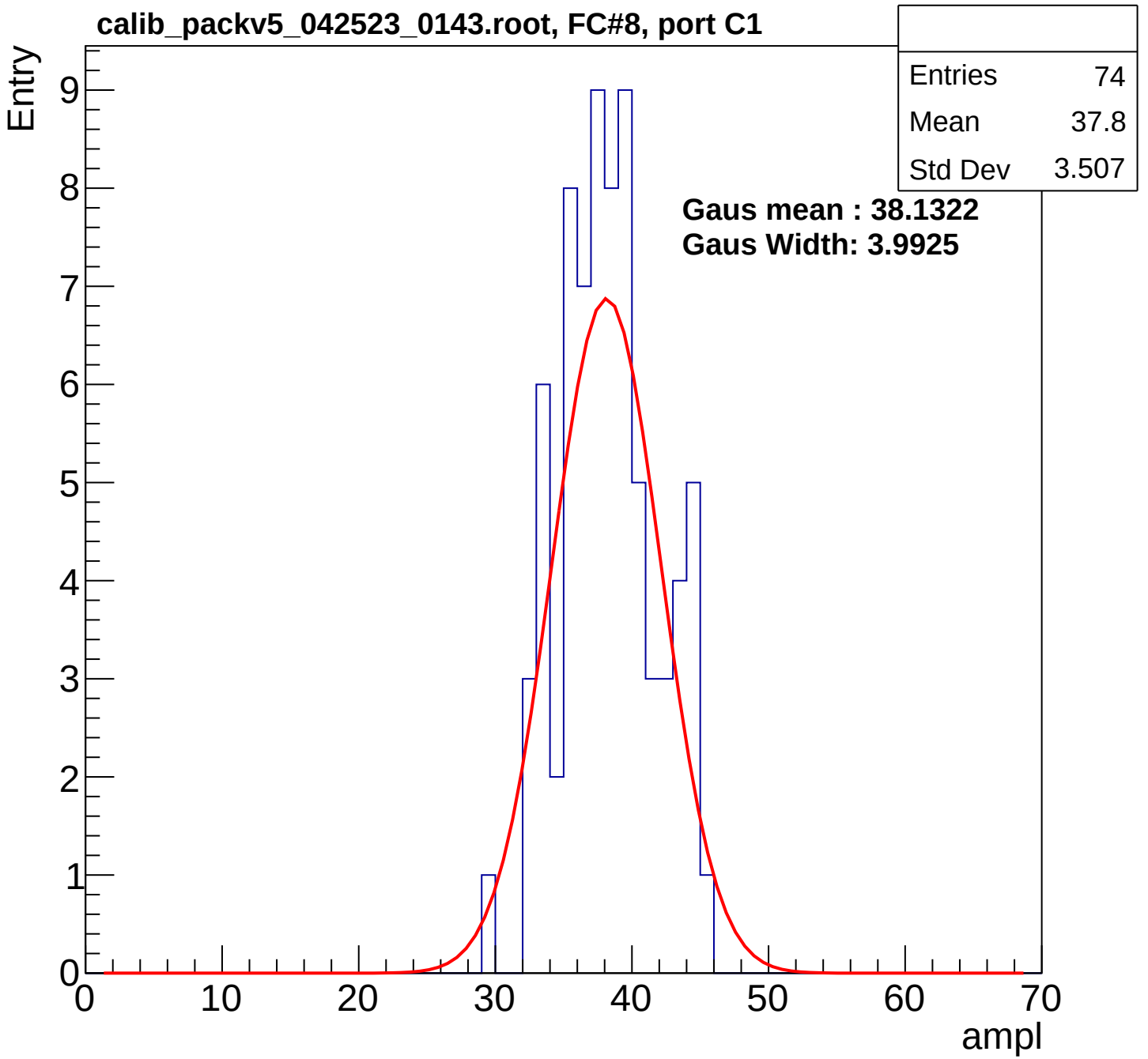
B0L002S, U11-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.8
Std Dev 3.507
Gaus mean : 38.1322
Gaus Width: 3.9925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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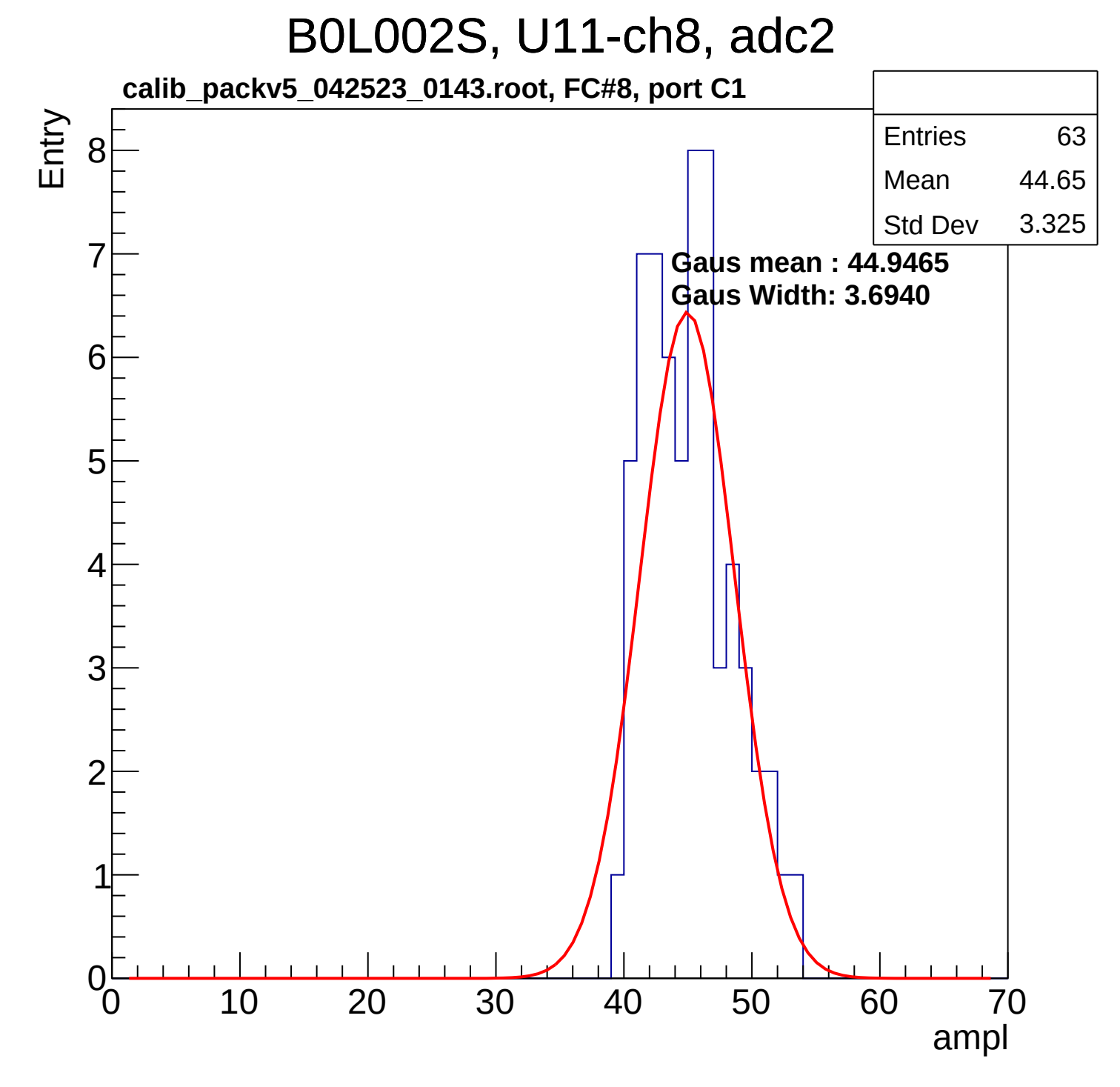
B0L002S, U11-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.65
Std Dev 3.325
Gaus mean : 44.9465
Gaus Width: 3.6940
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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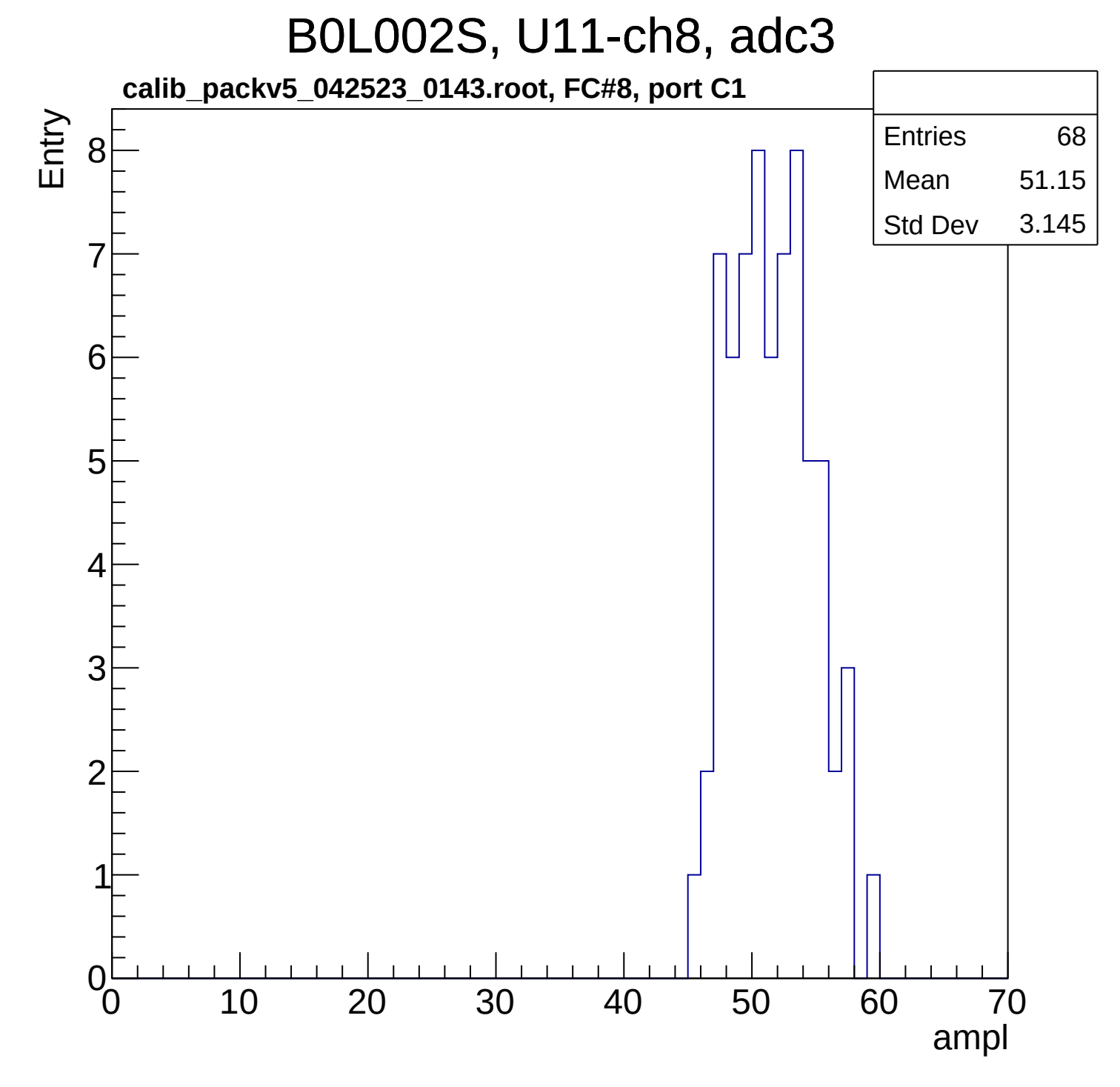
B0L002S, U11-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.15
Std Dev 3.145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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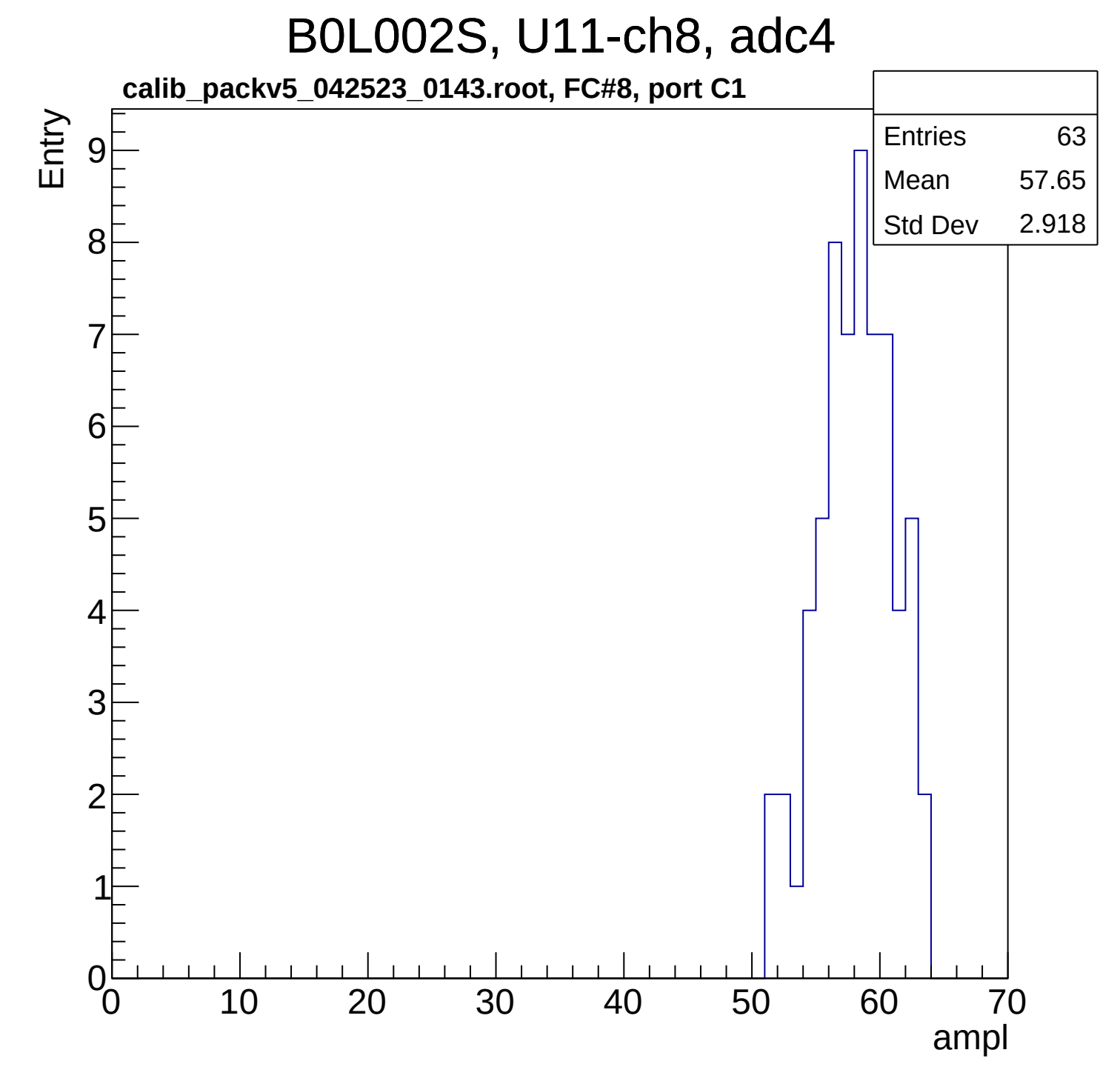
B0L002S, U11-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.65
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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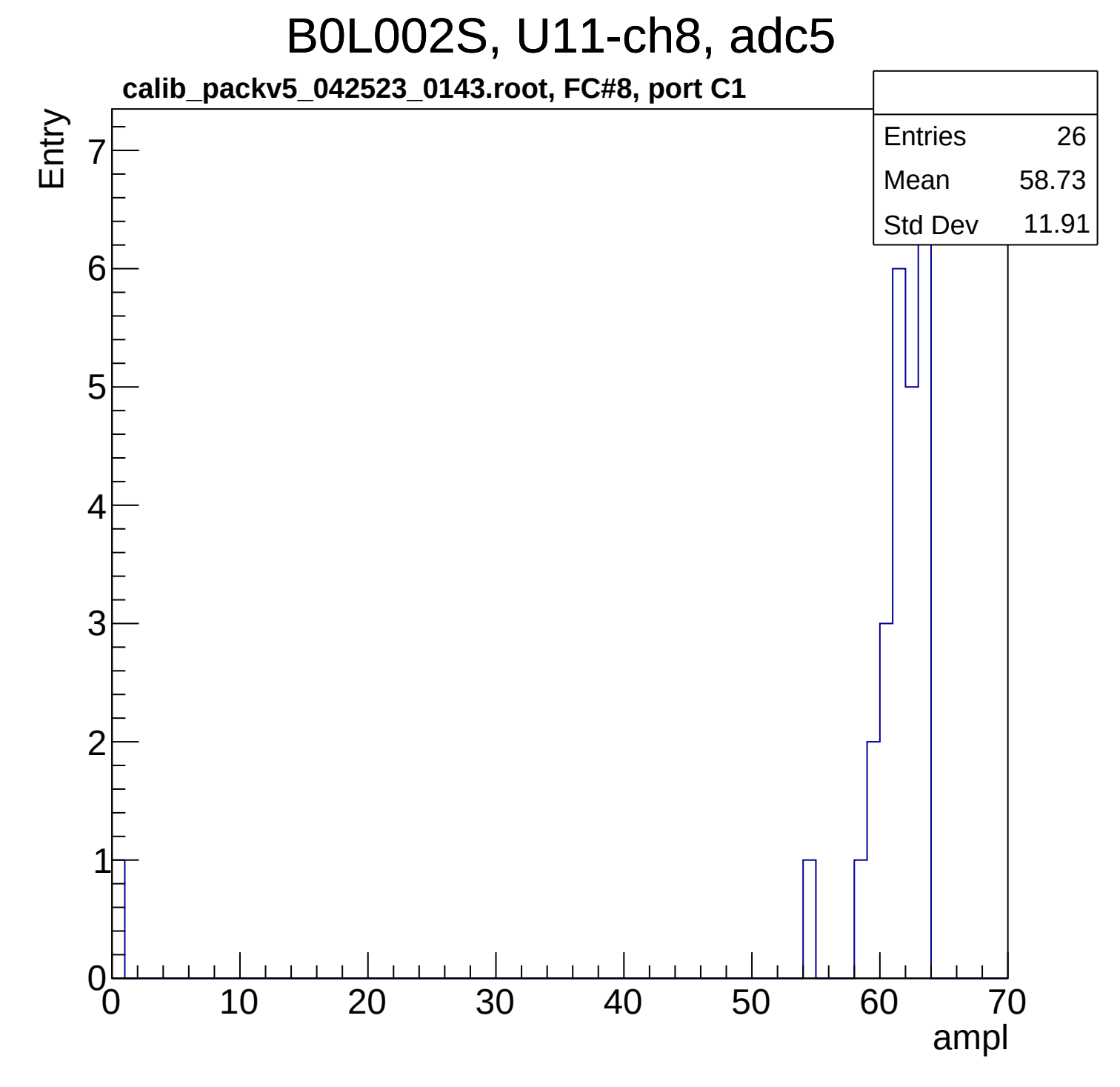
B0L002S, U11-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.73
Std Dev 11.91
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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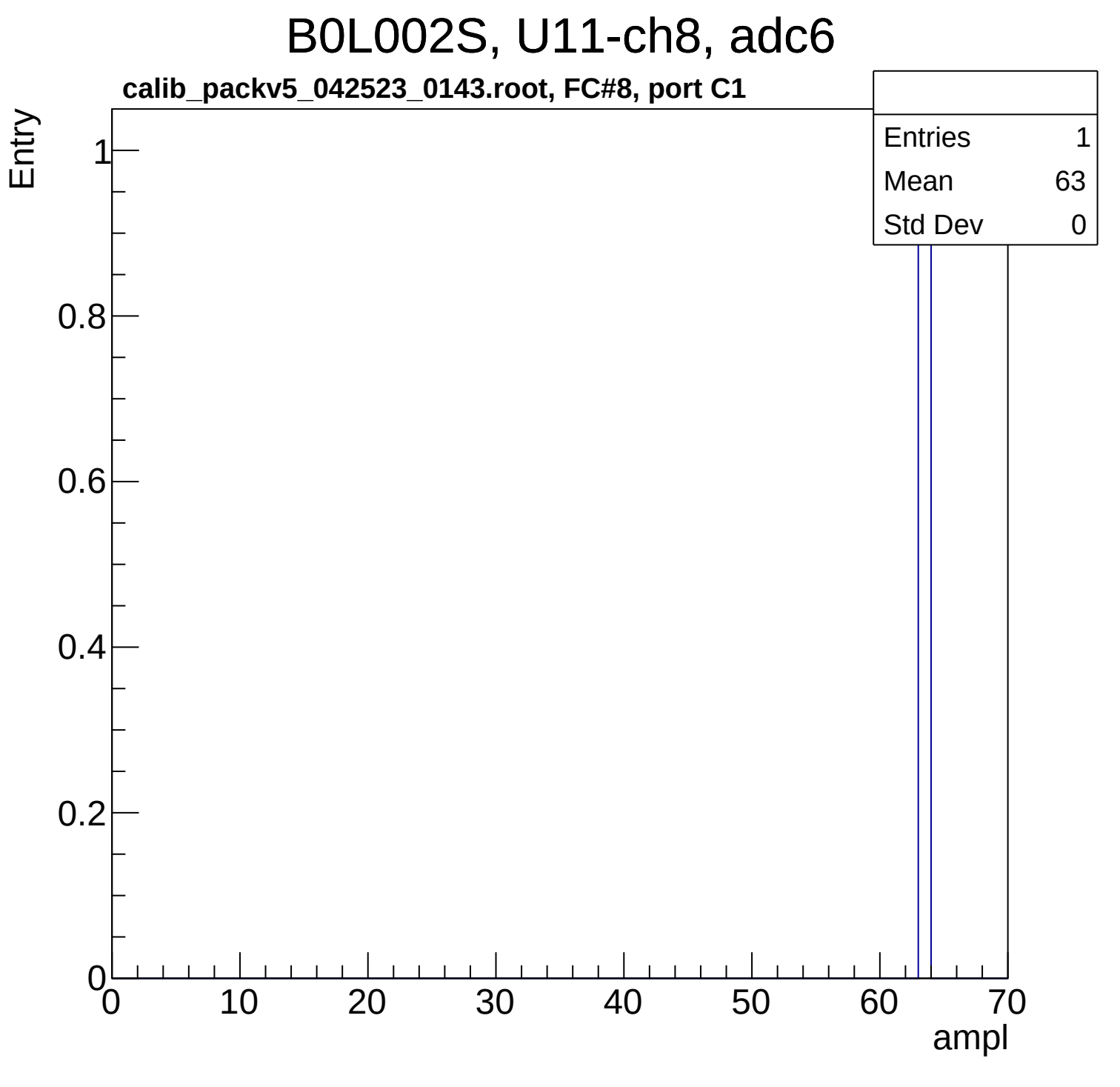
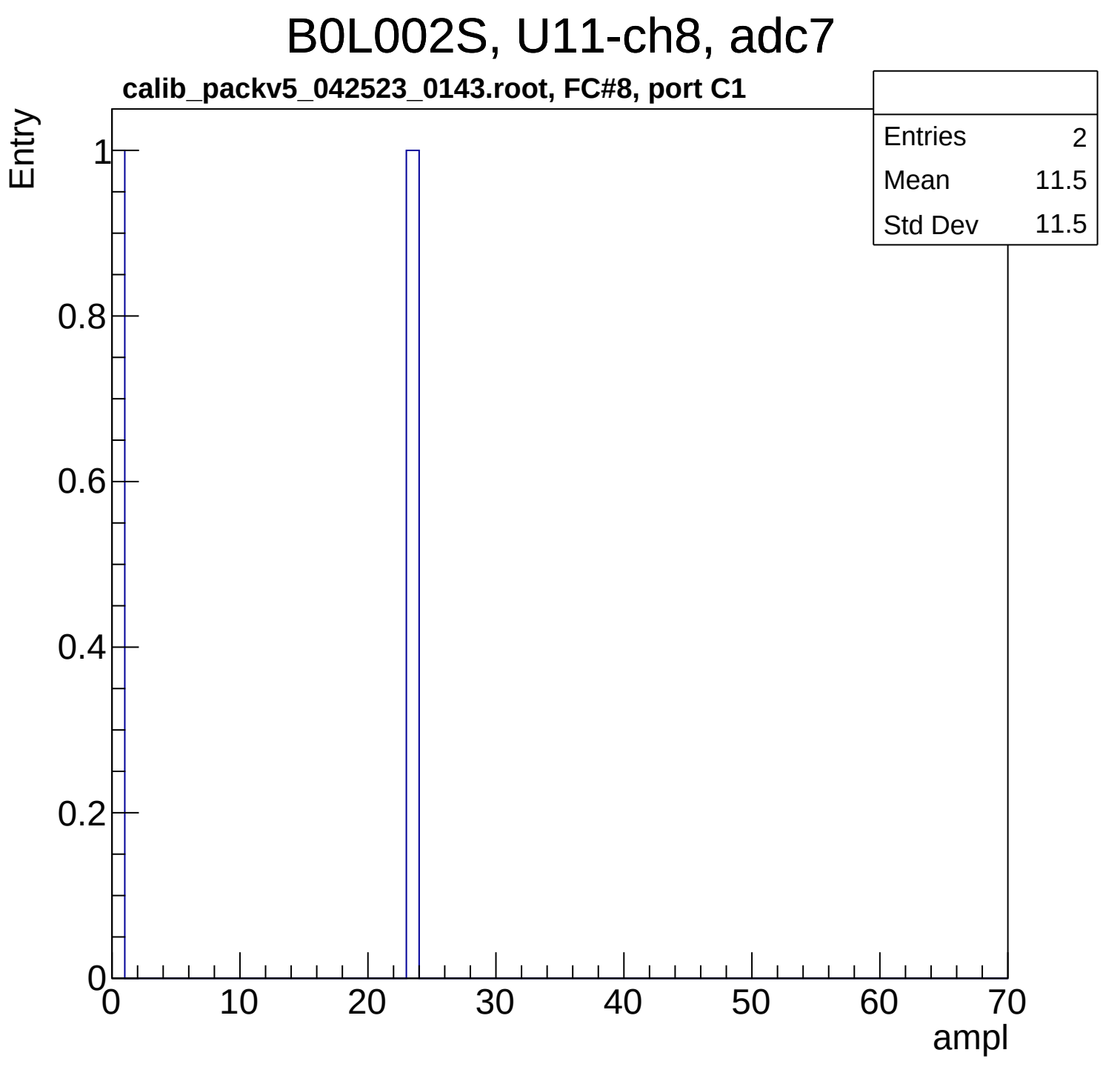
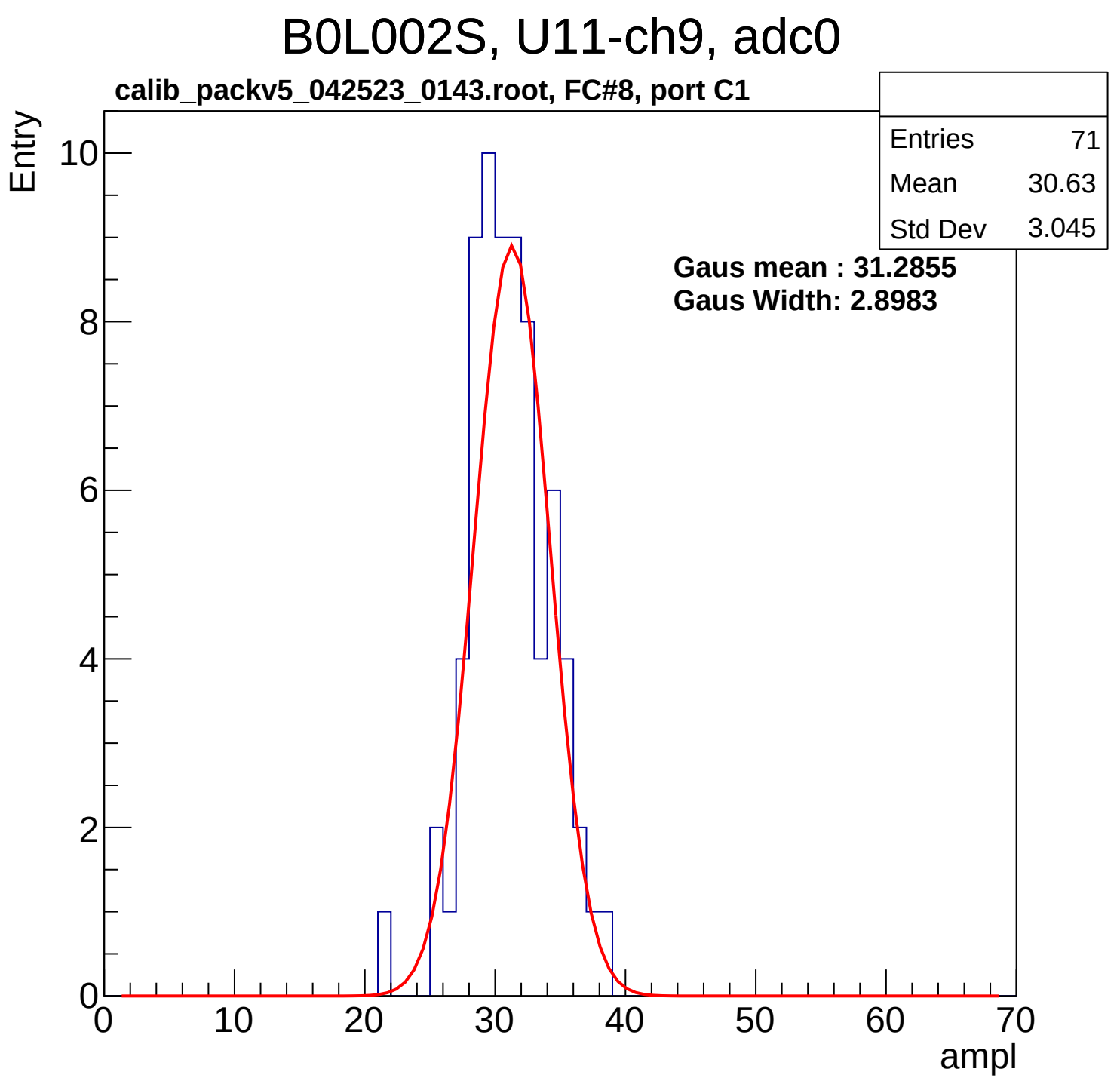
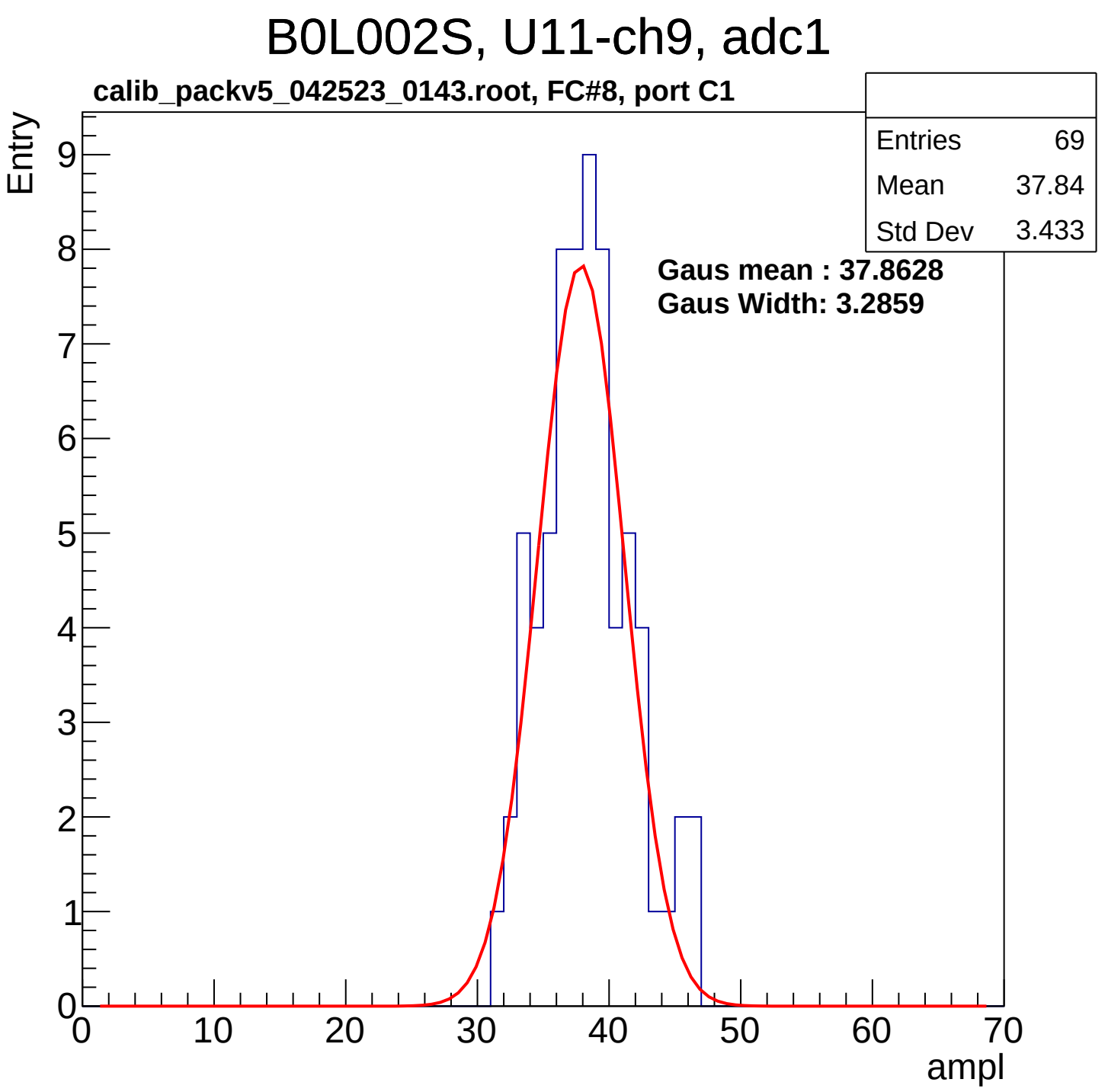

B0L002S, U11-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.63
Std Dev 3.045
Gaus mean : 31.2855
Gaus Width: 2.8983
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.84
Std Dev 3.433
Gaus mean : 37.8628
Gaus Width: 3.2859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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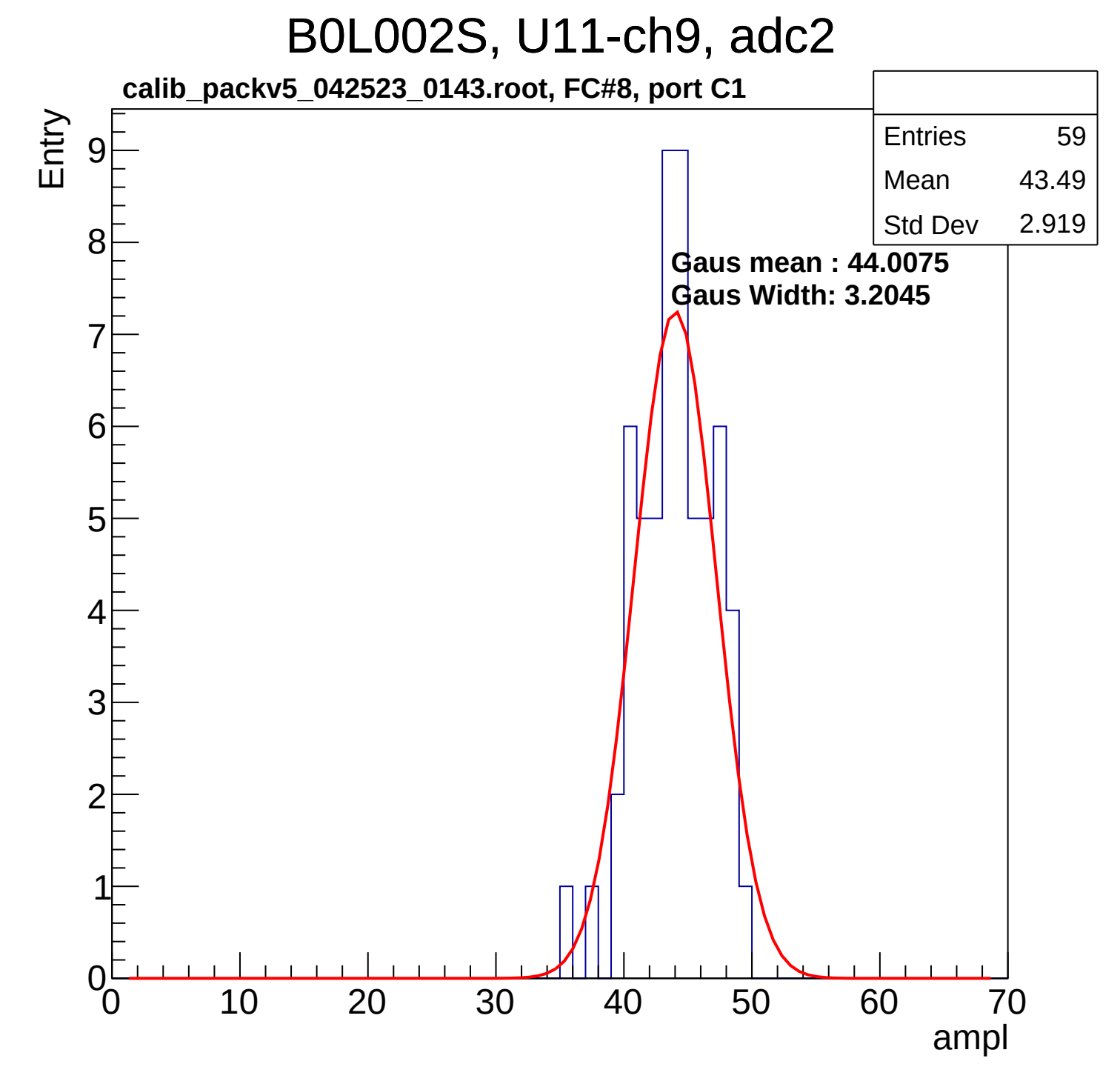
B0L002S, U11-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.49
Std Dev 2.919
Gaus mean : 44.0075
Gaus Width: 3.2045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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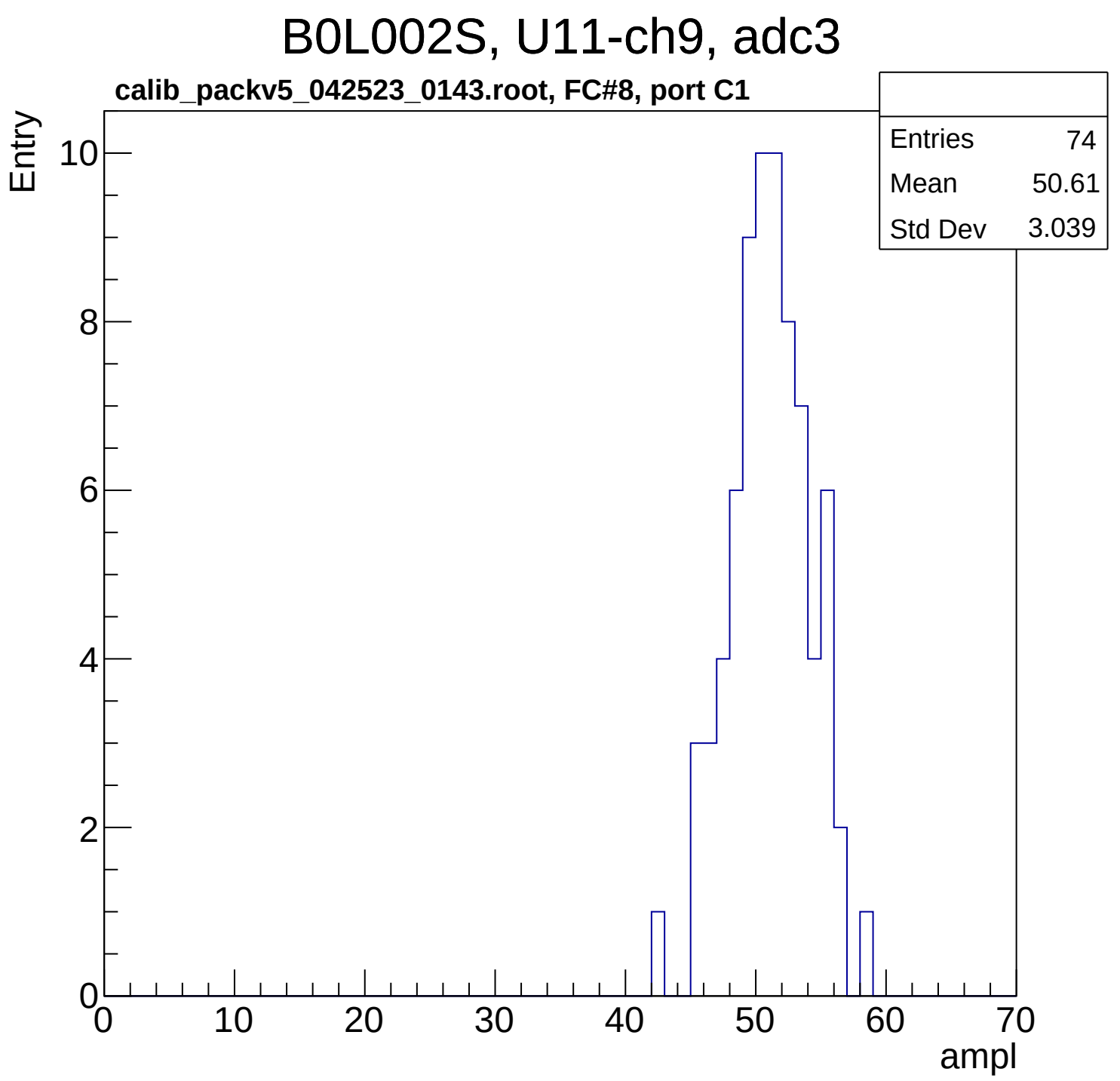
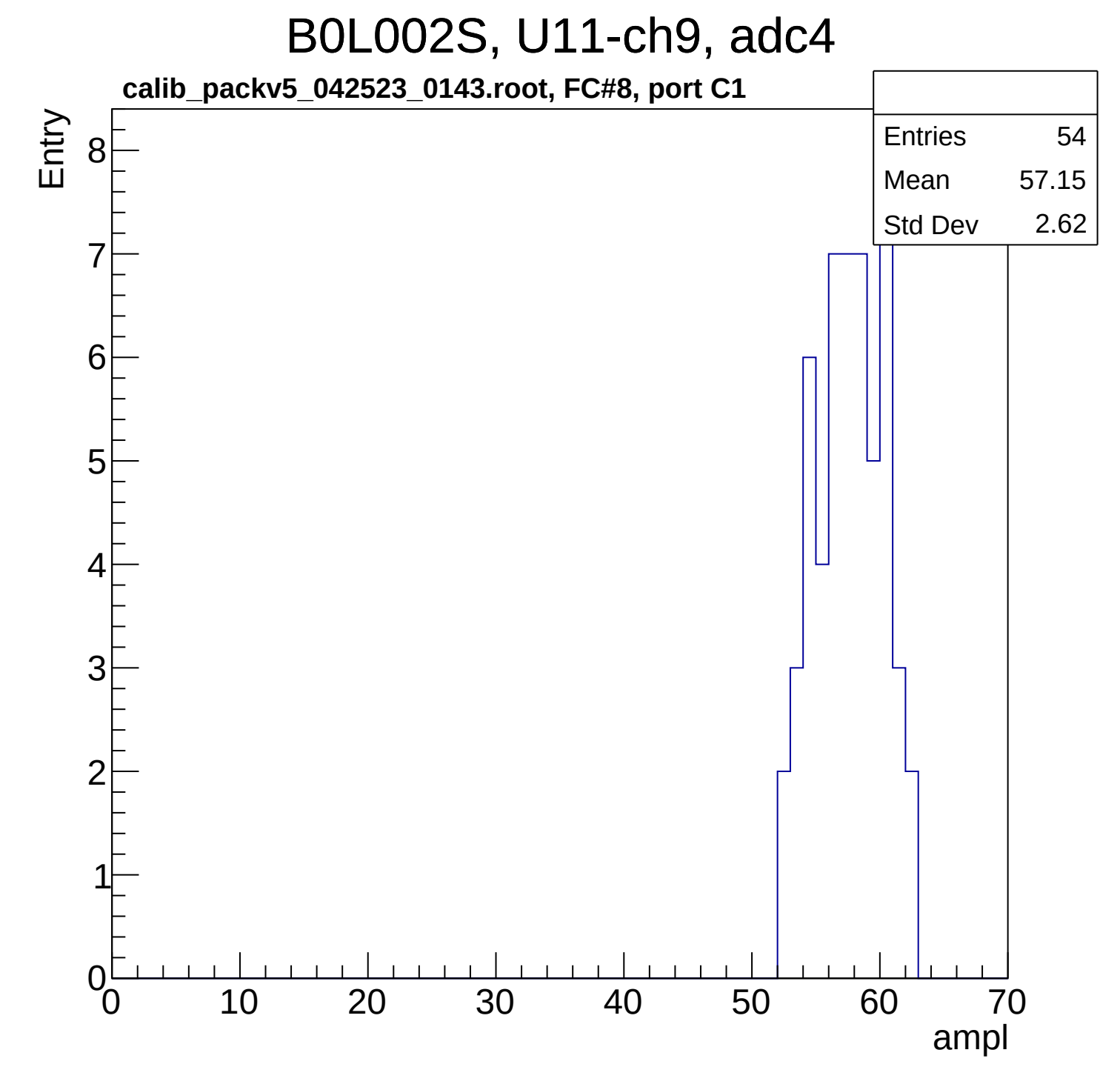
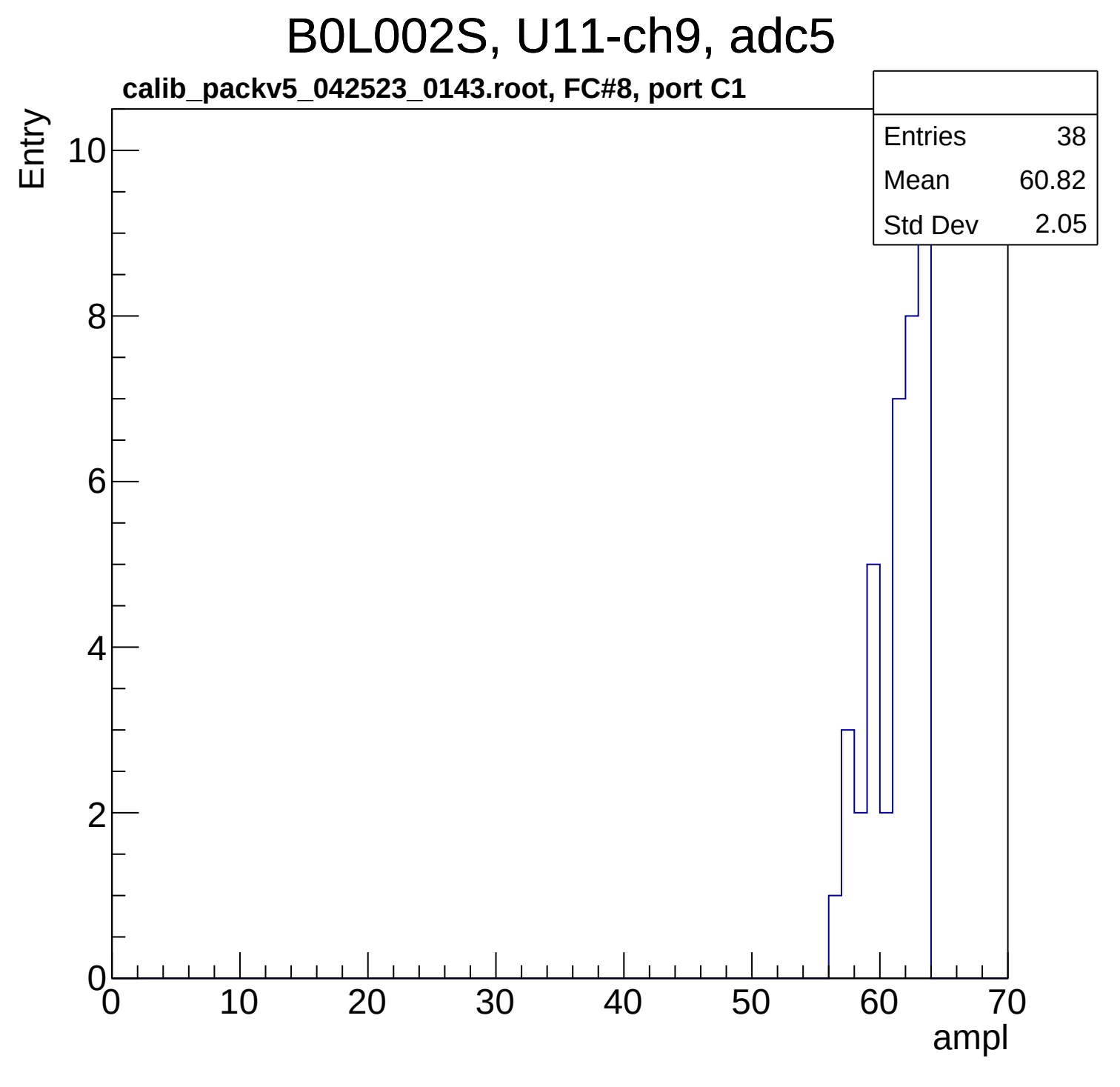
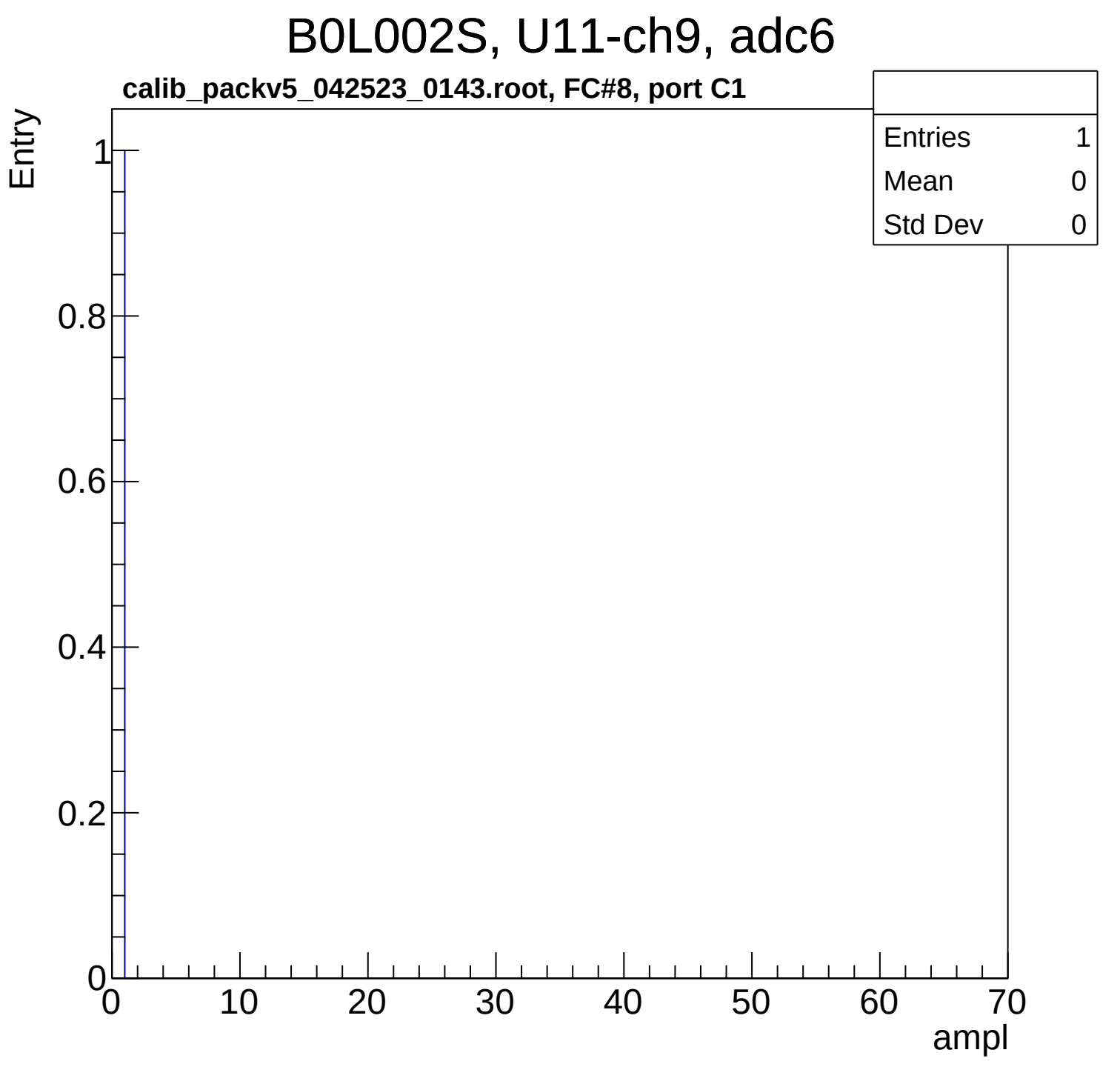
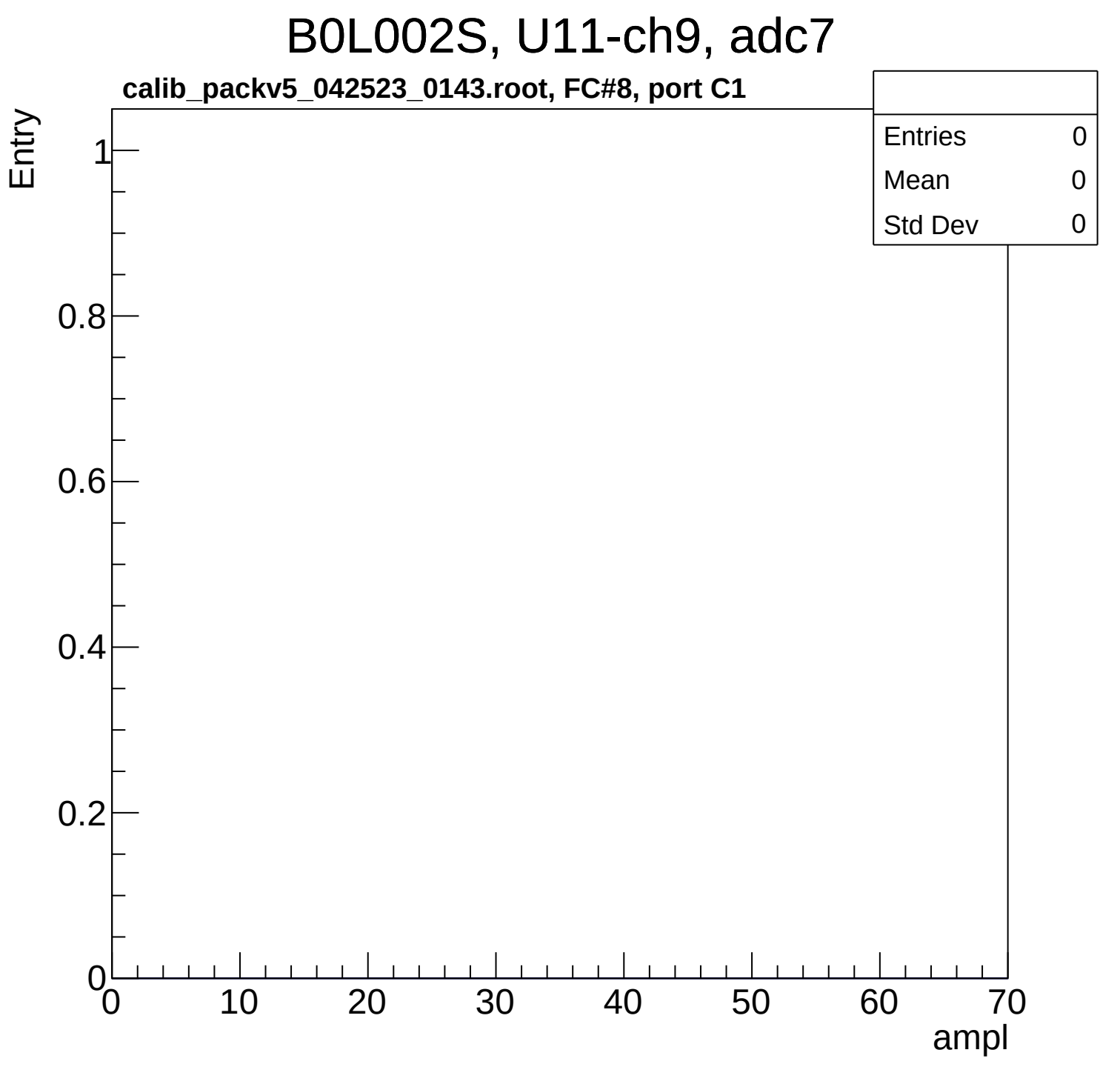

B0L002S, U11-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 50.61
Std Dev 3.039
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.15
Std Dev 2.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.82
Std Dev 2.05
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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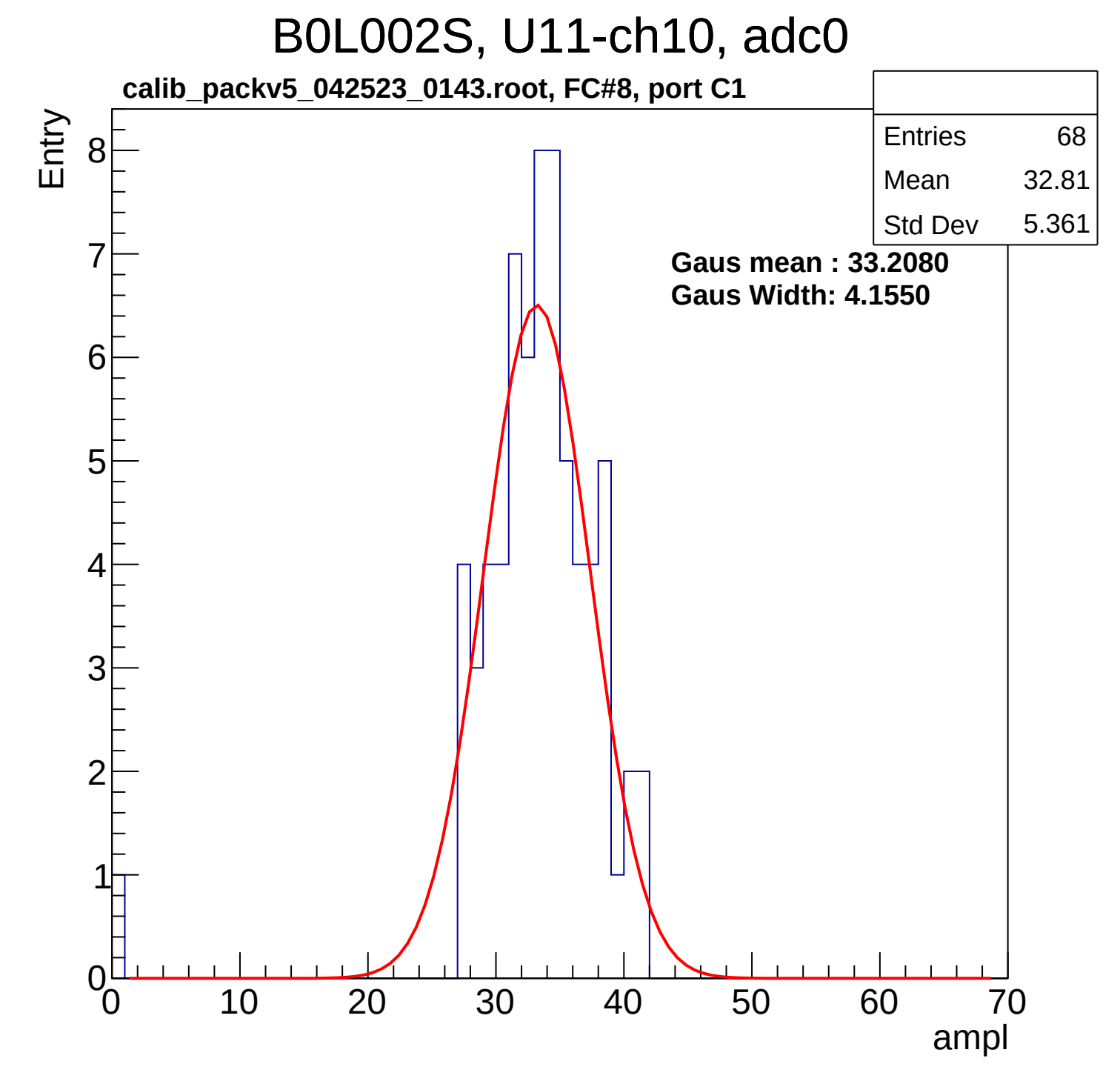
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 32.81
Std Dev 5.361
Gaus mean : 33.2080
Gaus Width: 4.1550
Entry
ampl
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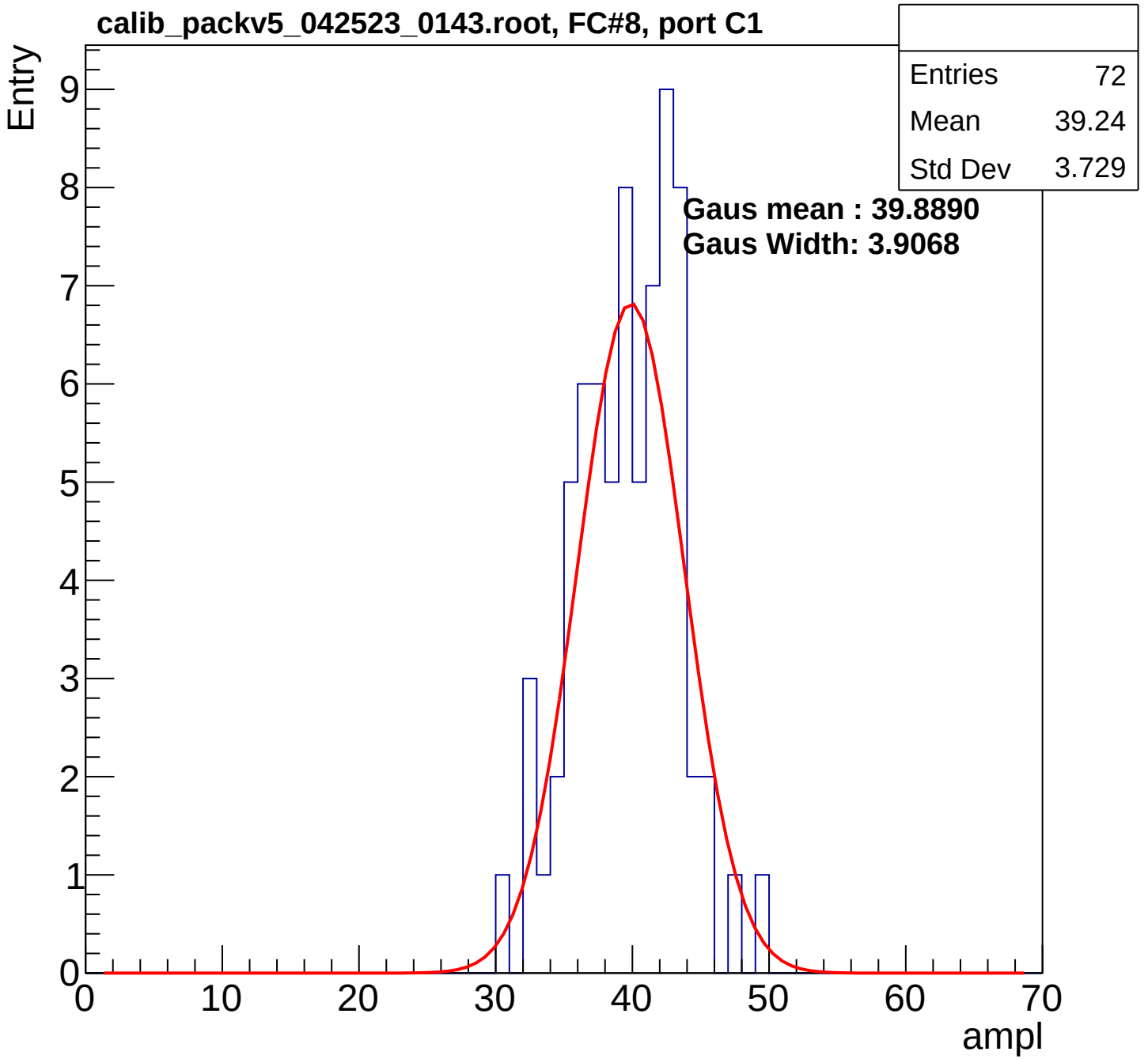
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 39.24
Std Dev 3.729
Gaus mean : 39.8890
Gaus Width: 3.9068
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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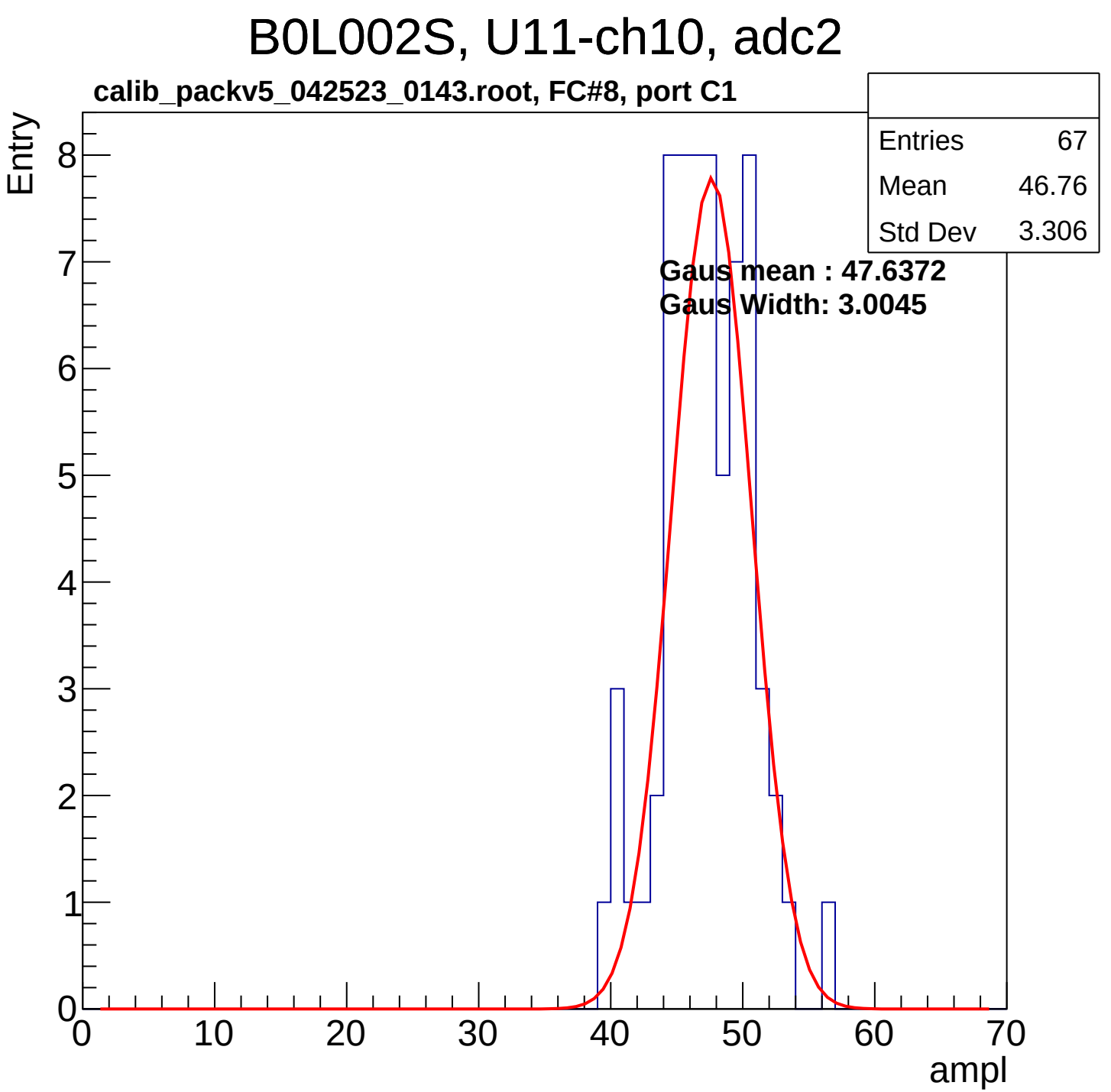
40
50
60
70


B0L002S, U11-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 46.76
Std Dev 3.306
Gaus mean : 47.6372
Gaus Width: 3.0045
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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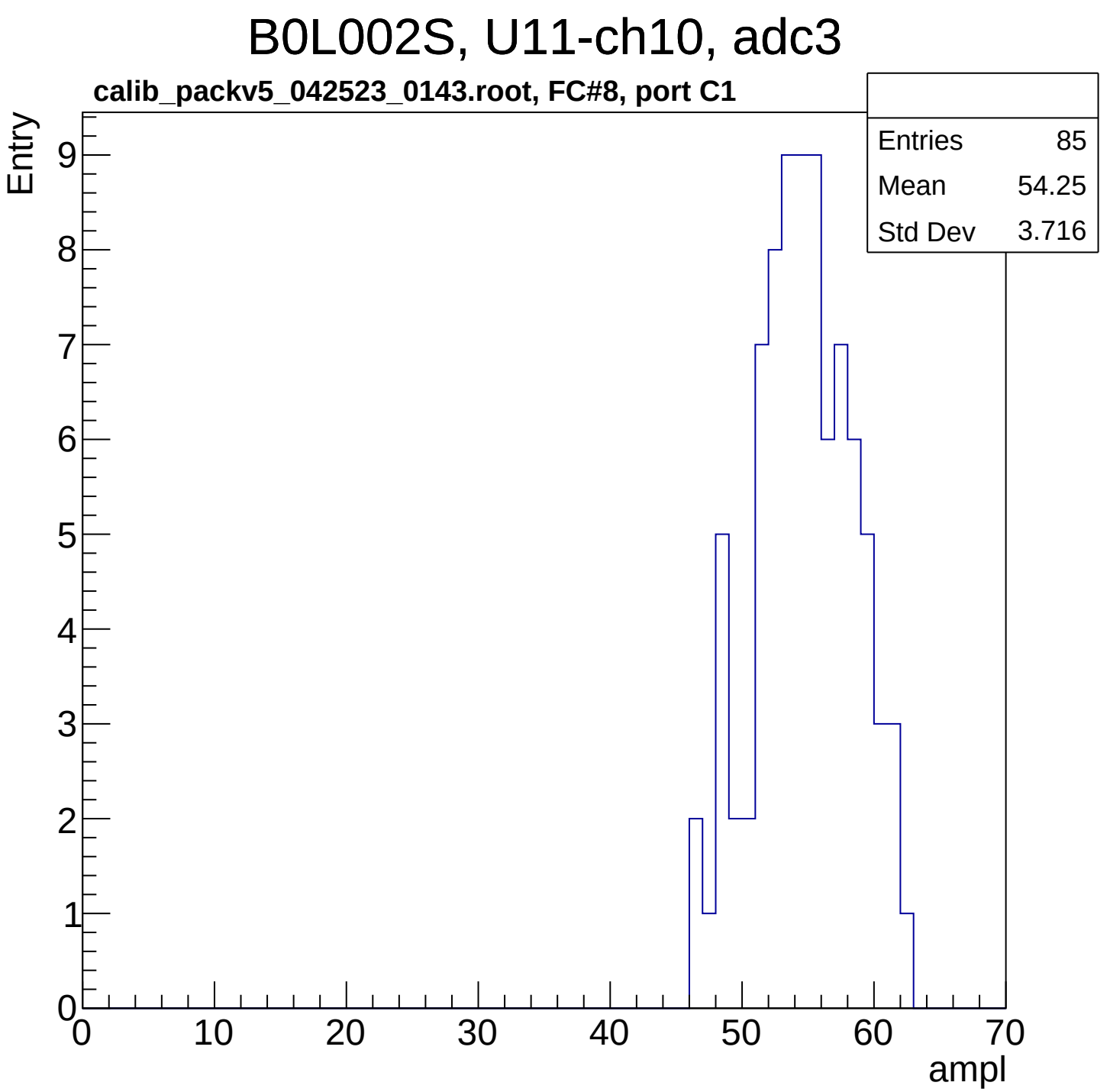
B0L002S, U11-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 54.25
Std Dev 3.716
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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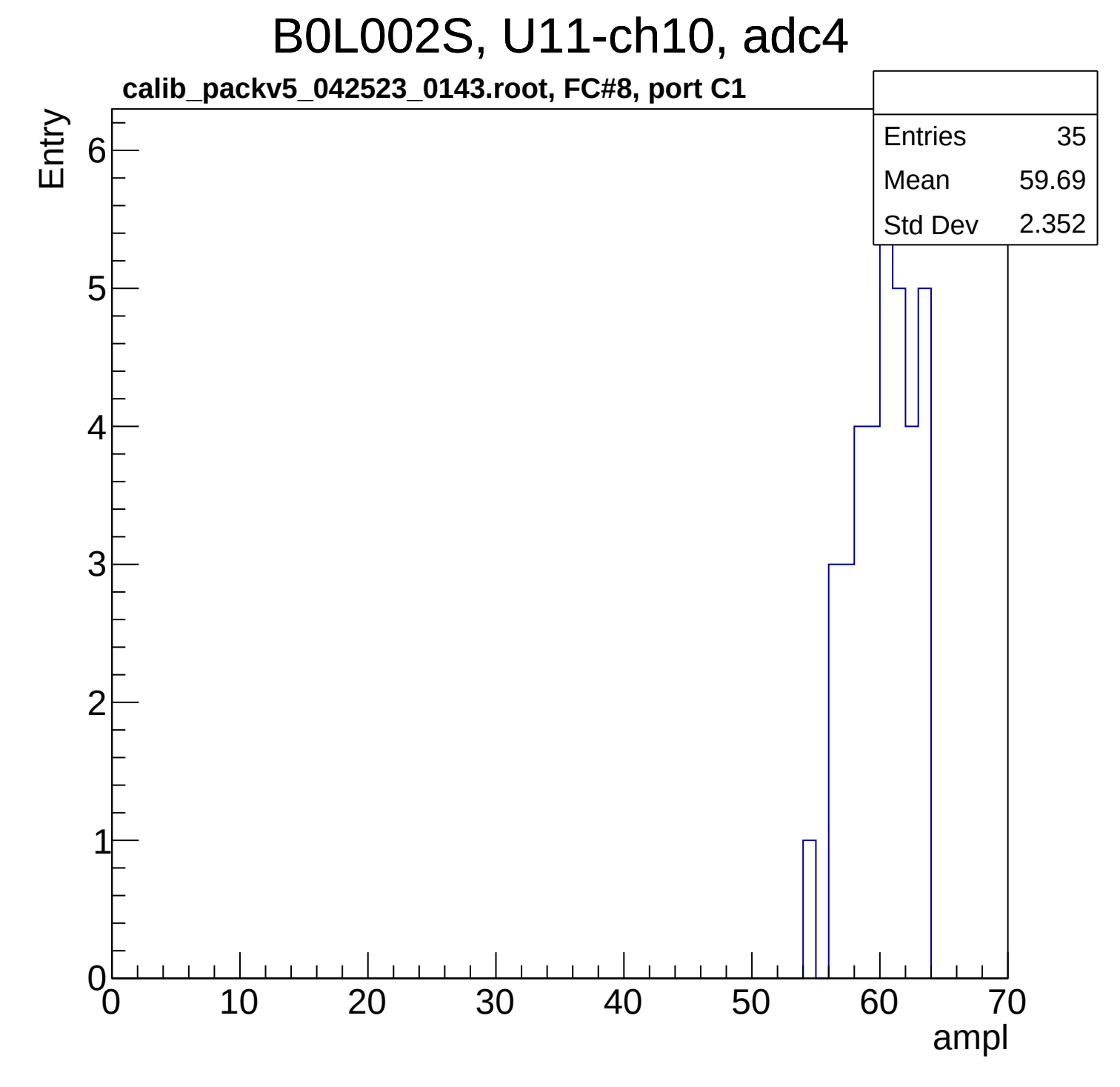
B0L002S, U11-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.69
Std Dev 2.352
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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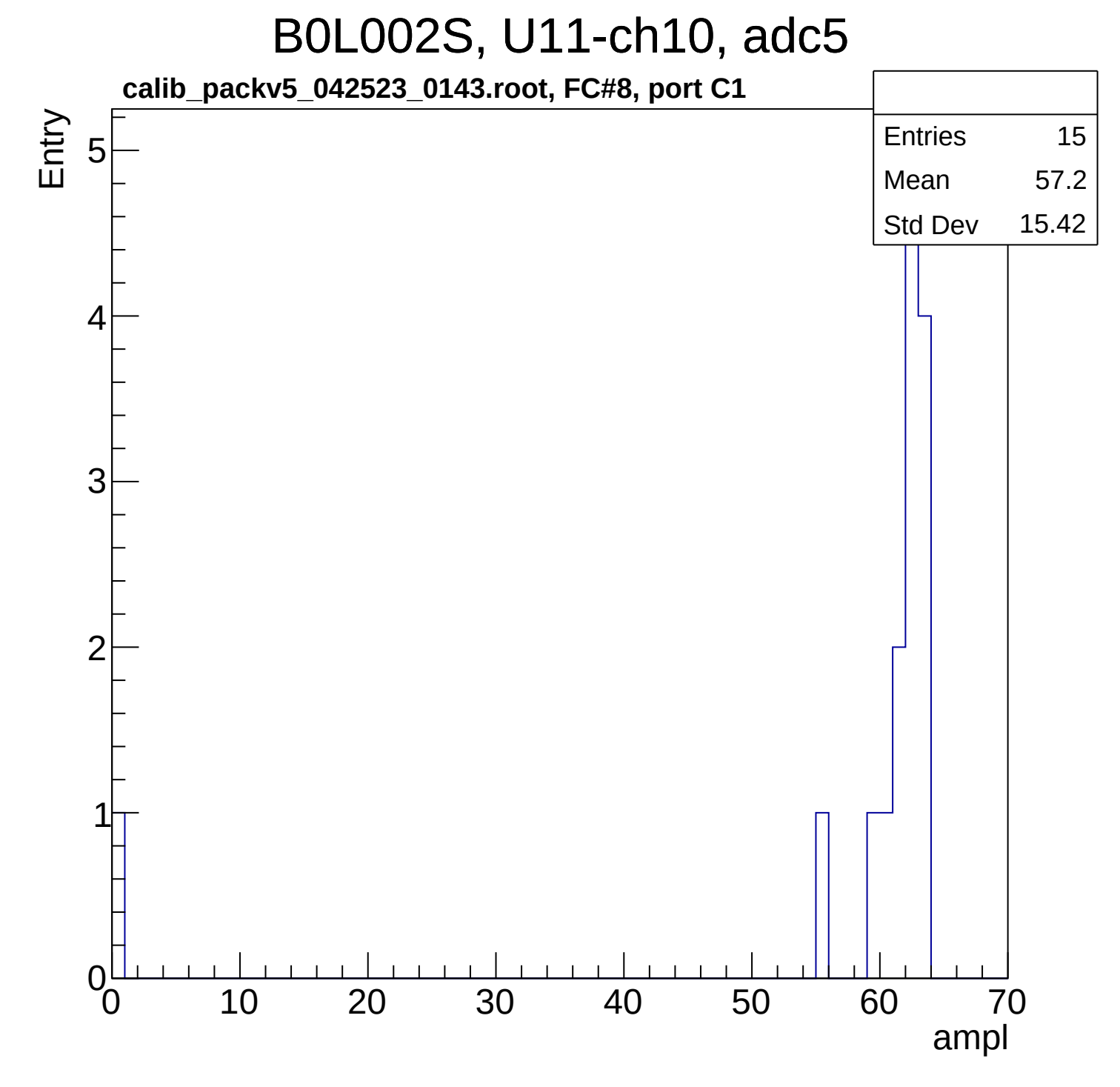
B0L002S, U11-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 57.2
Std Dev 15.42
Entry
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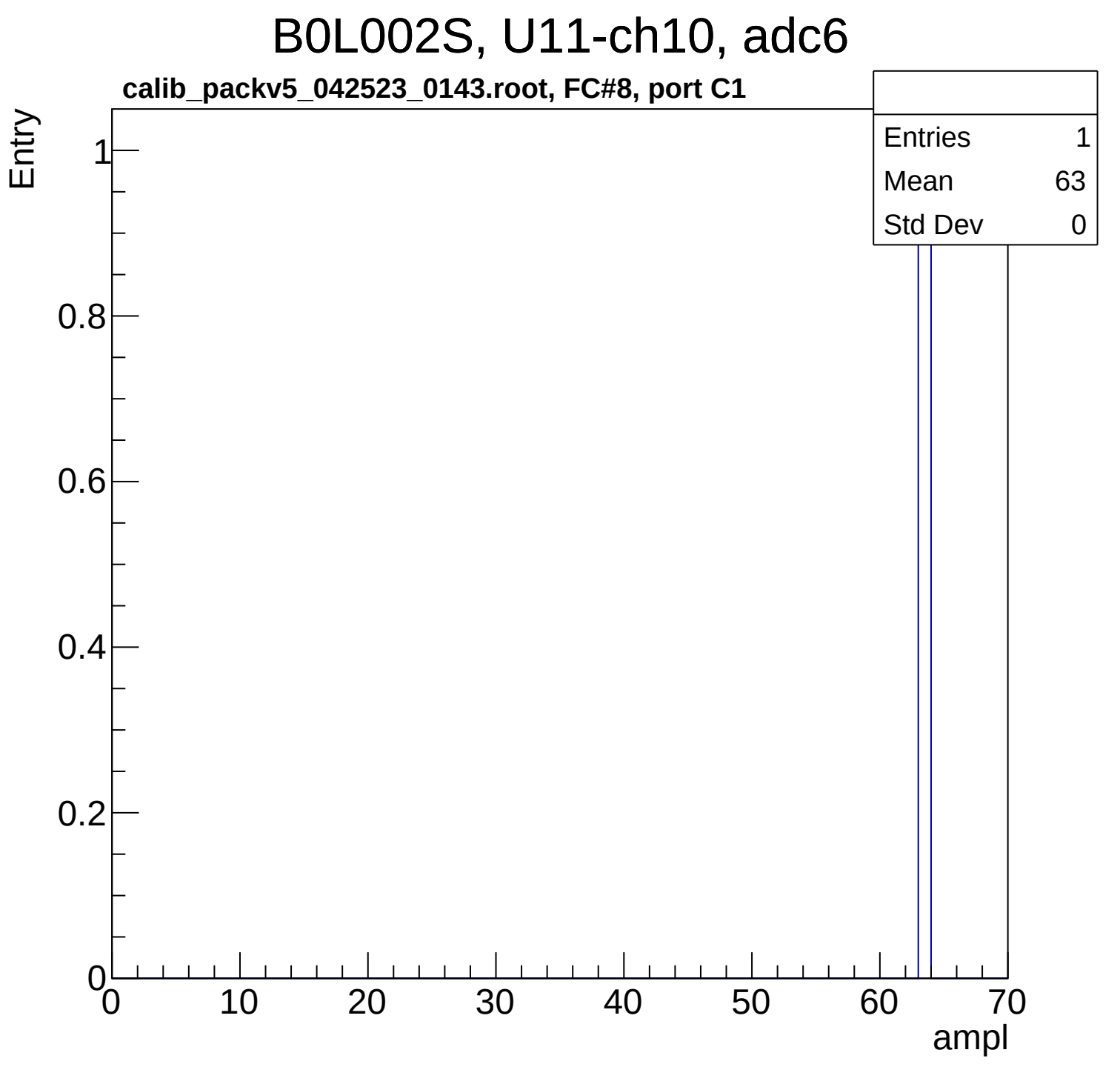
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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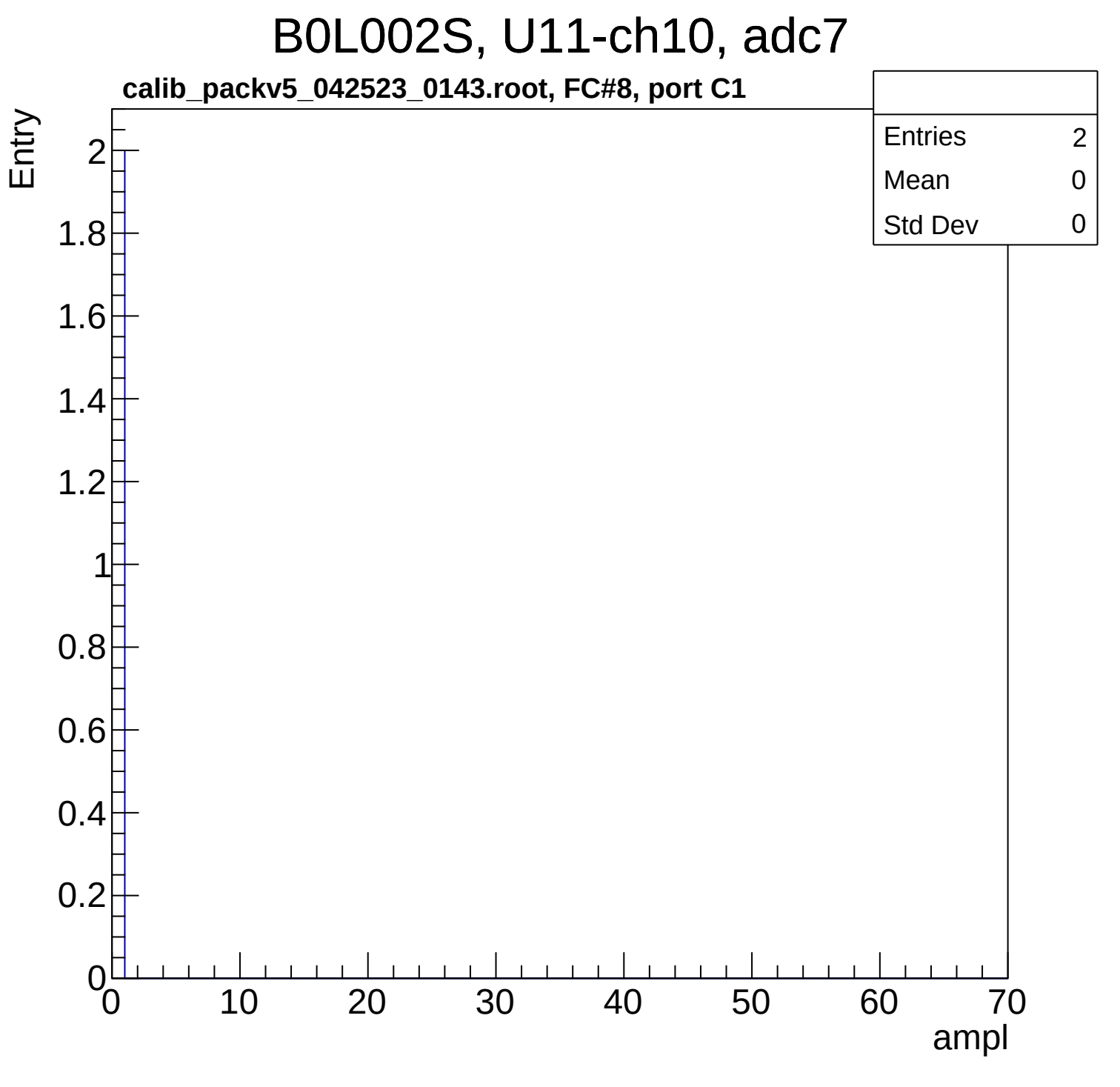
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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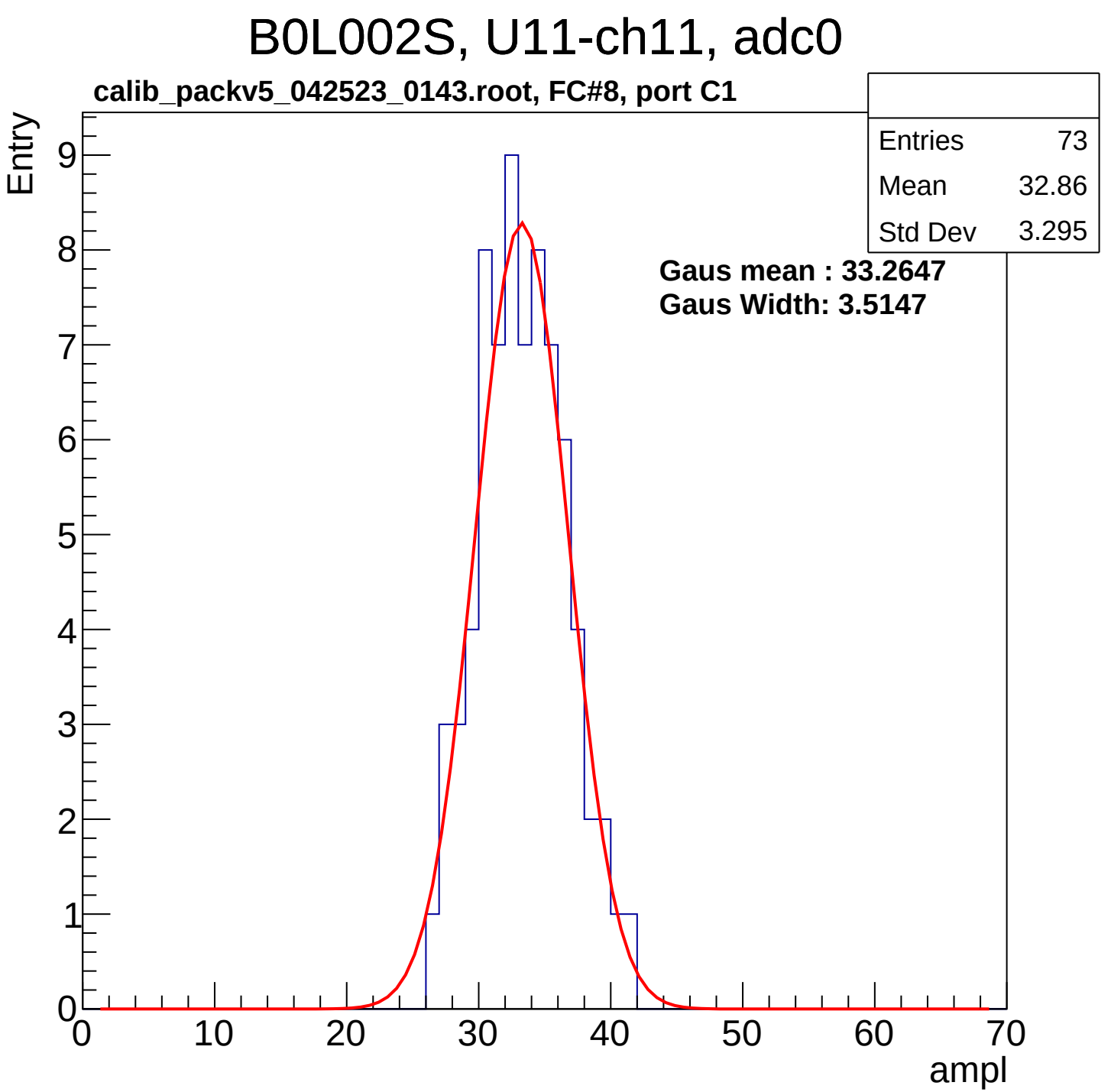
B0L002S, U11-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 32.86
Std Dev 3.295
Gaus mean : 33.2647
Gaus Width: 3.5147
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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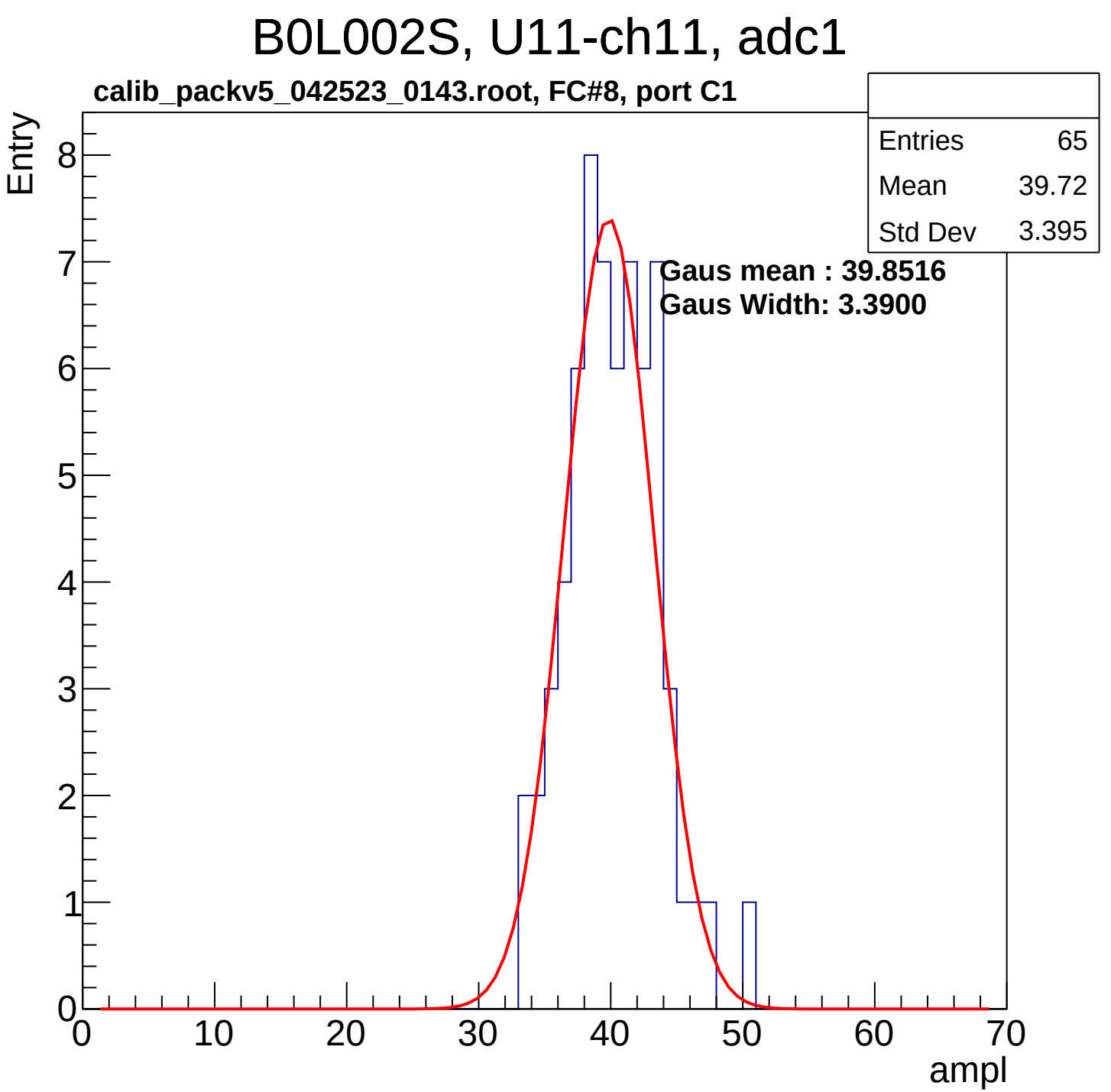
B0L002S, U11-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 39.72
Std Dev 3.395
Gaus mean : 39.8516
Gaus Width: 3.3900
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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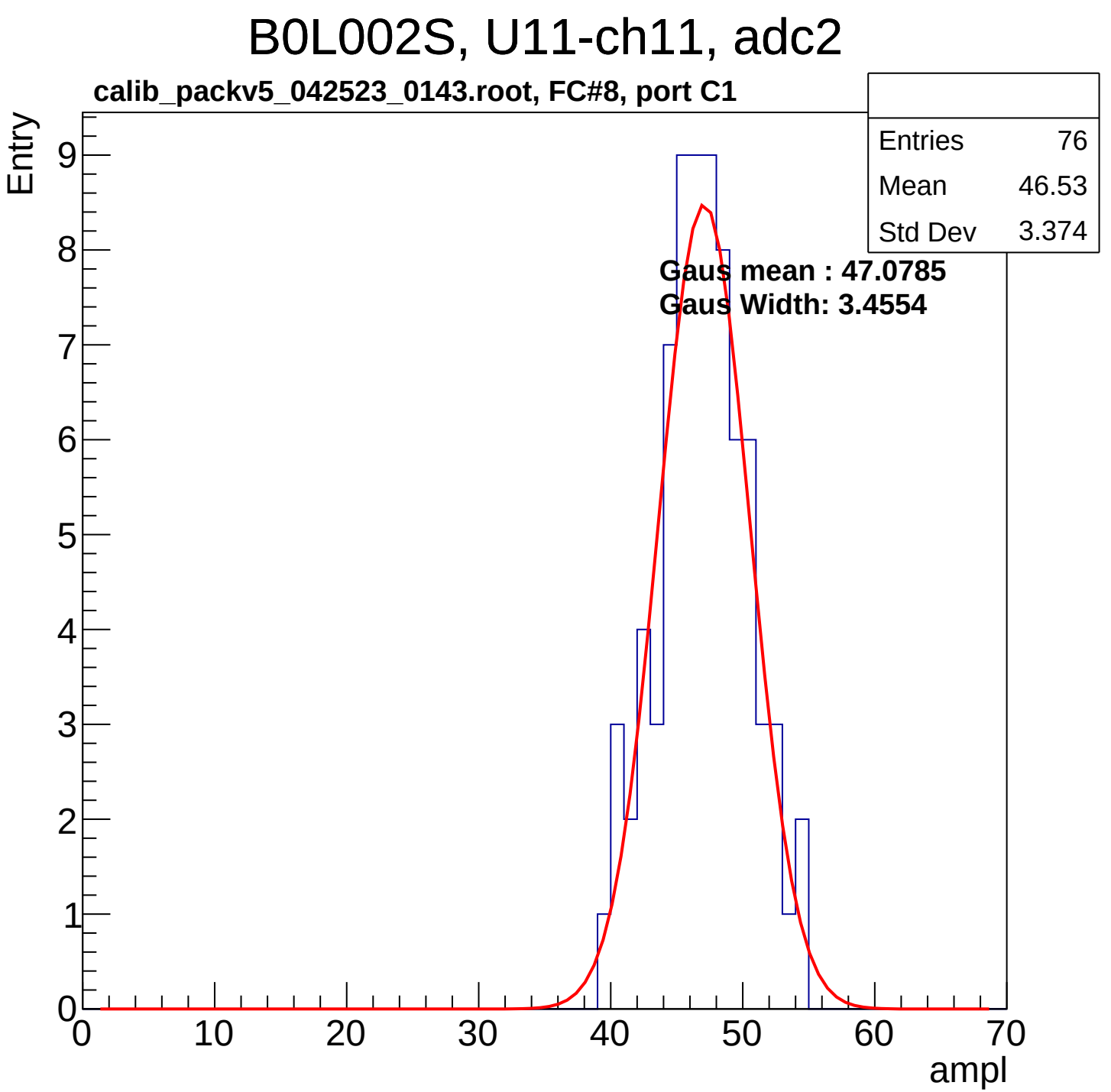
B0L002S, U11-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 46.53
Std Dev 3.374
Gaus mean : 47.0785
Gaus Width: 3.4554
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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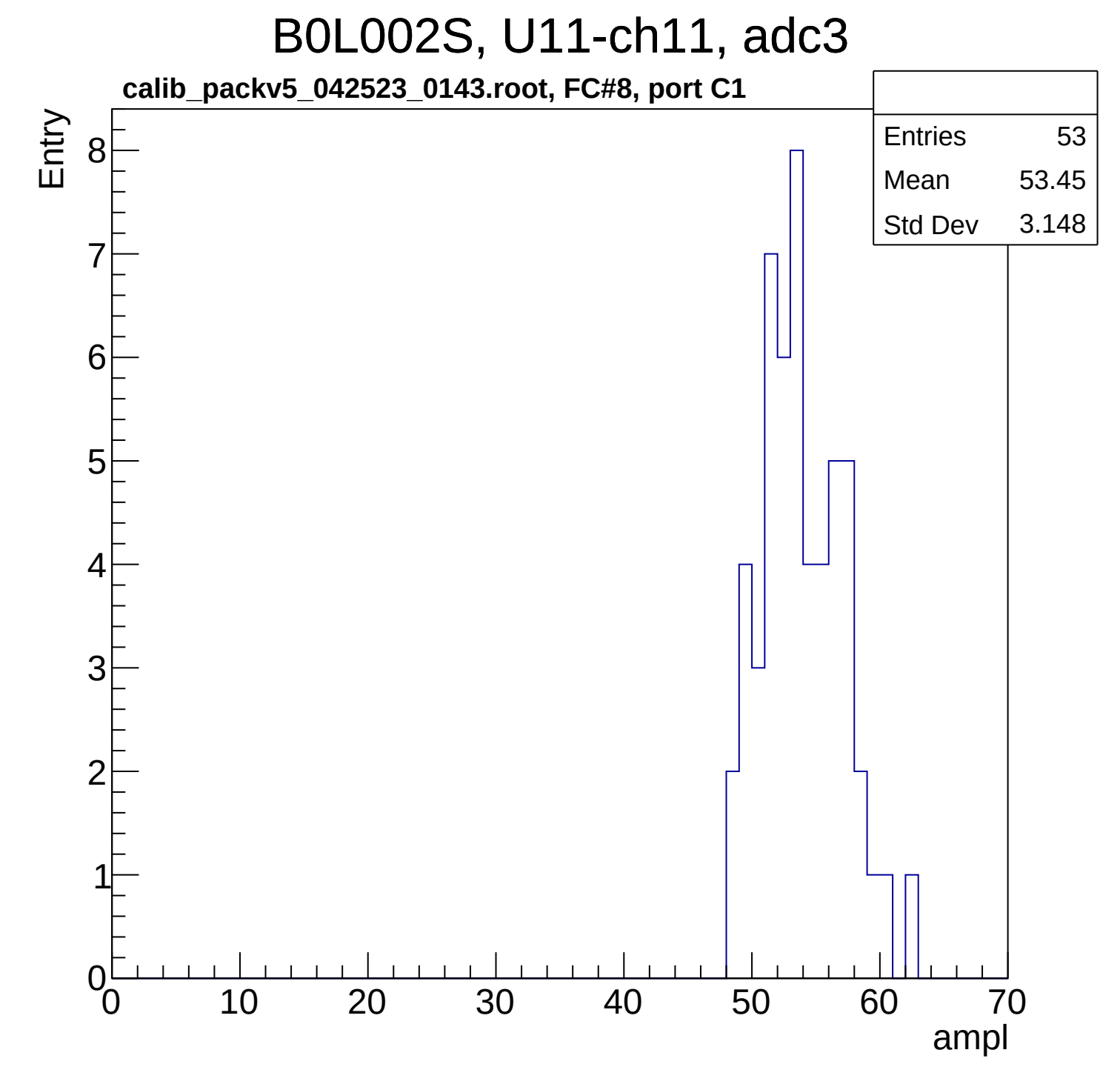
B0L002S, U11-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 53.45
Std Dev 3.148
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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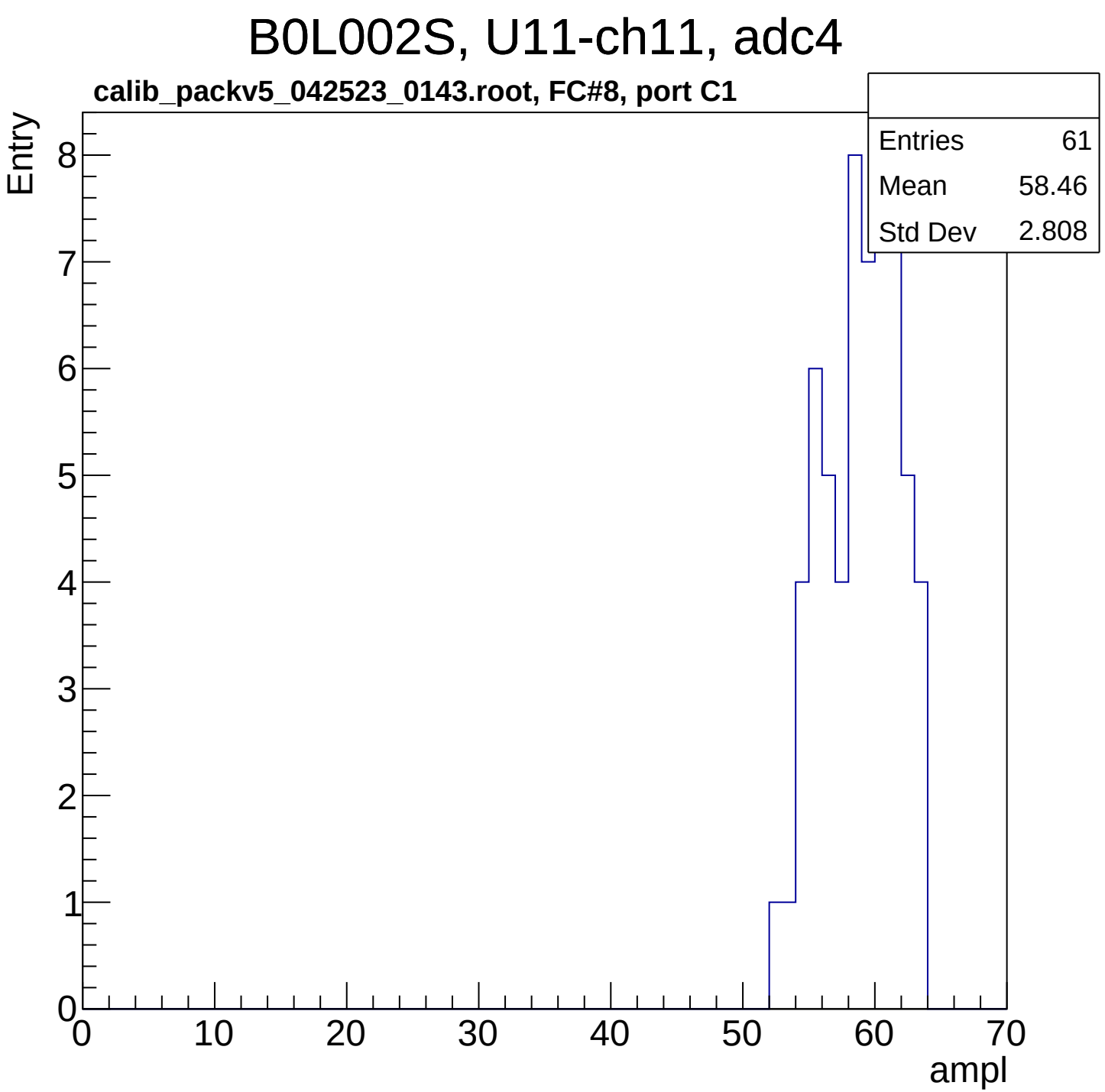
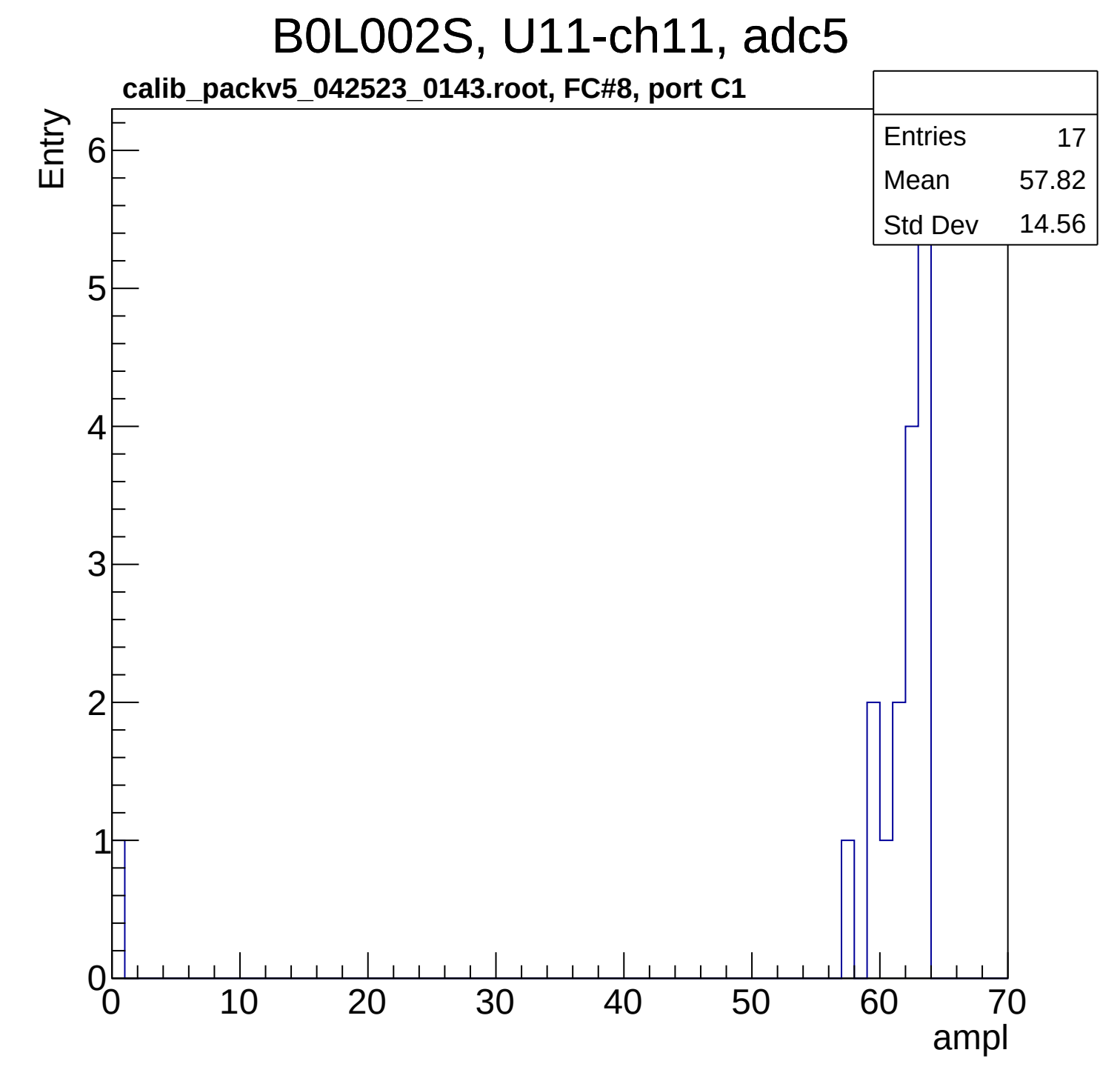

B0L002S, U11-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 58.46
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 57.82
Std Dev 14.56
Entry
0
1
2
3
4
5
6
0
10
20
30
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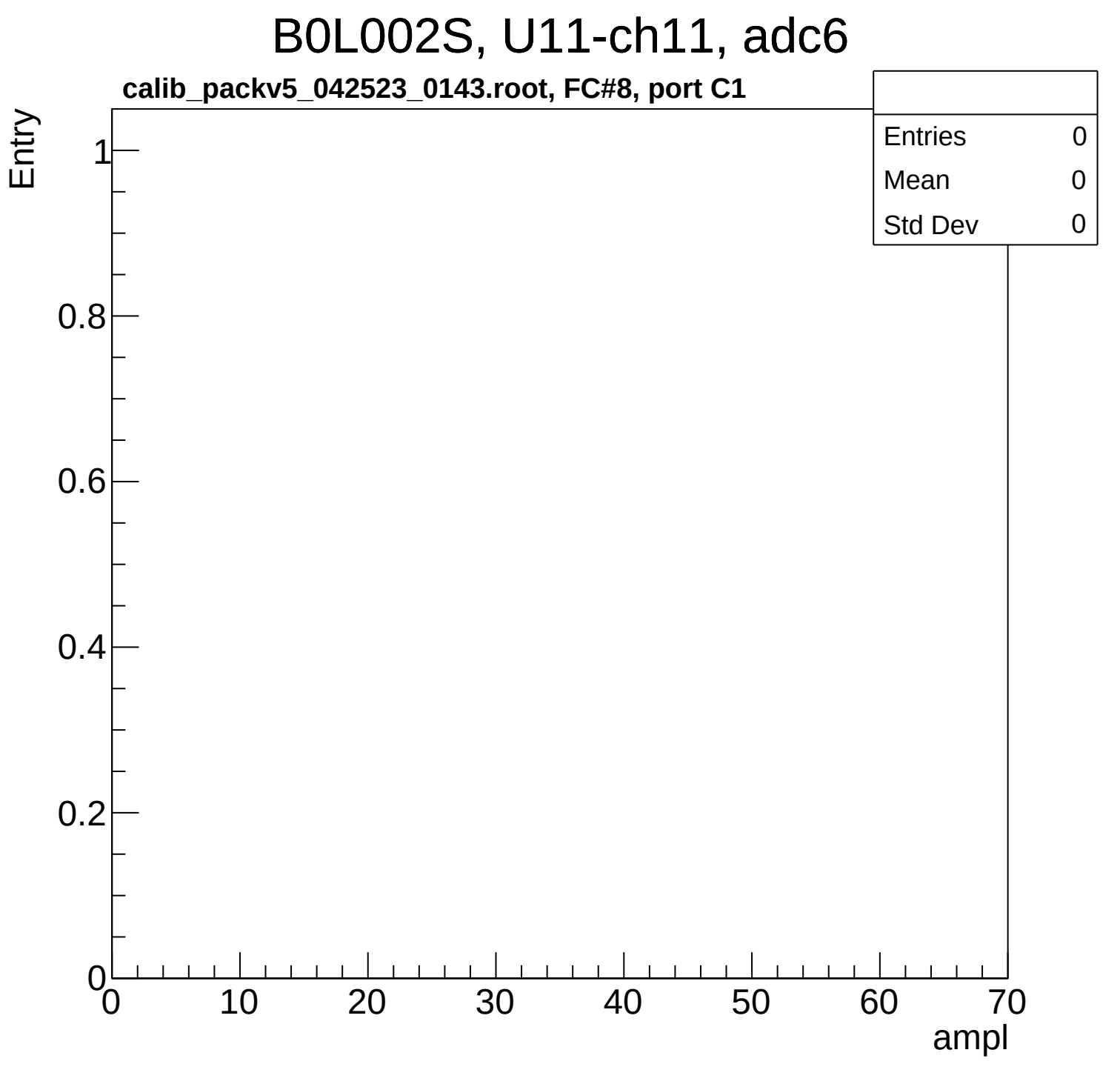
50
60
70
ampl


B0L002S, U11-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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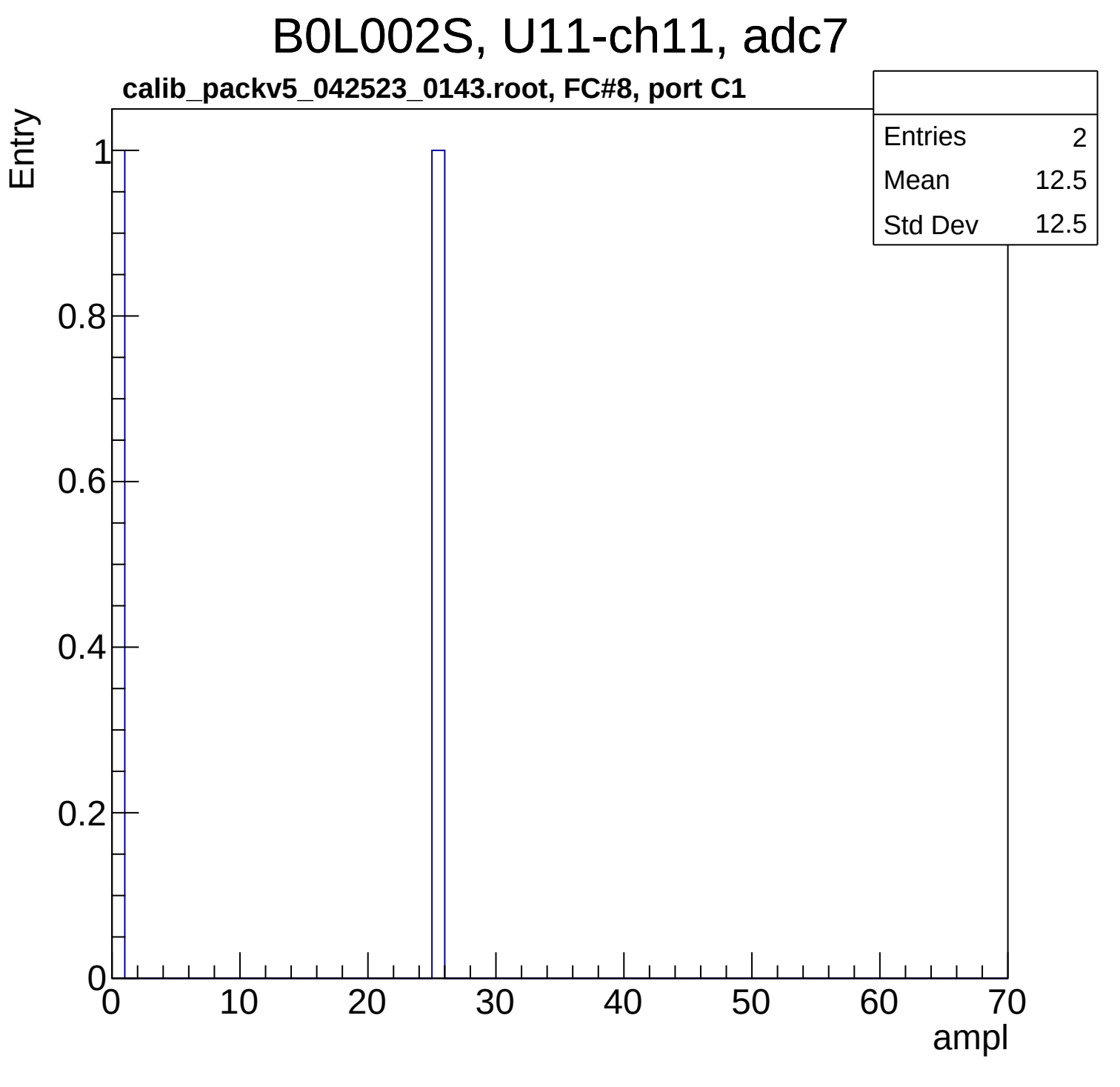
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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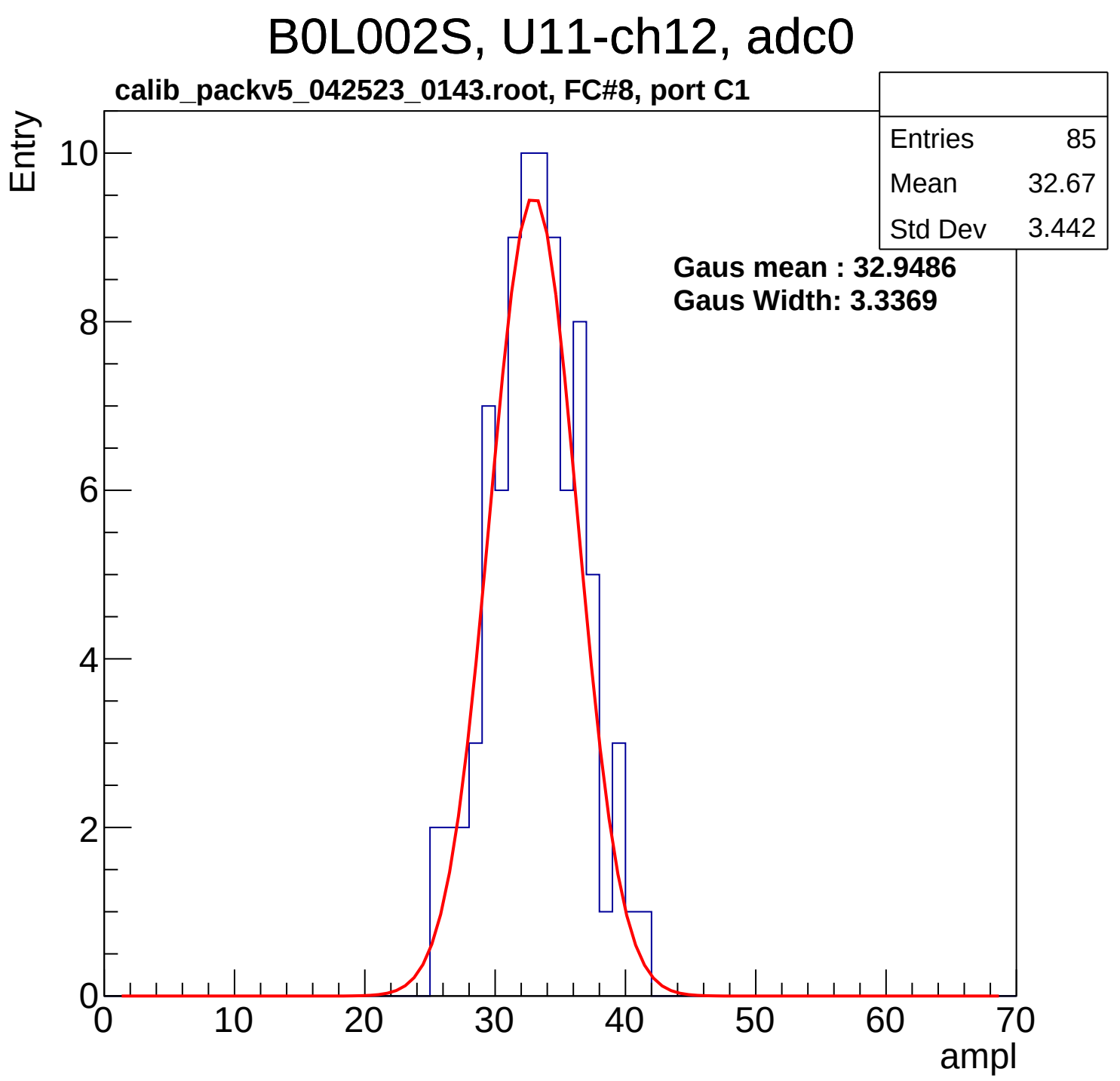
40
50
60
70
ampl


B0L002S, U11-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 32.67
Std Dev 3.442
Gaus mean : 32.9486
Gaus Width: 3.3369
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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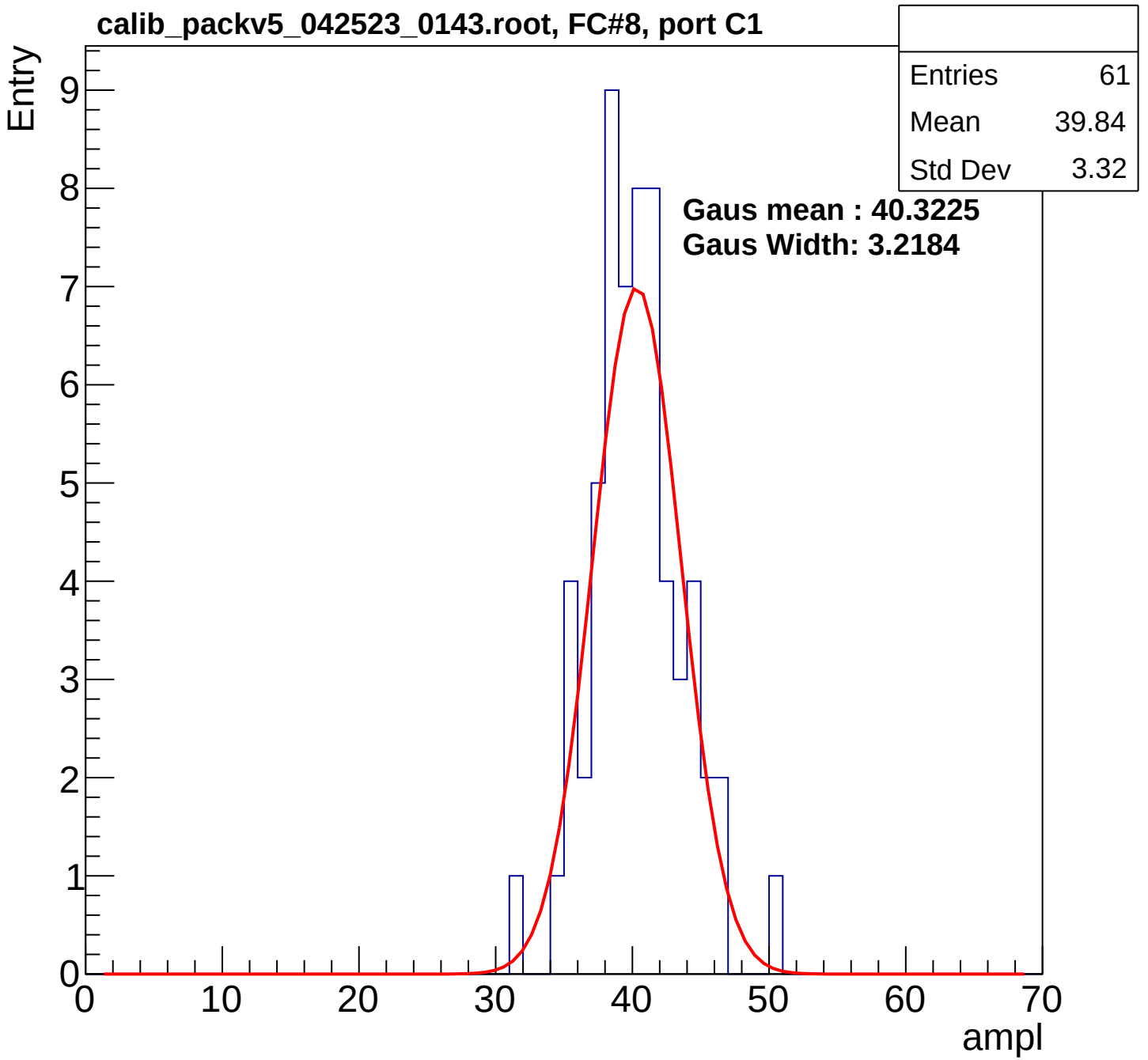
B0L002S, U11-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 39.84
Std Dev 3.32
Gaus mean : 40.3225
Gaus Width: 3.2184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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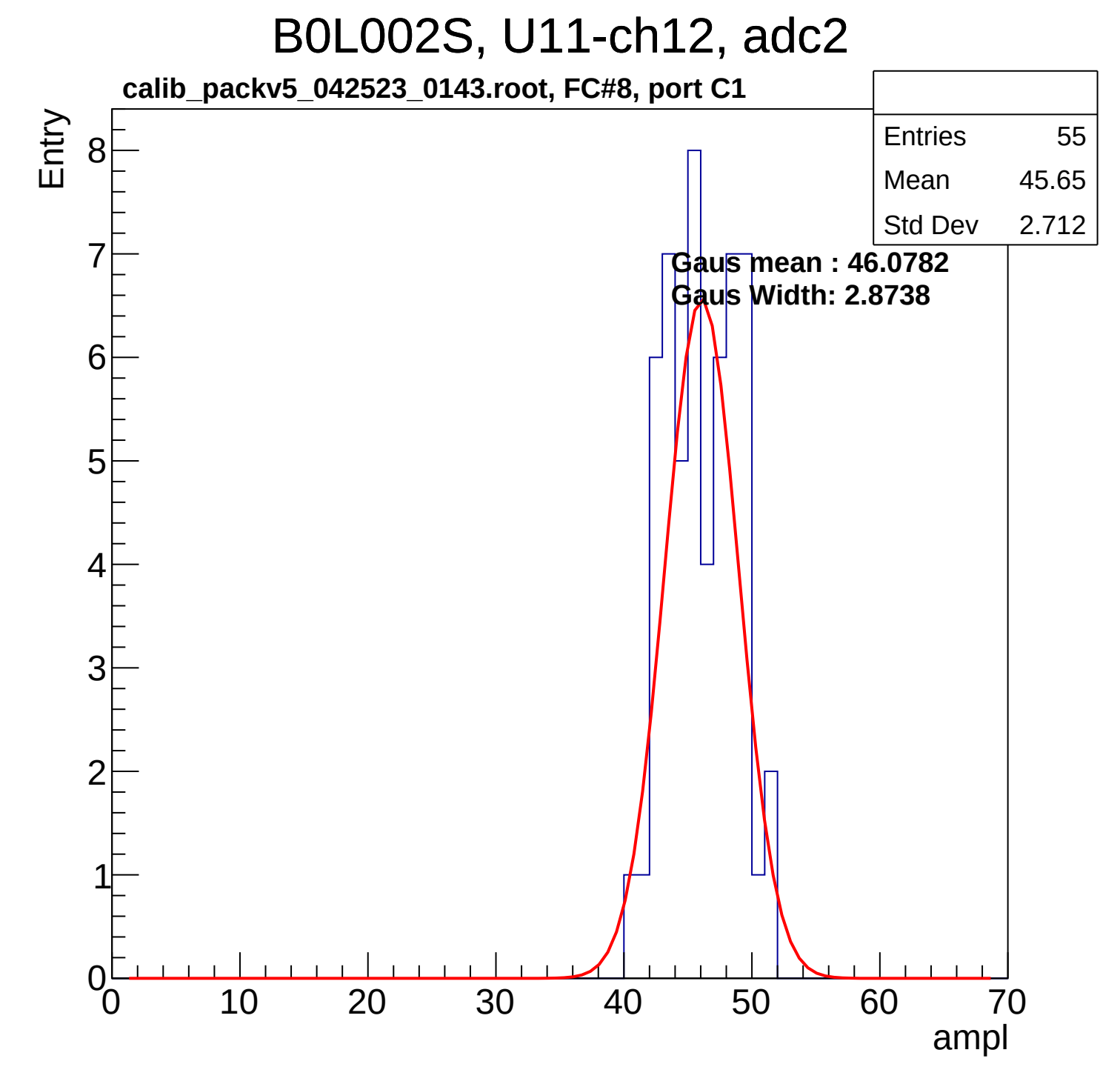
B0L002S, U11-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.65
Std Dev 2.712
Gaus mean : 46.0782
Gaus Width: 2.8738
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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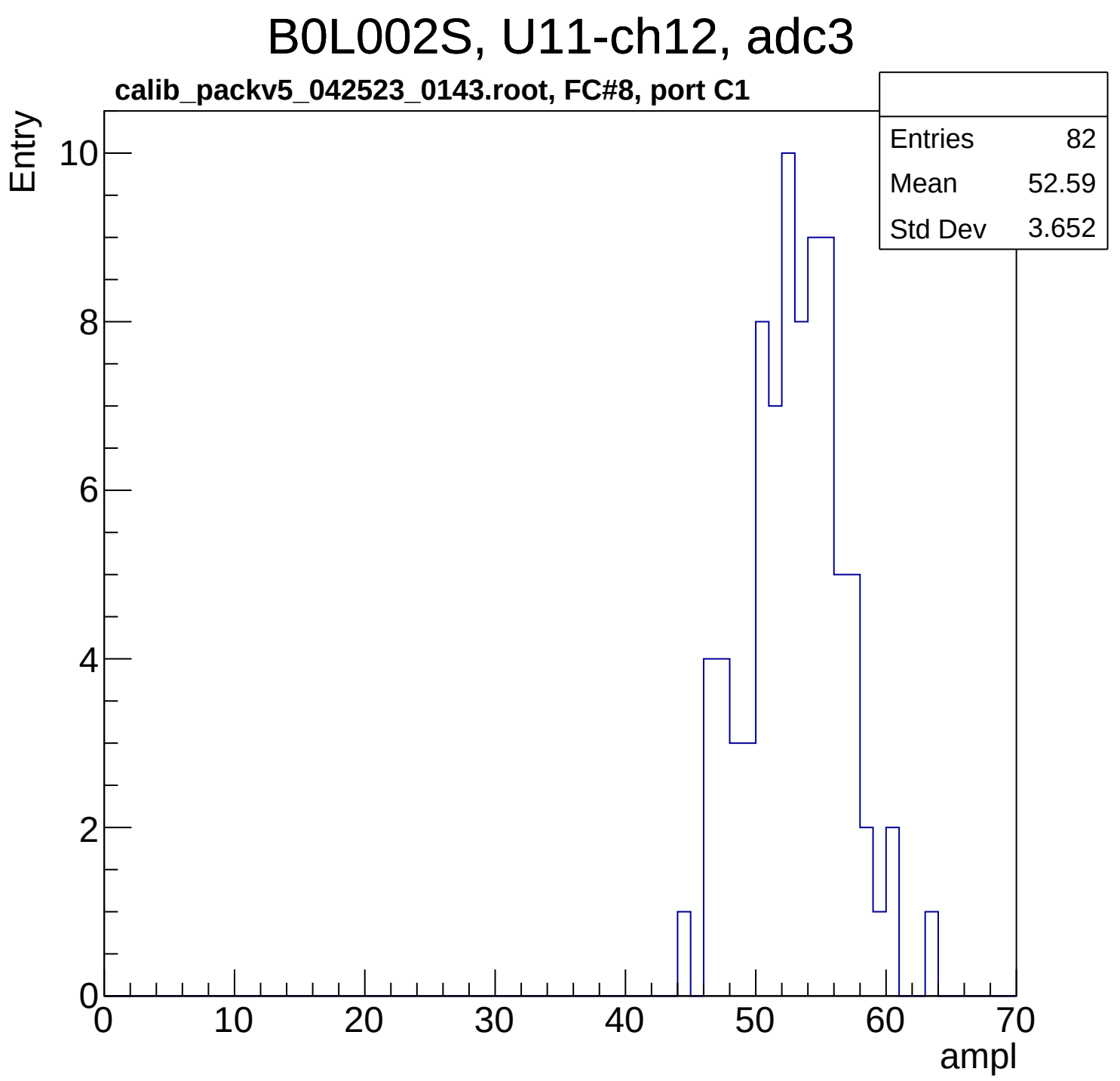
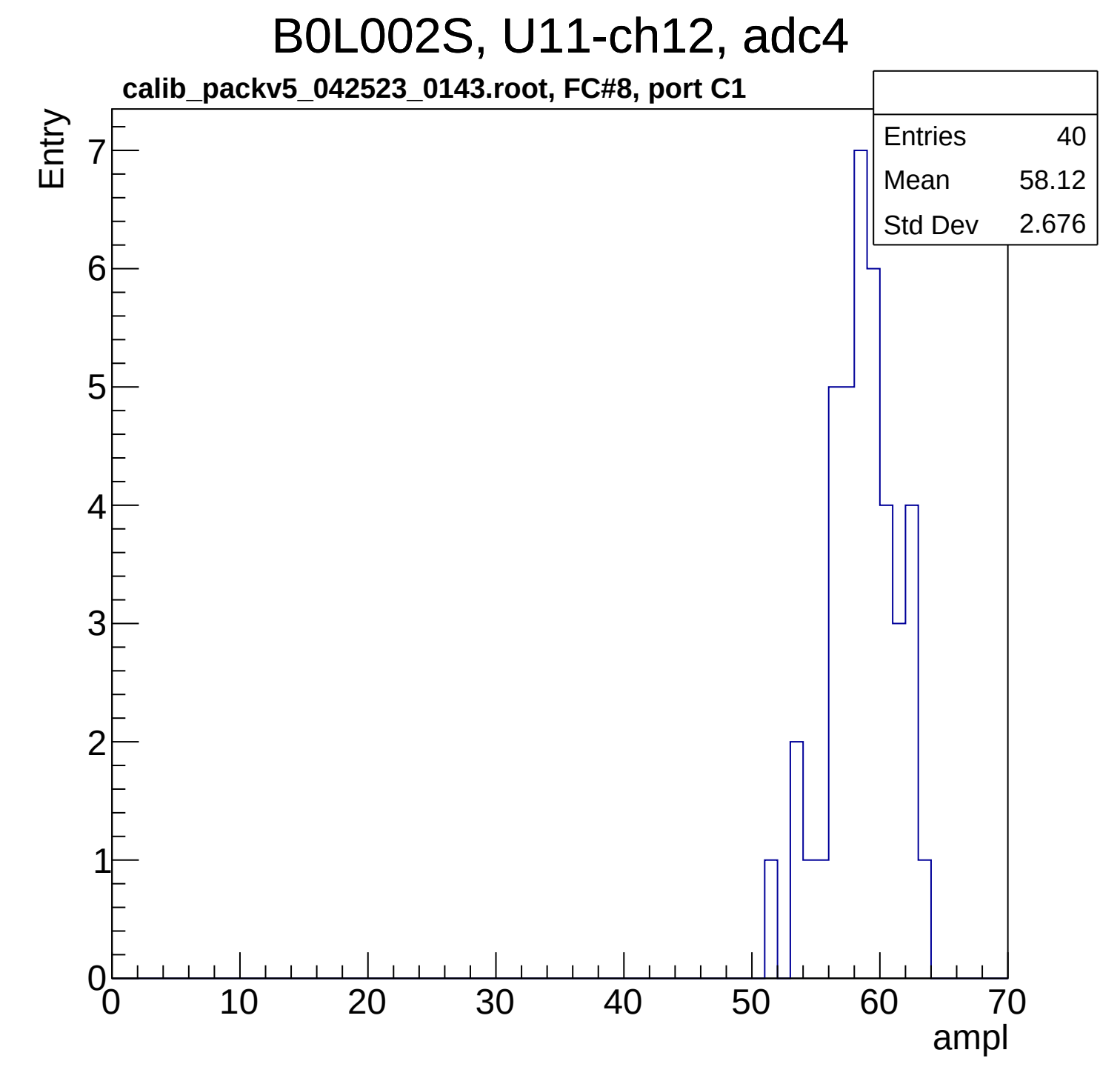

B0L002S, U11-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 52.59
Std Dev 3.652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.12
Std Dev 2.676
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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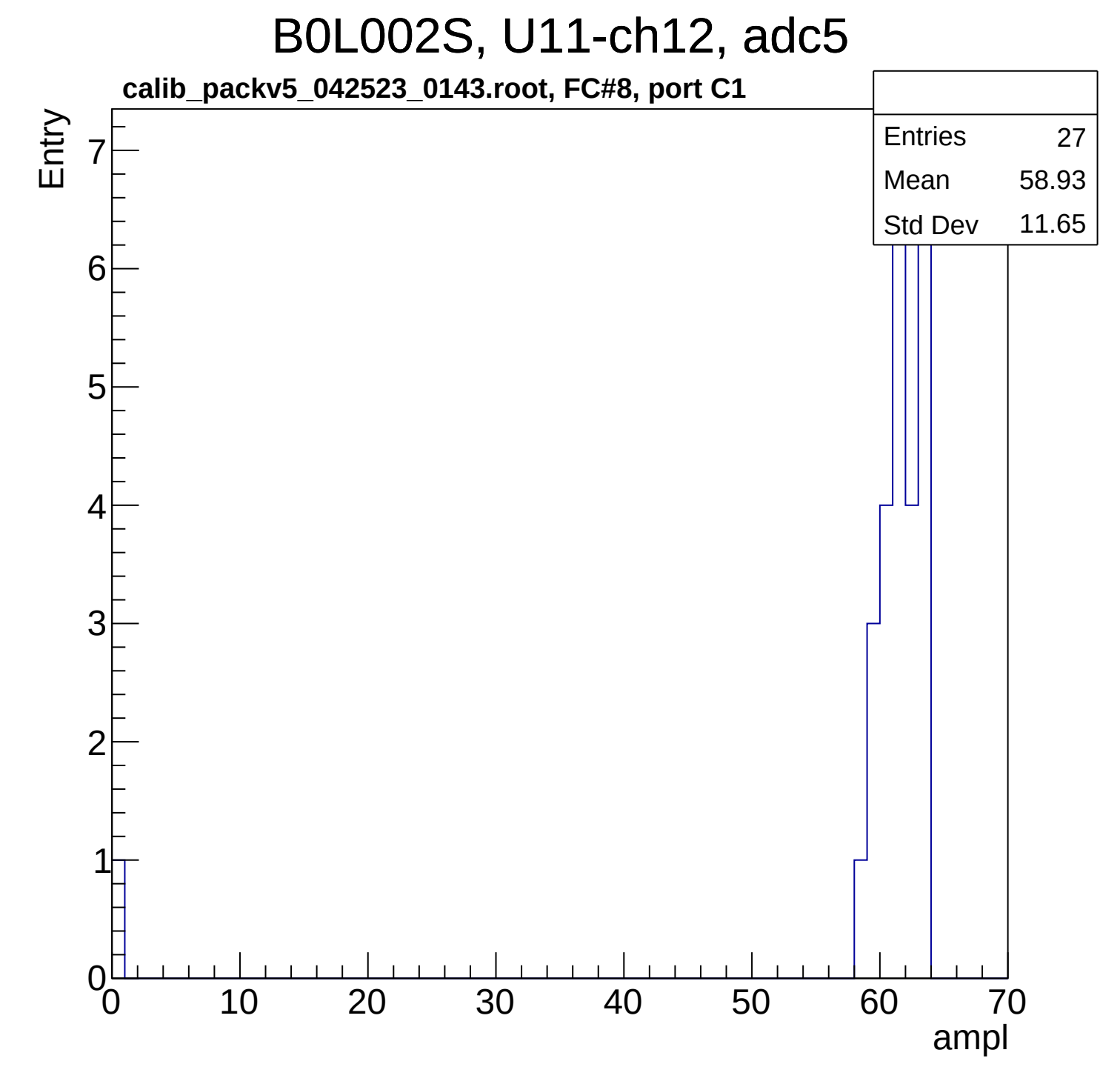
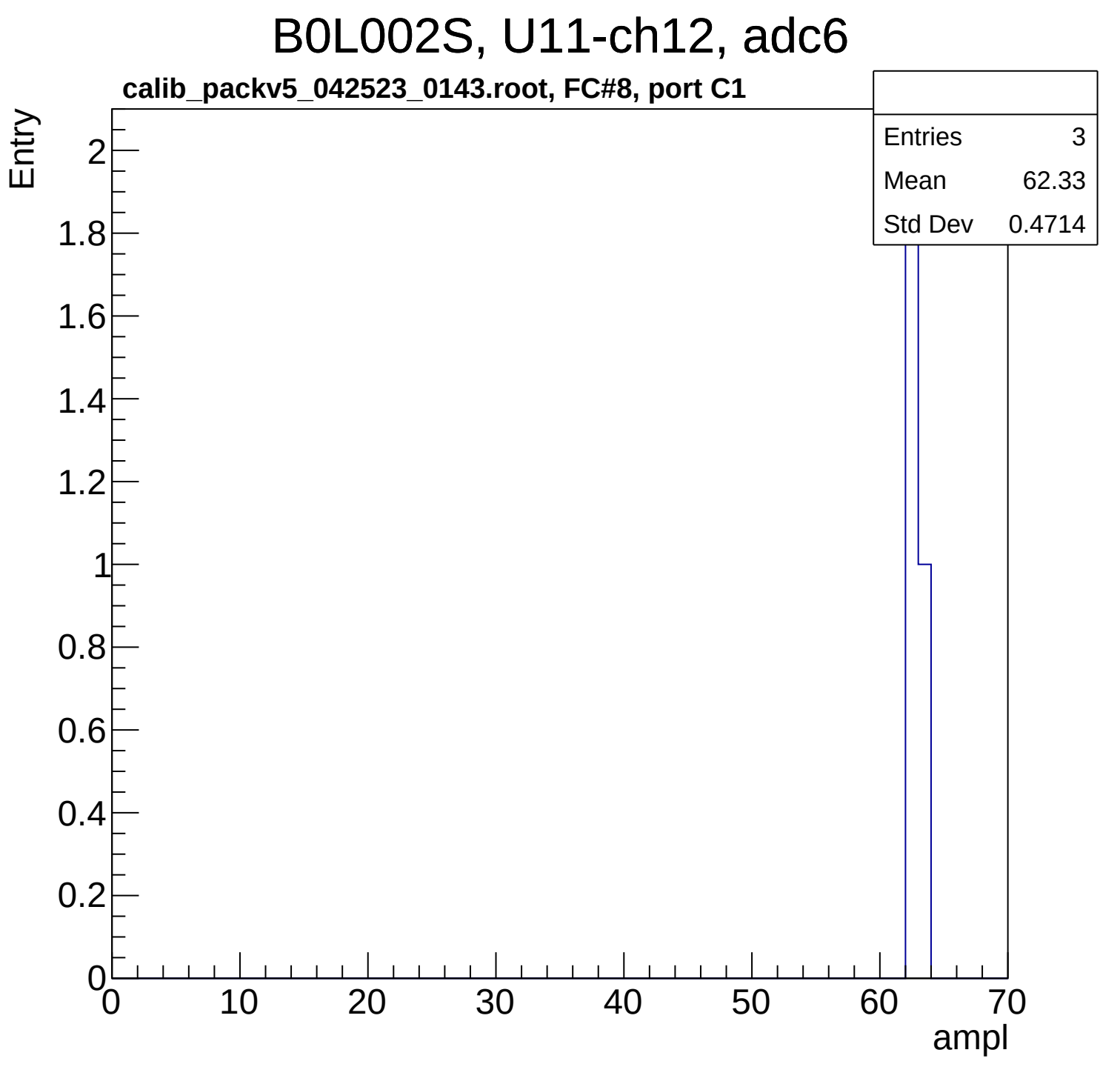
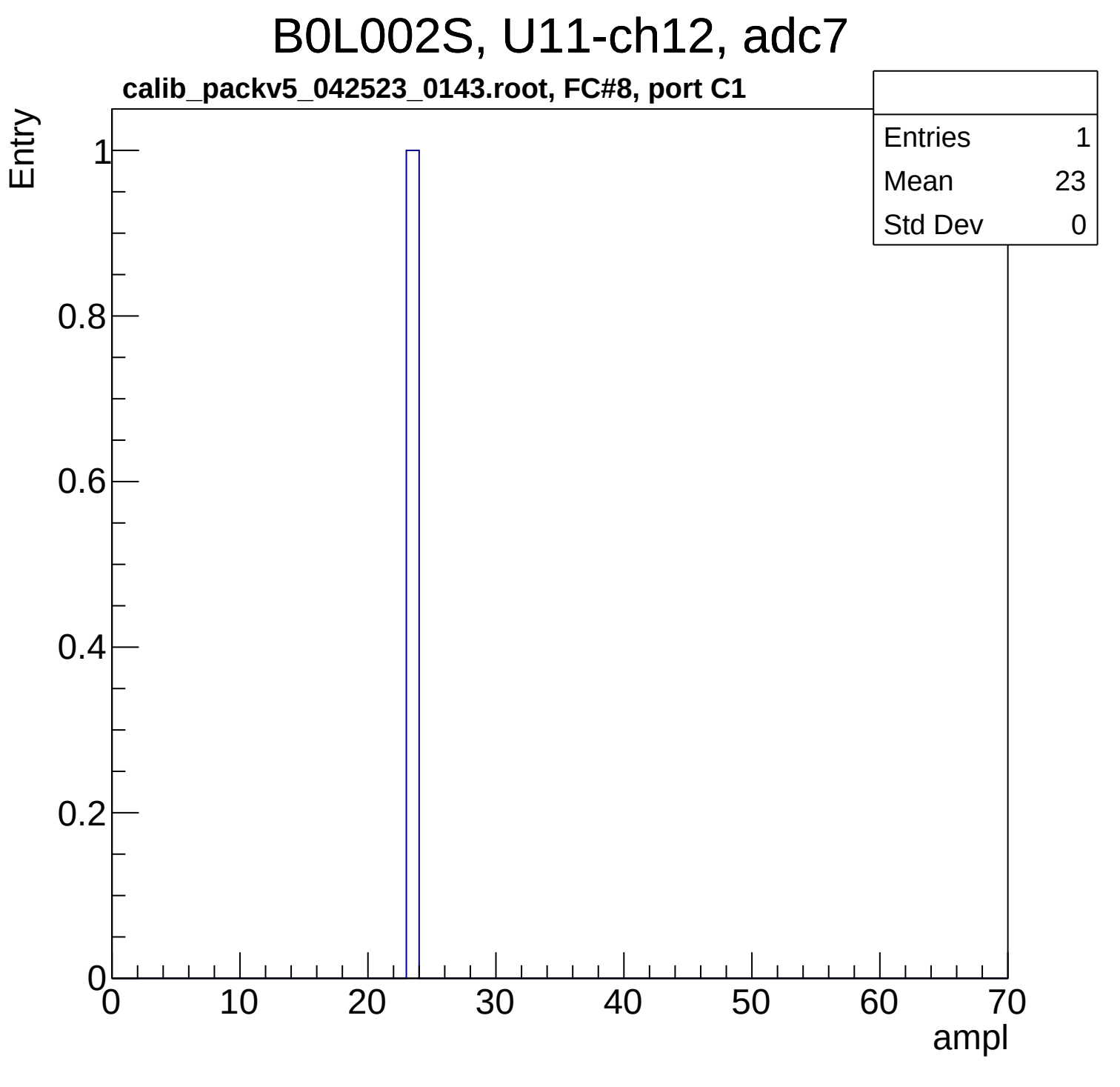

B0L002S, U11-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.93
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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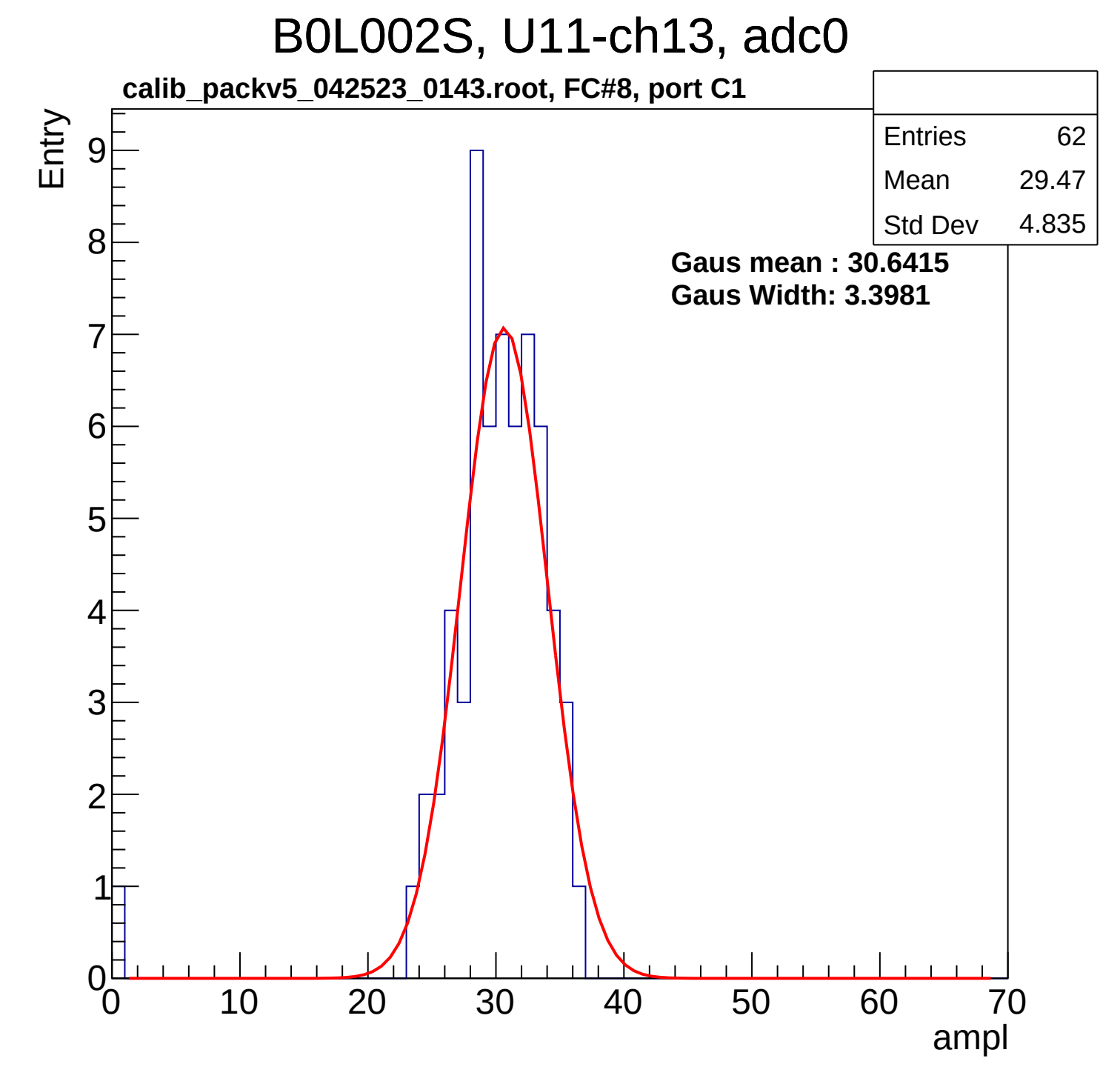
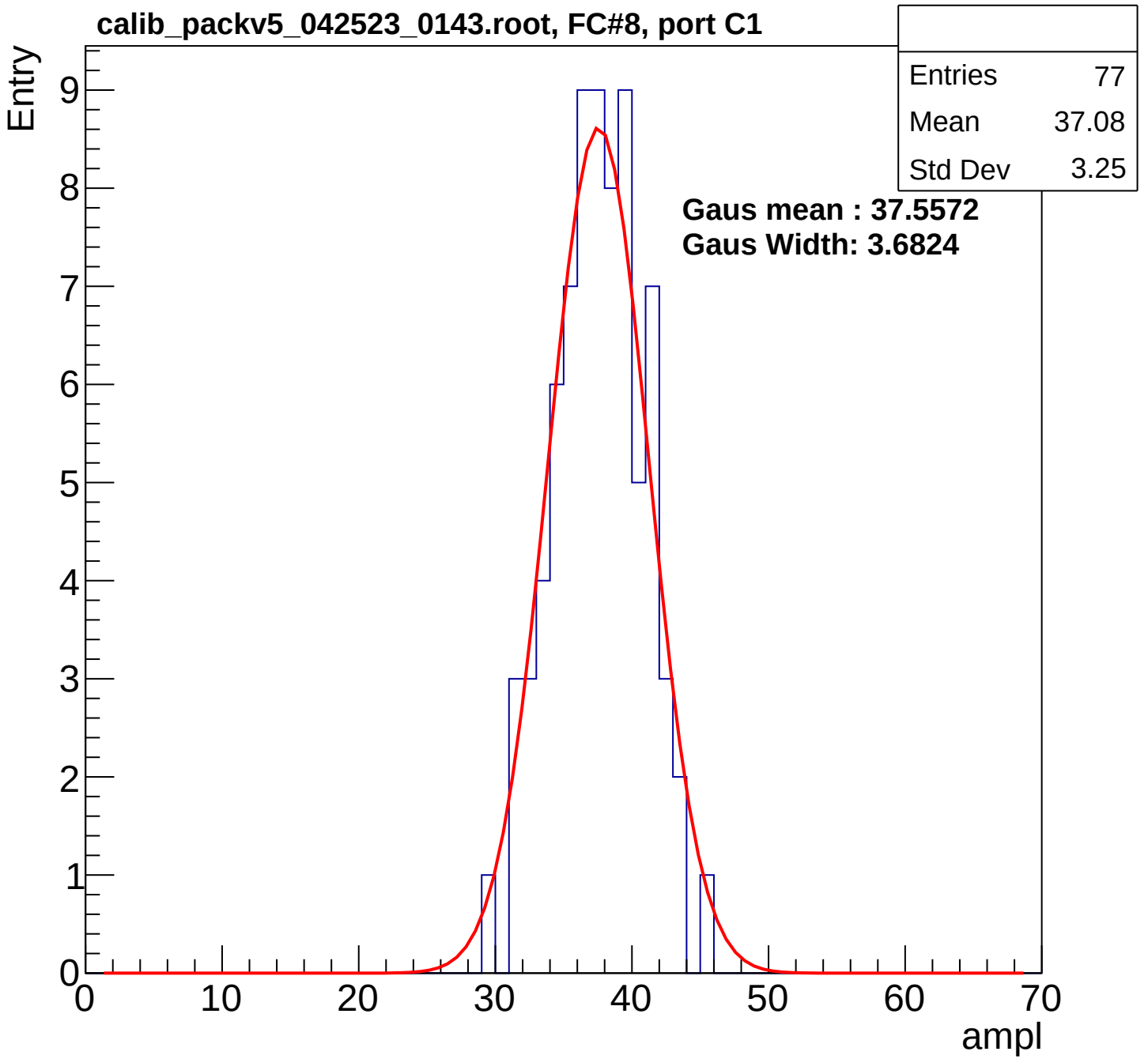
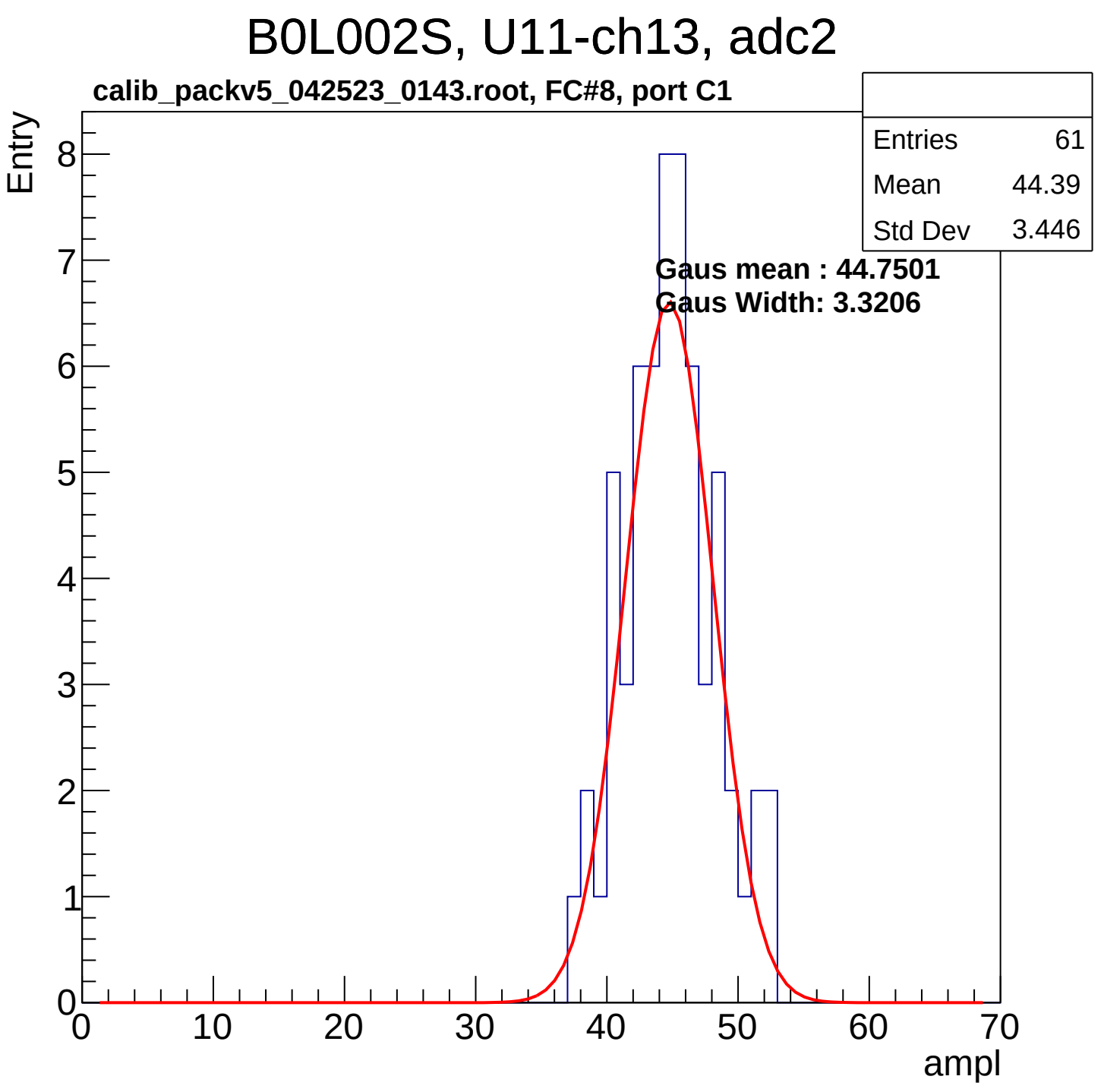
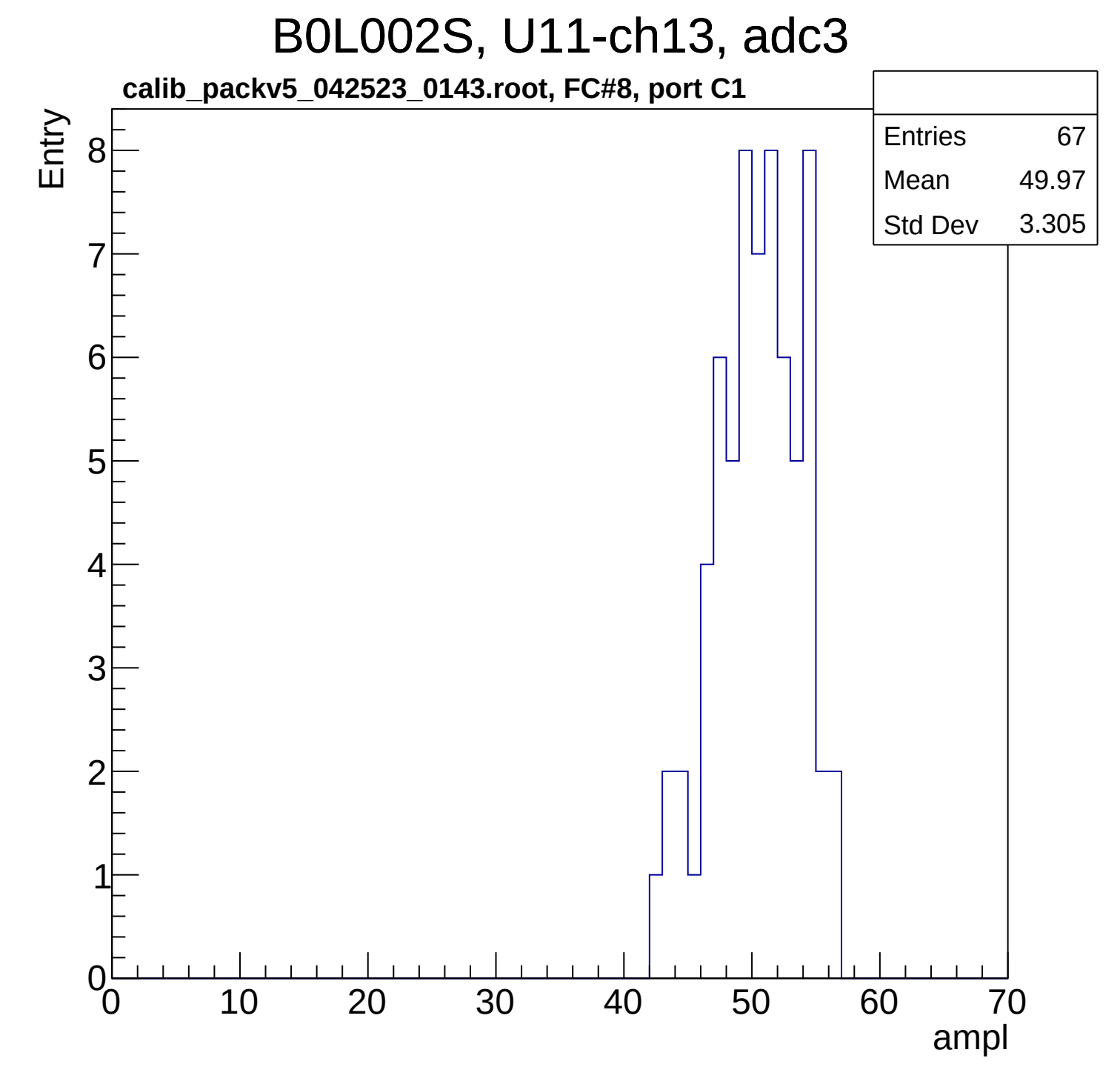
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 29.47
Std Dev 4.835
Gaus mean : 30.6415
Gaus Width: 3.3981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.08
Std Dev 3.25
Gaus mean : 37.5572
Gaus Width: 3.6824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.39
Std Dev 3.446
Gaus mean : 44.7501
Gaus Width: 3.3206
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 49.97
Std Dev 3.305
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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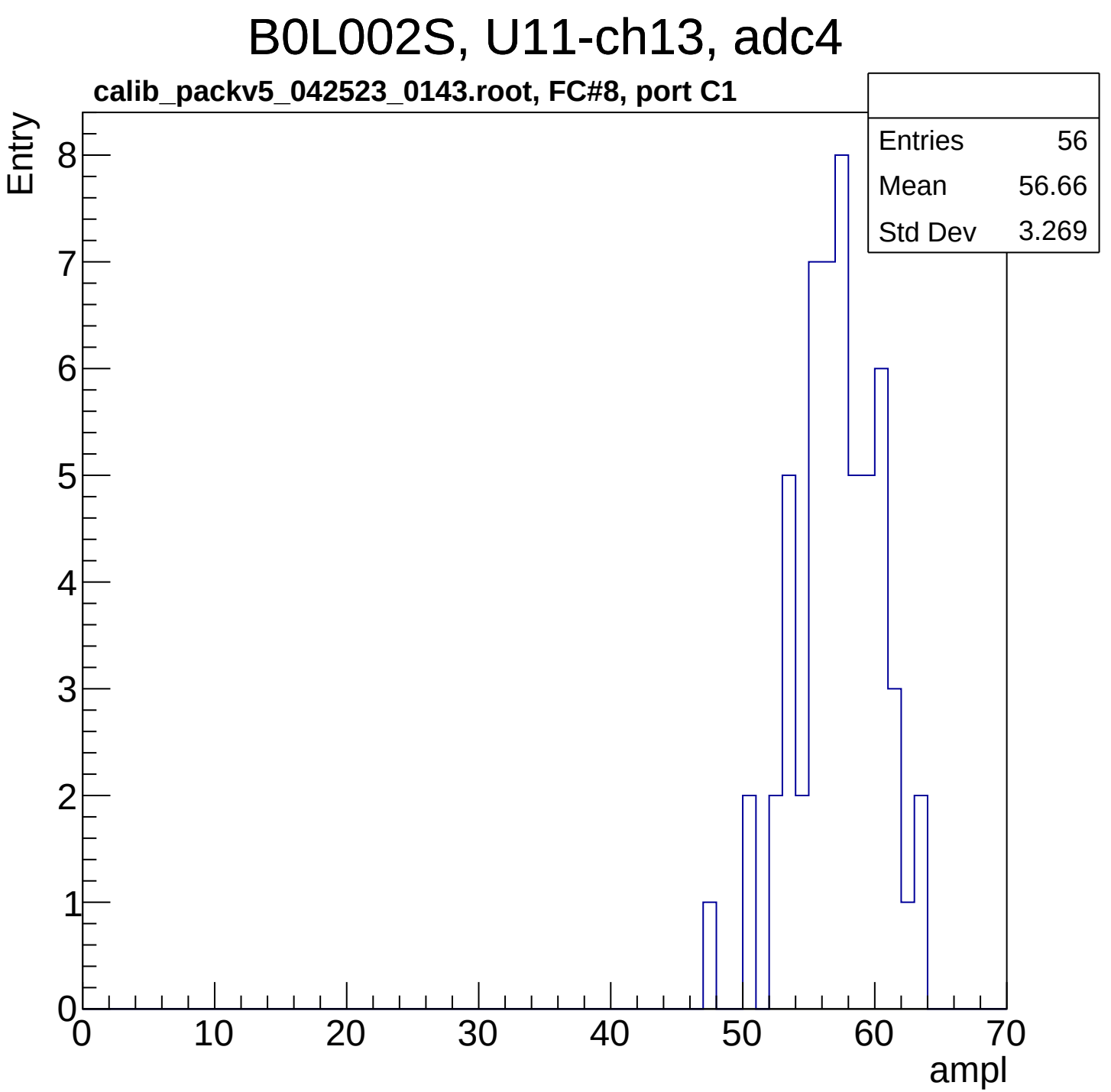
B0L002S, U11-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.66
Std Dev 3.269
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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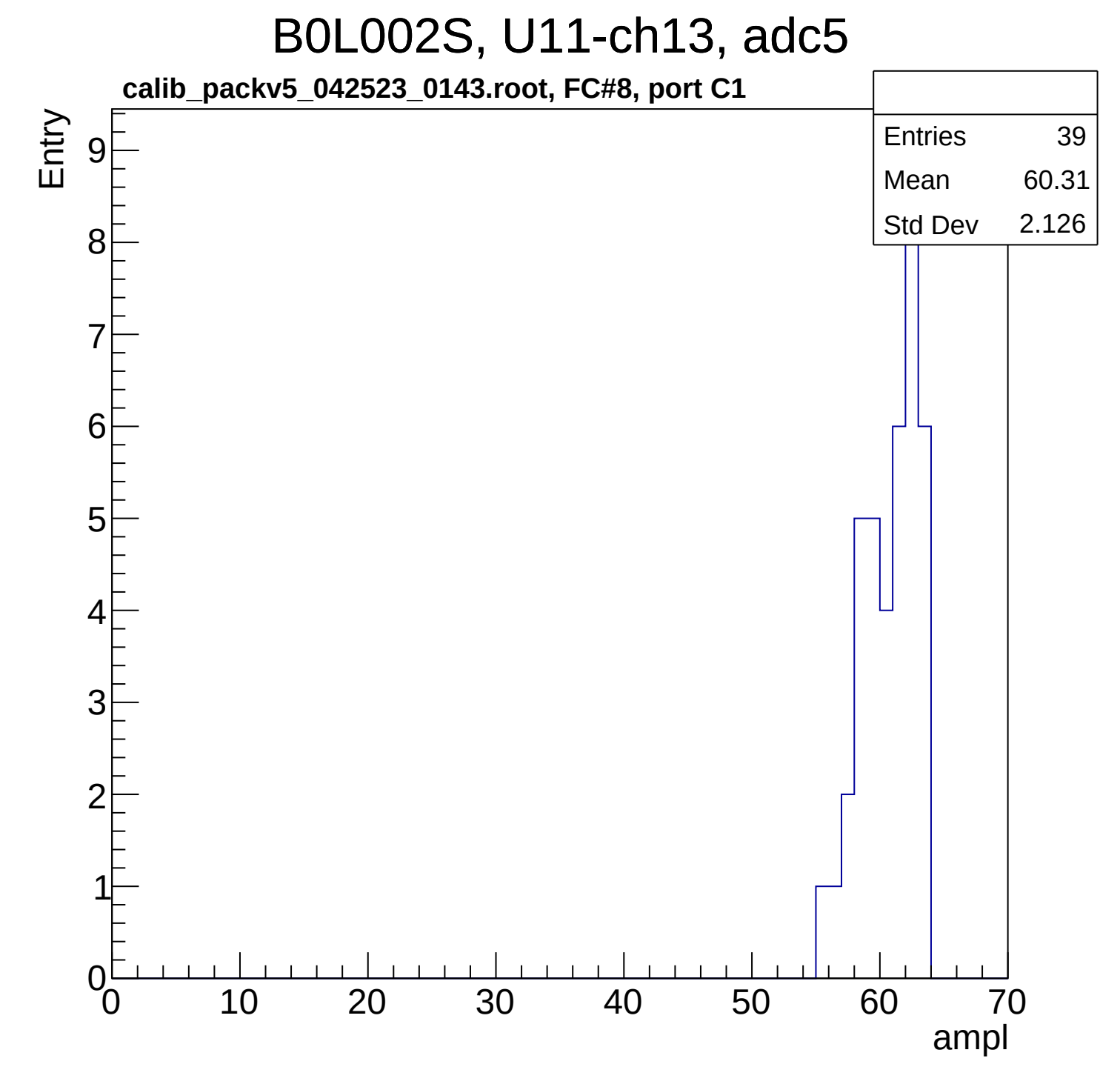
B0L002S, U11-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.31
Std Dev 2.126
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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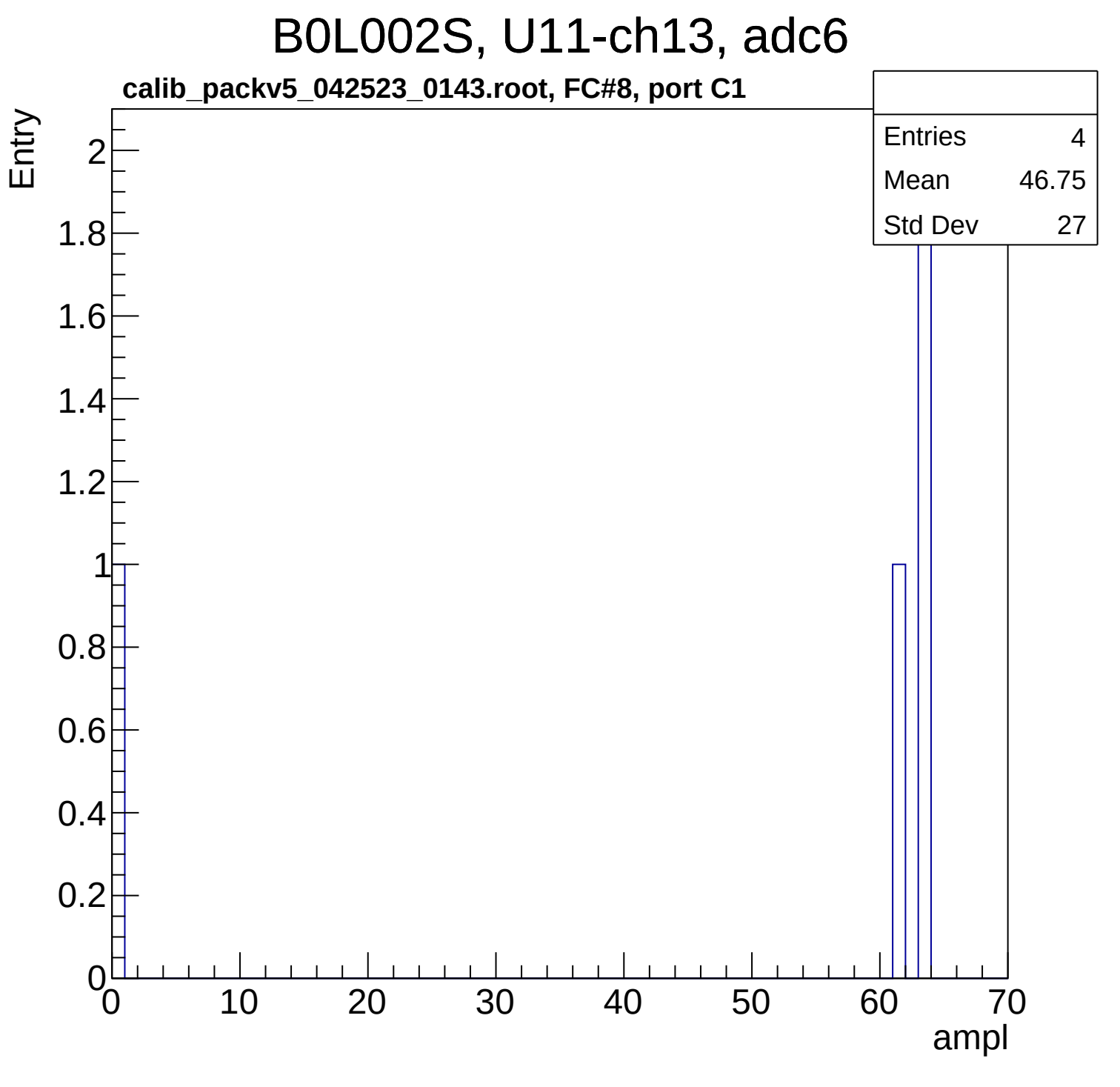
B0L002S, U11-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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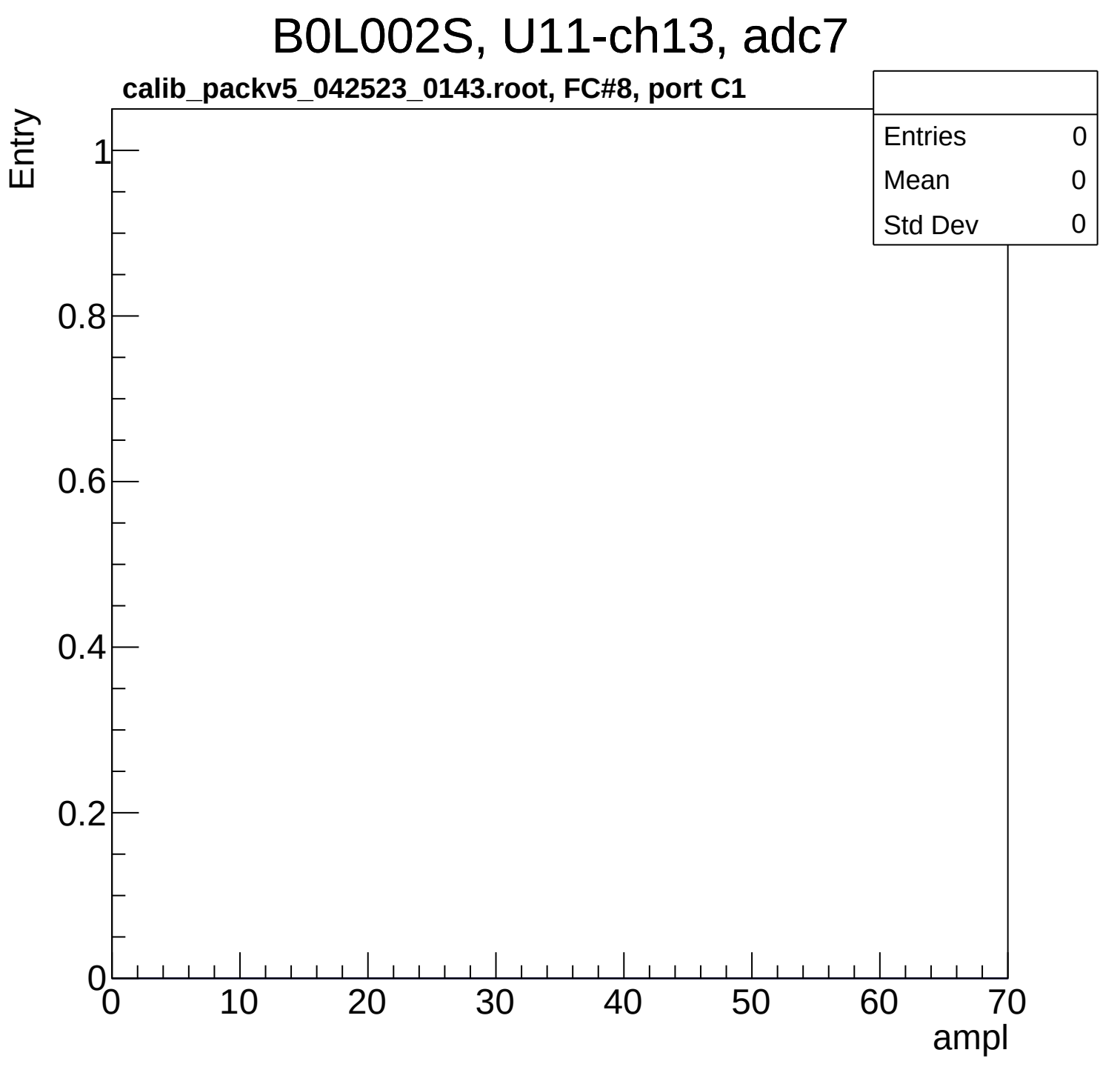
20
30
40
50
60
70


B0L002S, U11-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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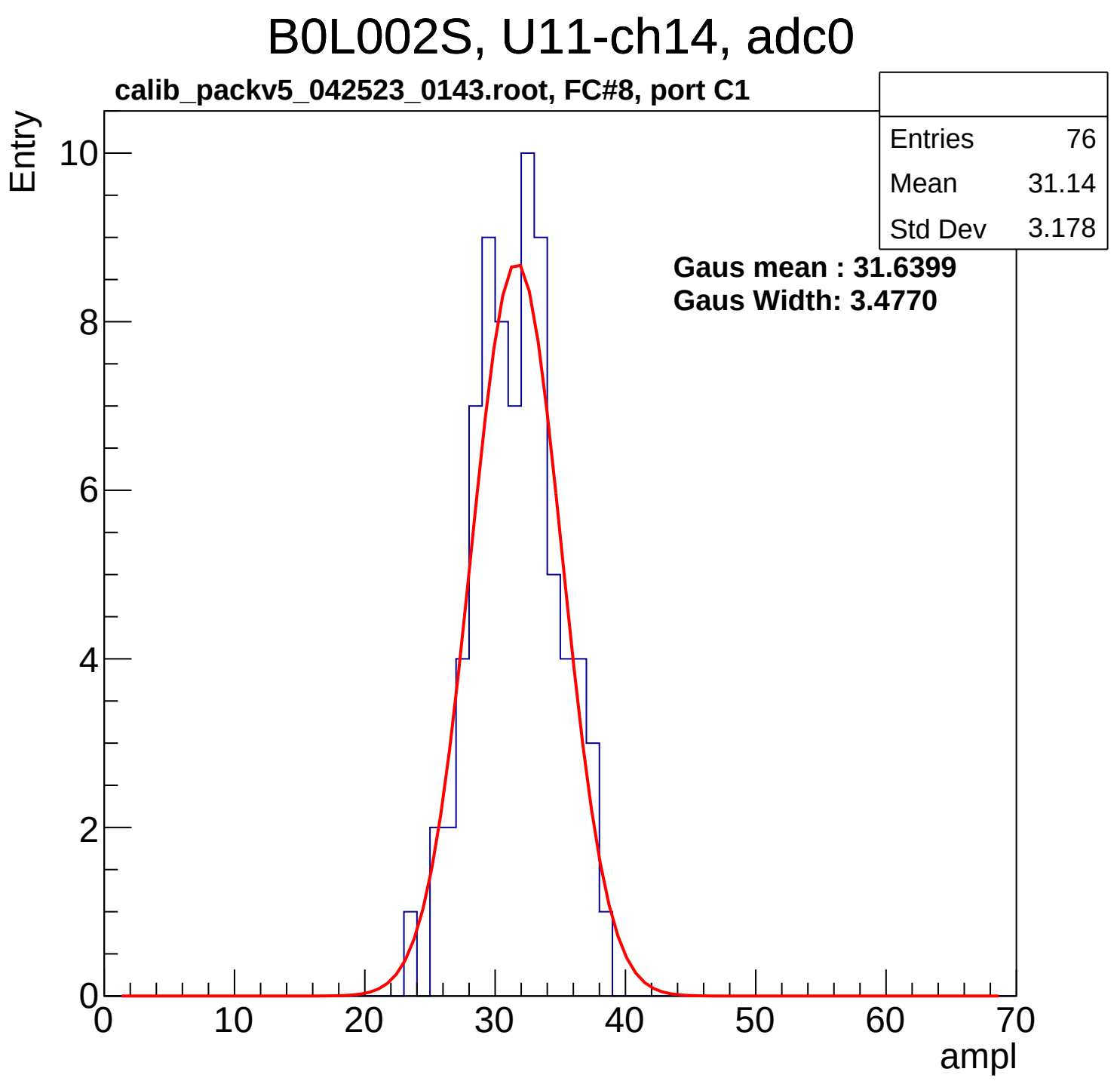
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 31.14
Std Dev 3.178
Gaus mean : 31.6399
Gaus Width: 3.4770
Entry
ampl
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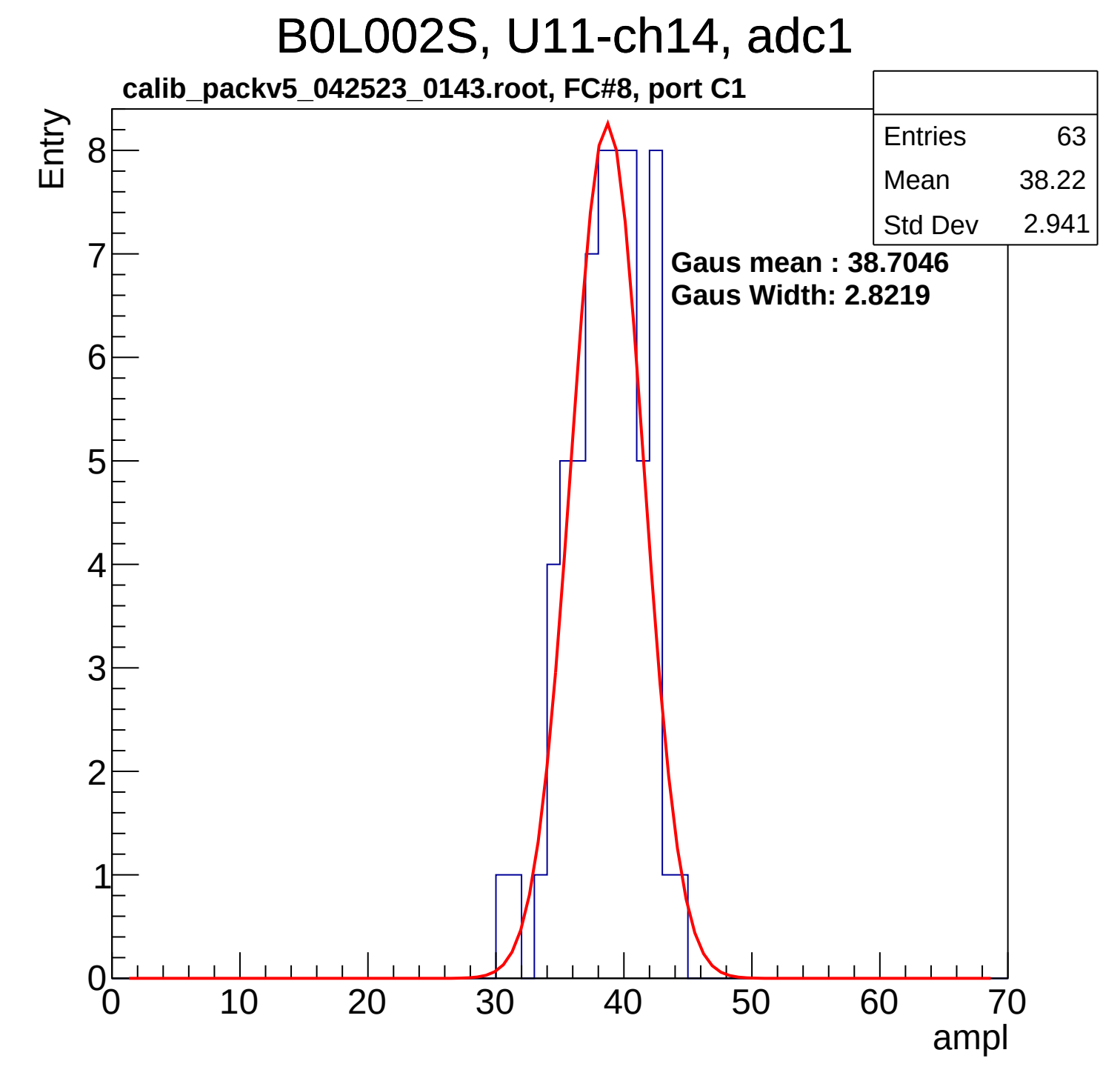
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.22
Std Dev 2.941
Gaus mean : 38.7046
Gaus Width: 2.8219
Entry
ampl
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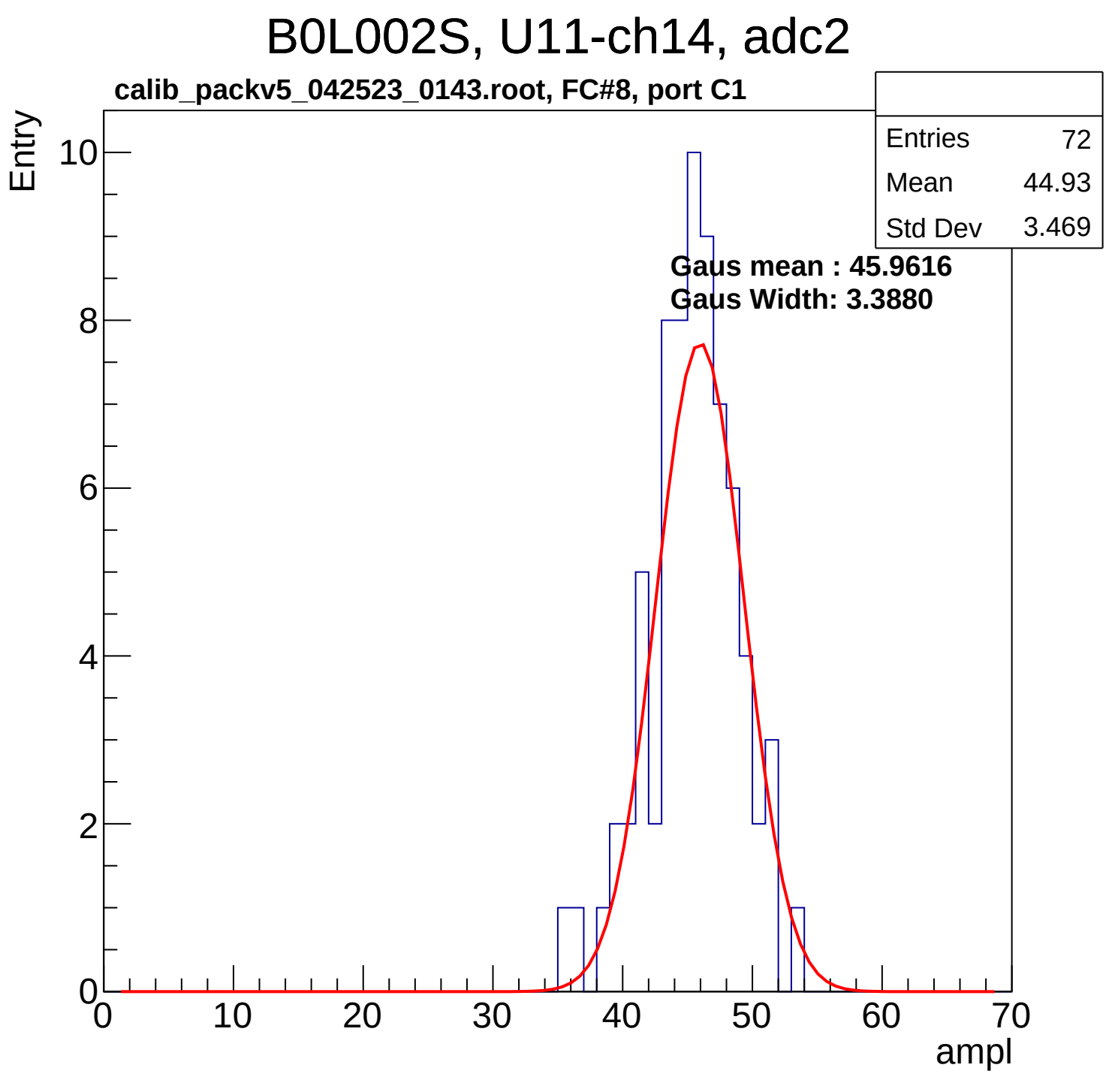
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.93
Std Dev 3.469
Gaus mean : 45.9616
Gaus Width: 3.3880
Entry
ampl
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4
6
8
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0
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20
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60
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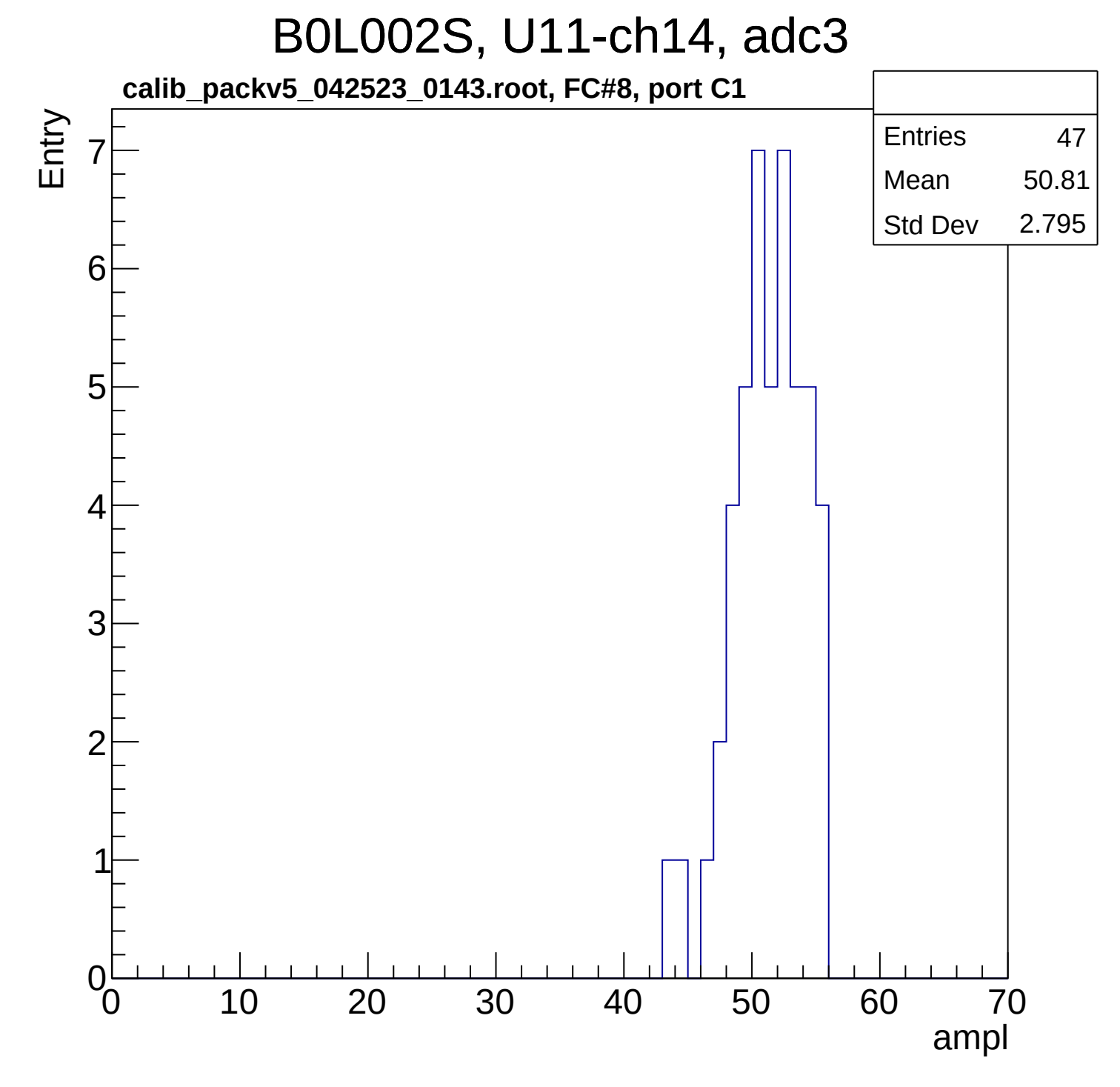
B0L002S, U11-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 50.81
Std Dev 2.795
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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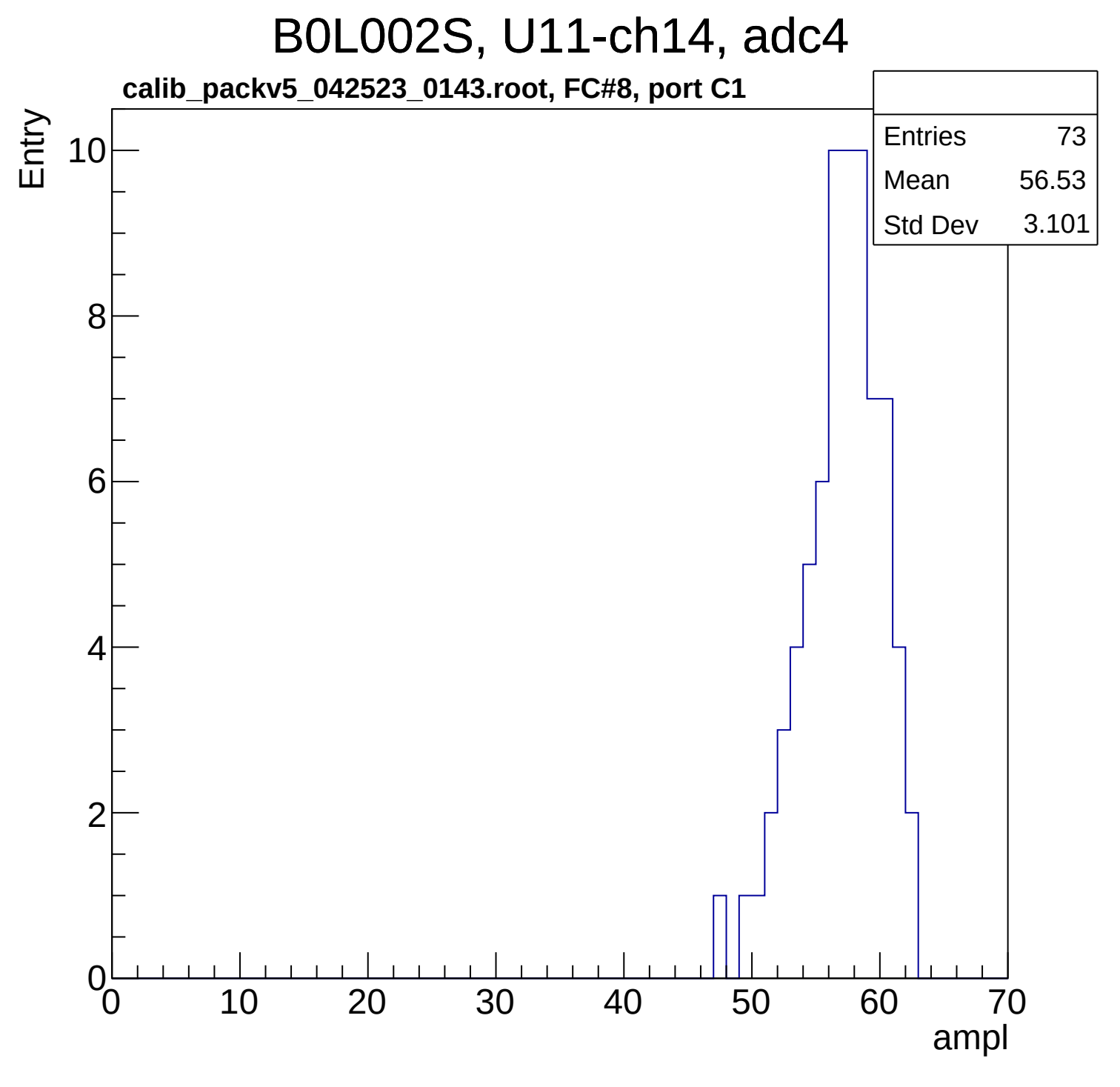
B0L002S, U11-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 56.53
Std Dev 3.101
Entry
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6
8
10
0
10
20
30
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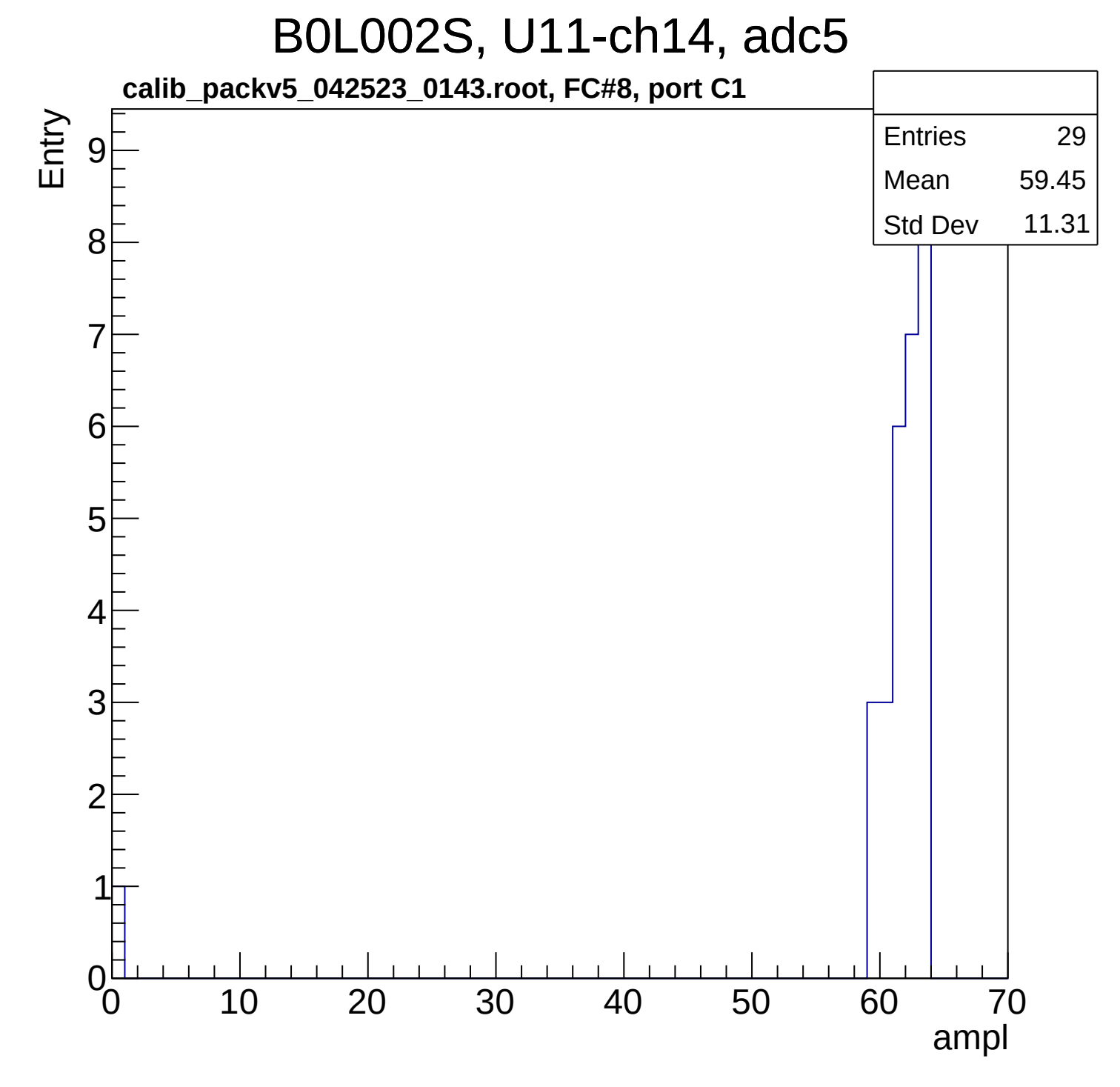
50
60
70
ampl


B0L002S, U11-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.45
Std Dev 11.31
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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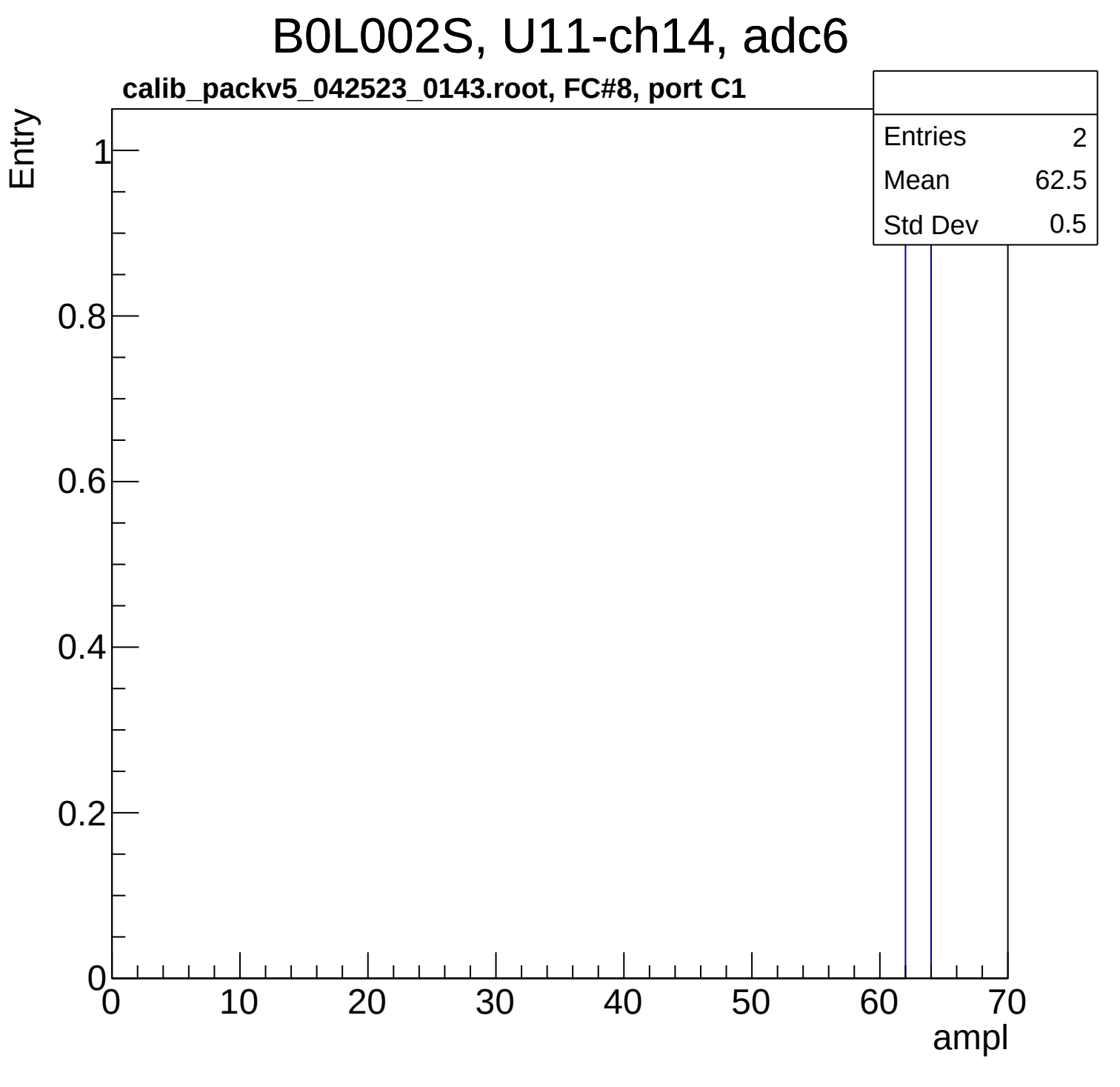
B0L002S, U11-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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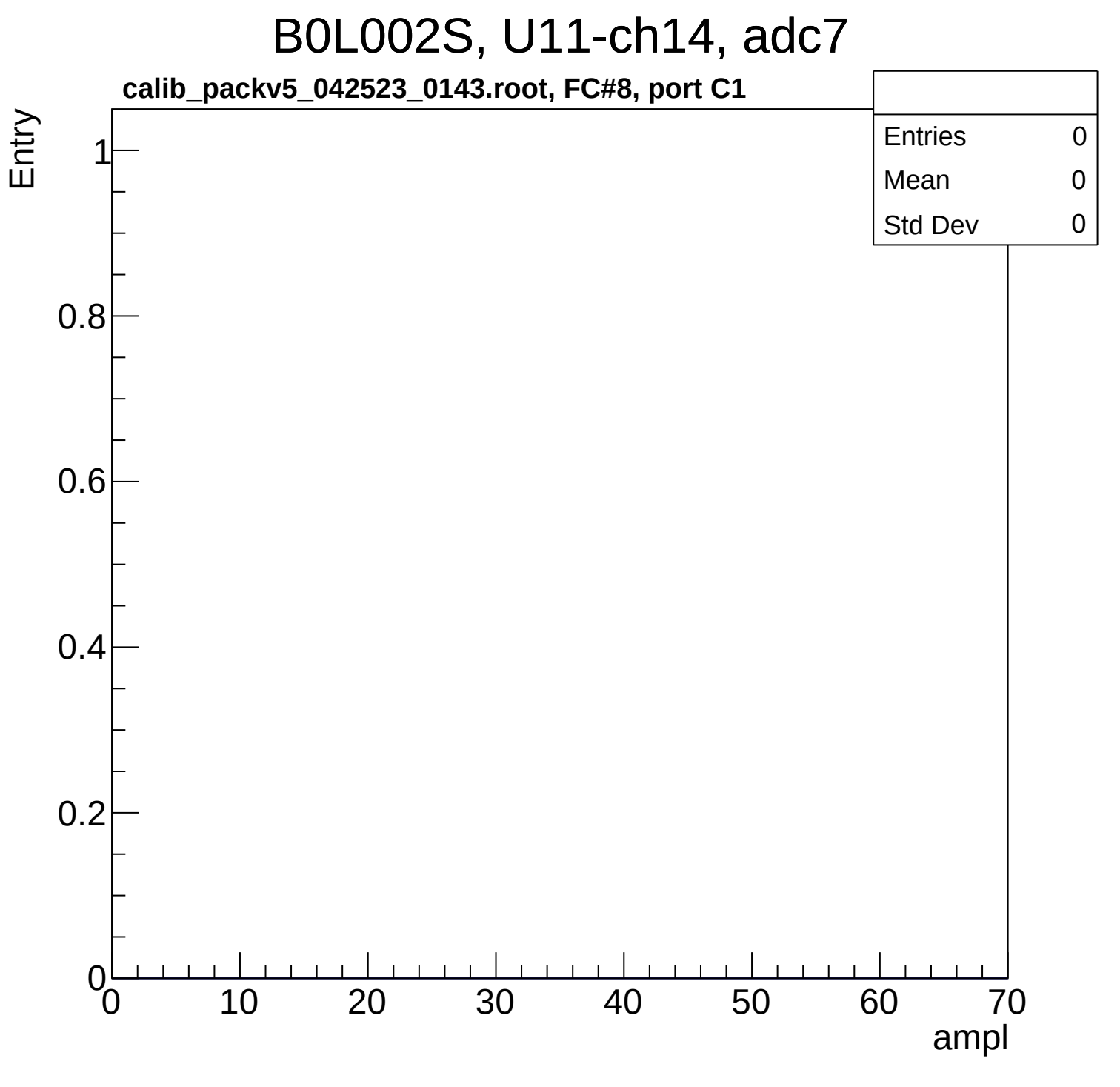
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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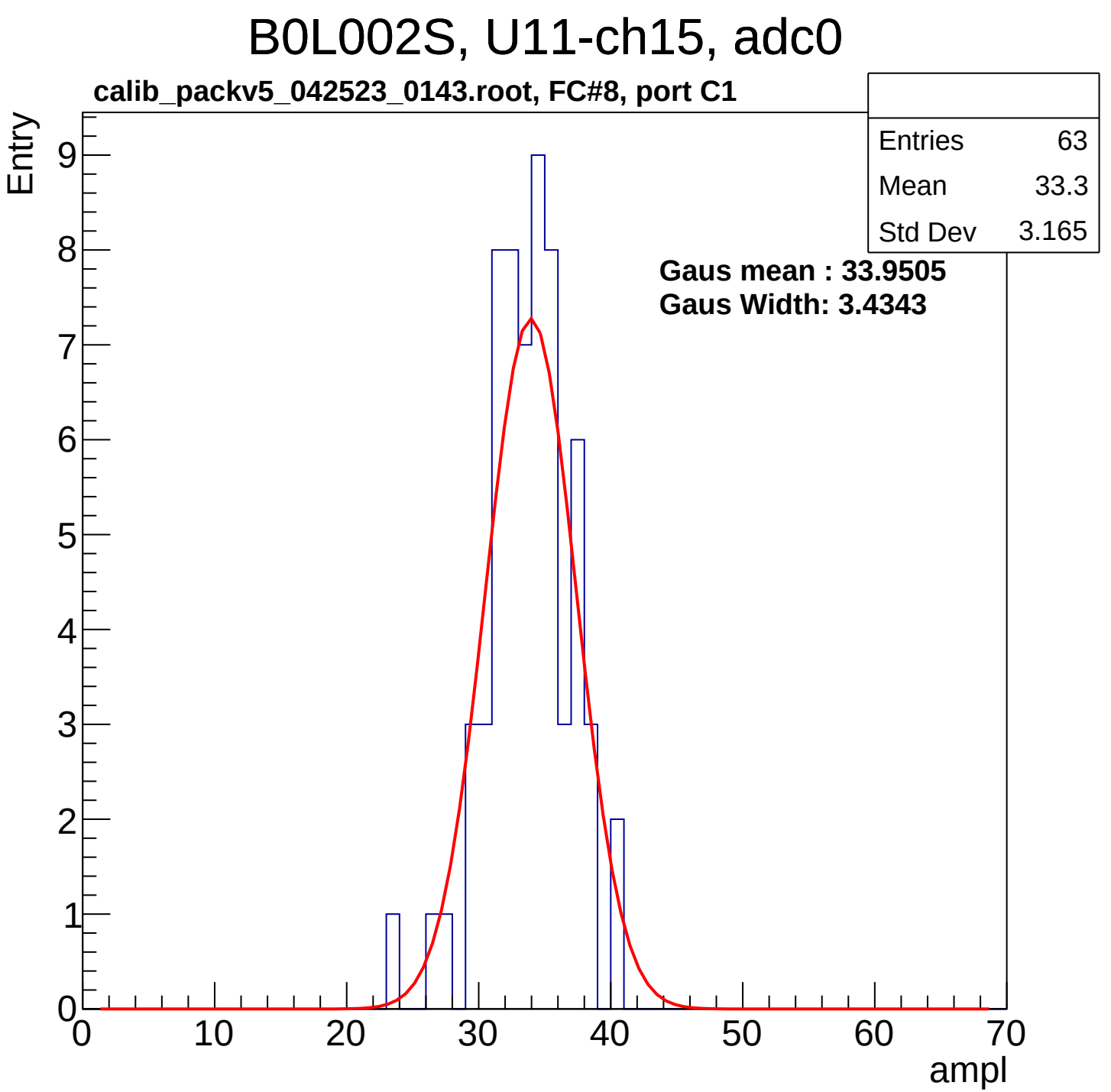
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 33.3
Std Dev 3.165
Gaus mean : 33.9505
Gaus Width: 3.4343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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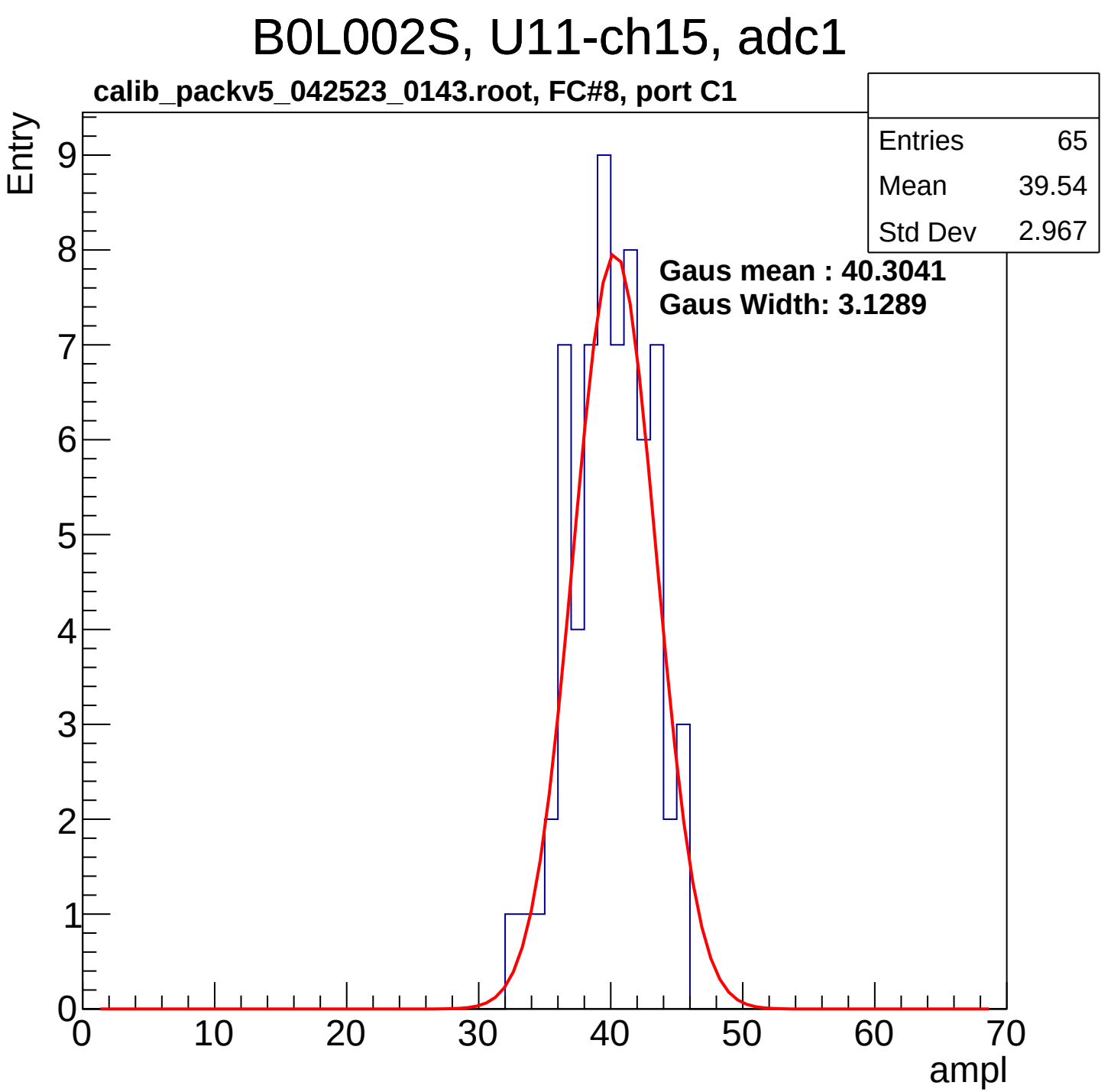
B0L002S, U11-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 39.54
Std Dev 2.967
Gaus mean : 40.3041
Gaus Width: 3.1289
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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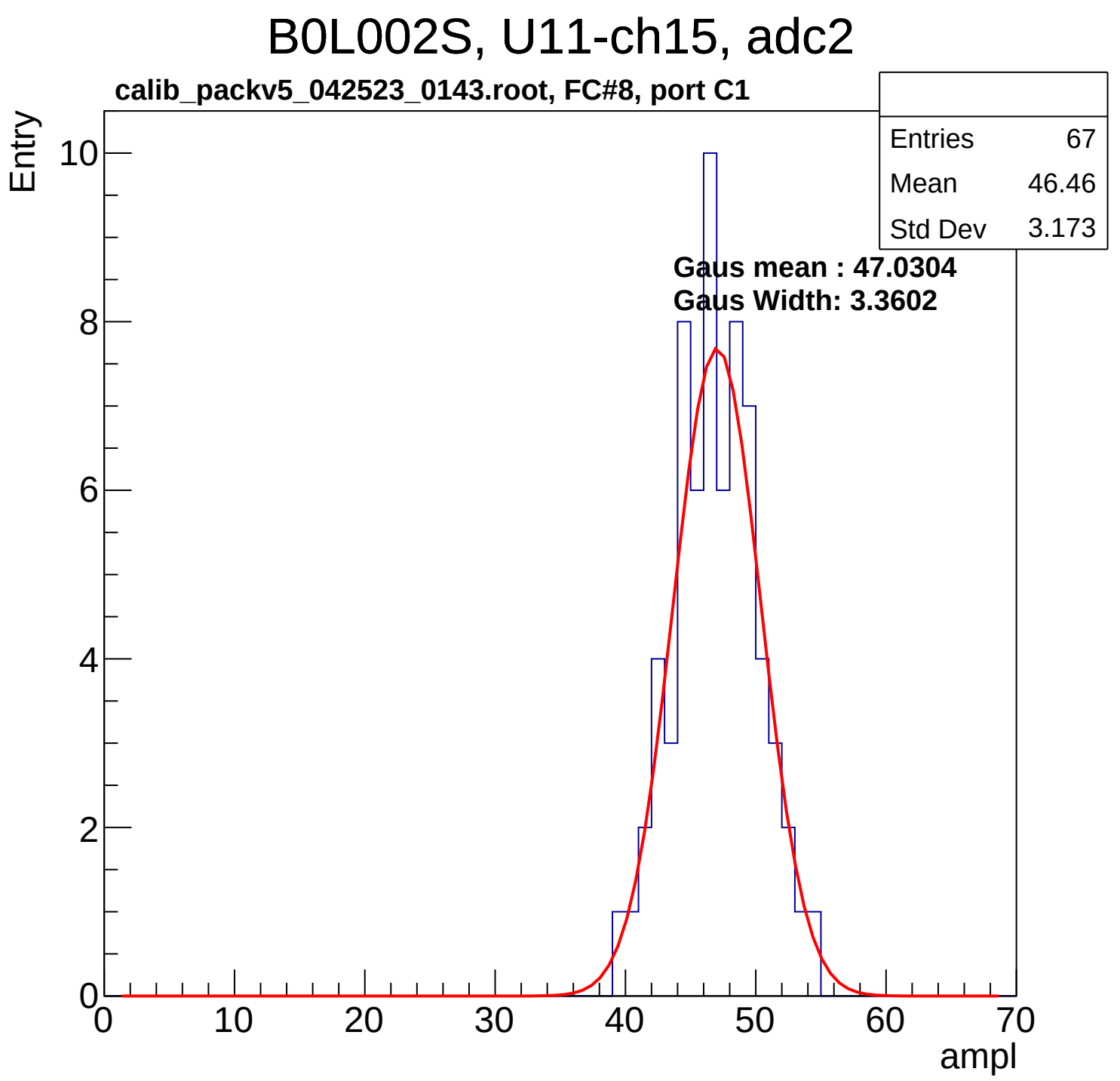
B0L002S, U11-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 46.46
Std Dev 3.173
Gaus mean : 47.0304
Gaus Width: 3.3602
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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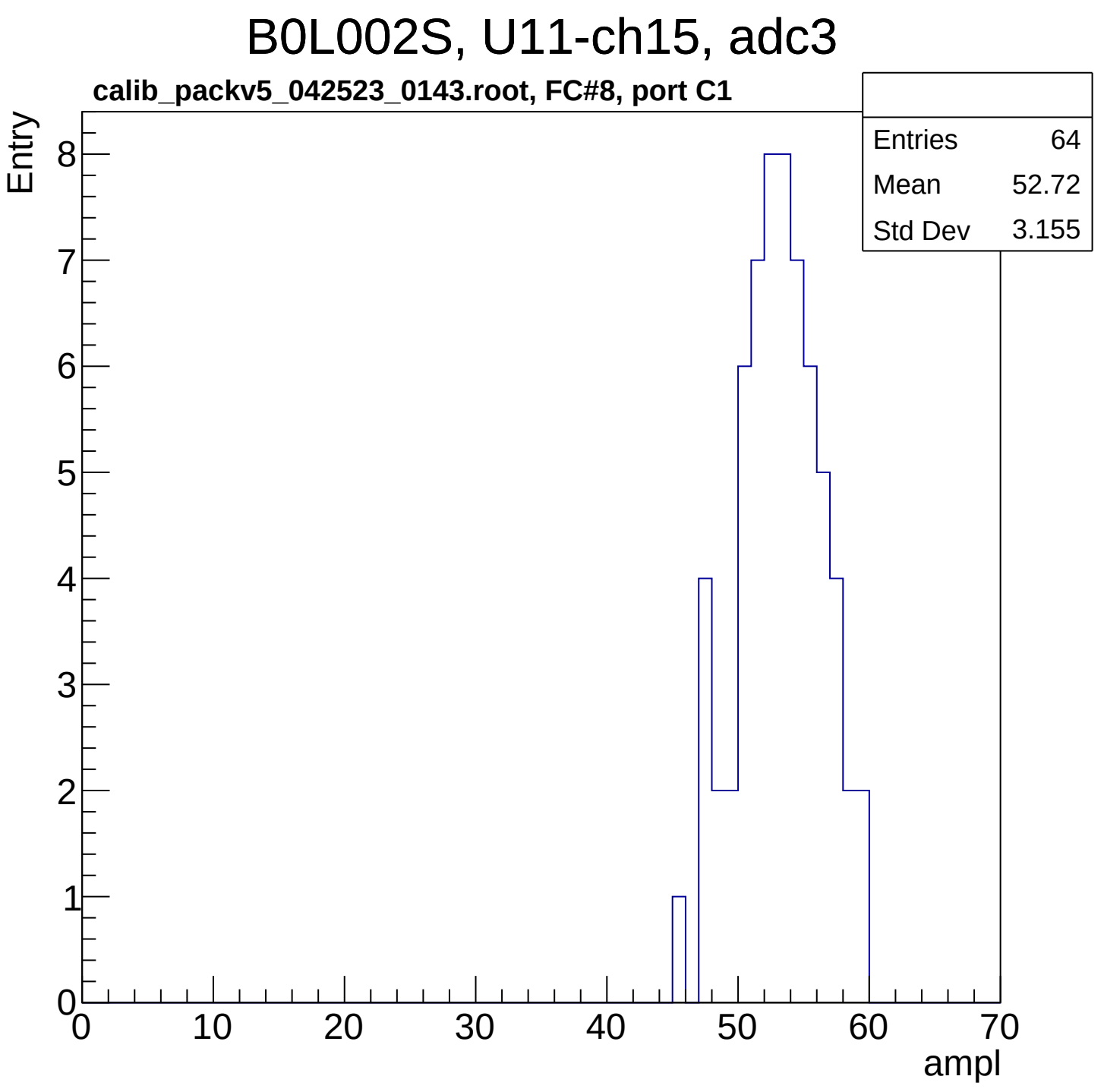
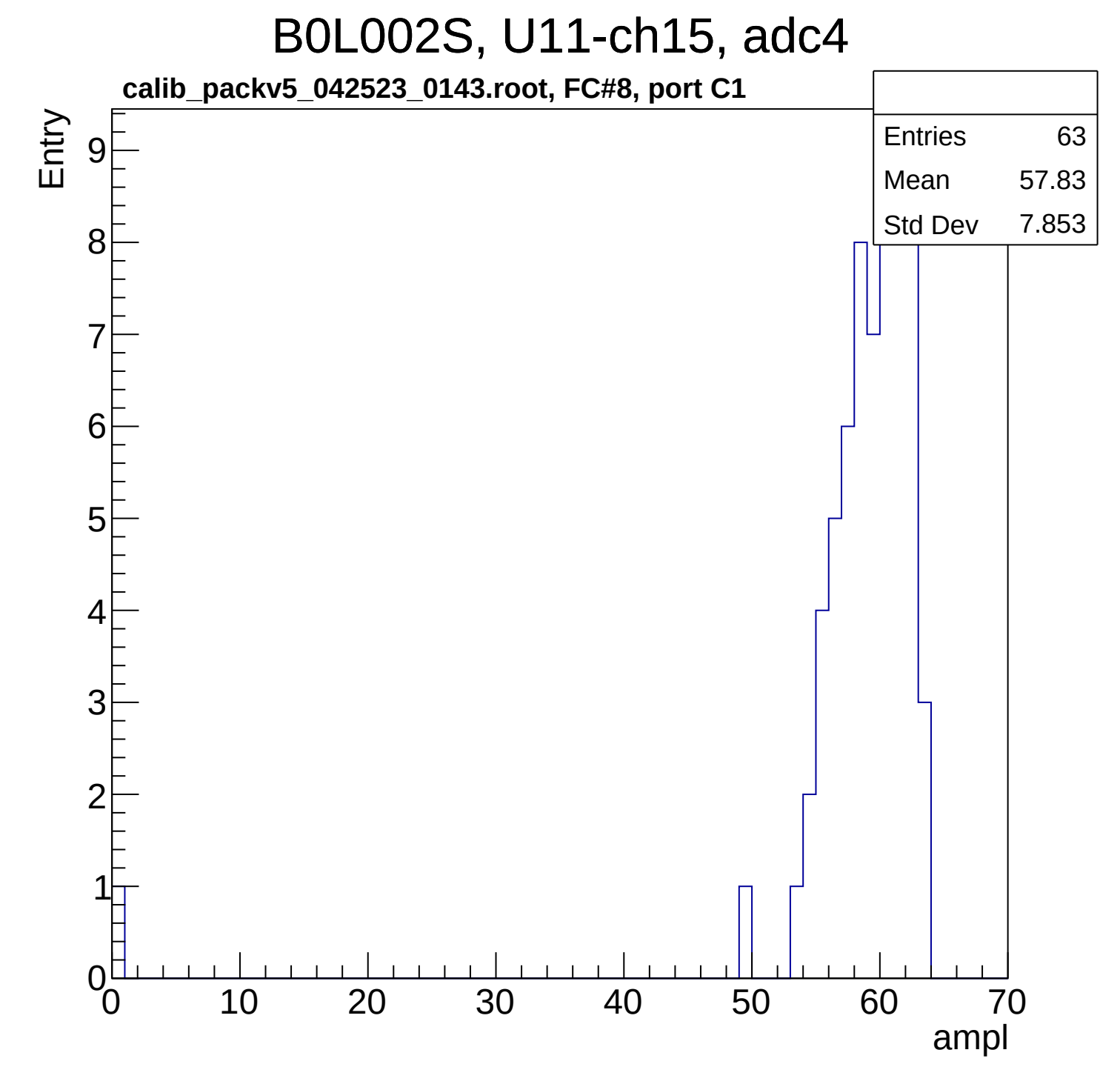

B0L002S, U11-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 52.72
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.83
Std Dev 7.853
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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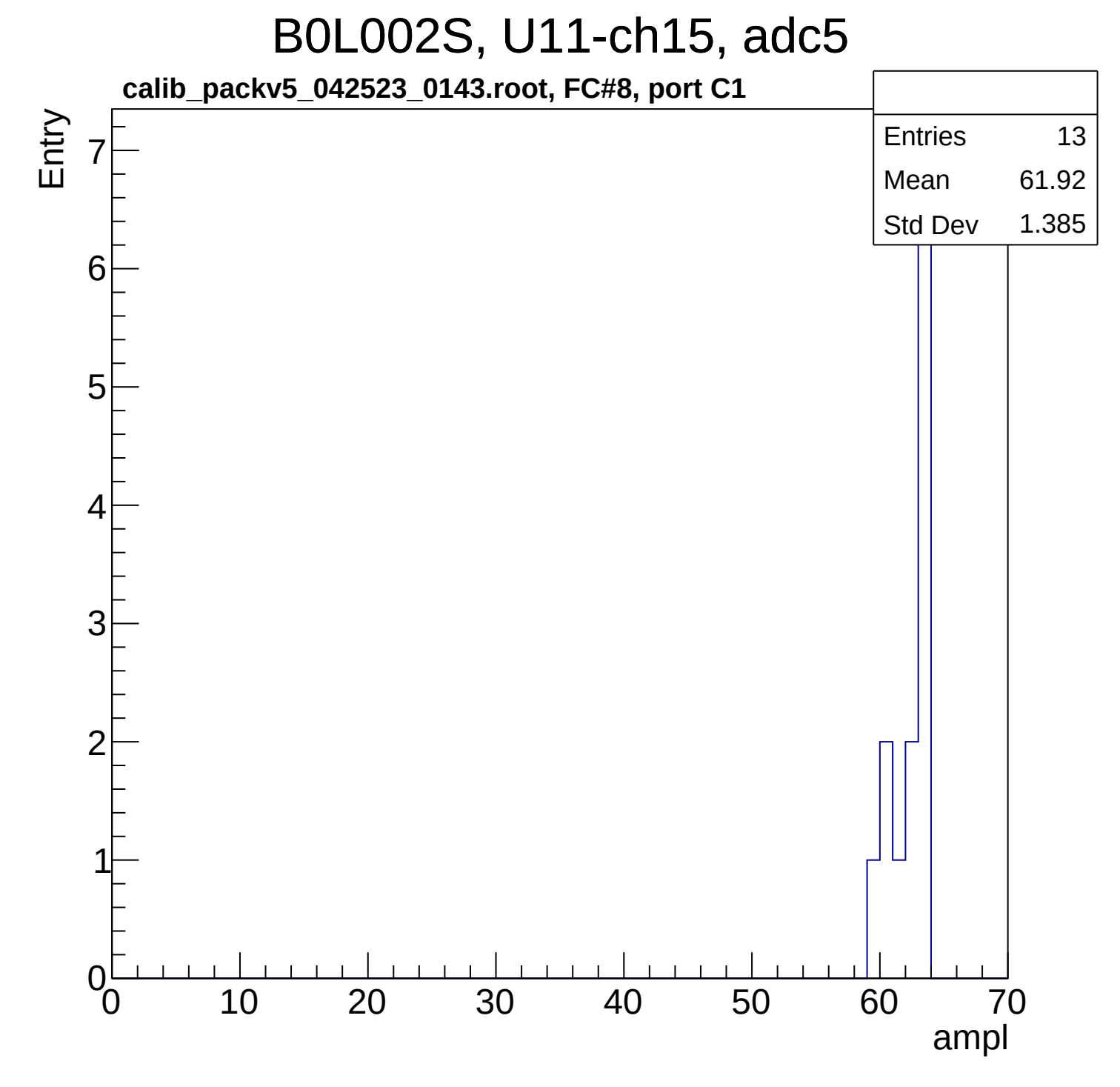
B0L002S, U11-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 61.92
Std Dev 1.385
Entry
ampl
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0
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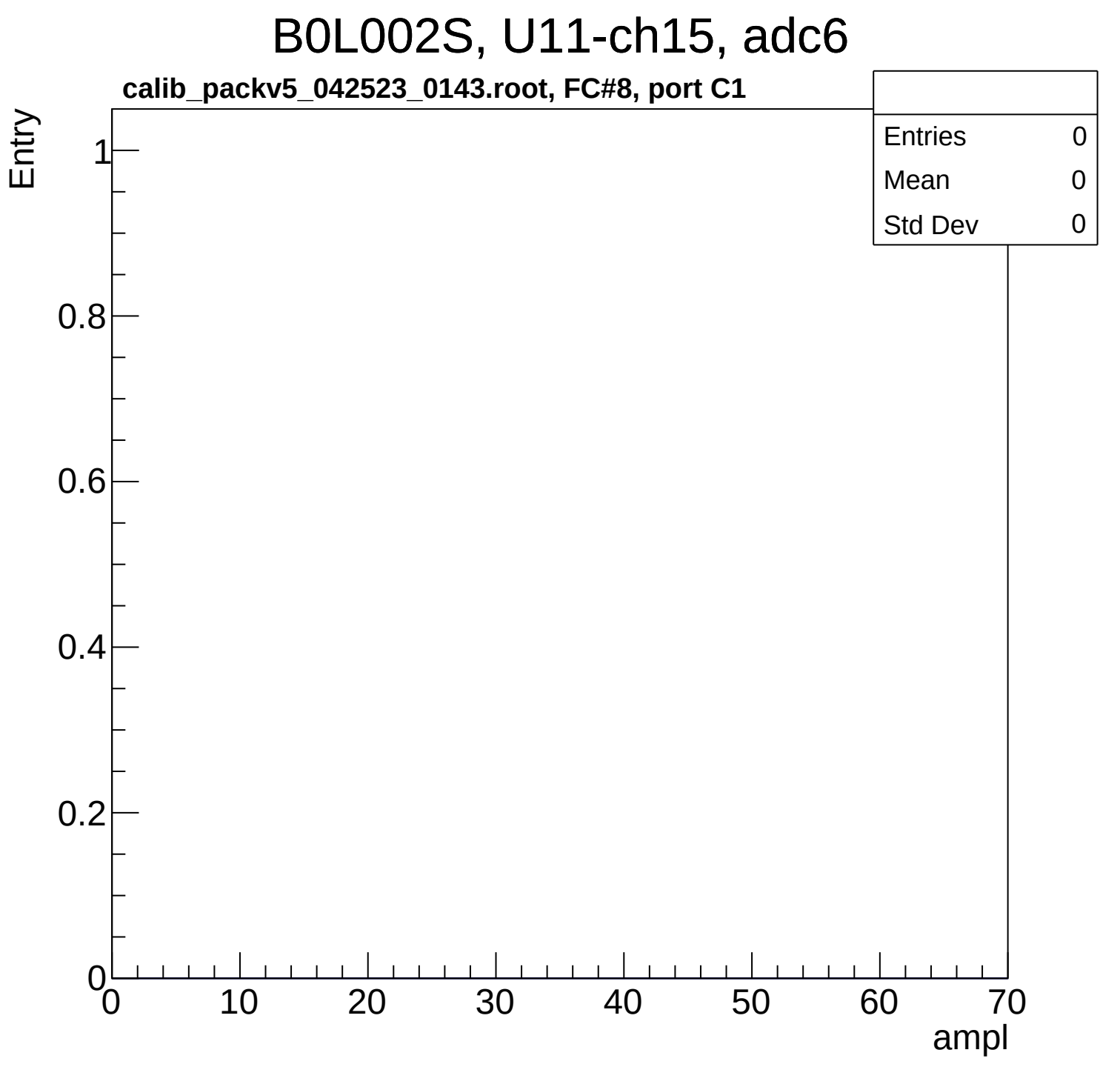
B0L002S, U11-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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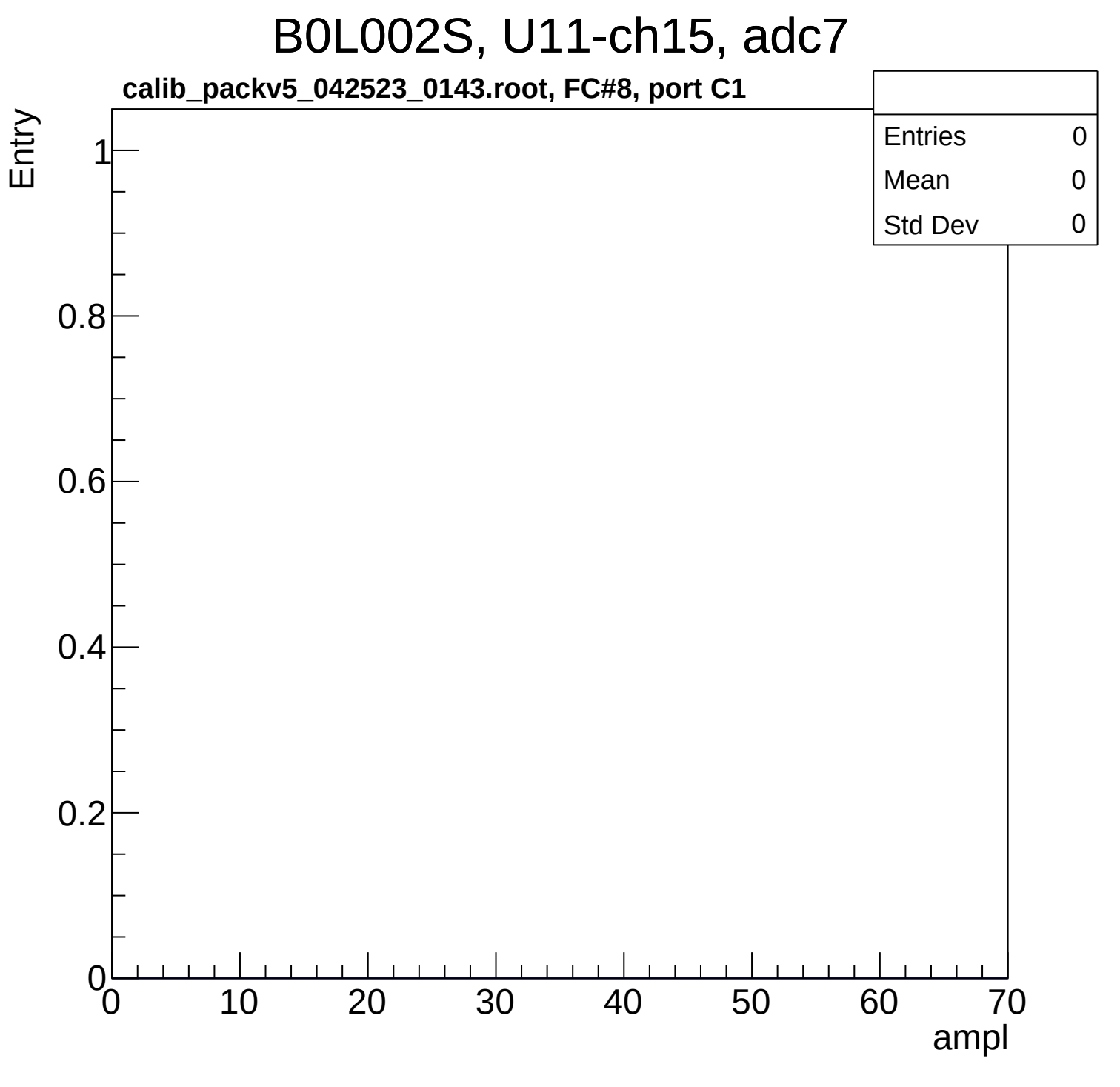
50
60
70
ampl


B0L002S, U11-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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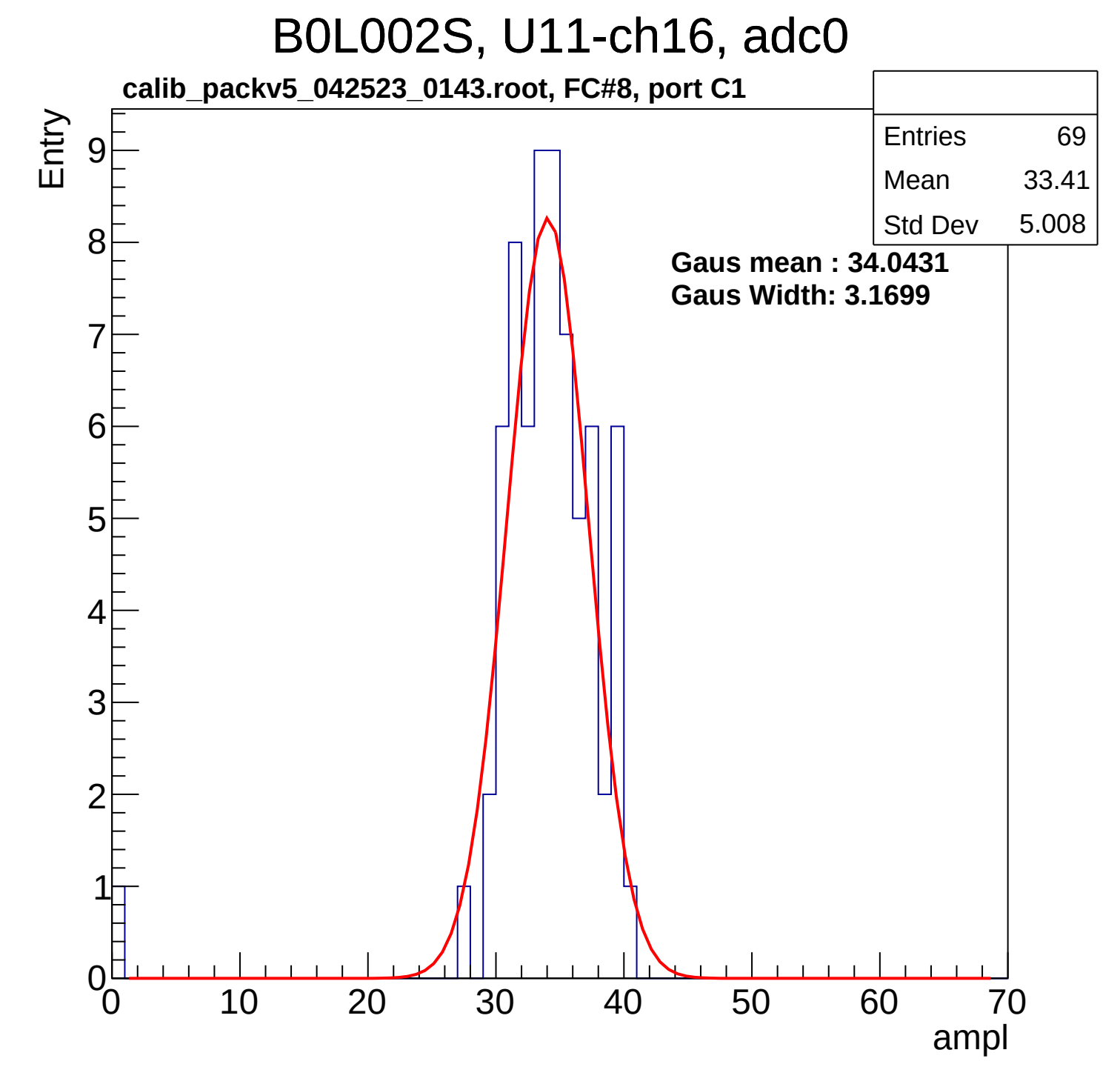
50
60
70
ampl


B0L002S, U11-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 33.41
Std Dev 5.008
Gaus mean : 34.0431
Gaus Width: 3.1699
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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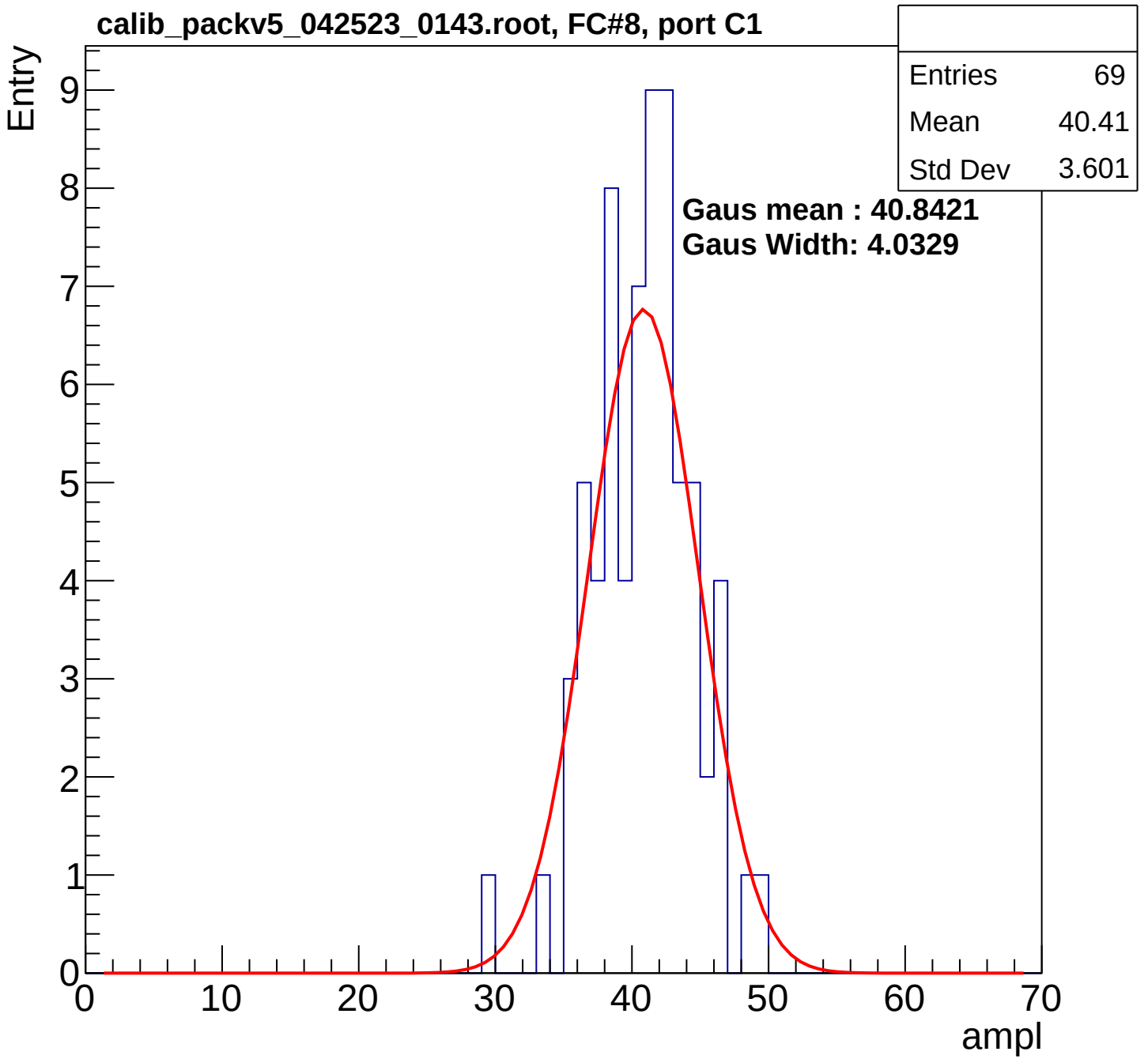
B0L002S, U11-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 40.41
Std Dev 3.601
Gaus mean : 40.8421
Gaus Width: 4.0329
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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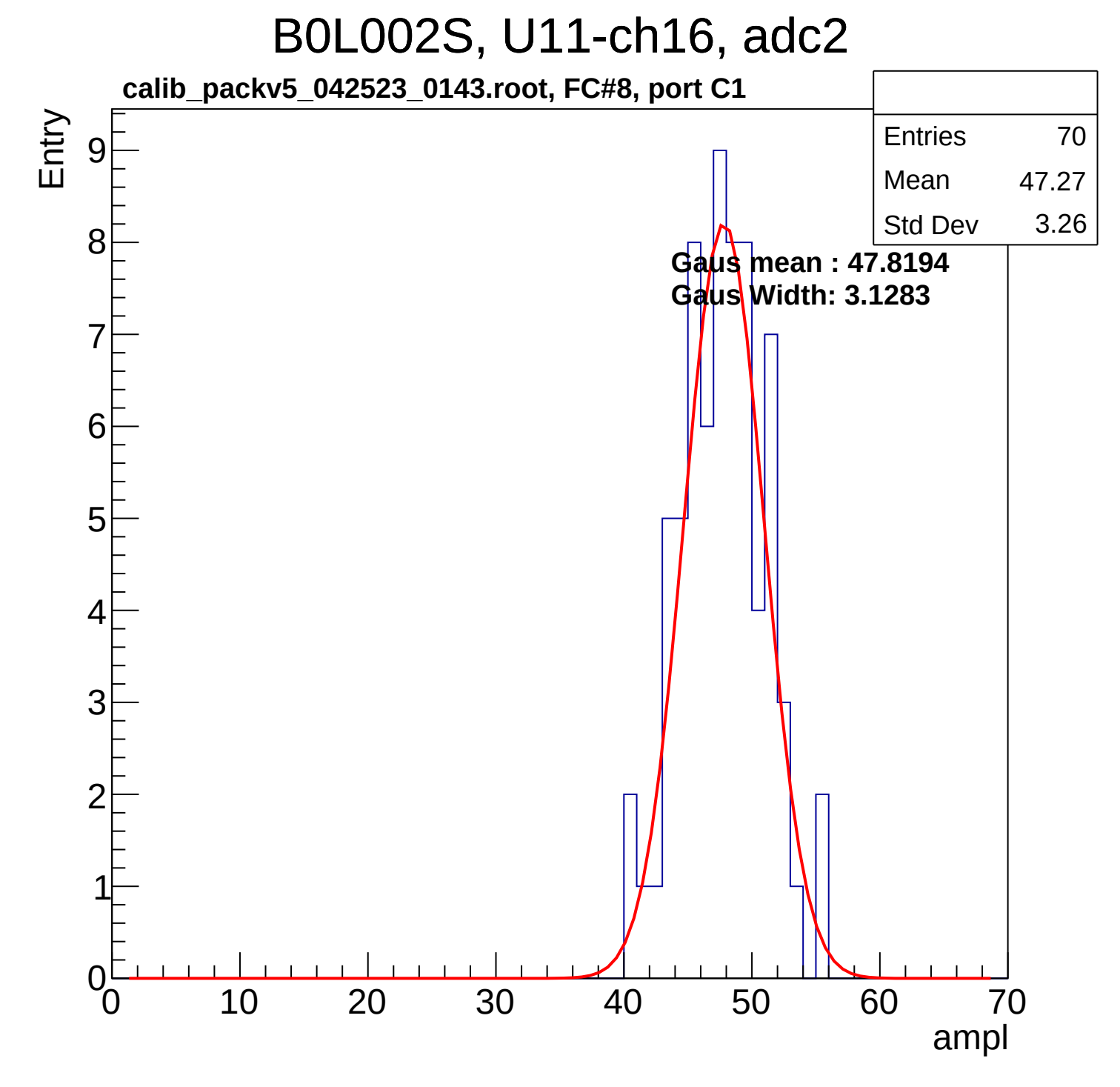
B0L002S, U11-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 47.27
Std Dev 3.26
Gaus mean : 47.8194
Gaus Width: 3.1283
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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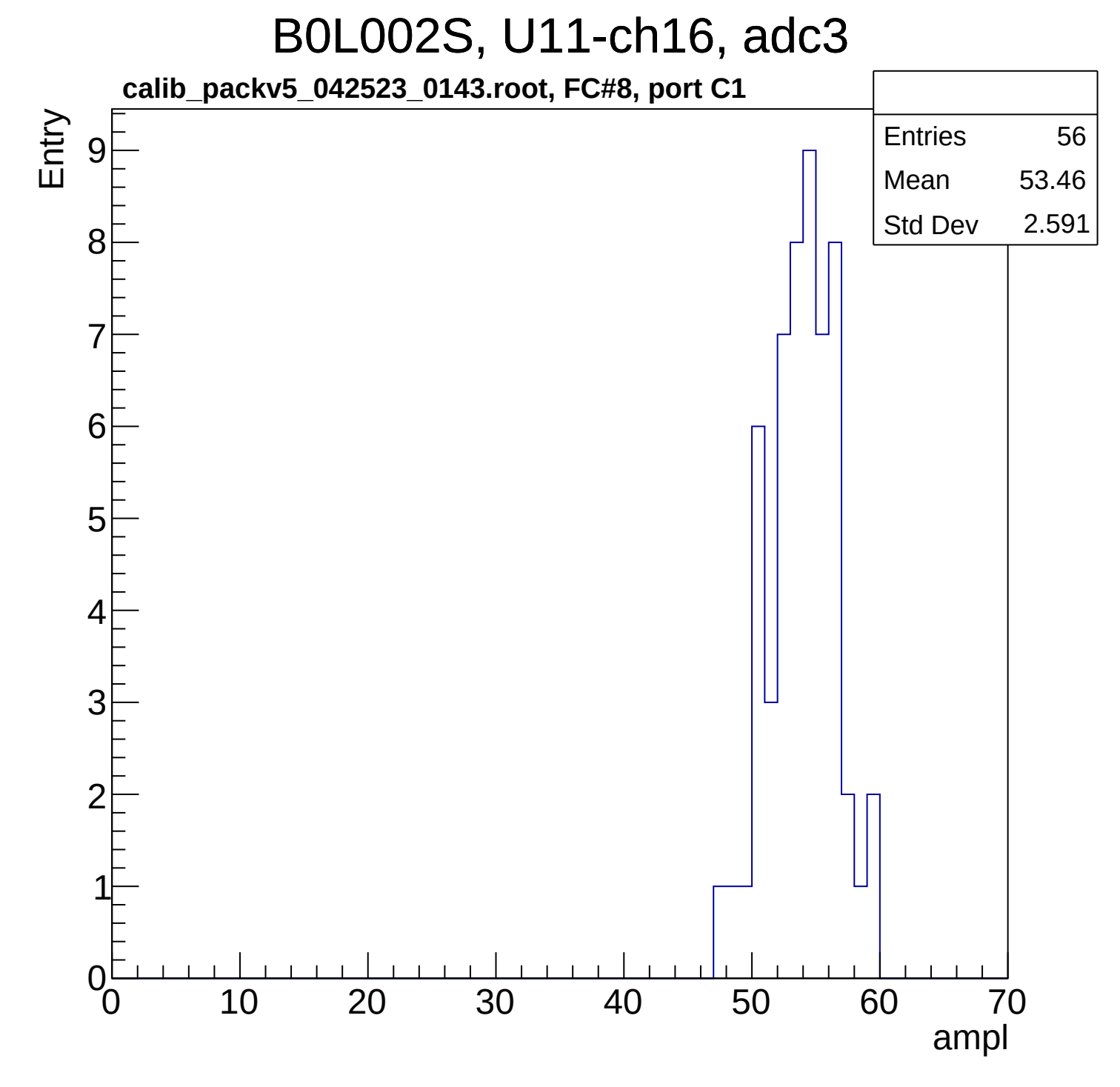
B0L002S, U11-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 53.46
Std Dev 2.591
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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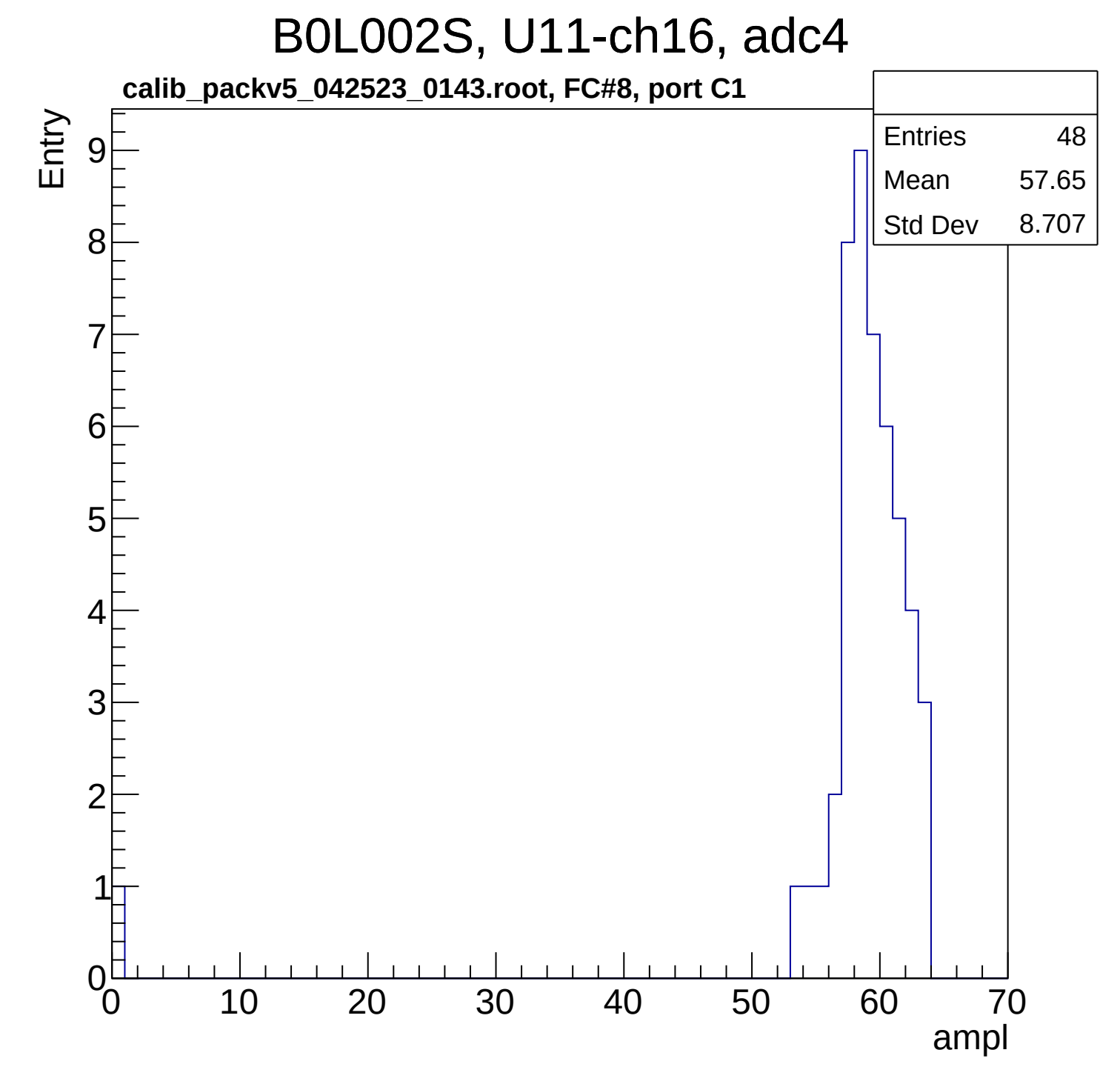
B0L002S, U11-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.65
Std Dev 8.707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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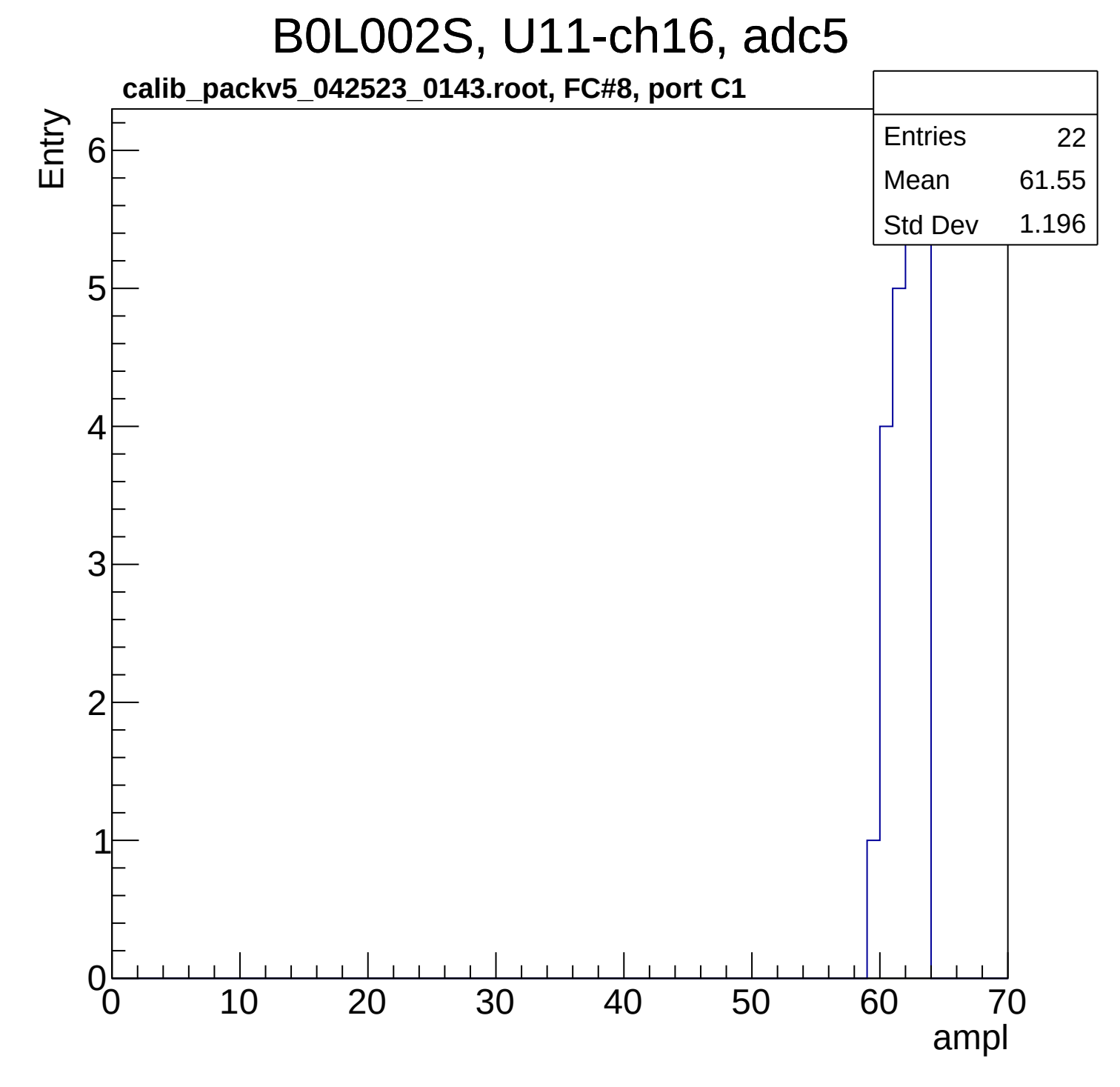
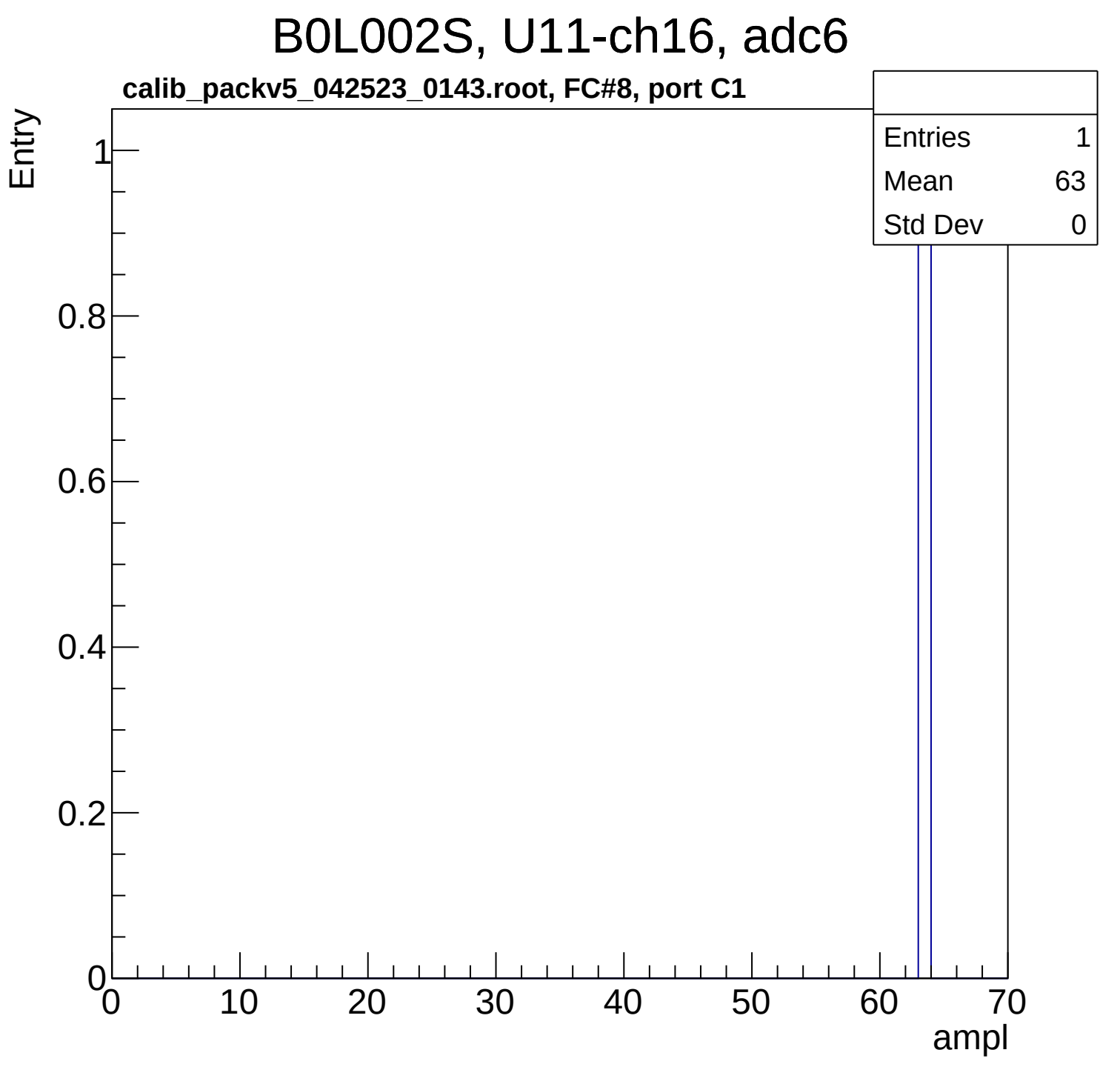
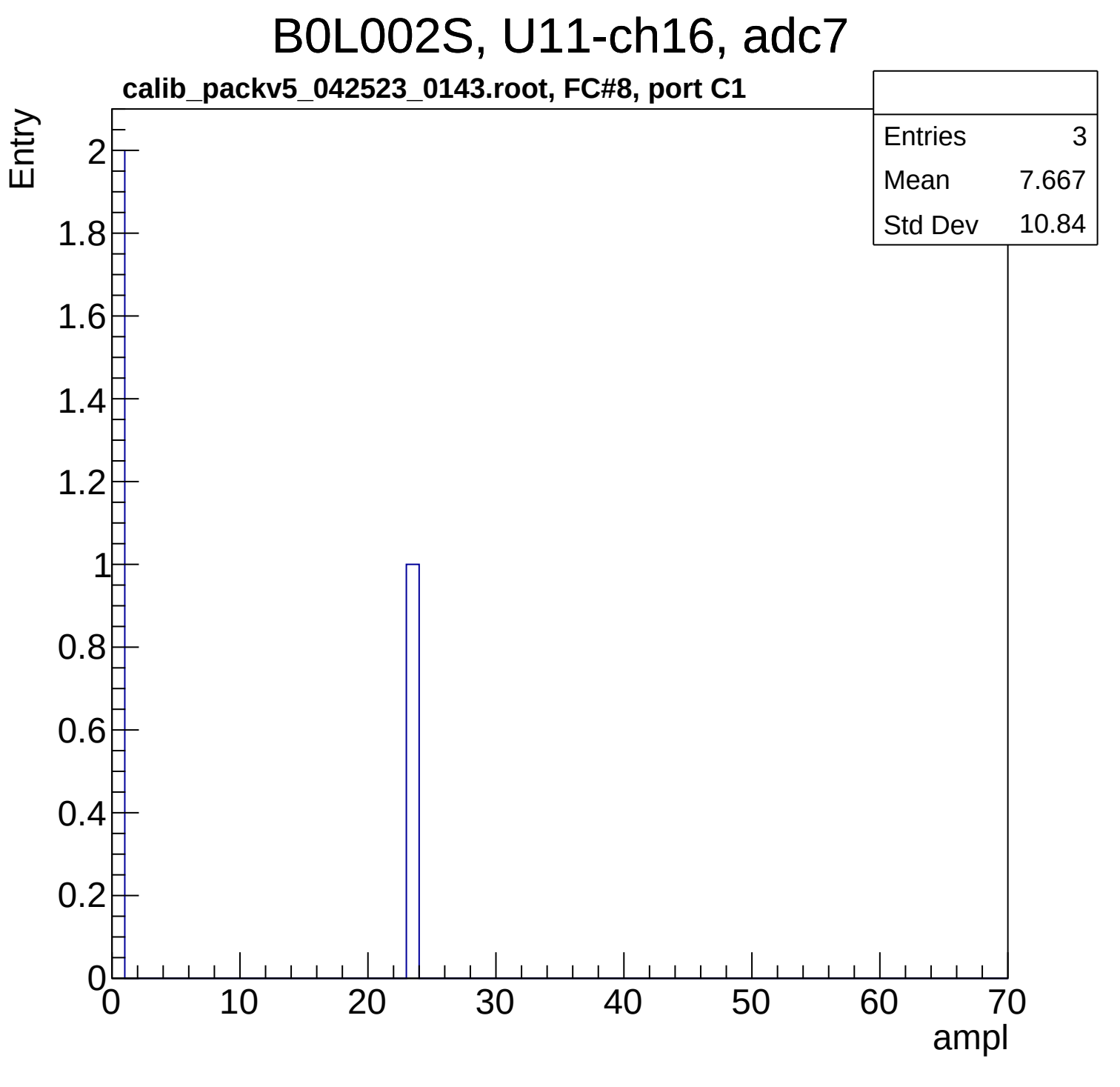

B0L002S, U11-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.55
Std Dev 1.196
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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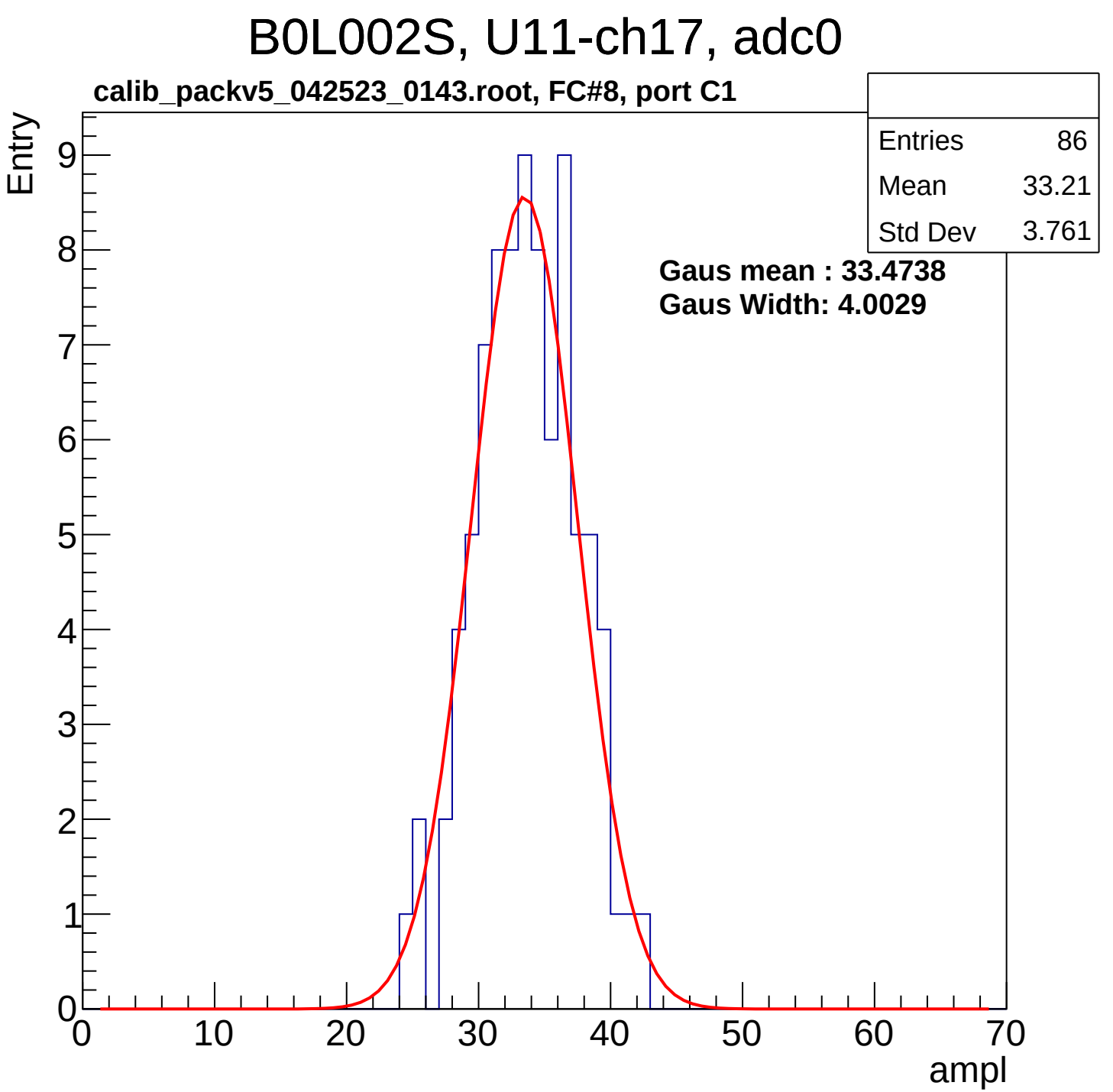
B0L002S, U11-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 33.21
Std Dev 3.761
Gaus mean : 33.4738
Gaus Width: 4.0029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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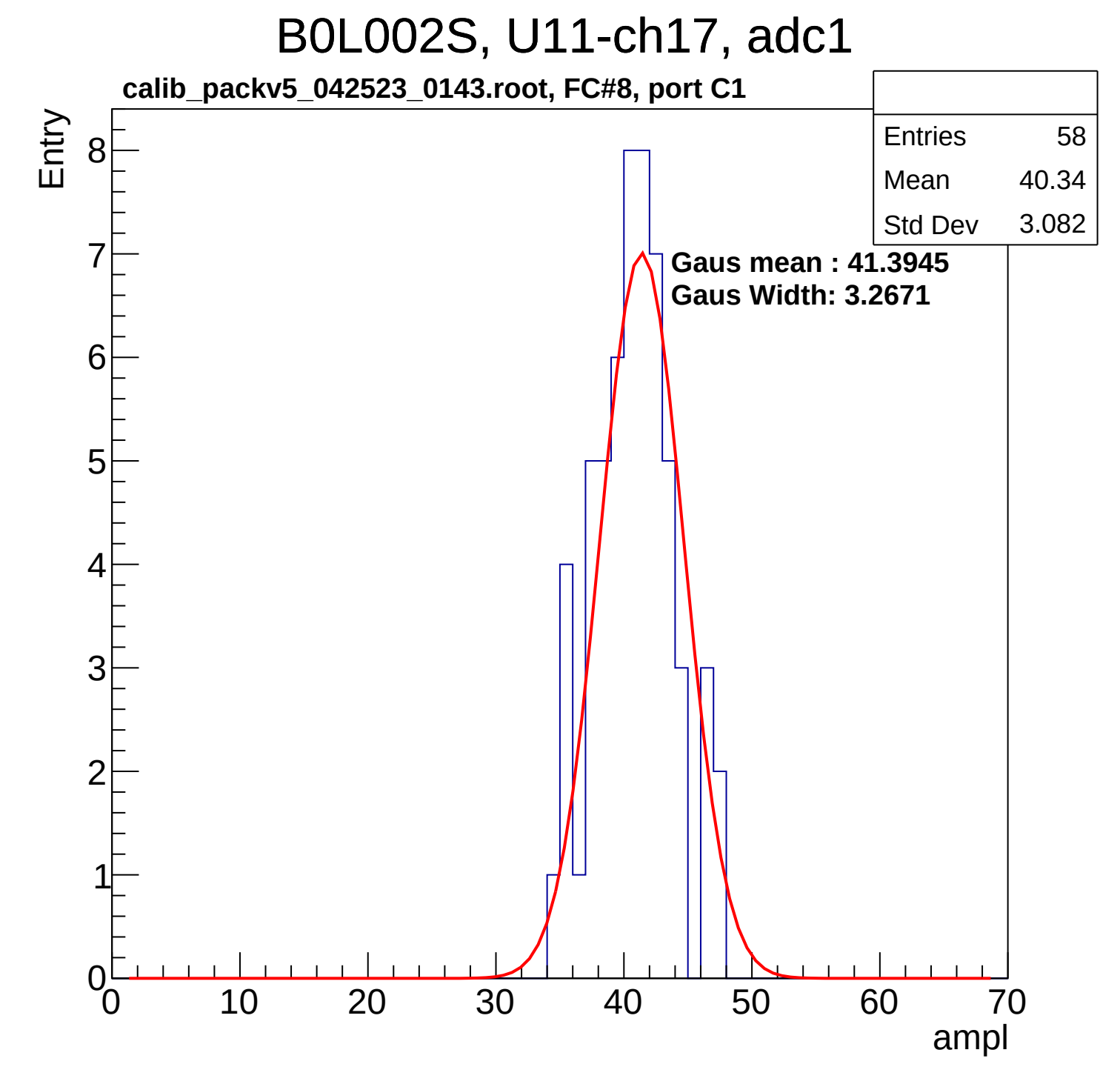
B0L002S, U11-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 40.34
Std Dev 3.082
Gaus mean : 41.3945
Gaus Width: 3.2671
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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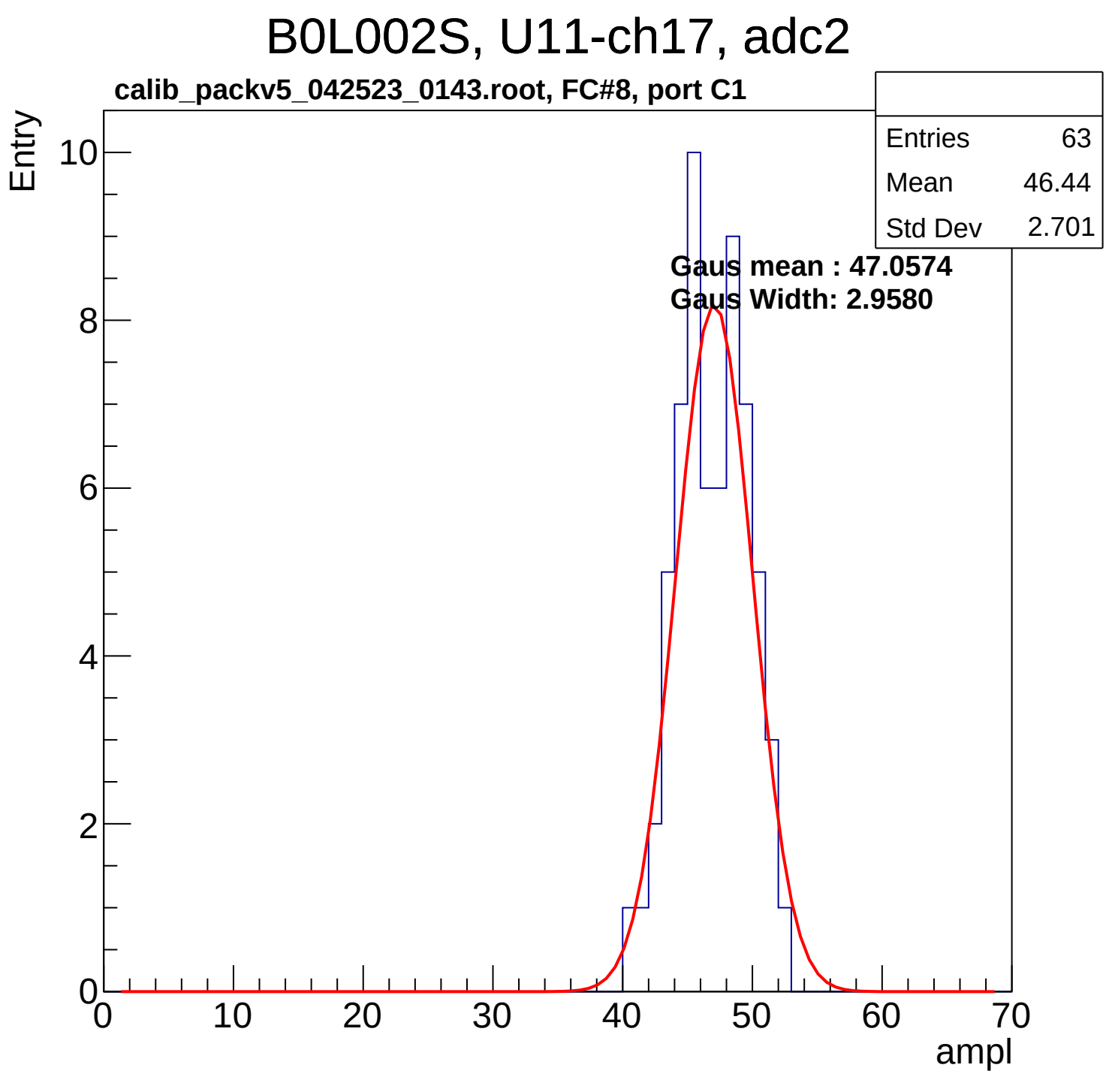
B0L002S, U11-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 46.44
Std Dev 2.701
Gaus mean : 47.0574
Gaus Width: 2.9580
Entry
ampl
0
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4
6
8
10
0
10
20
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40
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60
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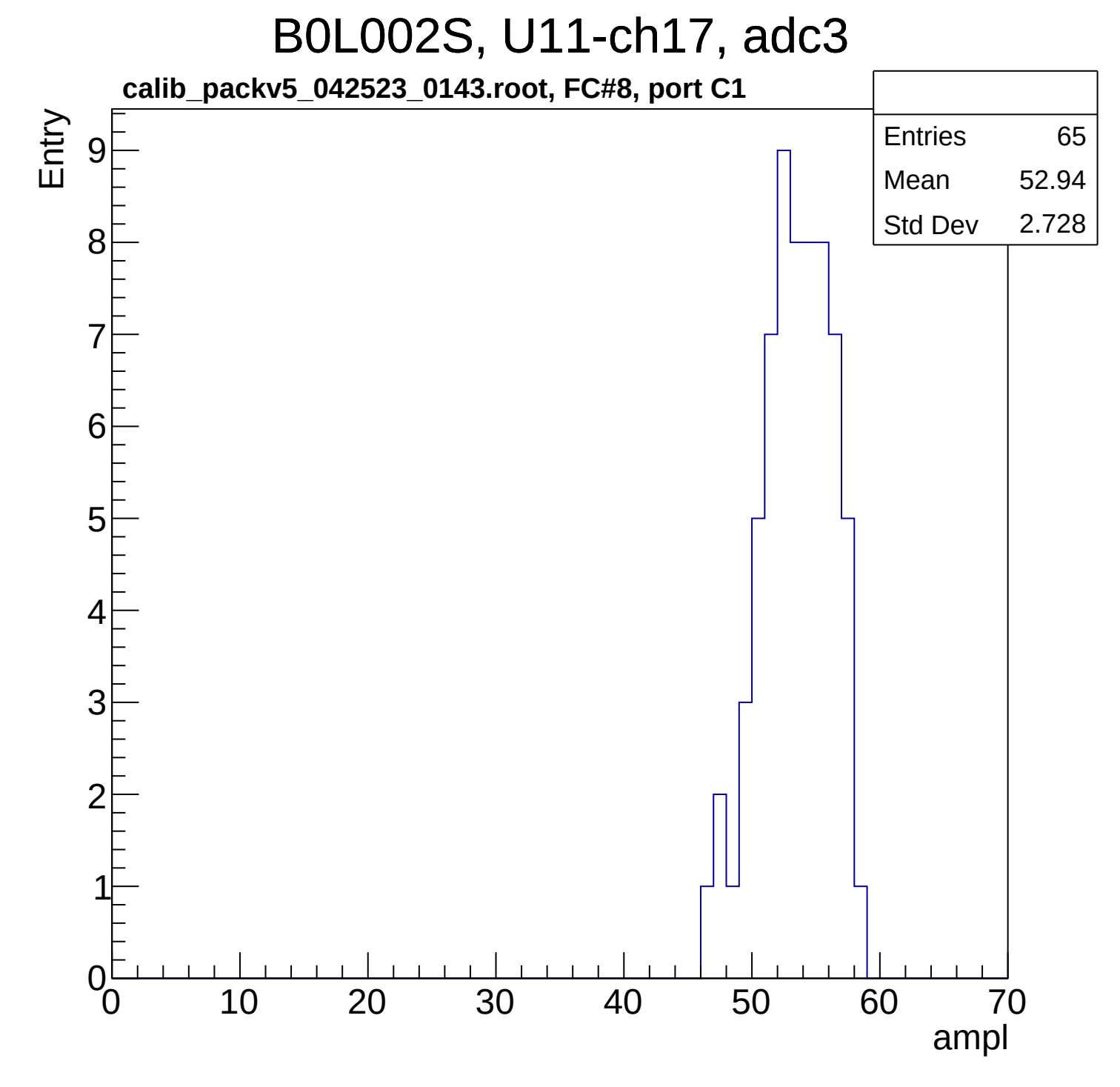
B0L002S, U11-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 52.94
Std Dev 2.728
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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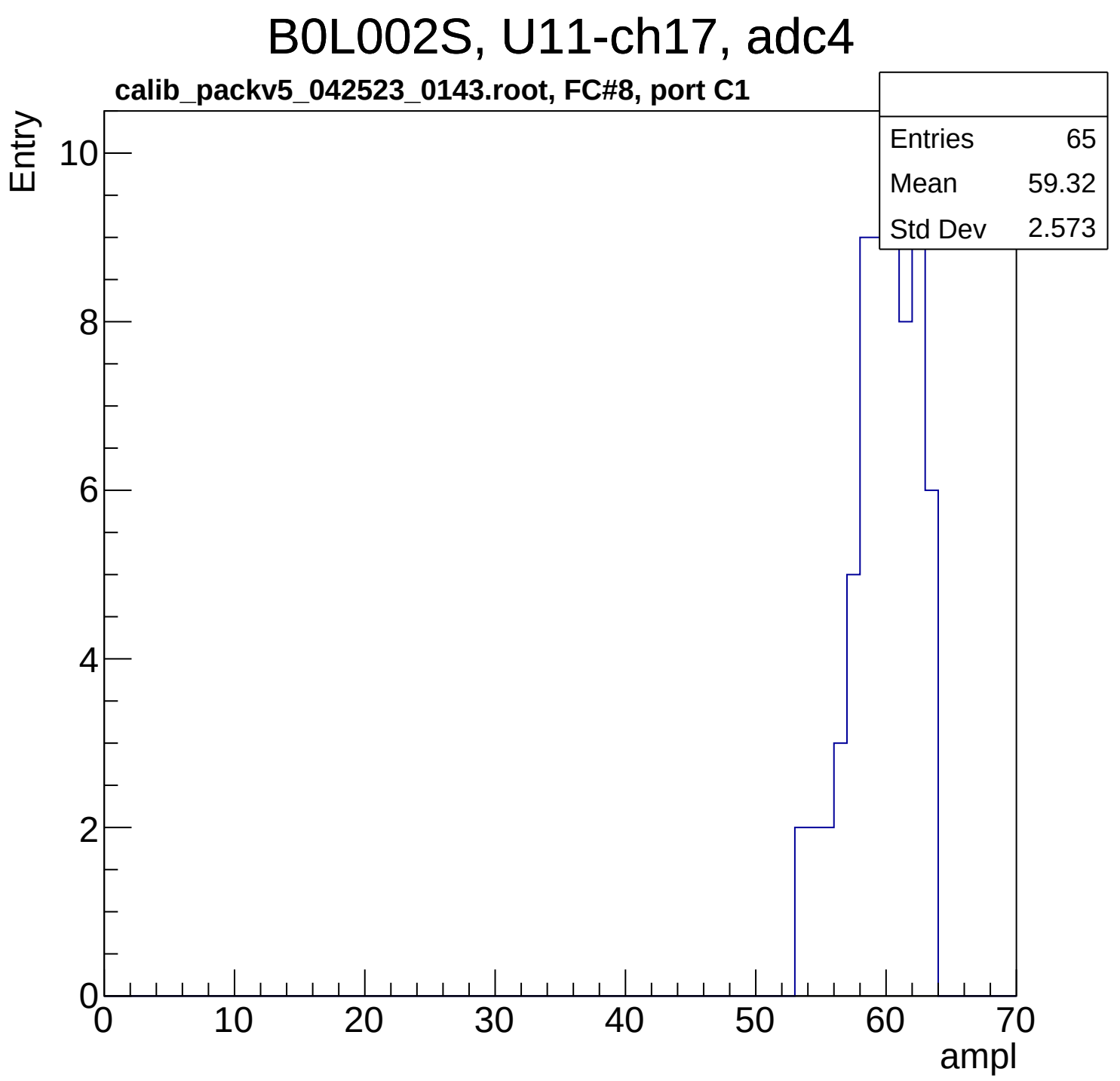
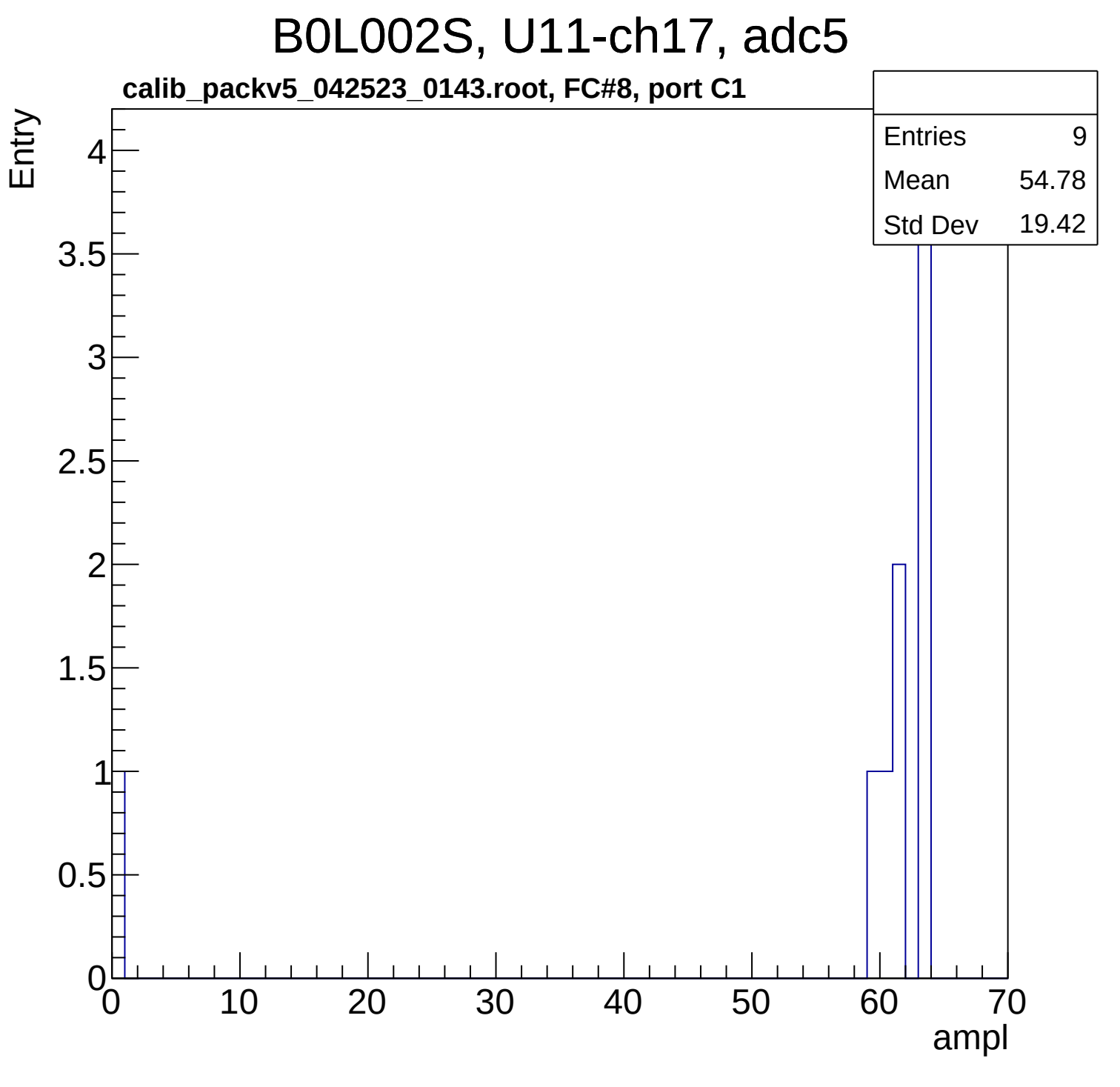

B0L002S, U11-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 59.32
Std Dev 2.573
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 54.78
Std Dev 19.42
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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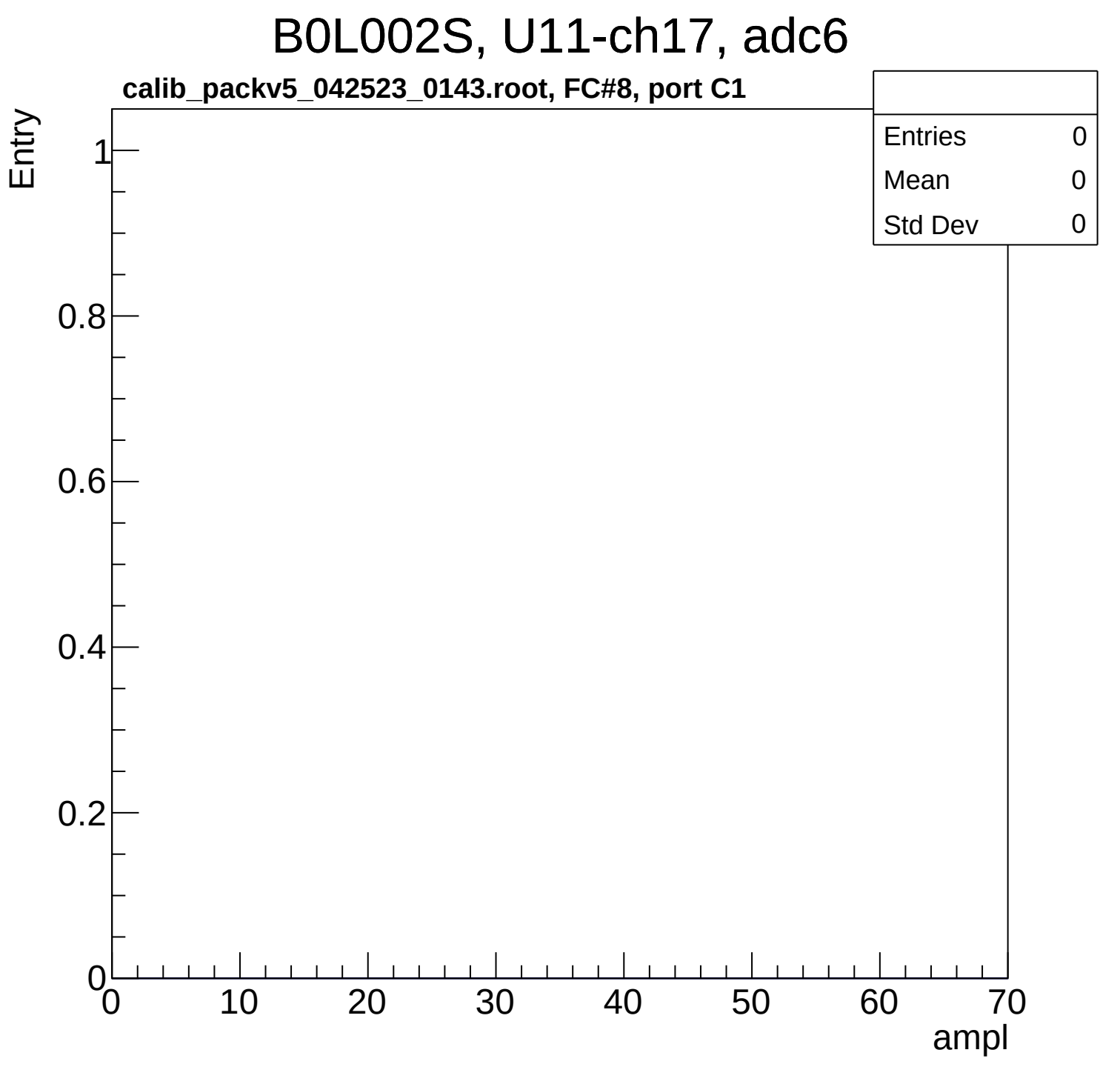
40
50
60
70


B0L002S, U11-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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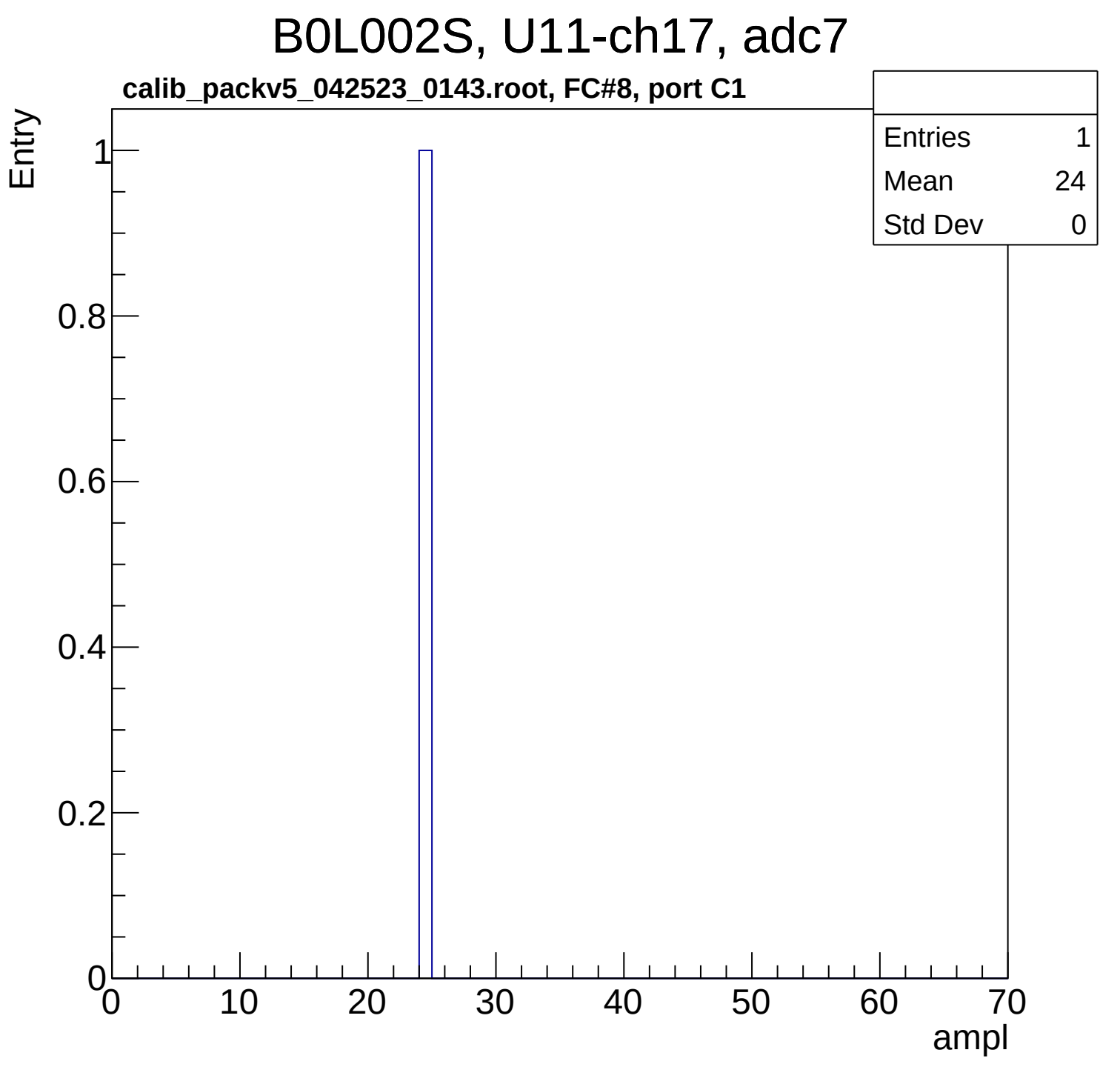
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
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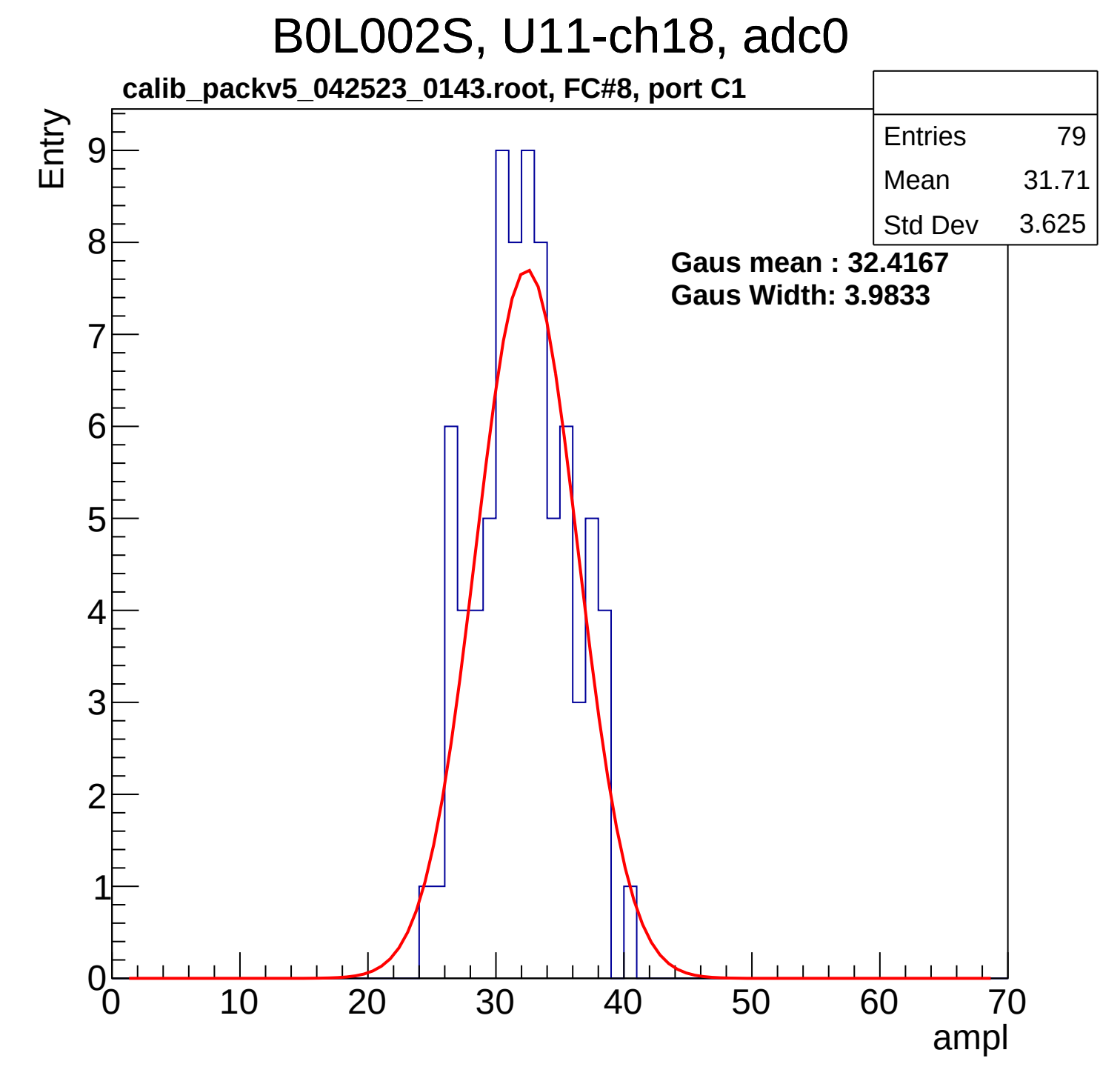
50
60
70
ampl


B0L002S, U11-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 31.71
Std Dev 3.625
Gaus mean : 32.4167
Gaus Width: 3.9833
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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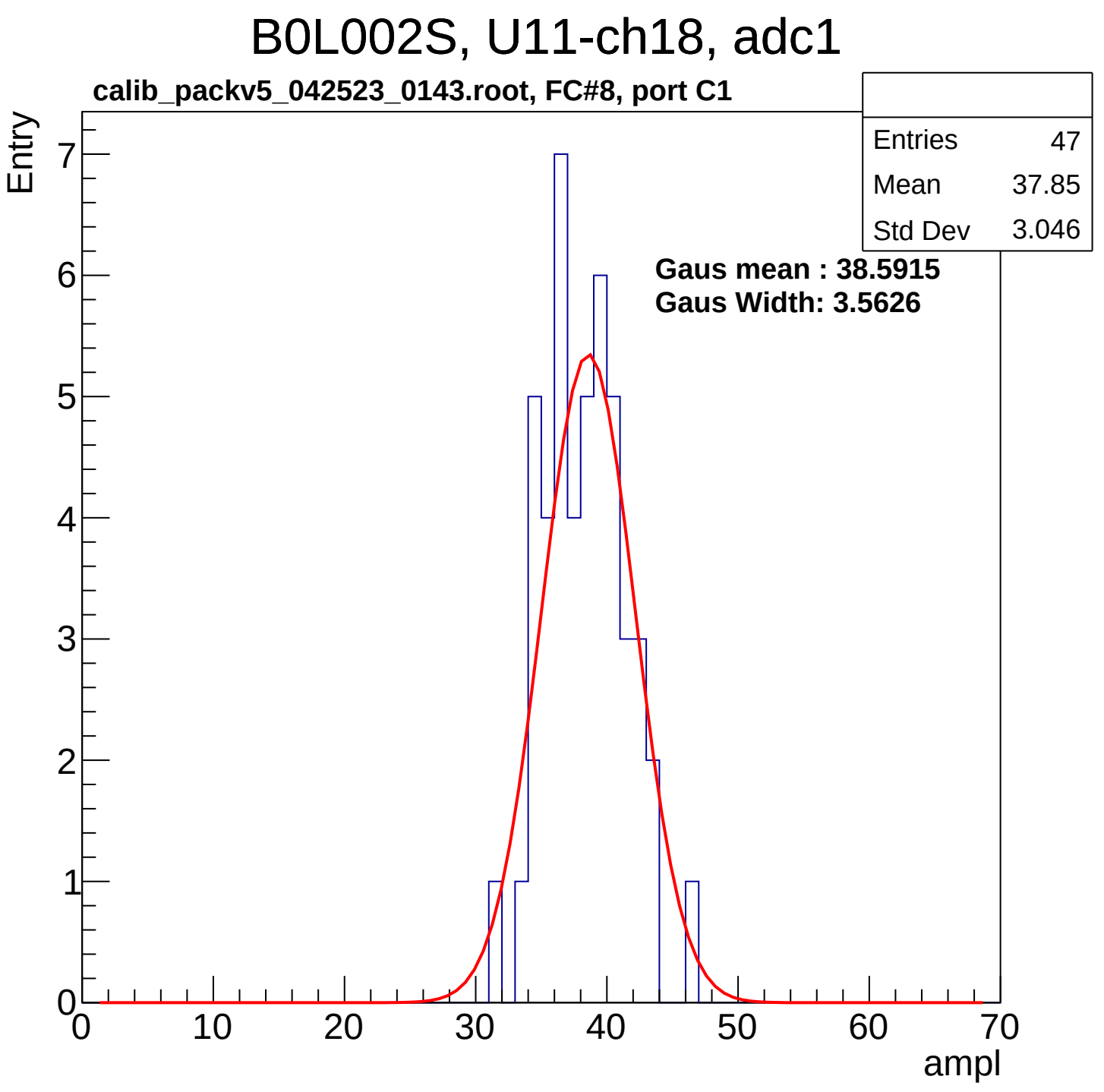
B0L002S, U11-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 37.85
Std Dev 3.046
Gaus mean : 38.5915
Gaus Width: 3.5626
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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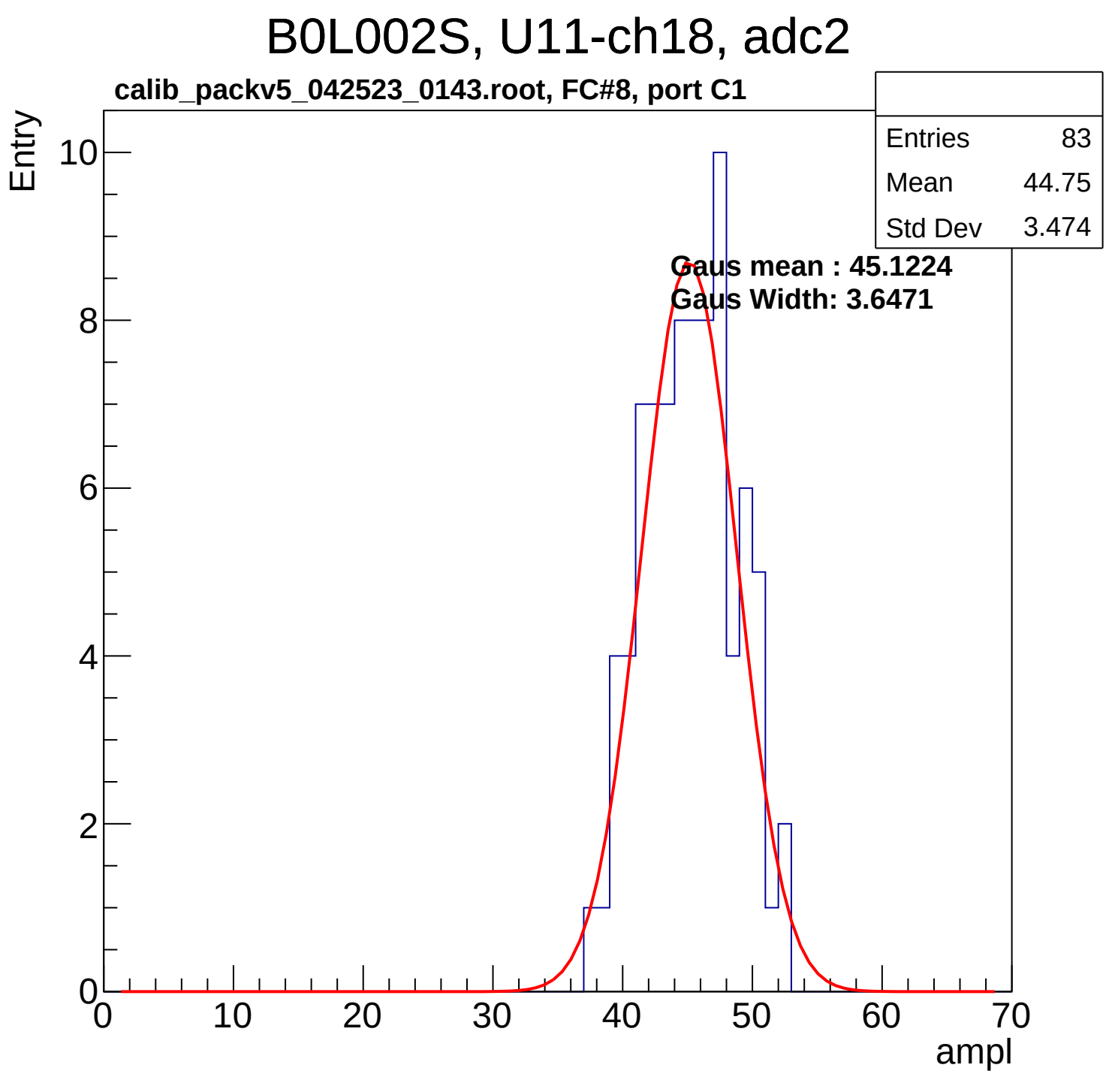
B0L002S, U11-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 44.75
Std Dev 3.474
Gaus mean : 45.1224
Gaus Width: 3.6471
Entry
ampl
0
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4
6
8
10
0
10
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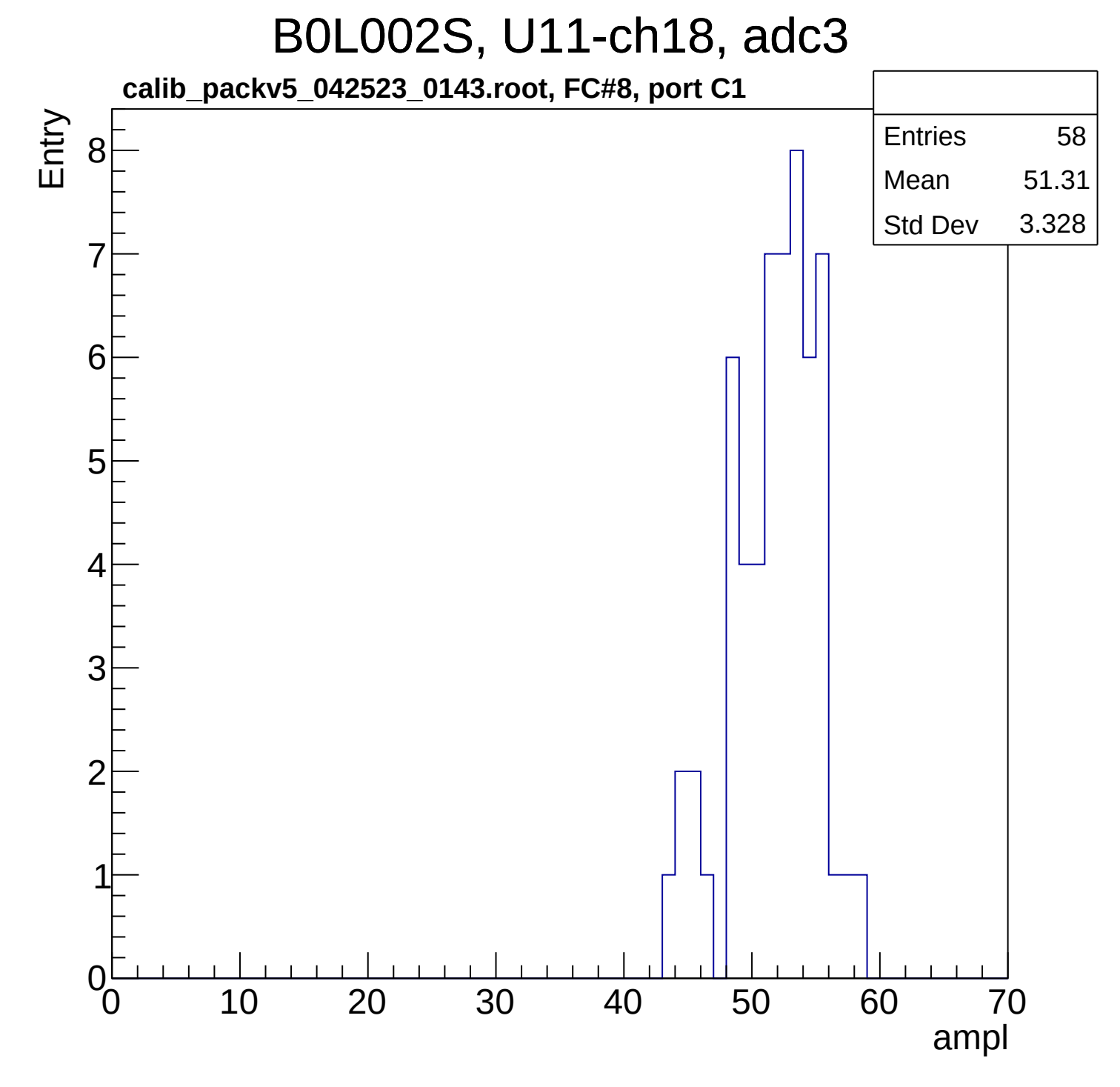
30
40
50
60
70


B0L002S, U11-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.31
Std Dev 3.328
Entry
ampl
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5
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7
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0
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60
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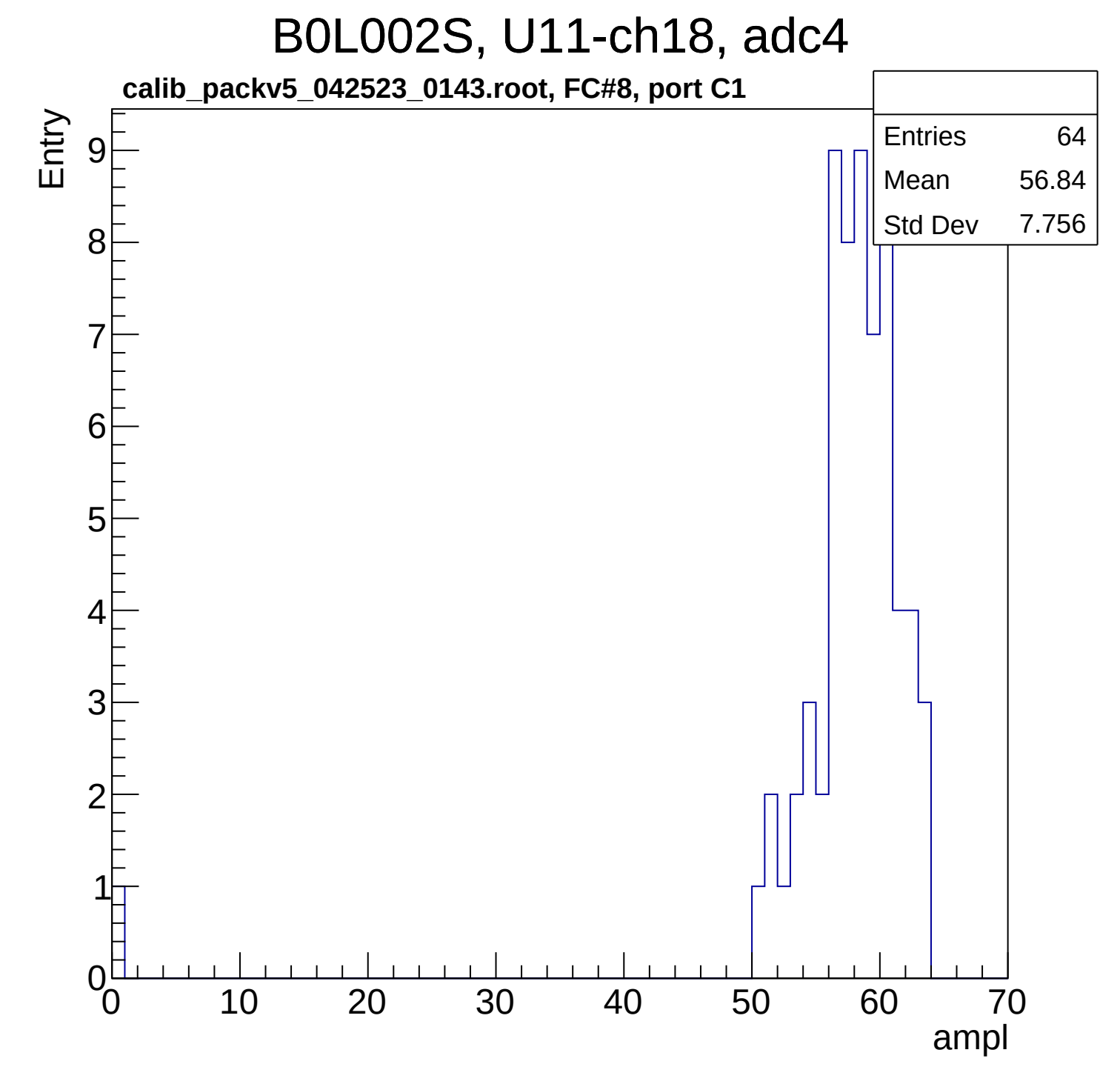
B0L002S, U11-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.84
Std Dev 7.756
Entry
ampl
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2
3
4
5
6
7
8
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0
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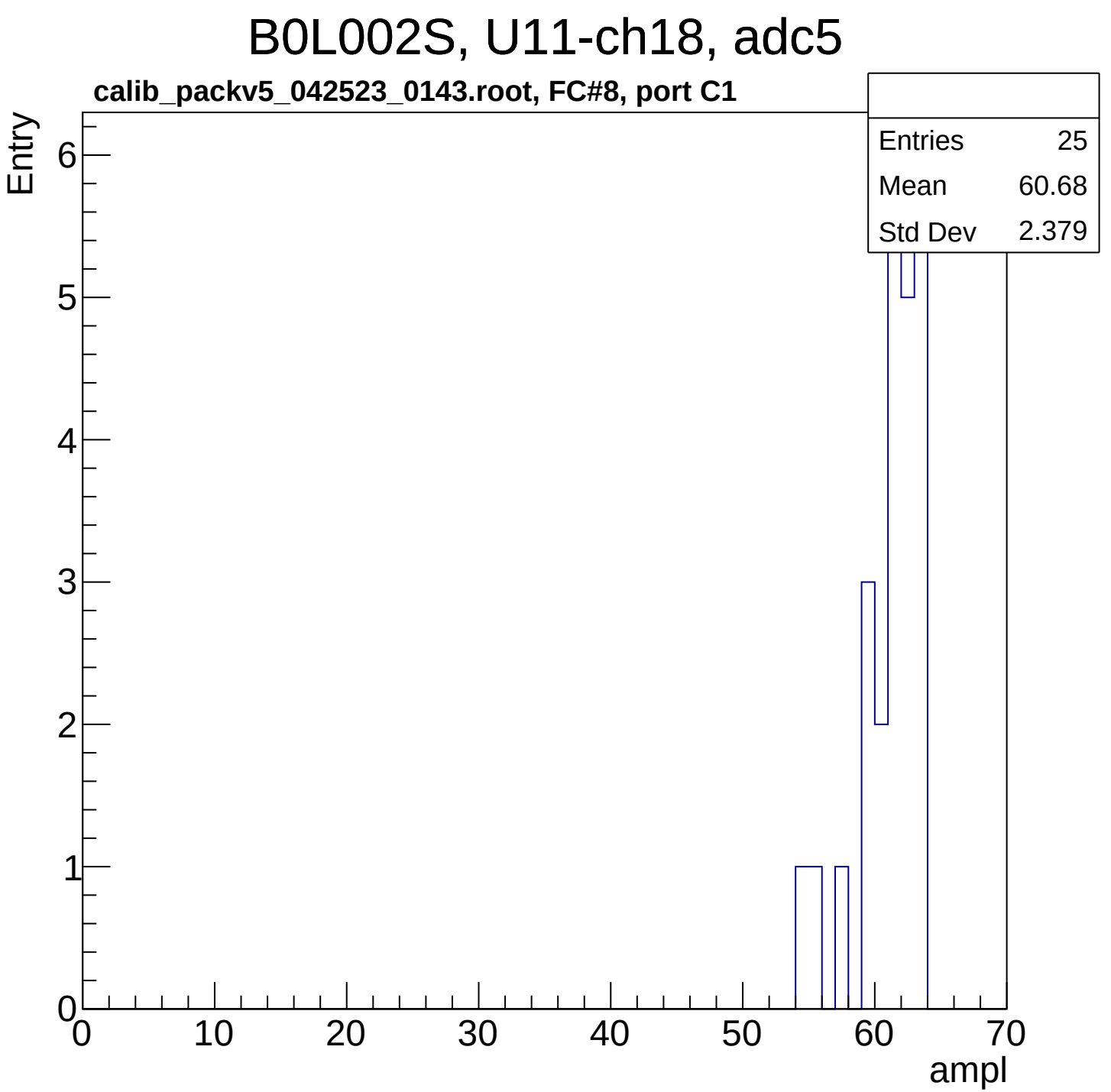
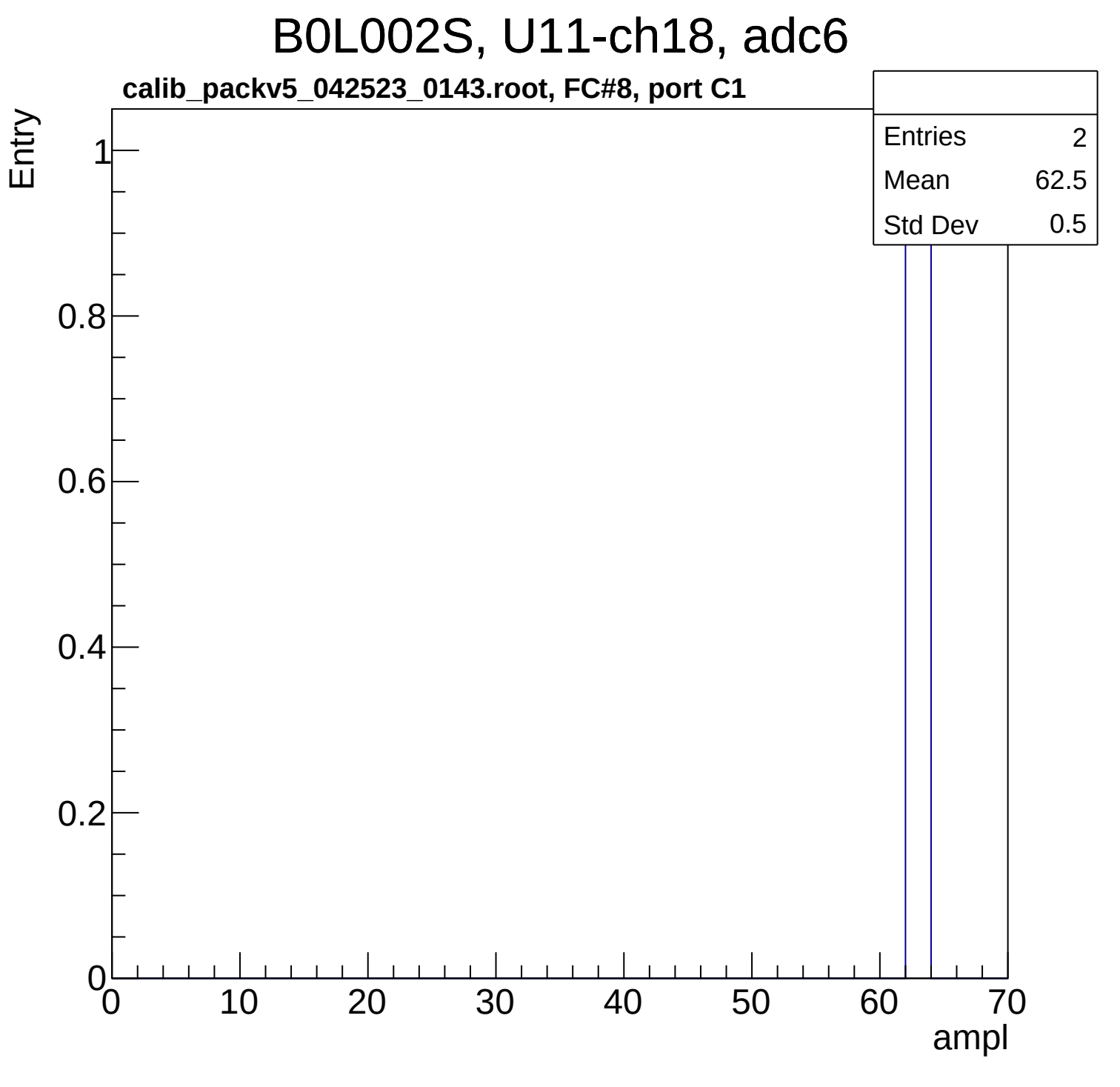

B0L002S, U11-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 60.68
Std Dev 2.379
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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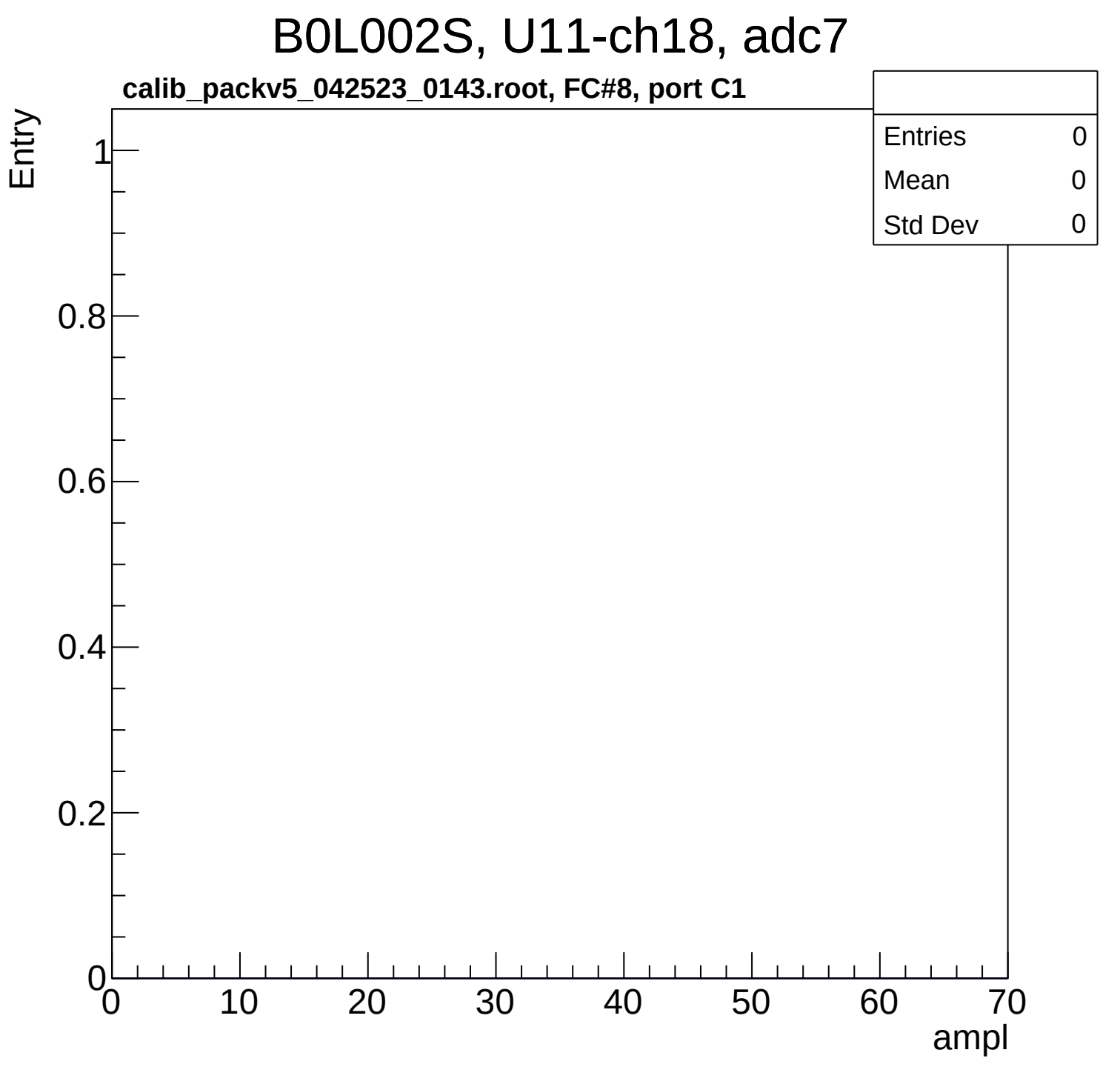
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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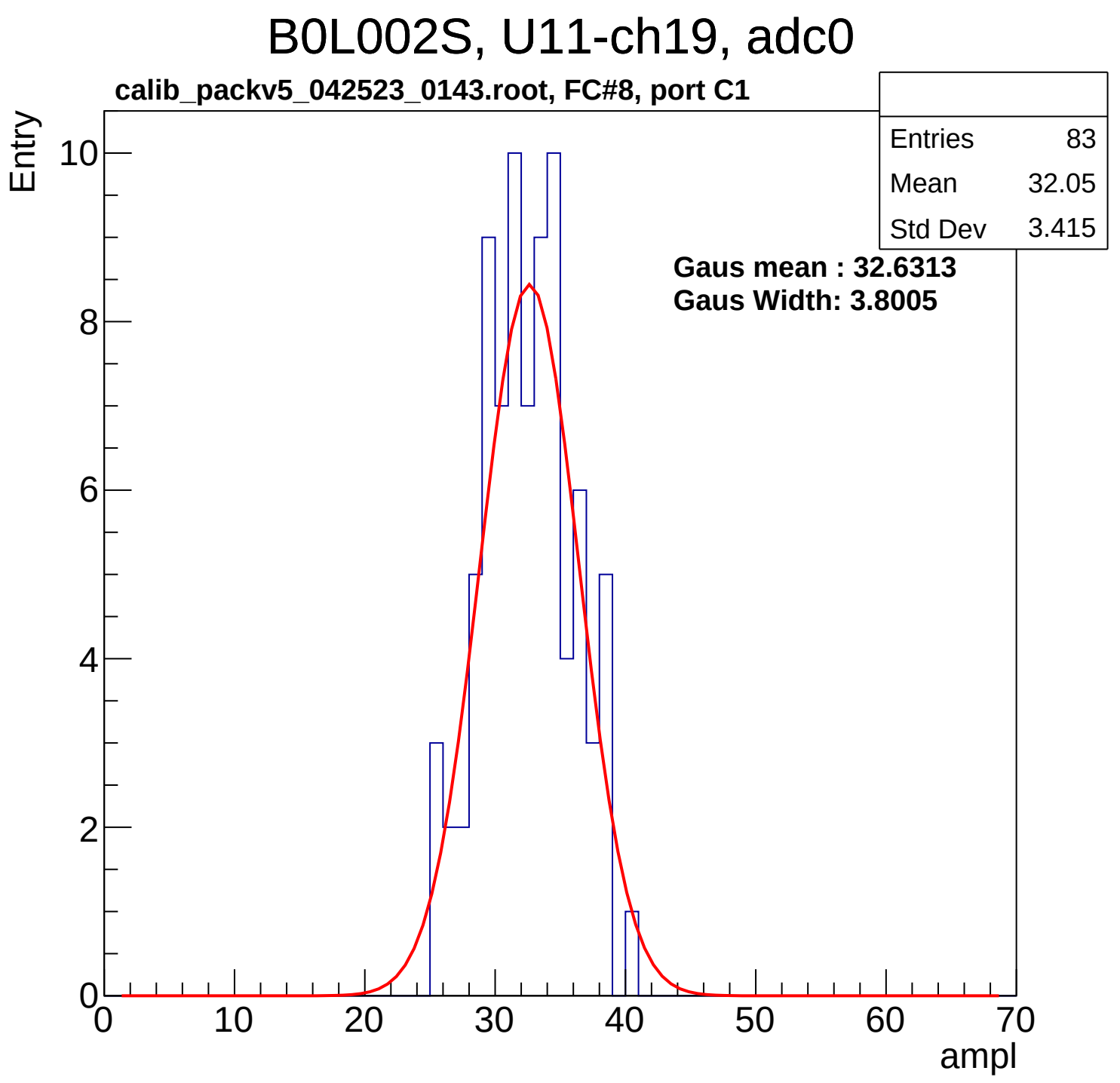
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 32.05
Std Dev 3.415
Gaus mean : 32.6313
Gaus Width: 3.8005
Entry
ampl
0
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6
8
10
0
10
20
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40
50
60
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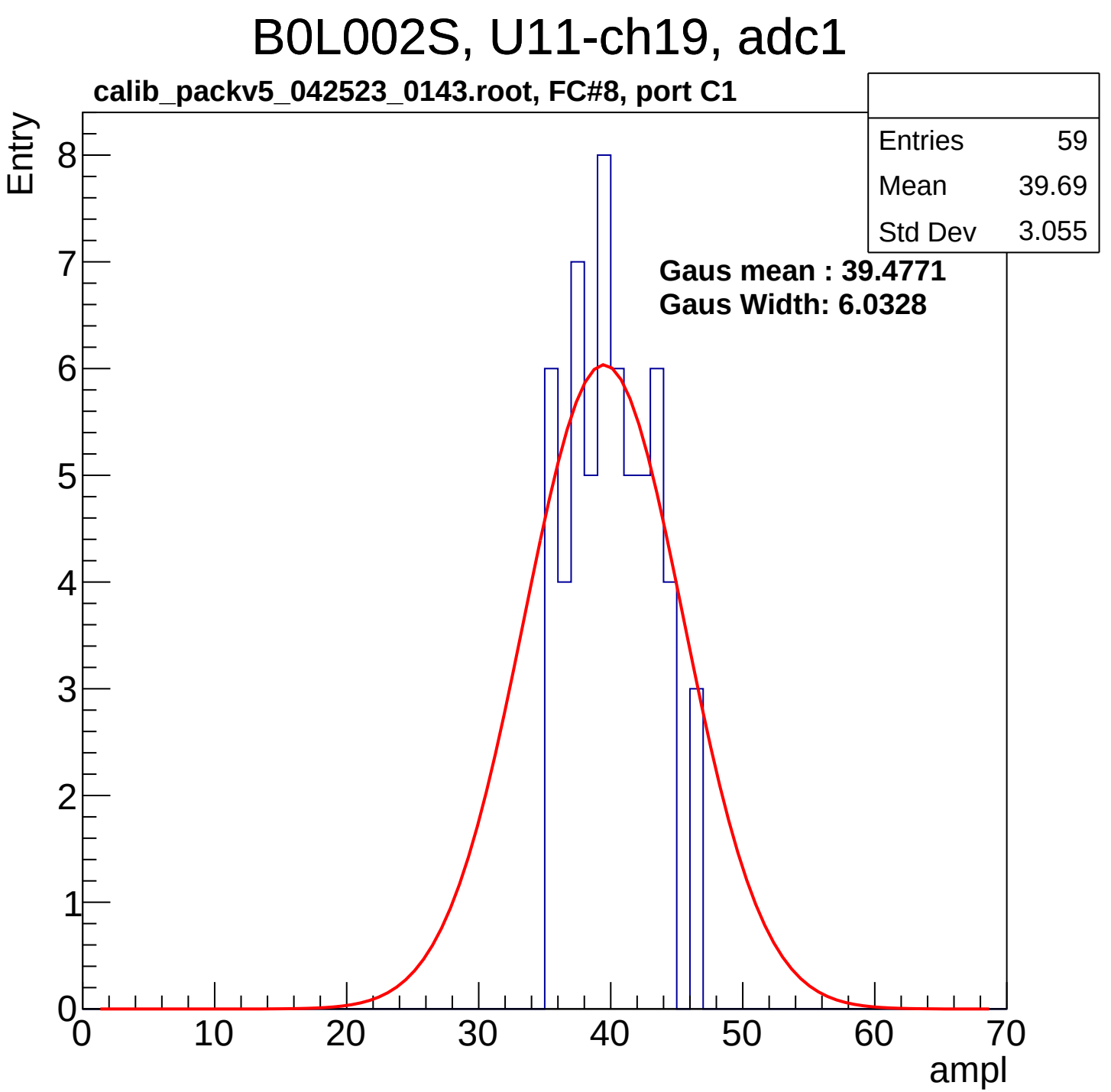
B0L002S, U11-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 39.69
Std Dev 3.055
Gaus mean : 39.4771
Gaus Width: 6.0328
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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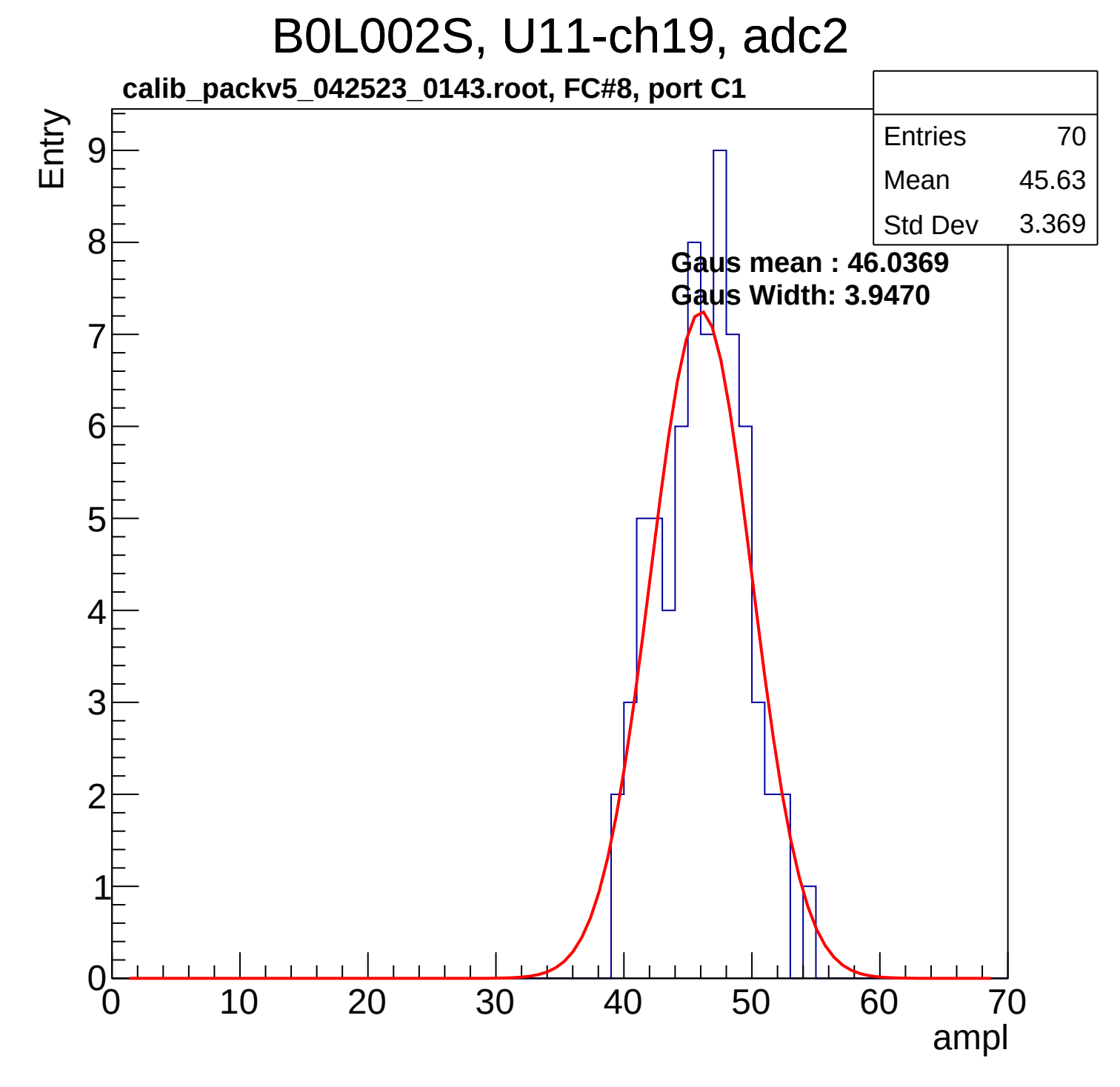
B0L002S, U11-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 45.63
Std Dev 3.369
Gaus mean : 46.0369
Gaus Width: 3.9470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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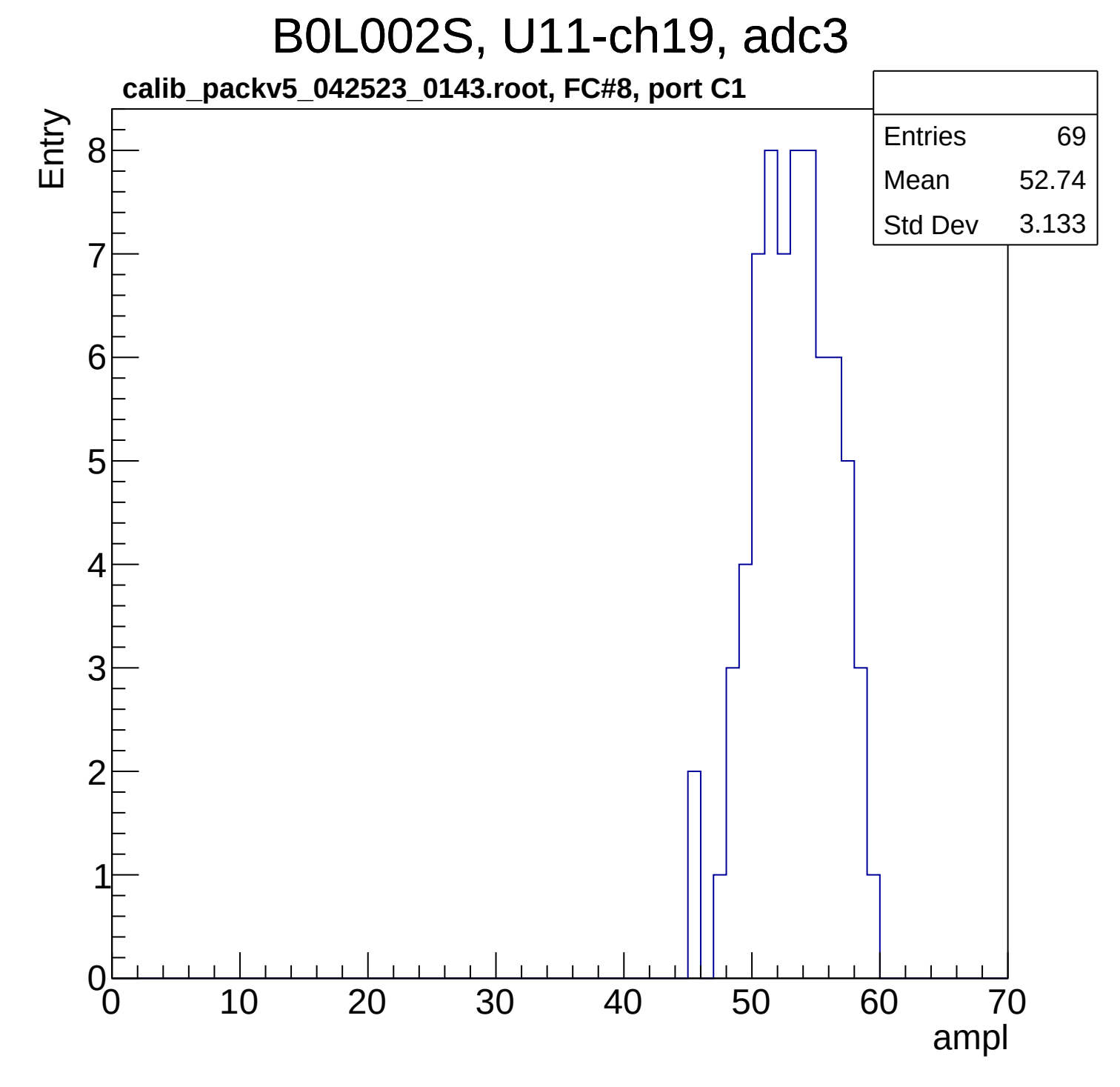
B0L002S, U11-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.74
Std Dev 3.133
Entry
ampl
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2
3
4
5
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0
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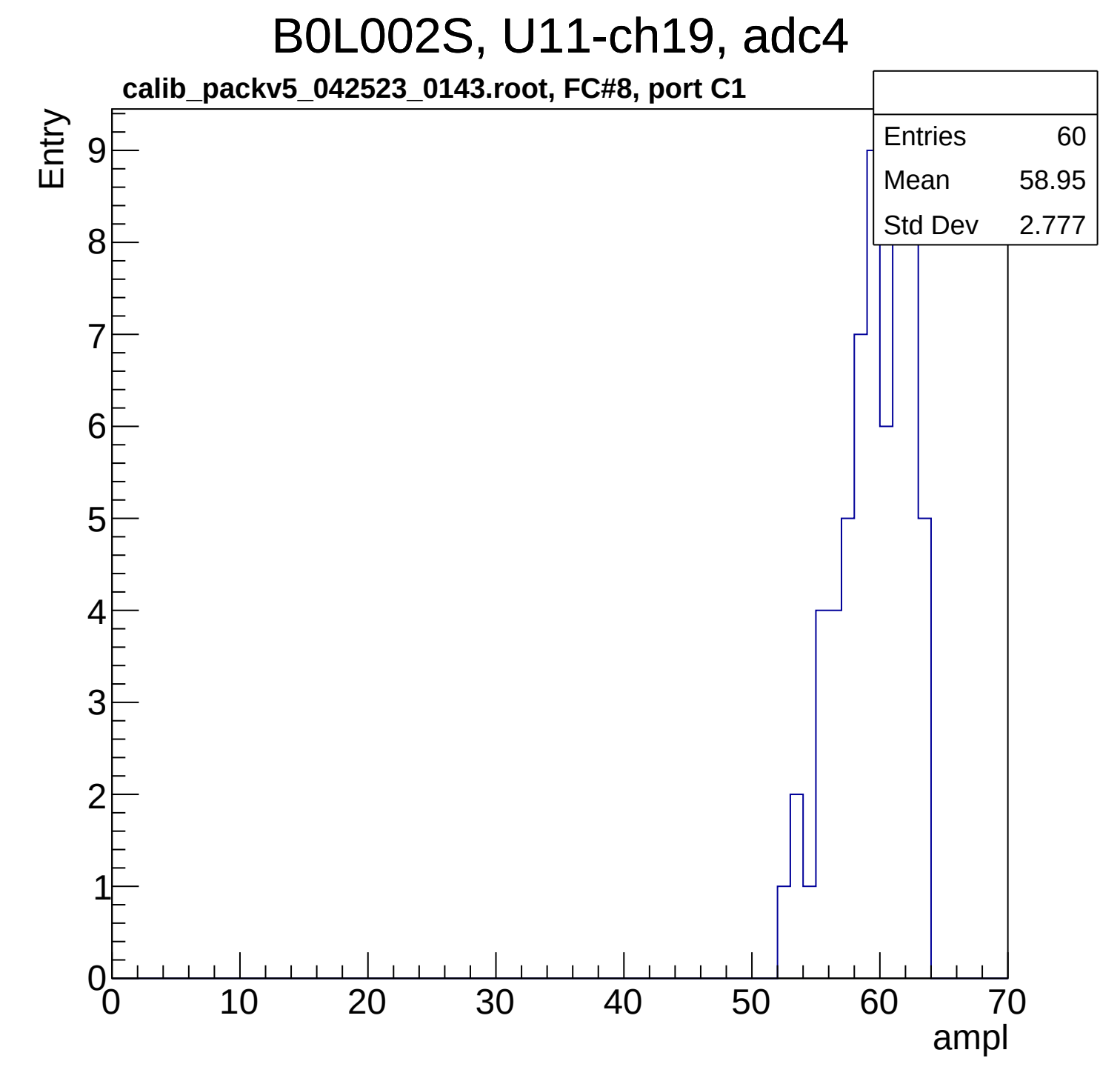
B0L002S, U11-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 58.95
Std Dev 2.777
Entry
ampl
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2
3
4
5
6
7
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0
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60
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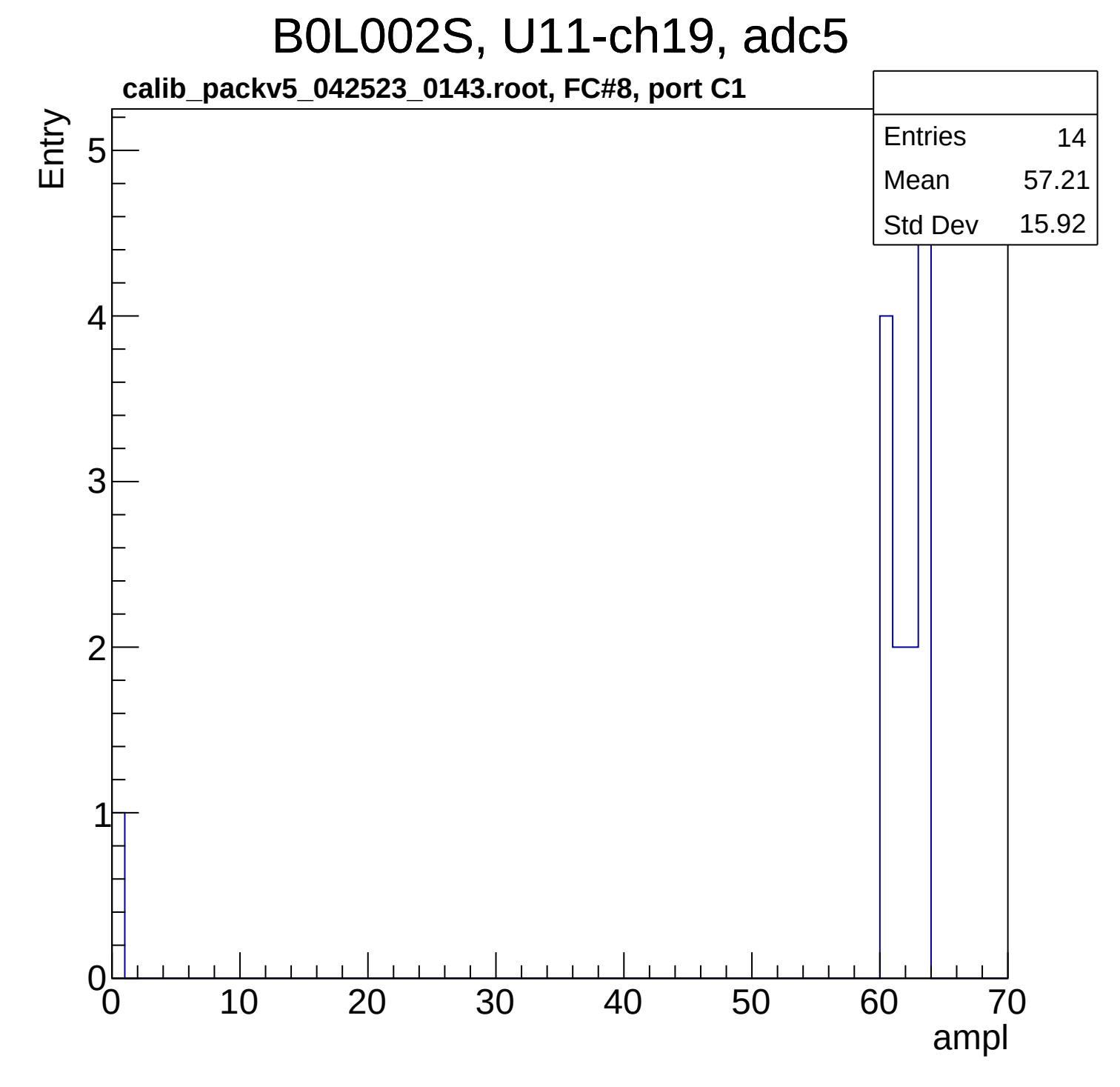
B0L002S, U11-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 57.21
Std Dev 15.92
Entry
ampl
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3
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0
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40
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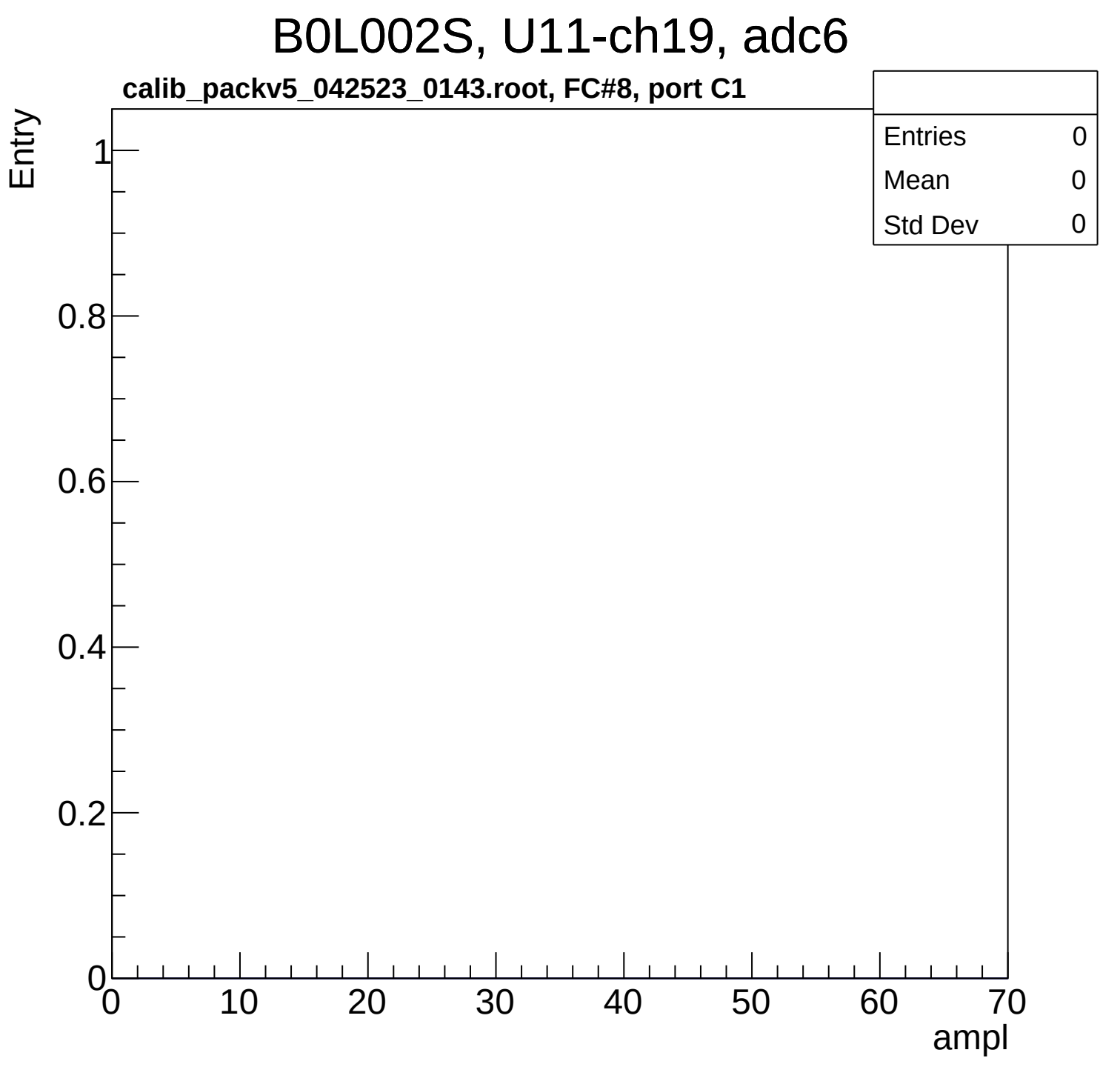
B0L002S, U11-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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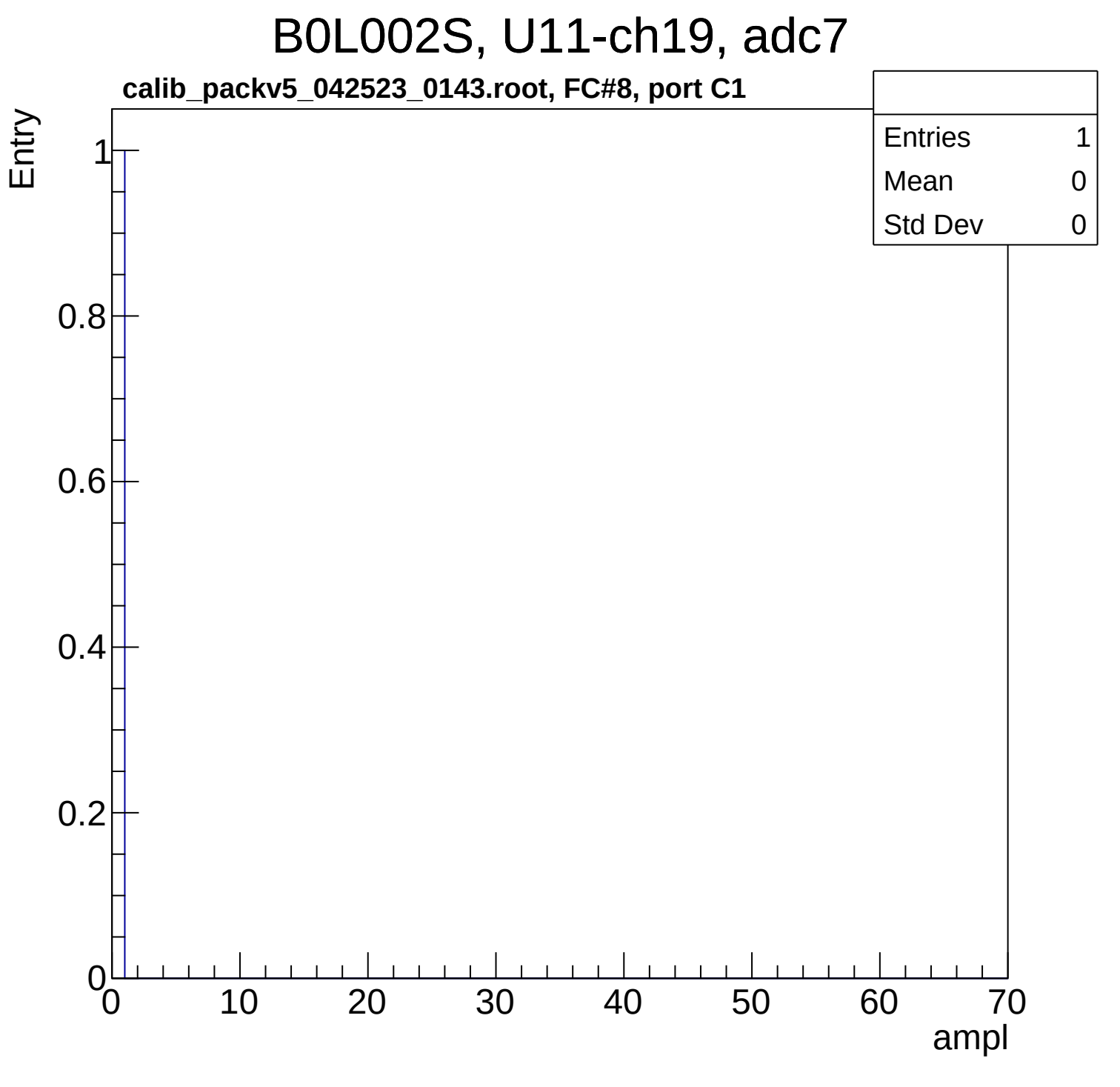
50
60
70
ampl


B0L002S, U11-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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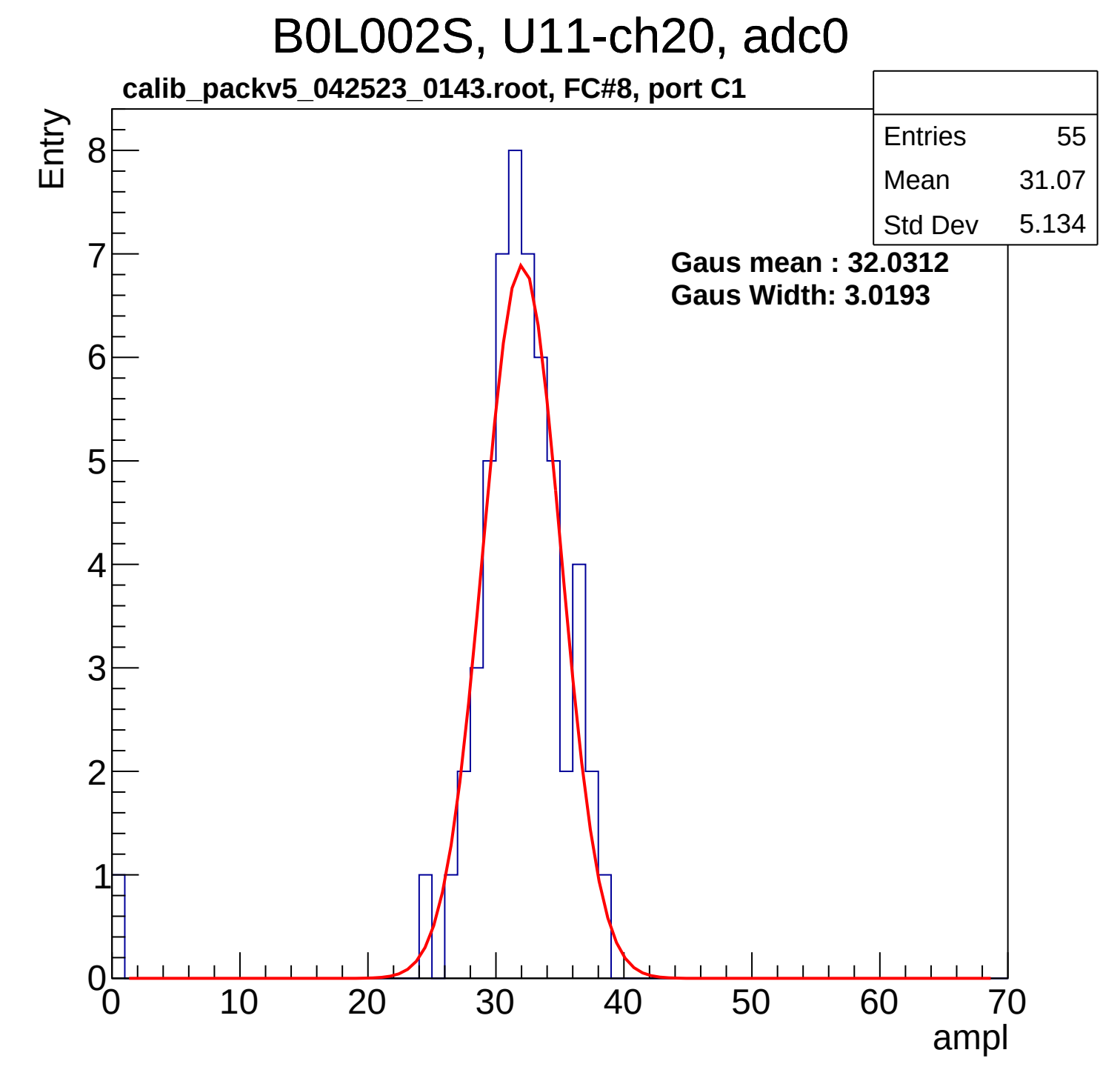
50
60
70
ampl


B0L002S, U11-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 31.07
Std Dev 5.134
Gaus mean : 32.0312
Gaus Width: 3.0193
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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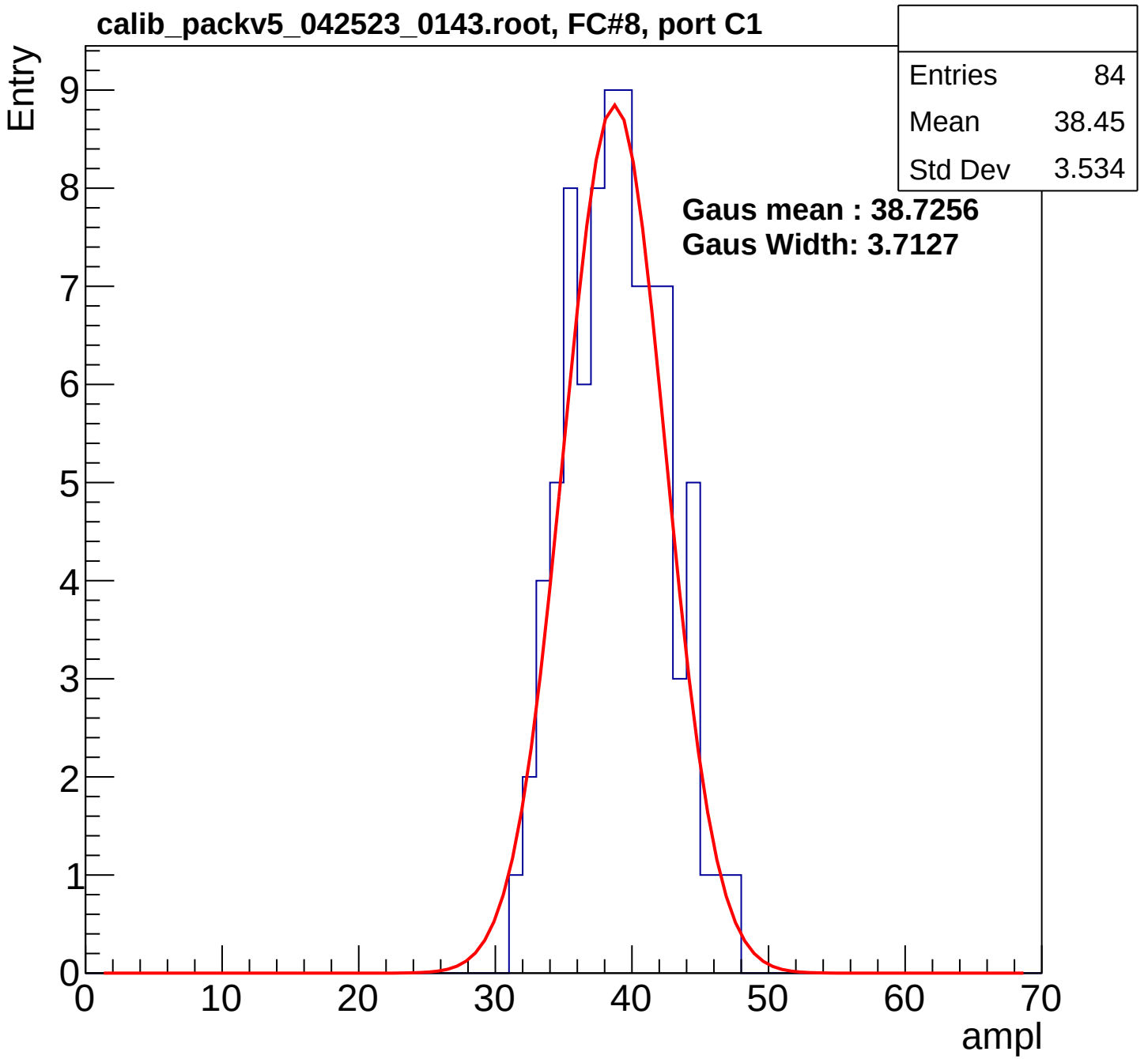
B0L002S, U11-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 38.45
Std Dev 3.534
Gaus mean : 38.7256
Gaus Width: 3.7127
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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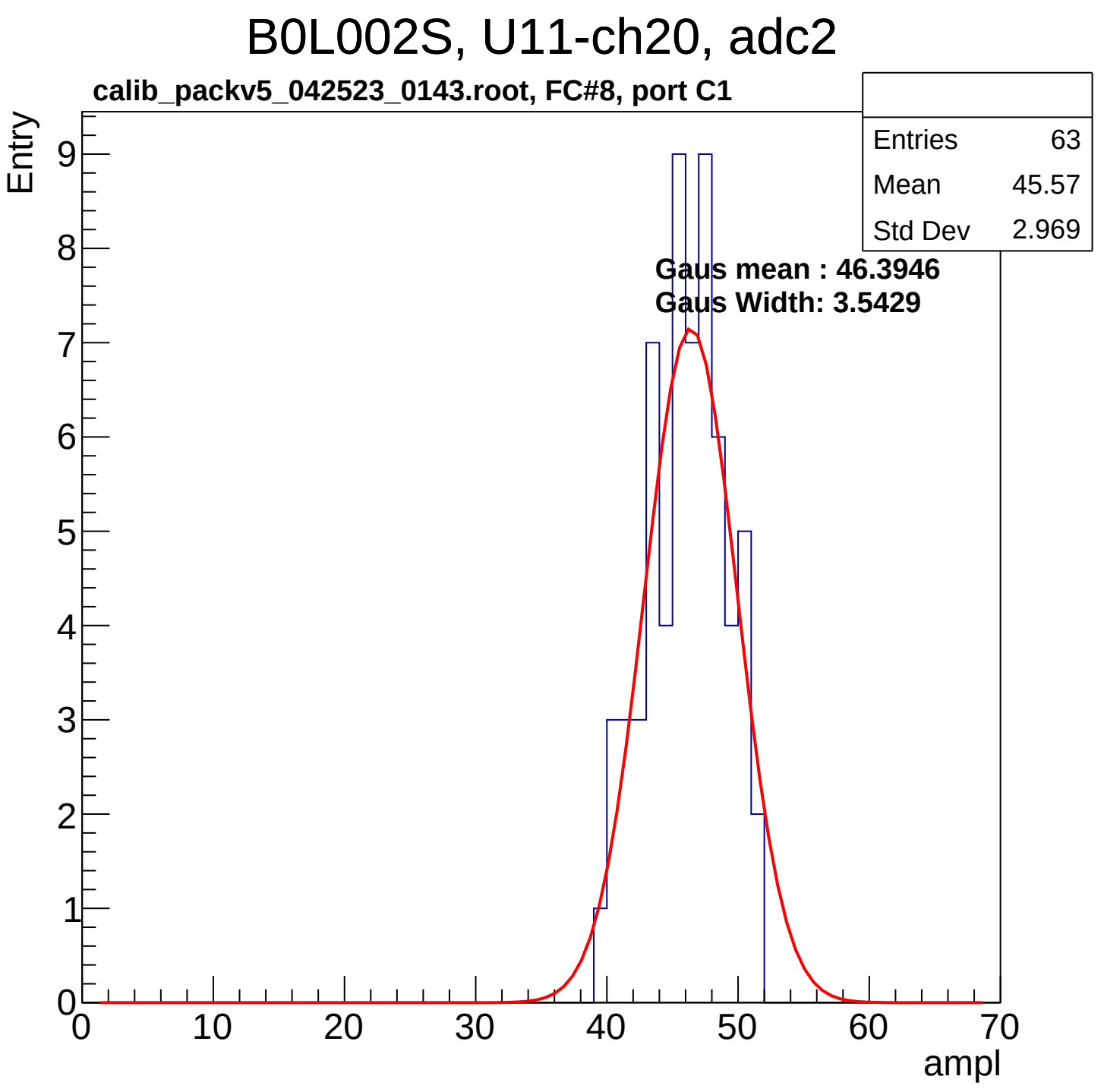
B0L002S, U11-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 45.57
Std Dev 2.969
Gaus mean : 46.3946
Gaus Width: 3.5429
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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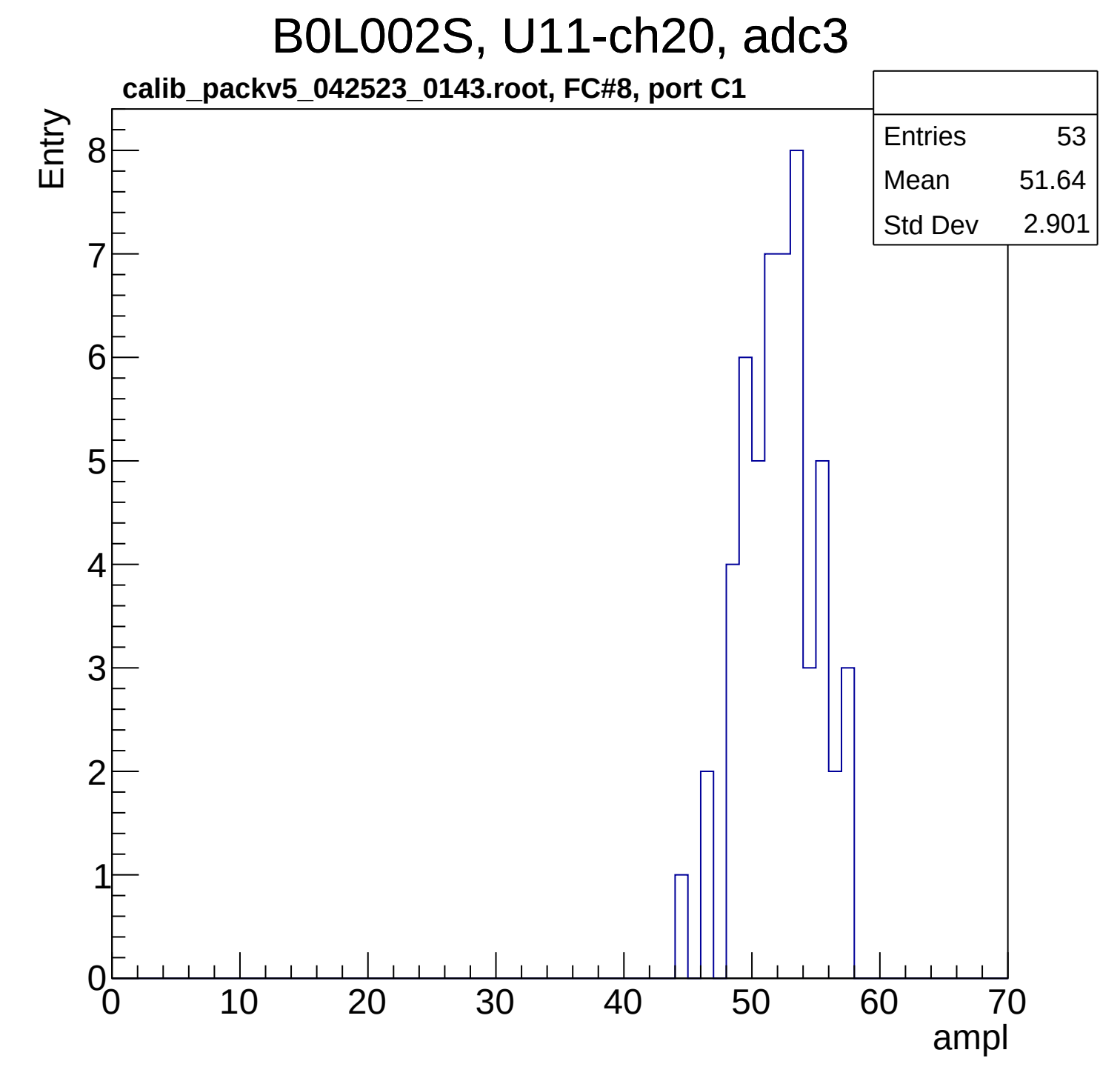
B0L002S, U11-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.64
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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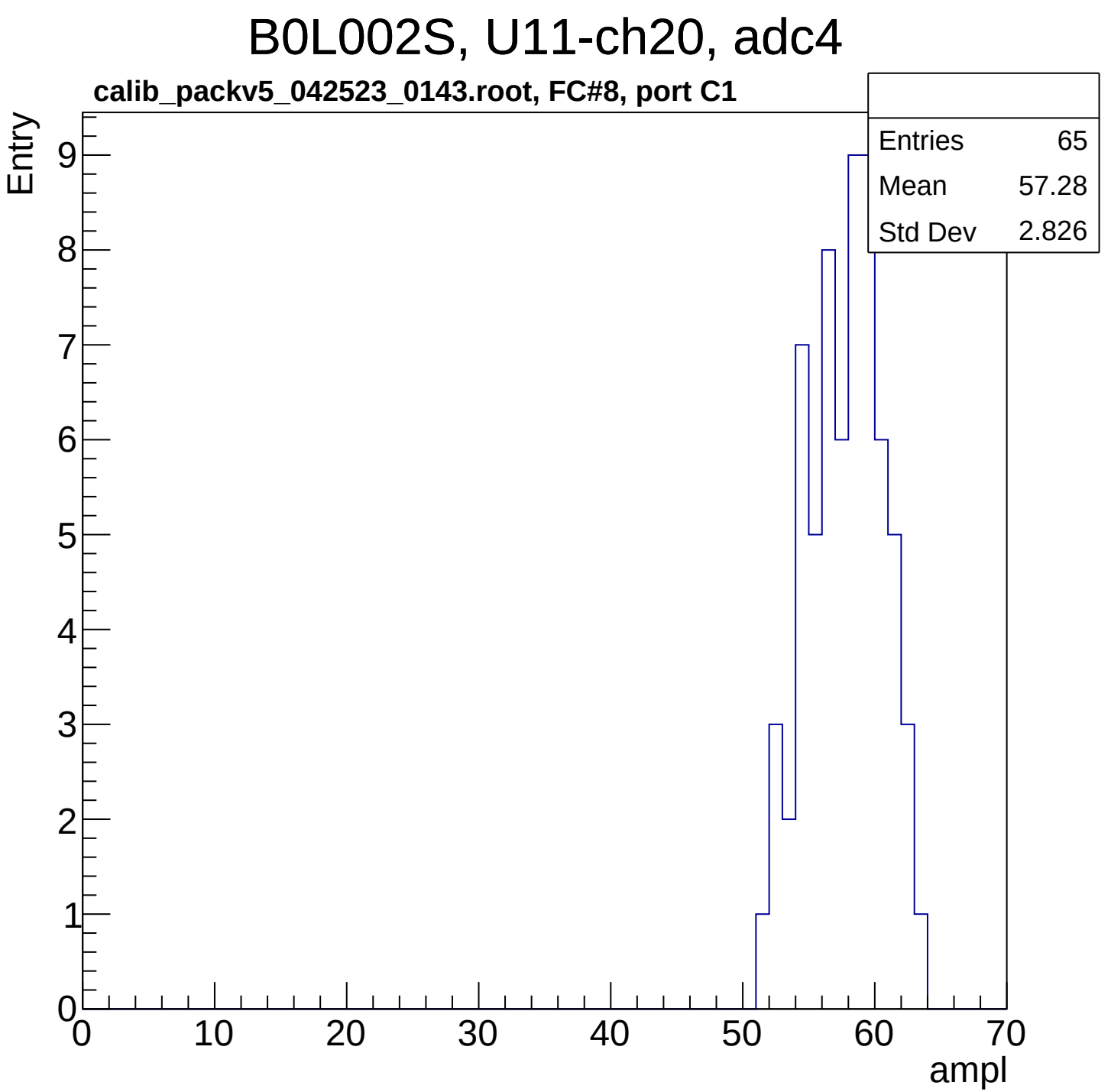
B0L002S, U11-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.28
Std Dev 2.826
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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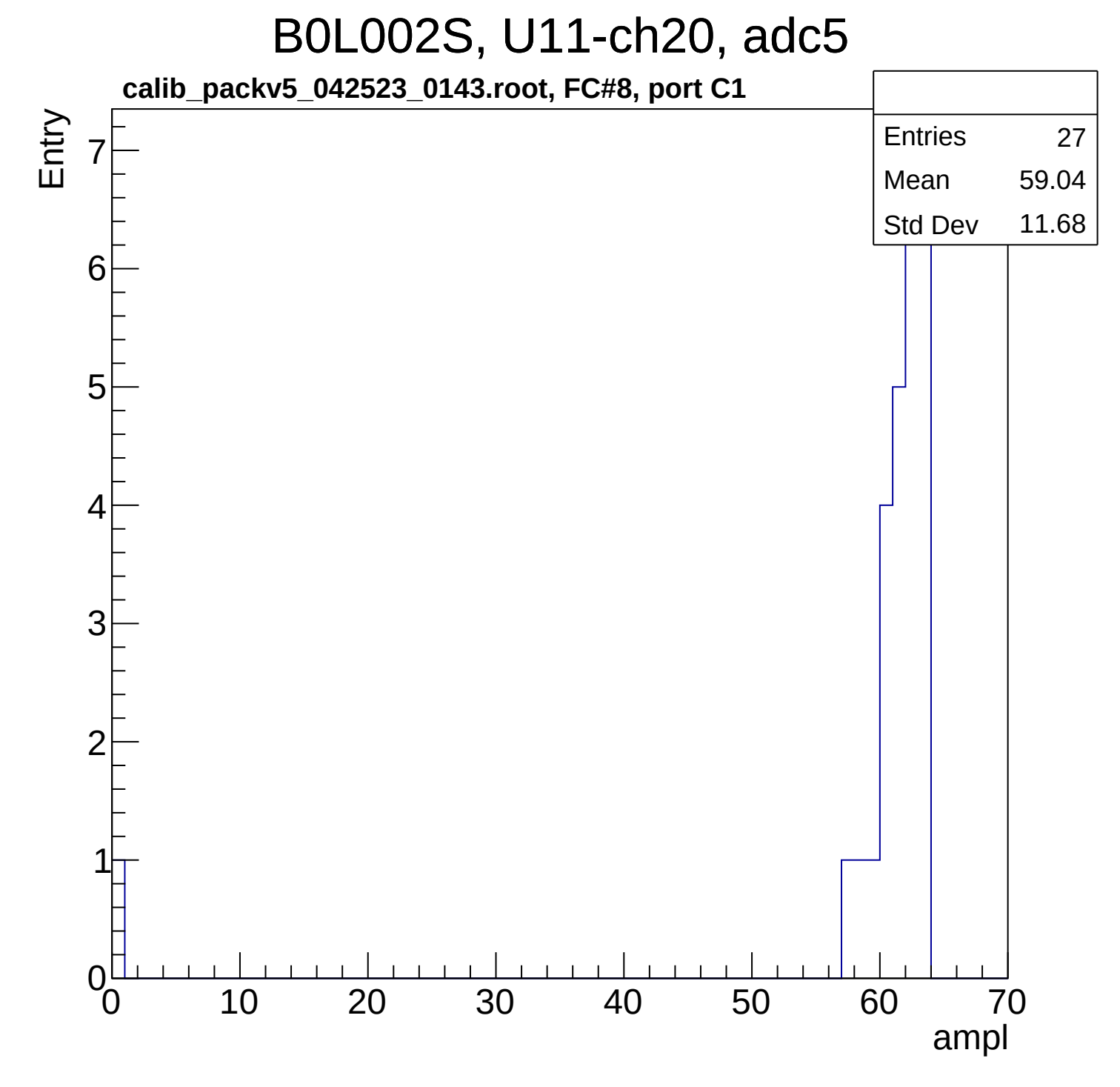
B0L002S, U11-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 59.04
Std Dev 11.68
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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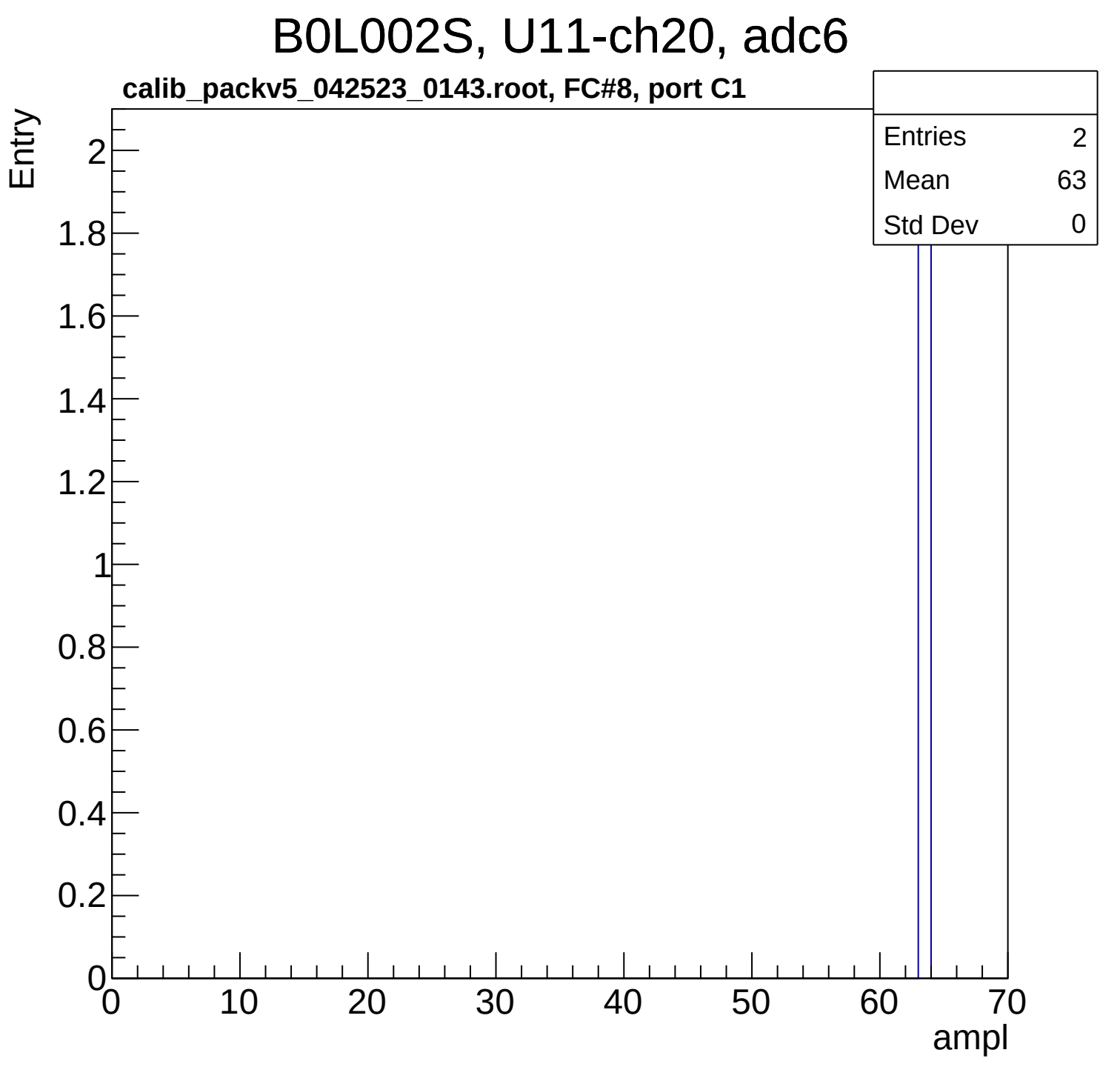
B0L002S, U11-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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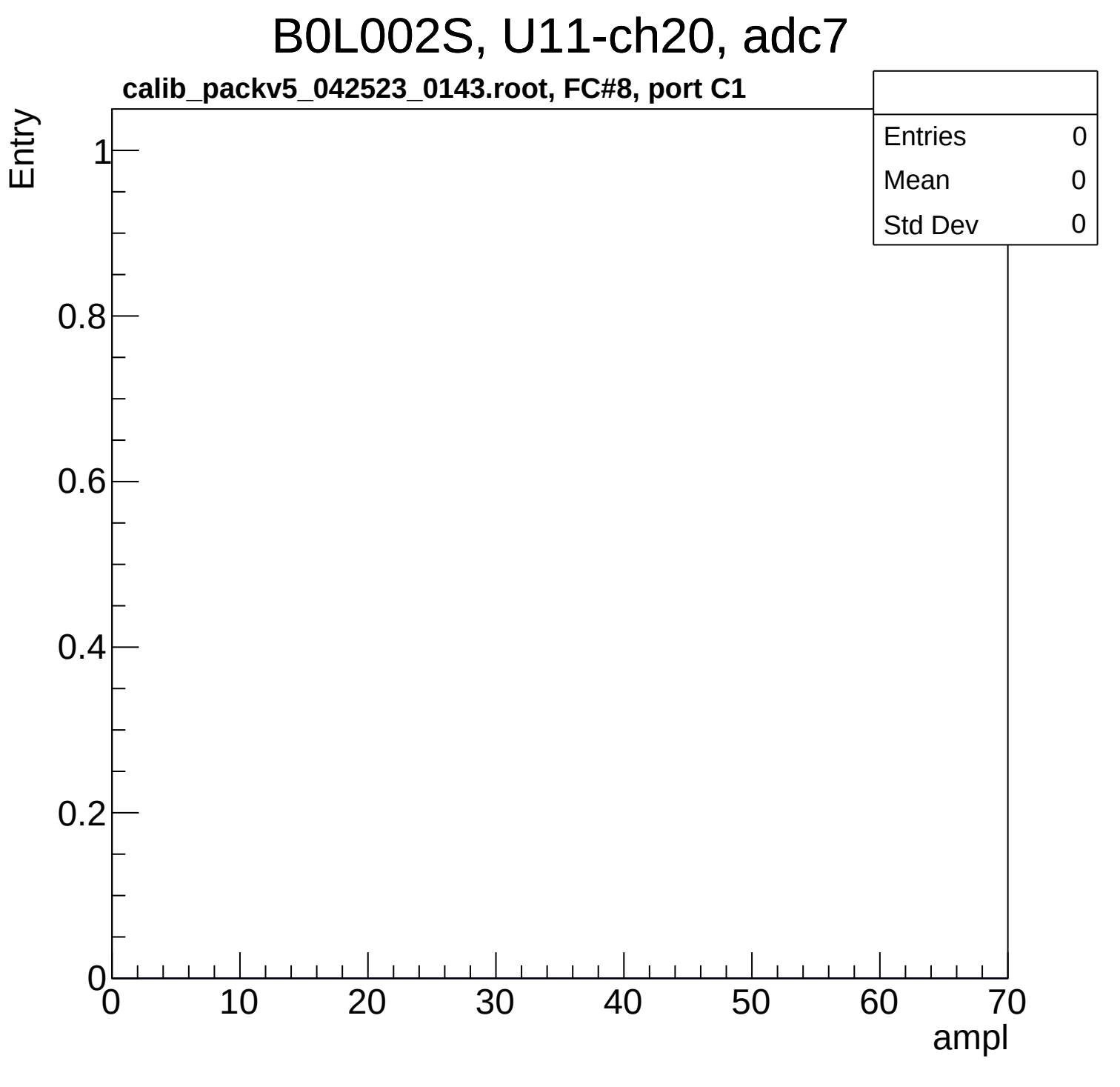
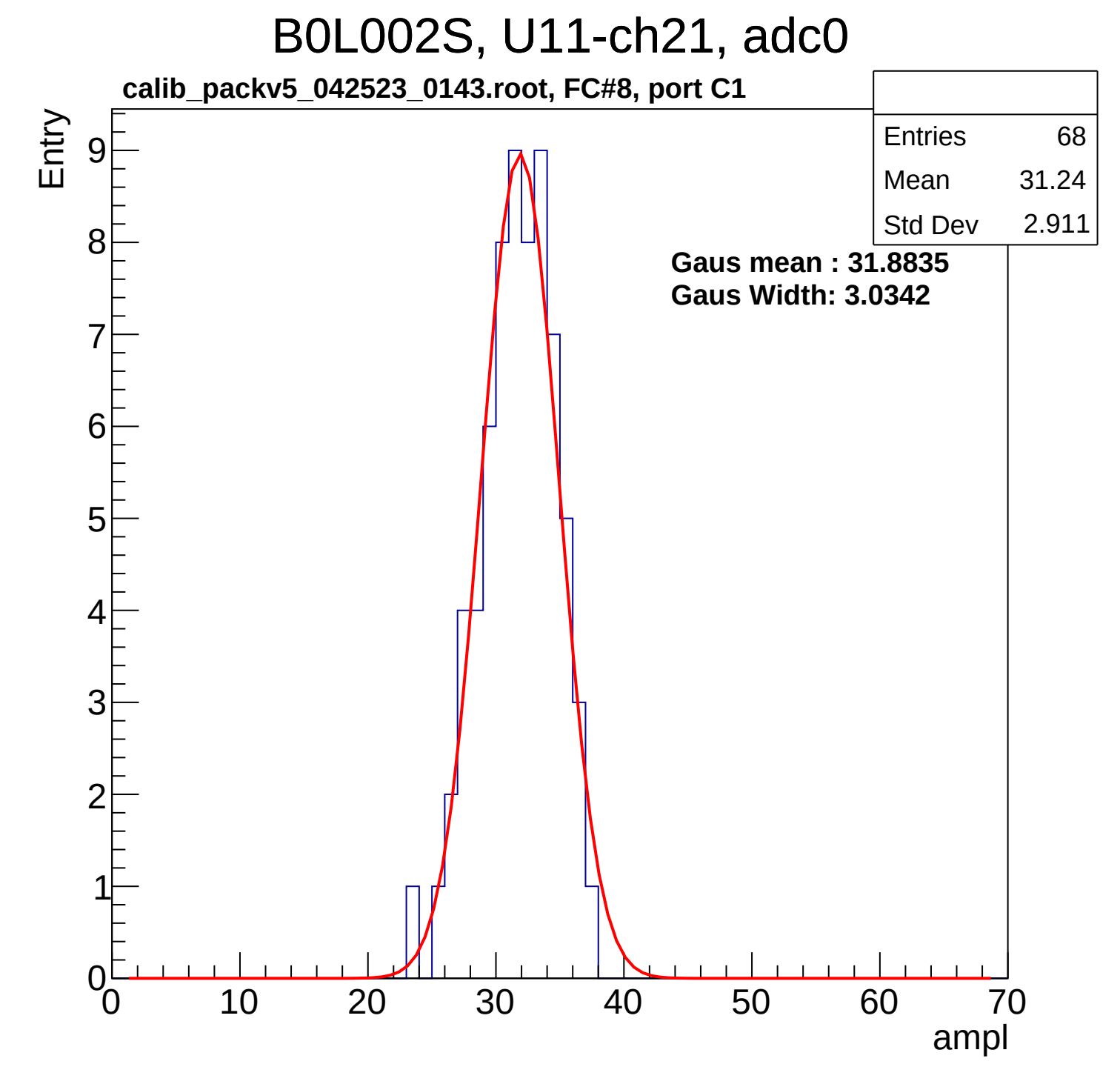
40
50
60
70


B0L002S, U11-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.24
Std Dev 2.911
Gaus mean : 31.8835
Gaus Width: 3.0342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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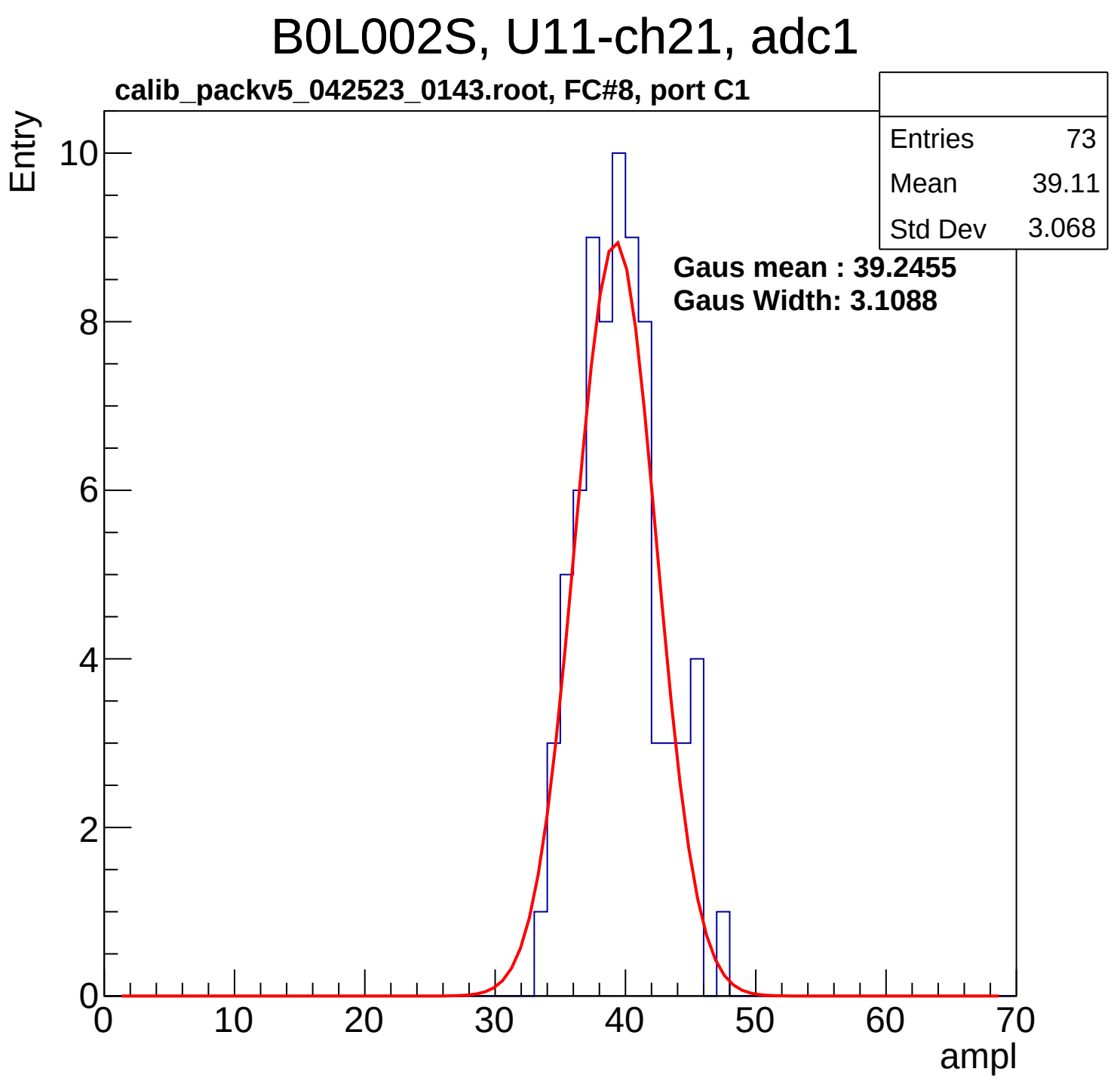
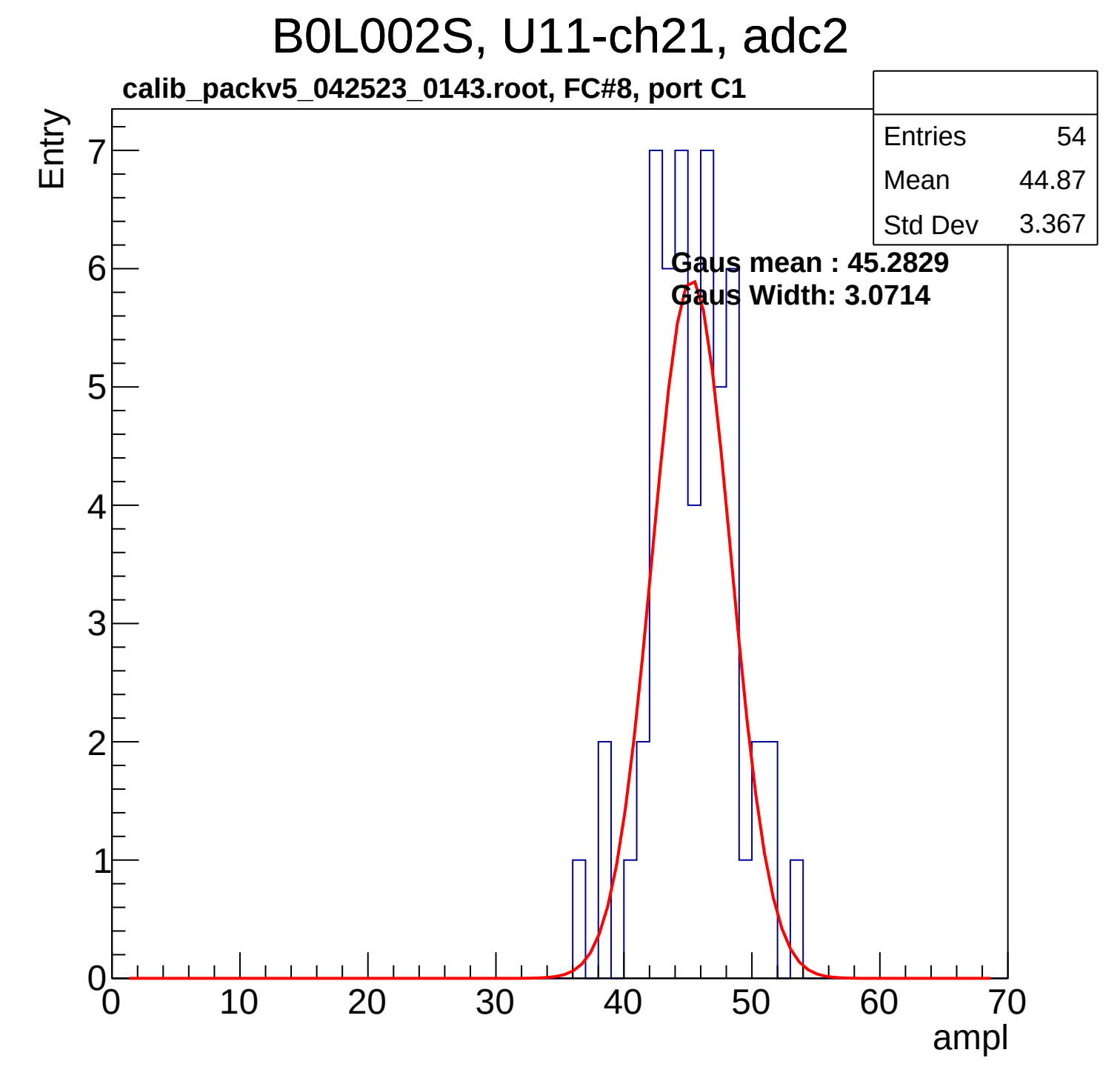

B0L002S, U11-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 39.11
Std Dev 3.068
Gaus mean : 39.2455
Gaus Width: 3.1088
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.87
Std Dev 3.367
Gaus mean : 45.2829
Gaus Width: 3.0714
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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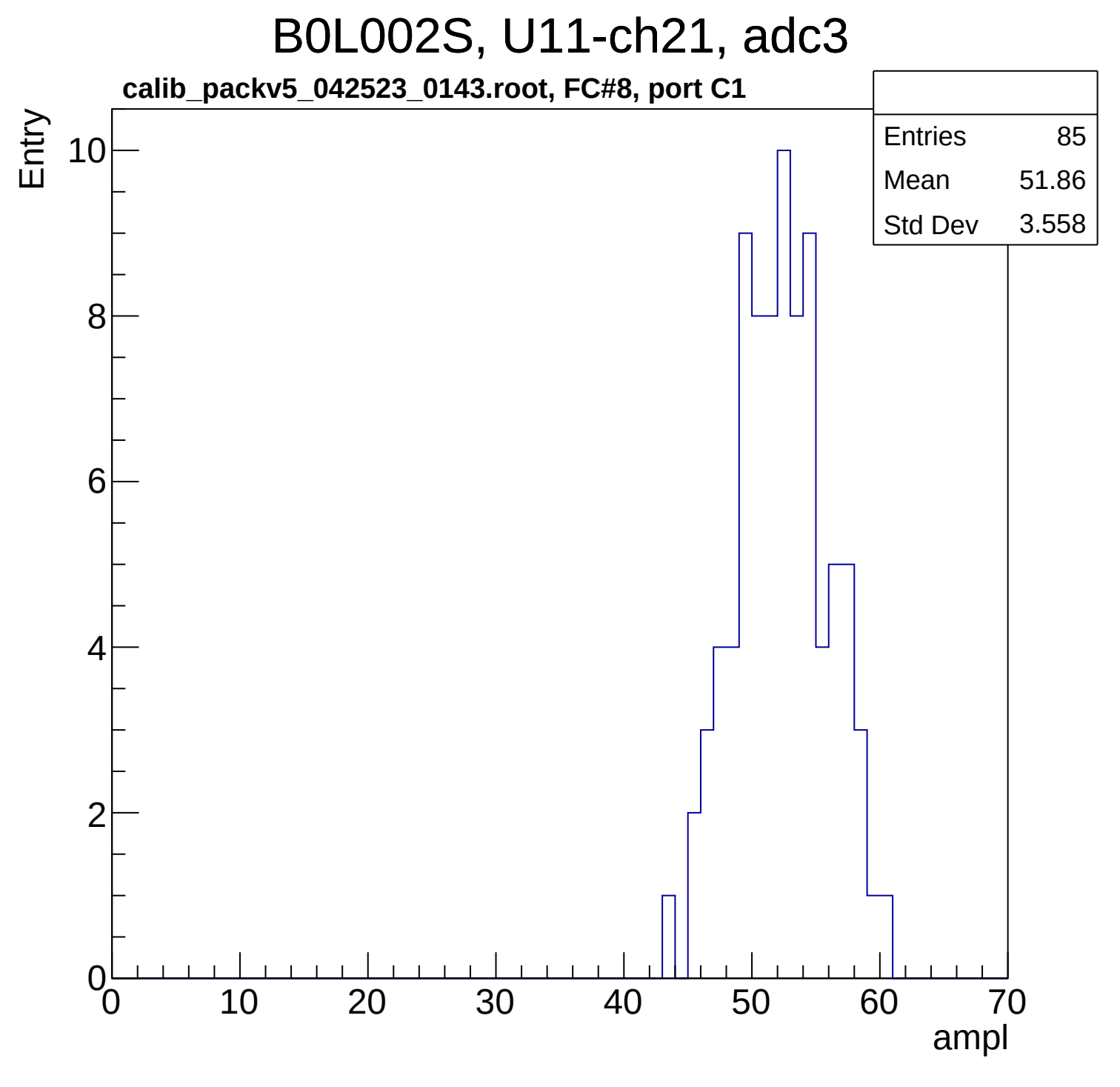
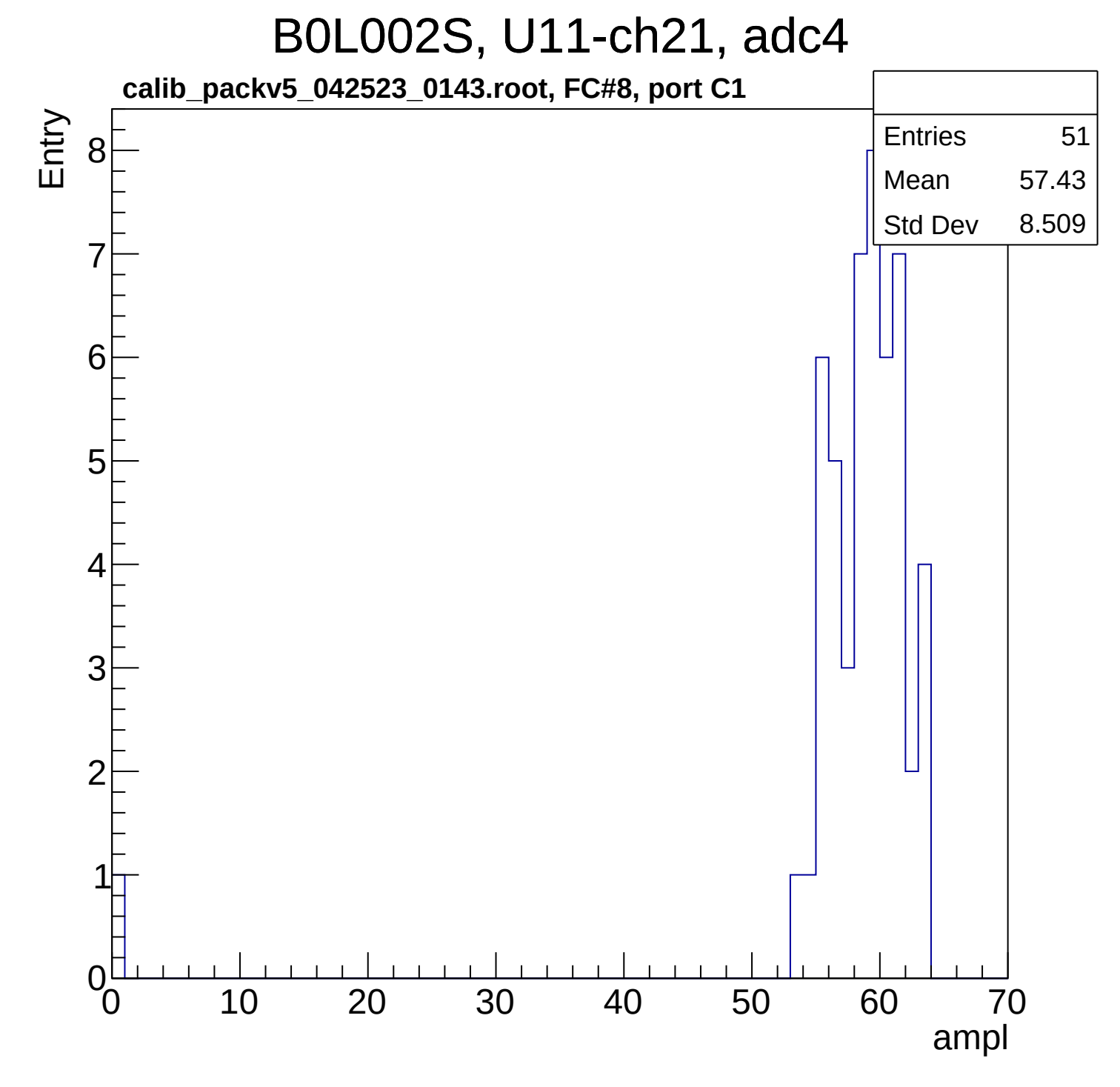

B0L002S, U11-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 51.86
Std Dev 3.558
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.43
Std Dev 8.509
Entry
ampl
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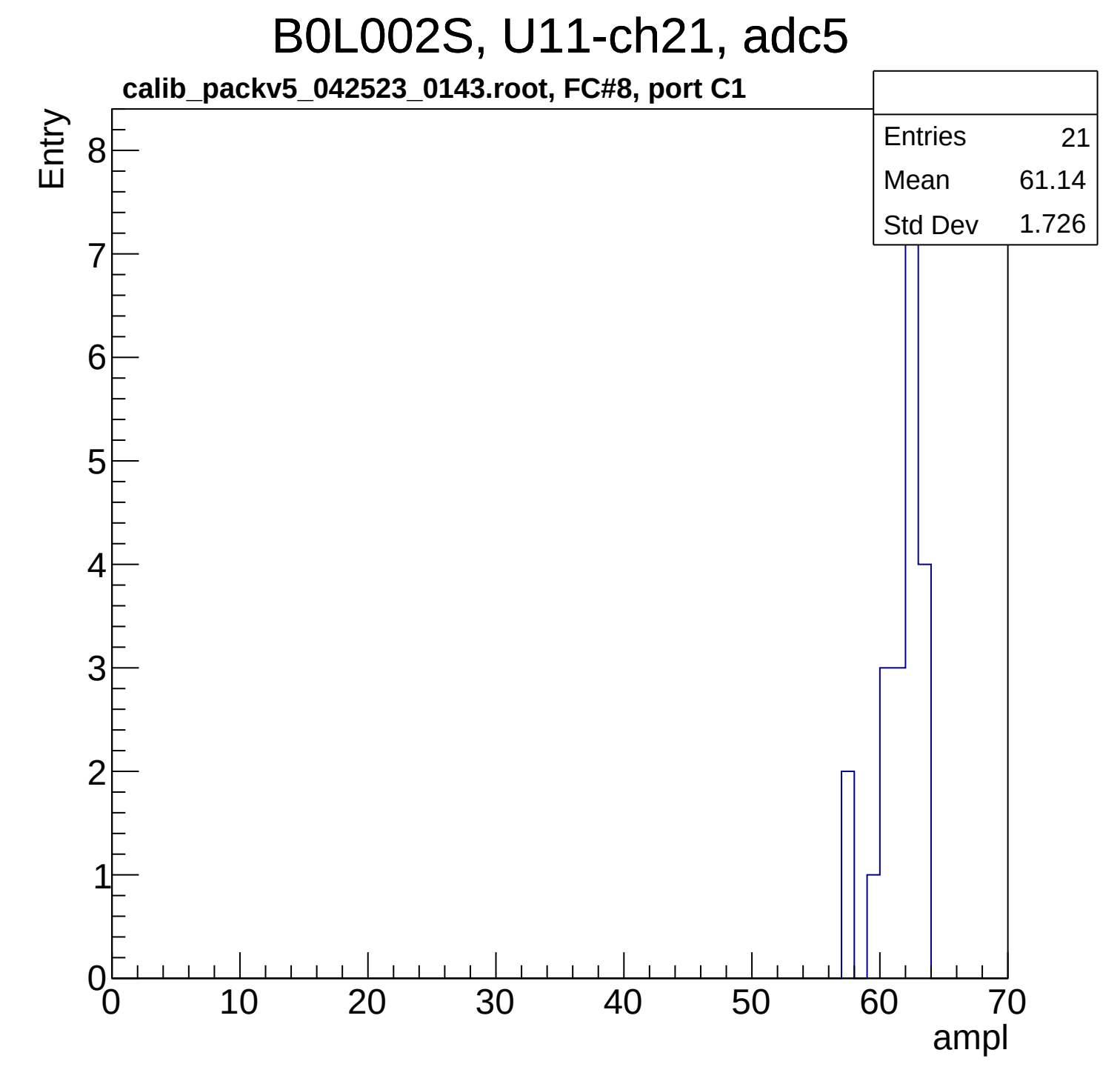
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.14
Std Dev 1.726
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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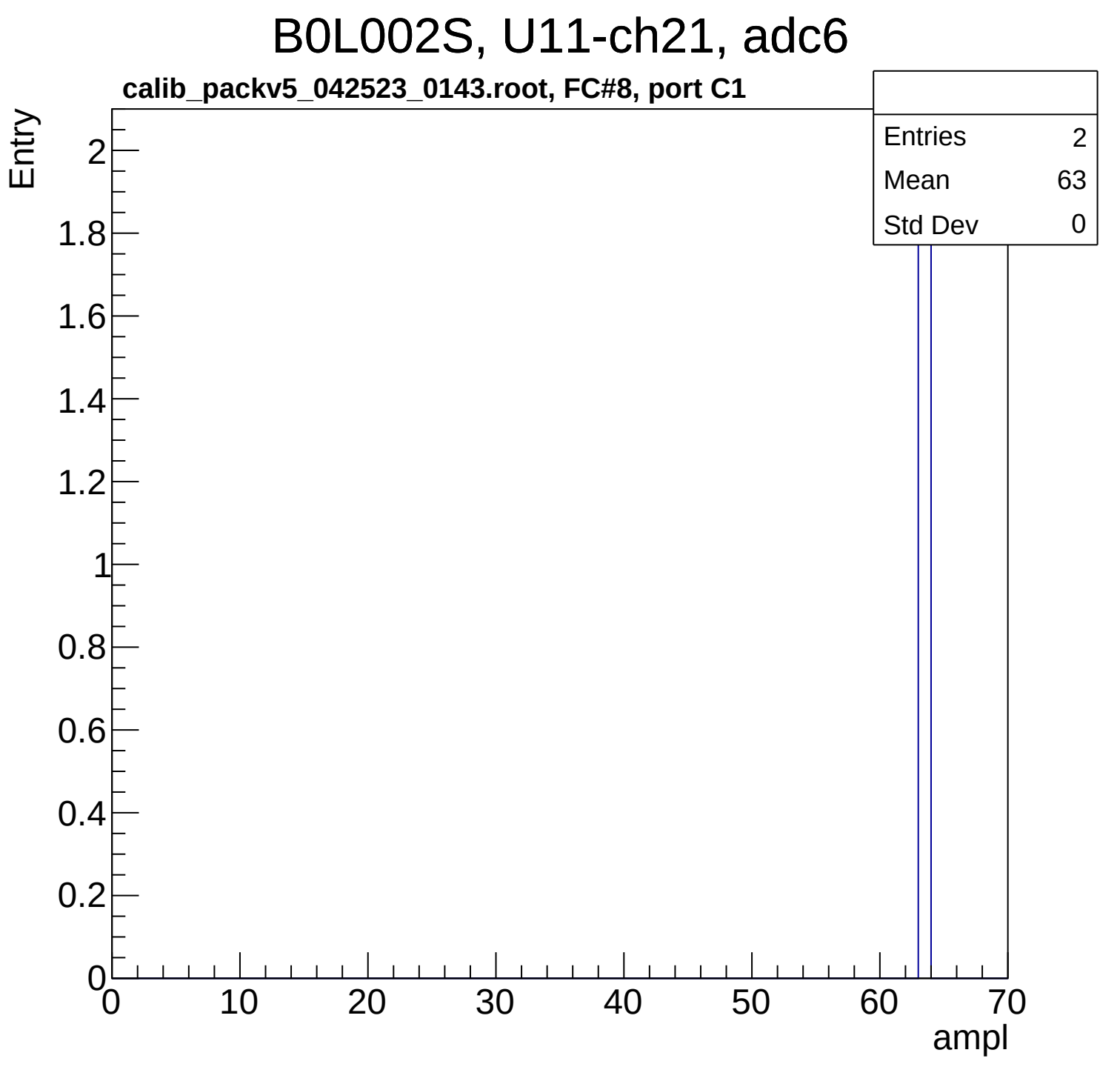
B0L002S, U11-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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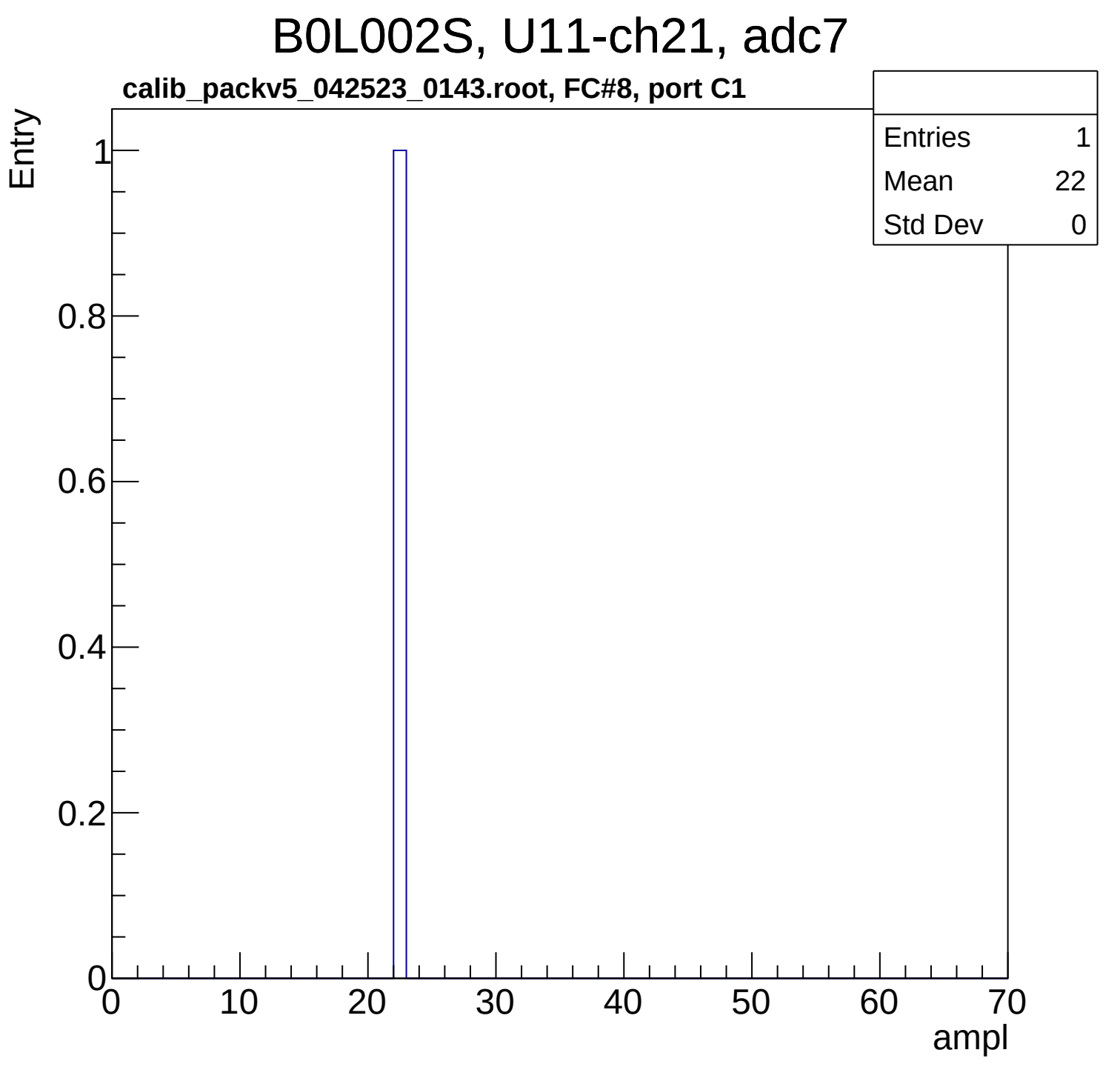
50
60
70


B0L002S, U11-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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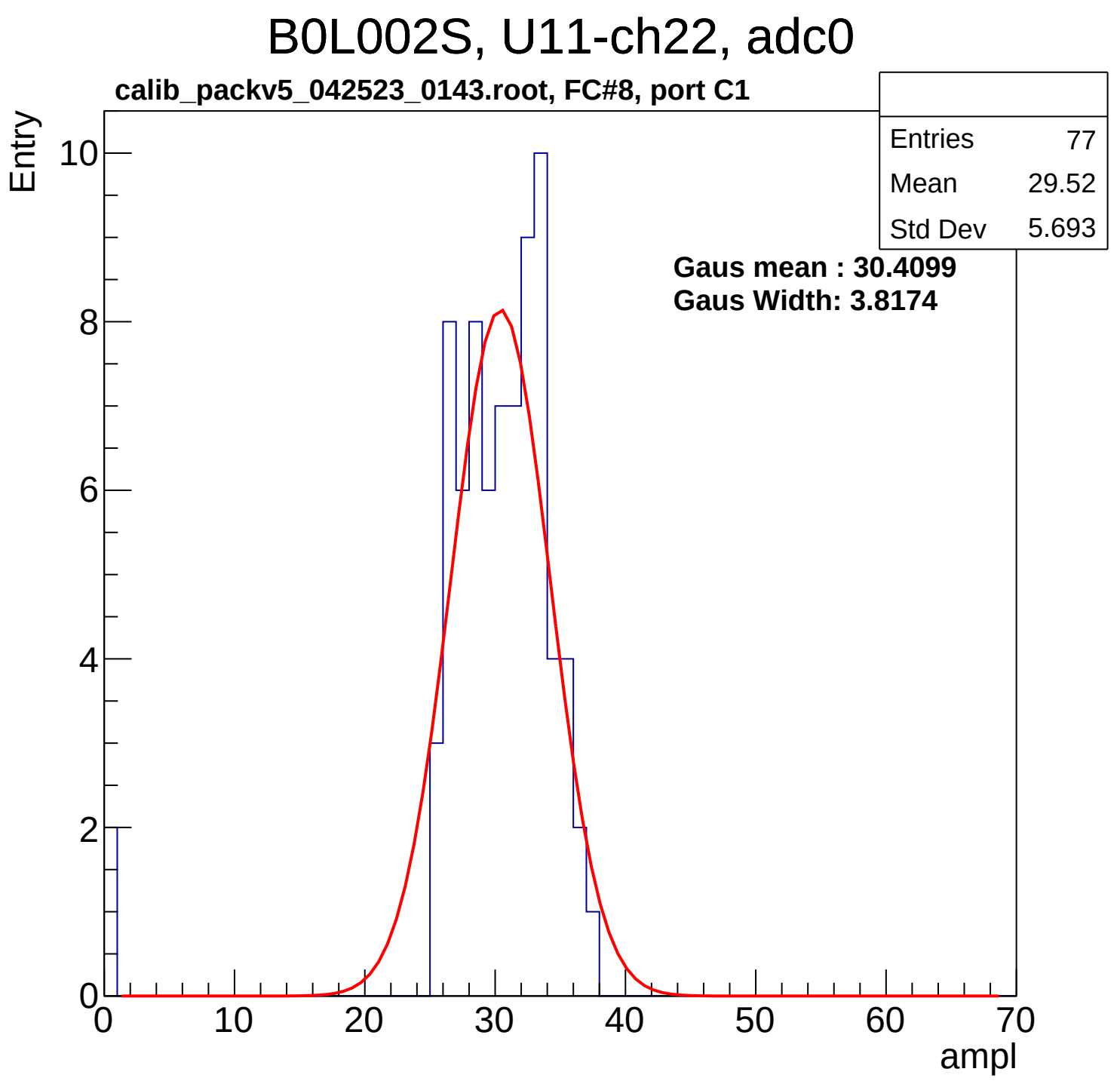
40
50
60
70
ampl


B0L002S, U11-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 29.52
Std Dev 5.693
Gaus mean : 30.4099
Gaus Width: 3.8174
Entry
ampl
0
2
4
6
8
10
0
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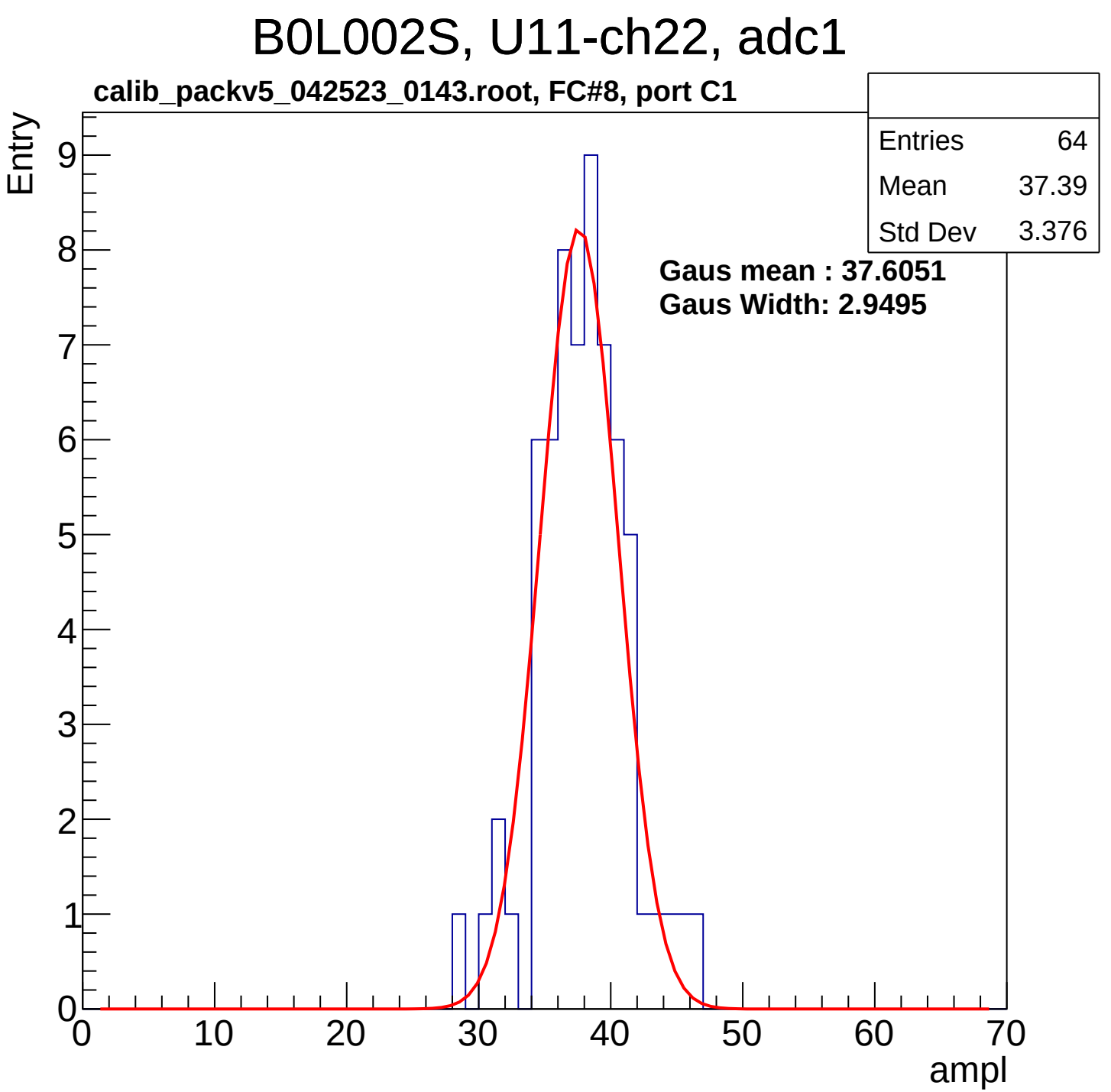
20
30
40
50
60
70


B0L002S, U11-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.39
Std Dev 3.376
Gaus mean : 37.6051
Gaus Width: 2.9495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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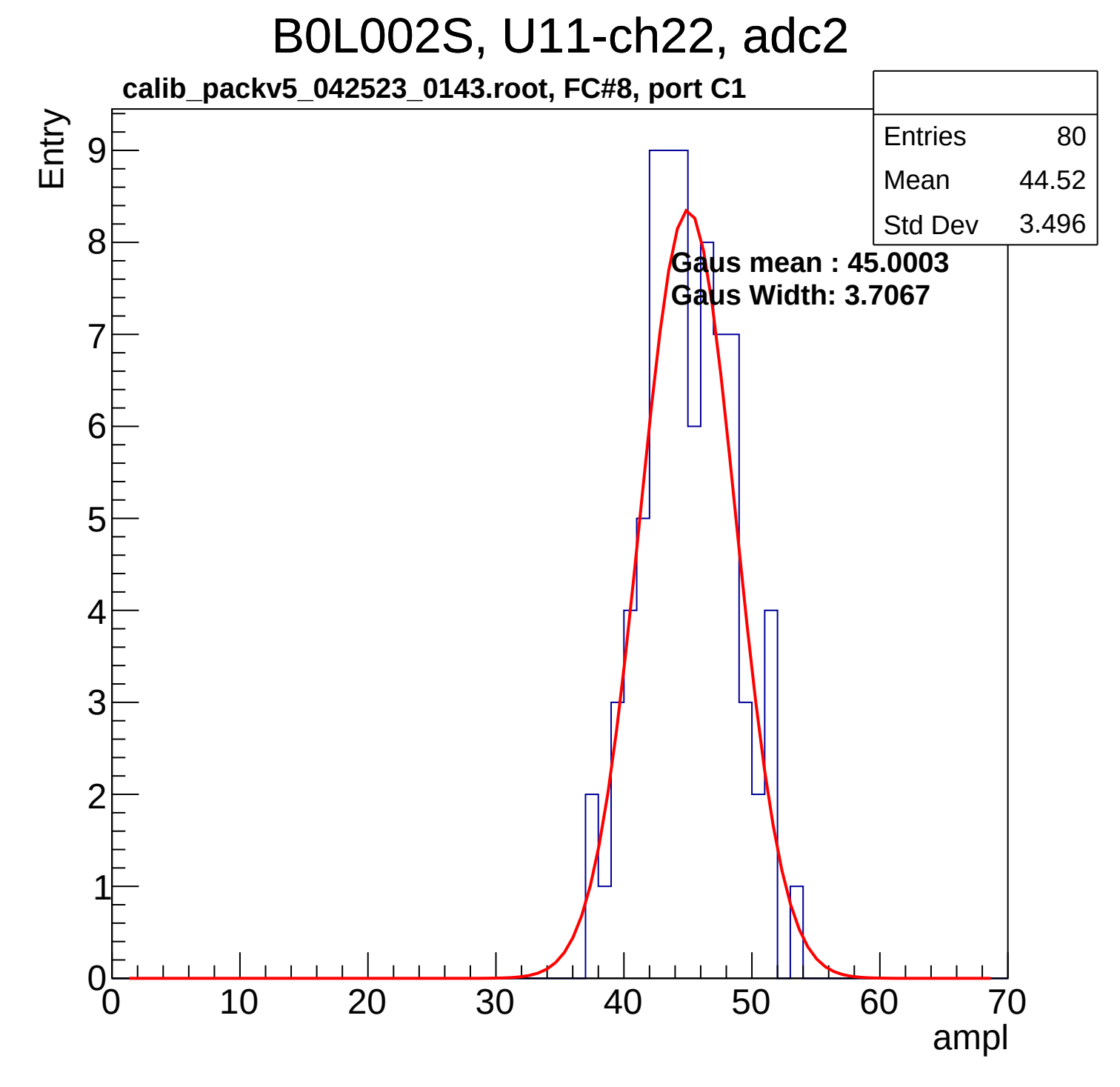
40
50
60
70


B0L002S, U11-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 44.52
Std Dev 3.496
Gaus mean : 45.0003
Gaus Width: 3.7067
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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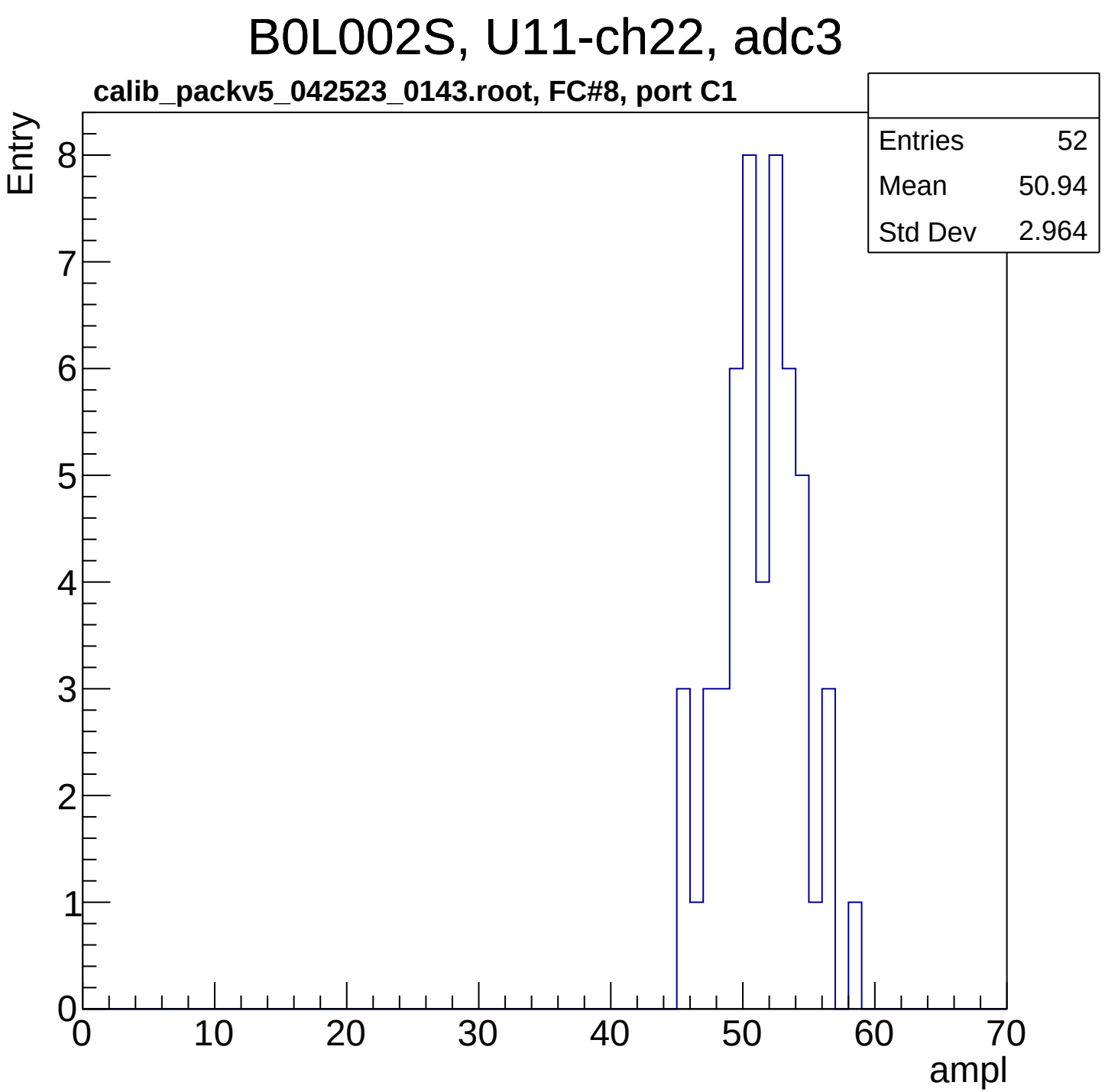
B0L002S, U11-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.94
Std Dev 2.964
Entry
ampl
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4
5
6
7
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0
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20
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40
50
60
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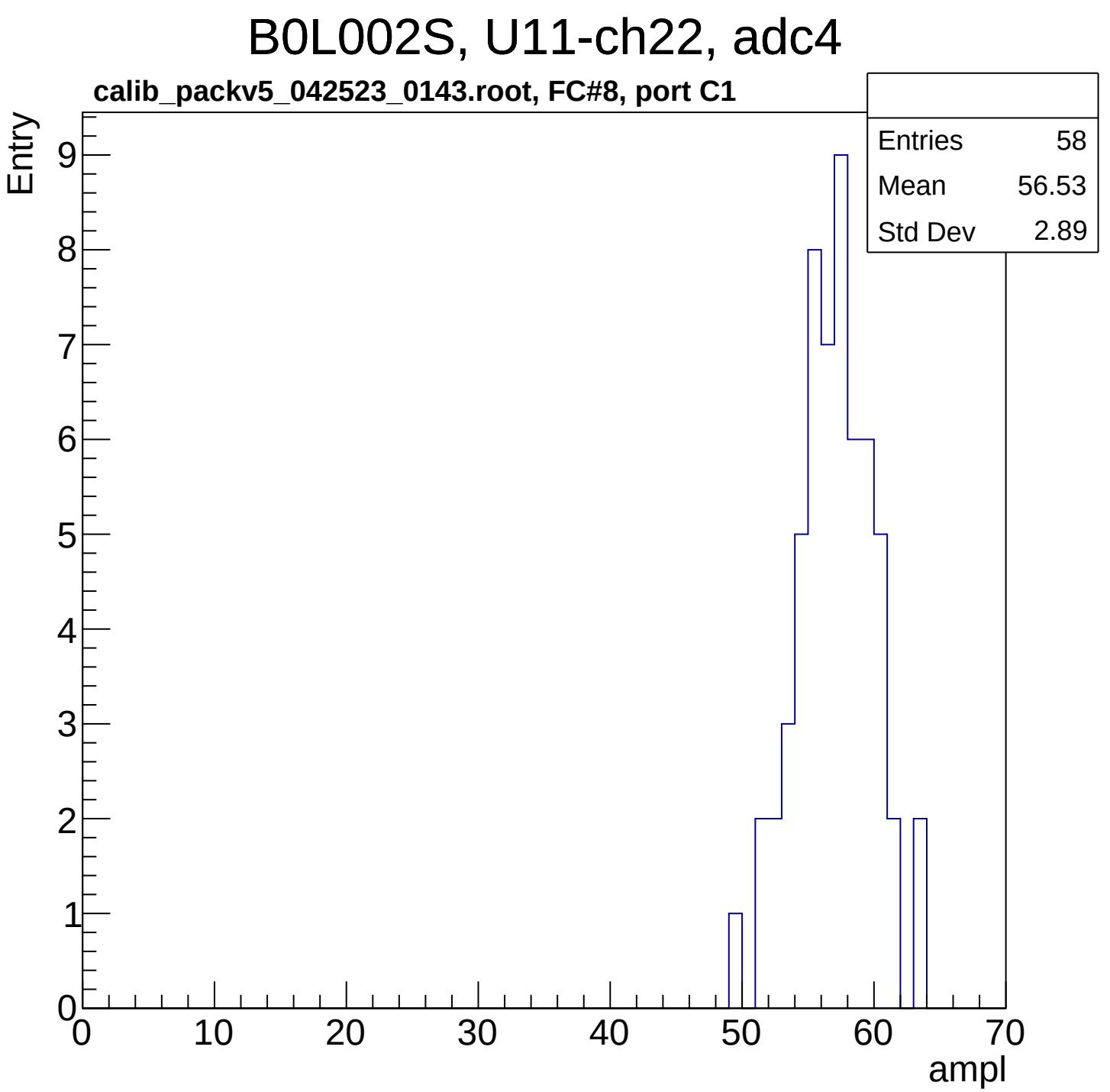
B0L002S, U11-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.53
Std Dev 2.89
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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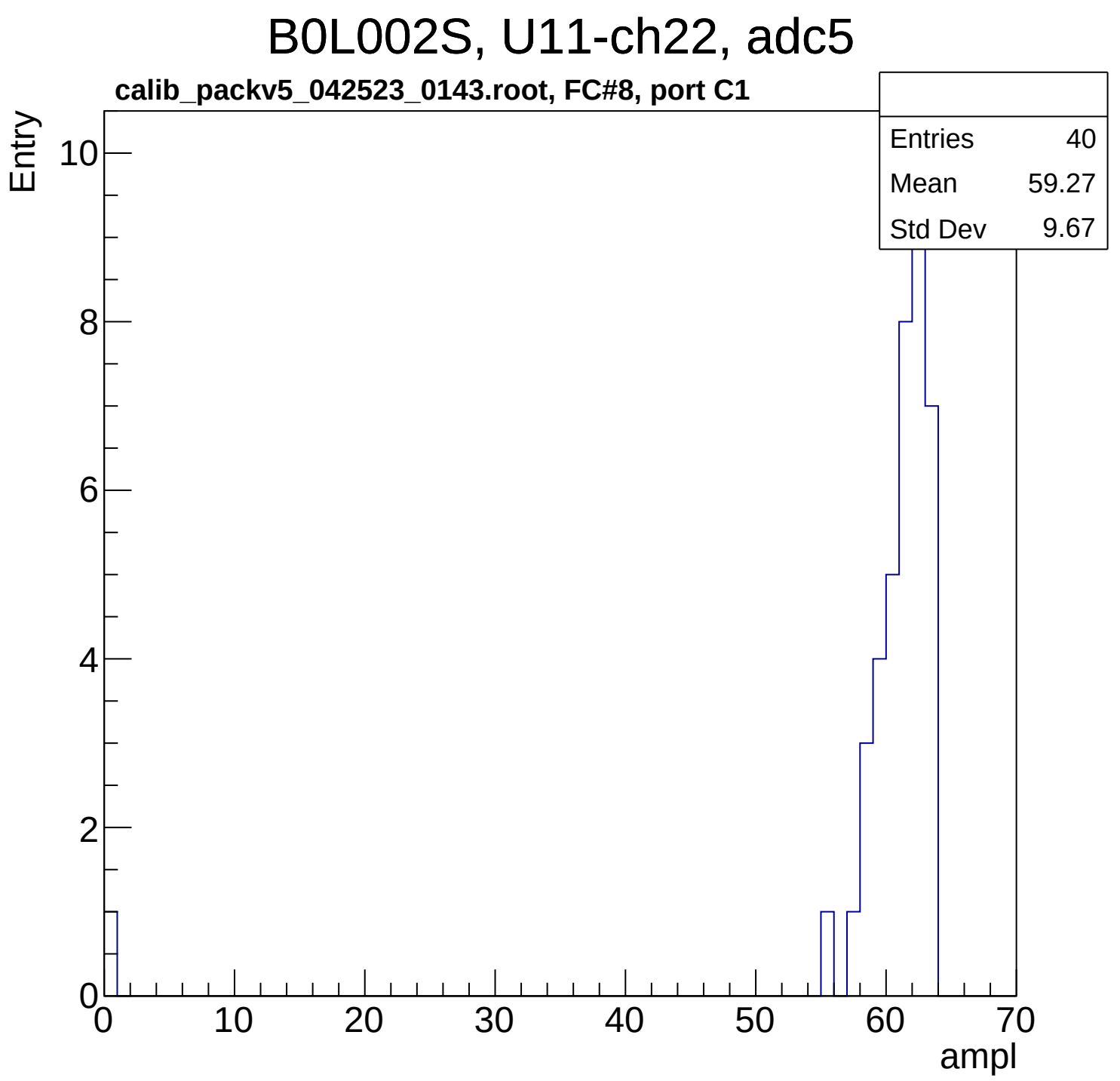
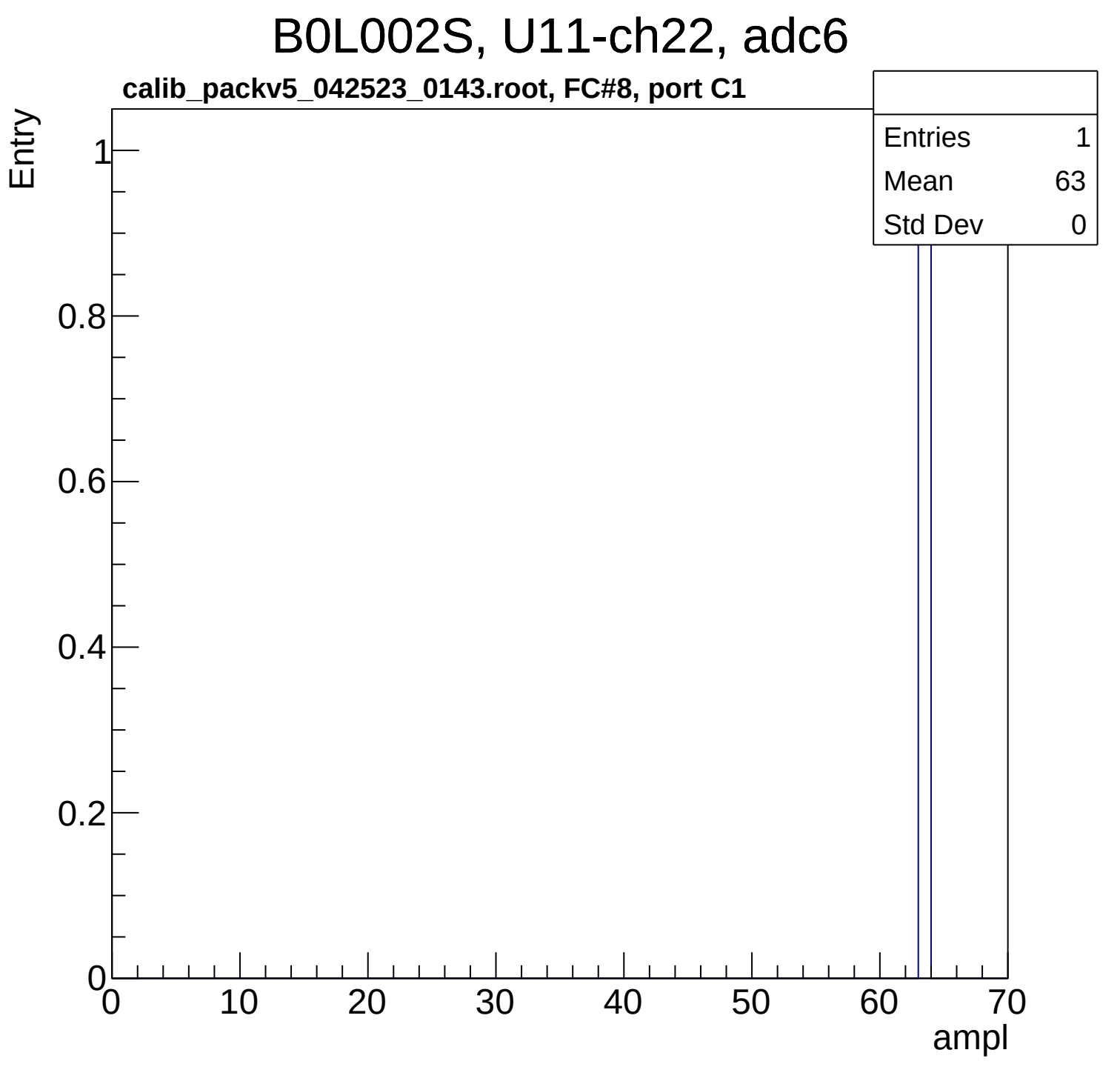
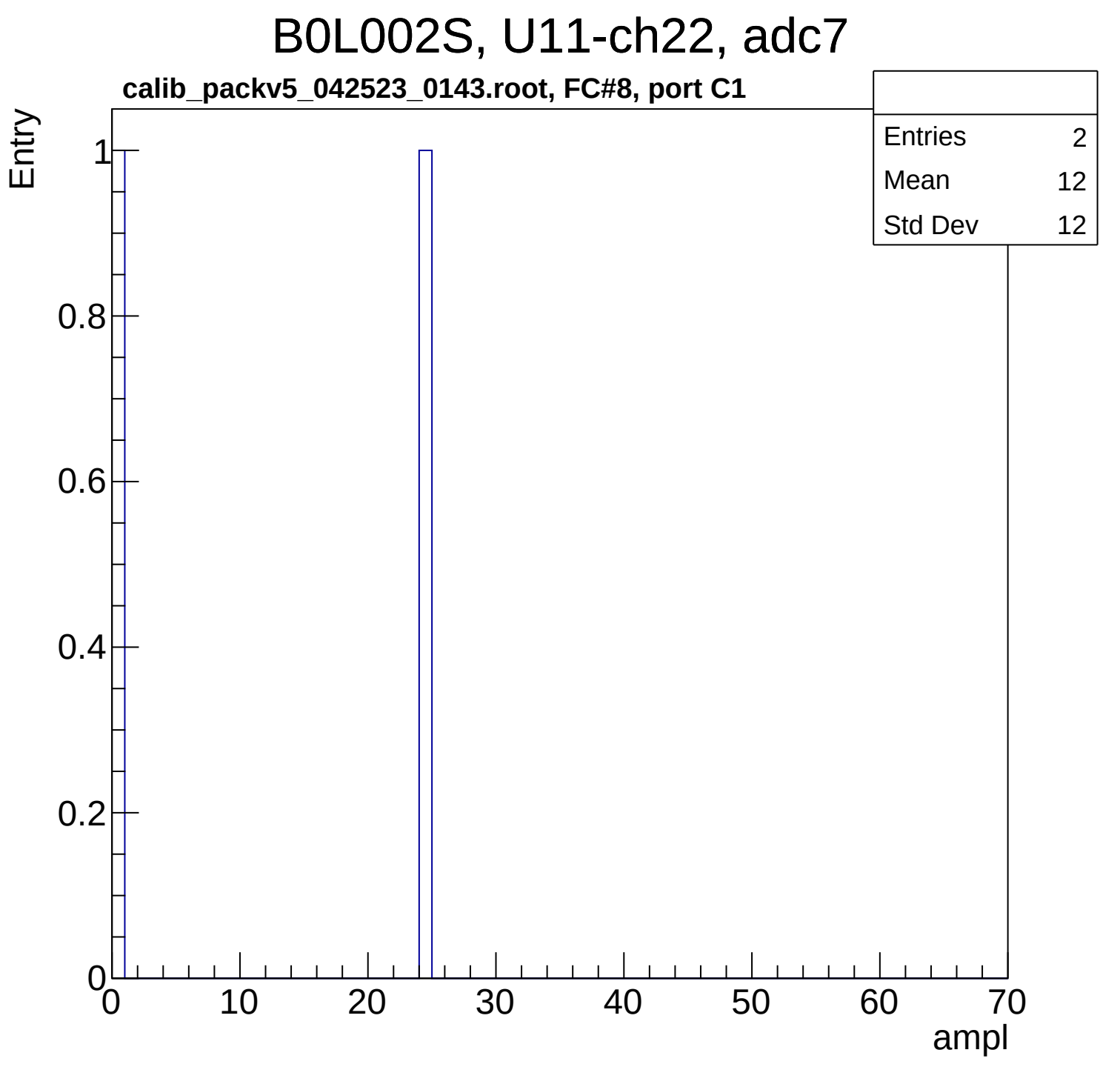
60
70


B0L002S, U11-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.27
Std Dev 9.67
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
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0.4
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10
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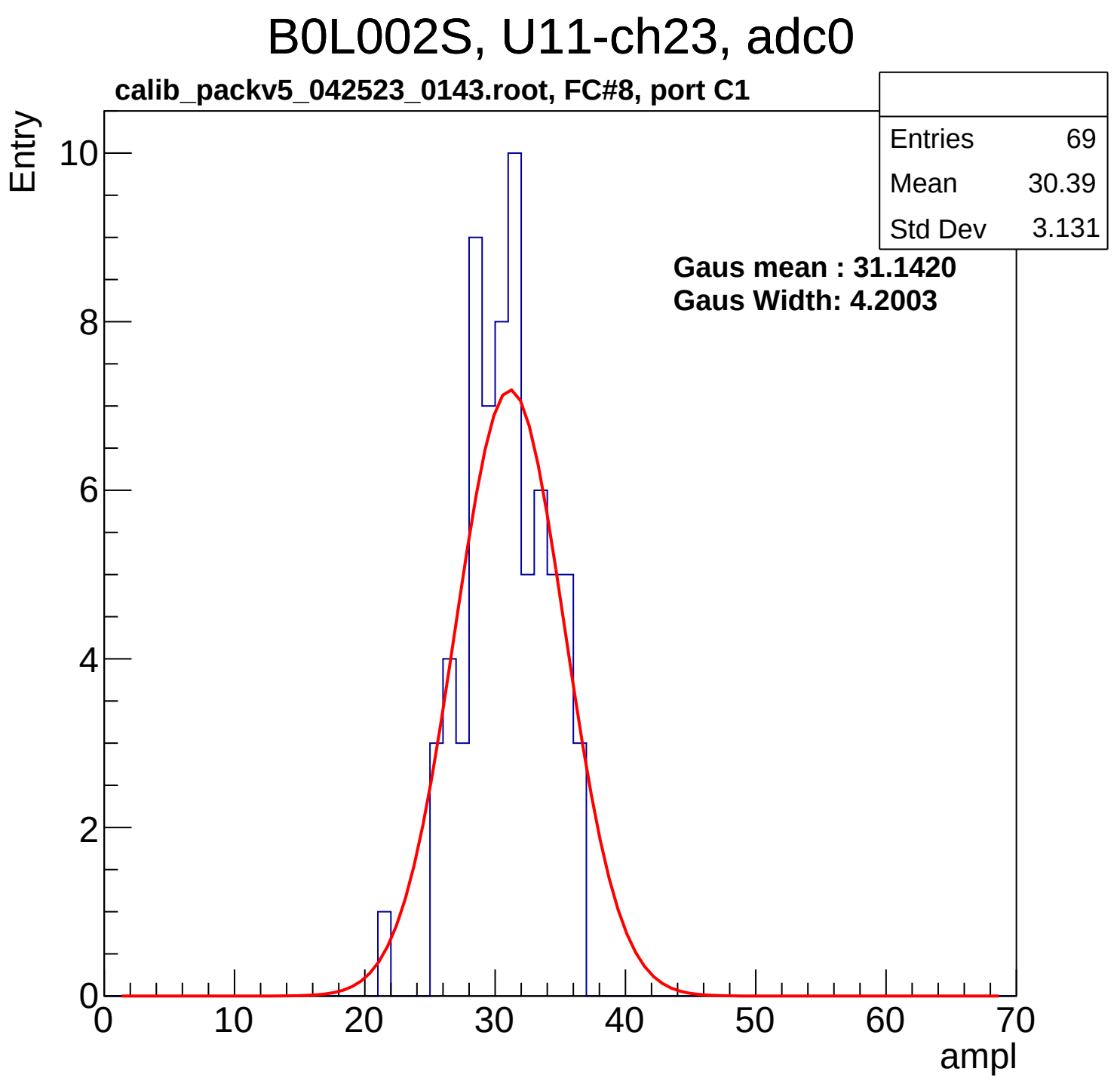
70
ampl


B0L002S, U11-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.39
Std Dev 3.131
Gaus mean : 31.1420
Gaus Width: 4.2003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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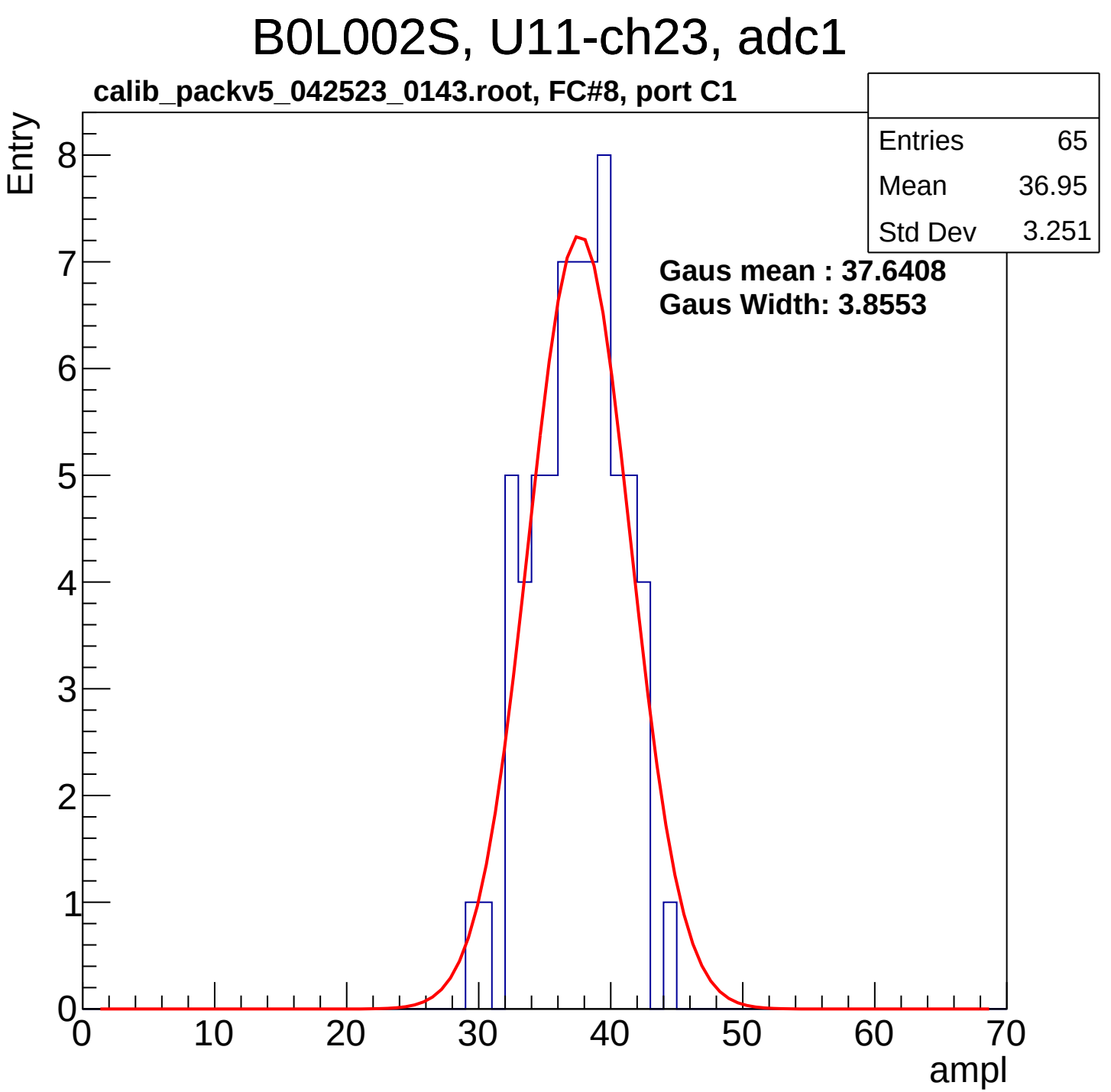
B0L002S, U11-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.95
Std Dev 3.251
Gaus mean : 37.6408
Gaus Width: 3.8553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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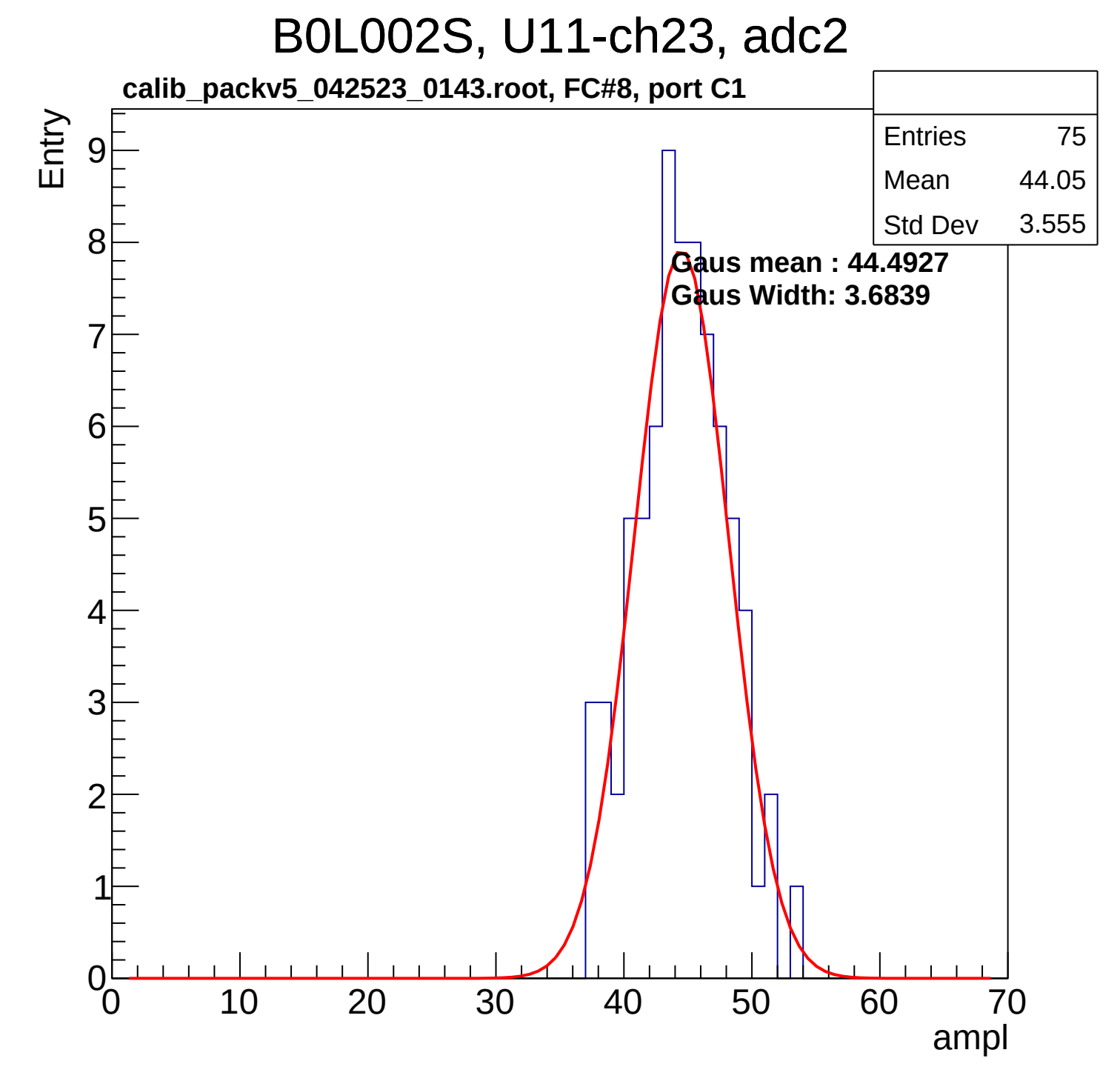
B0L002S, U11-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.05
Std Dev 3.555
Gaus mean : 44.4927
Gaus Width: 3.6839
Entry
ampl
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2
3
4
5
6
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0
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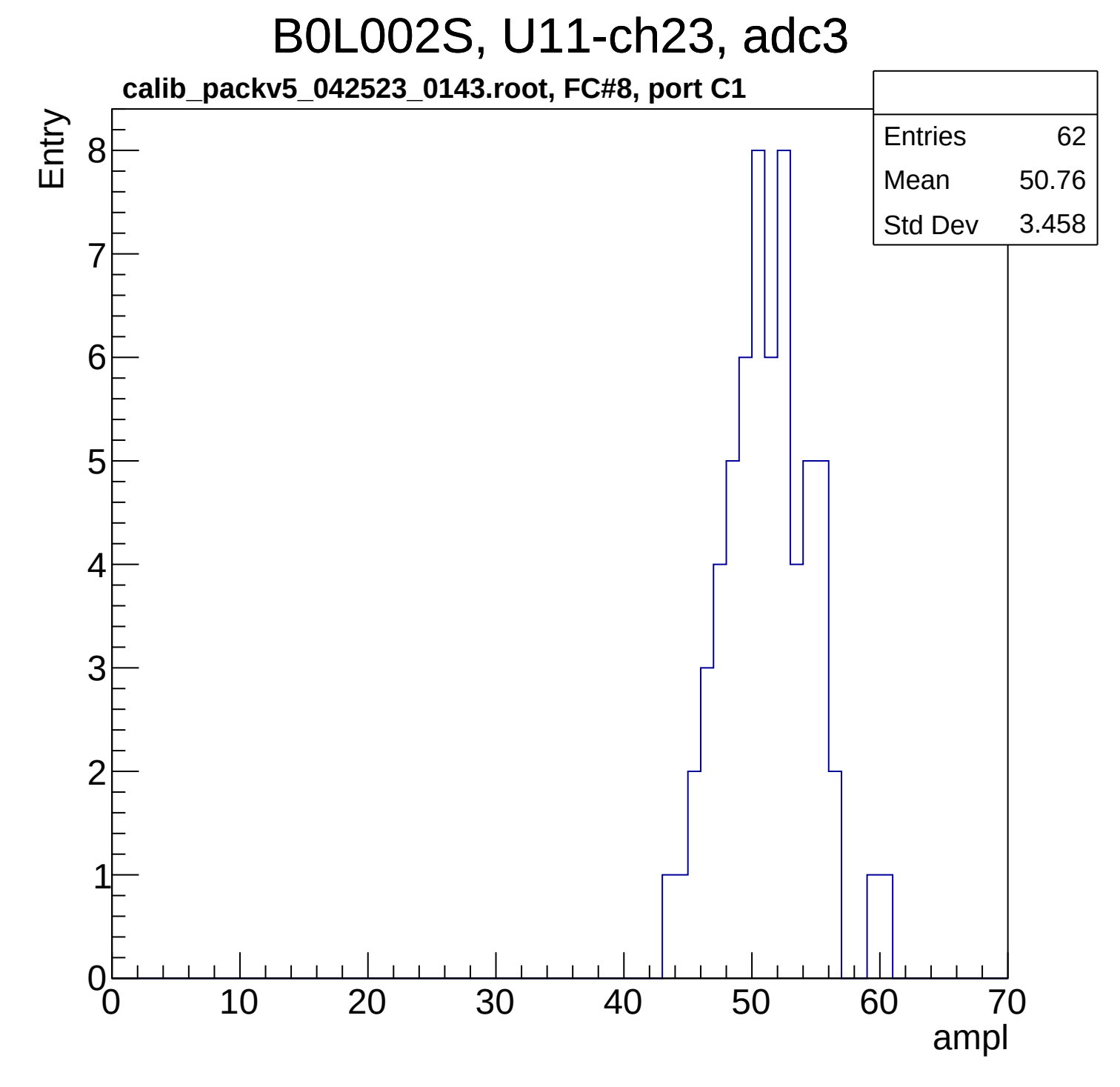
B0L002S, U11-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.76
Std Dev 3.458
Entry
ampl
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0
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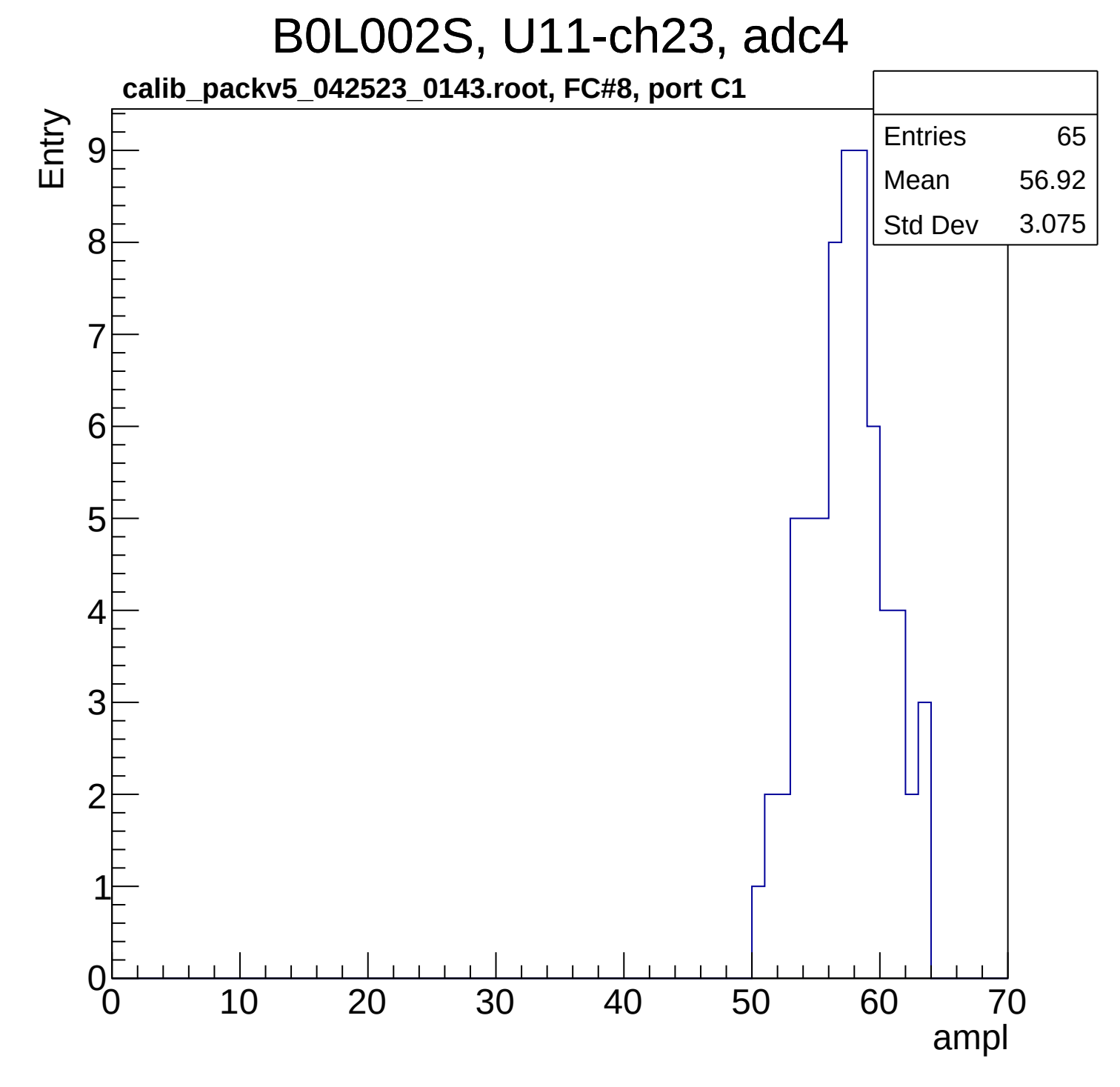
B0L002S, U11-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.92
Std Dev 3.075
Entry
ampl
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2
3
4
5
6
7
8
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0
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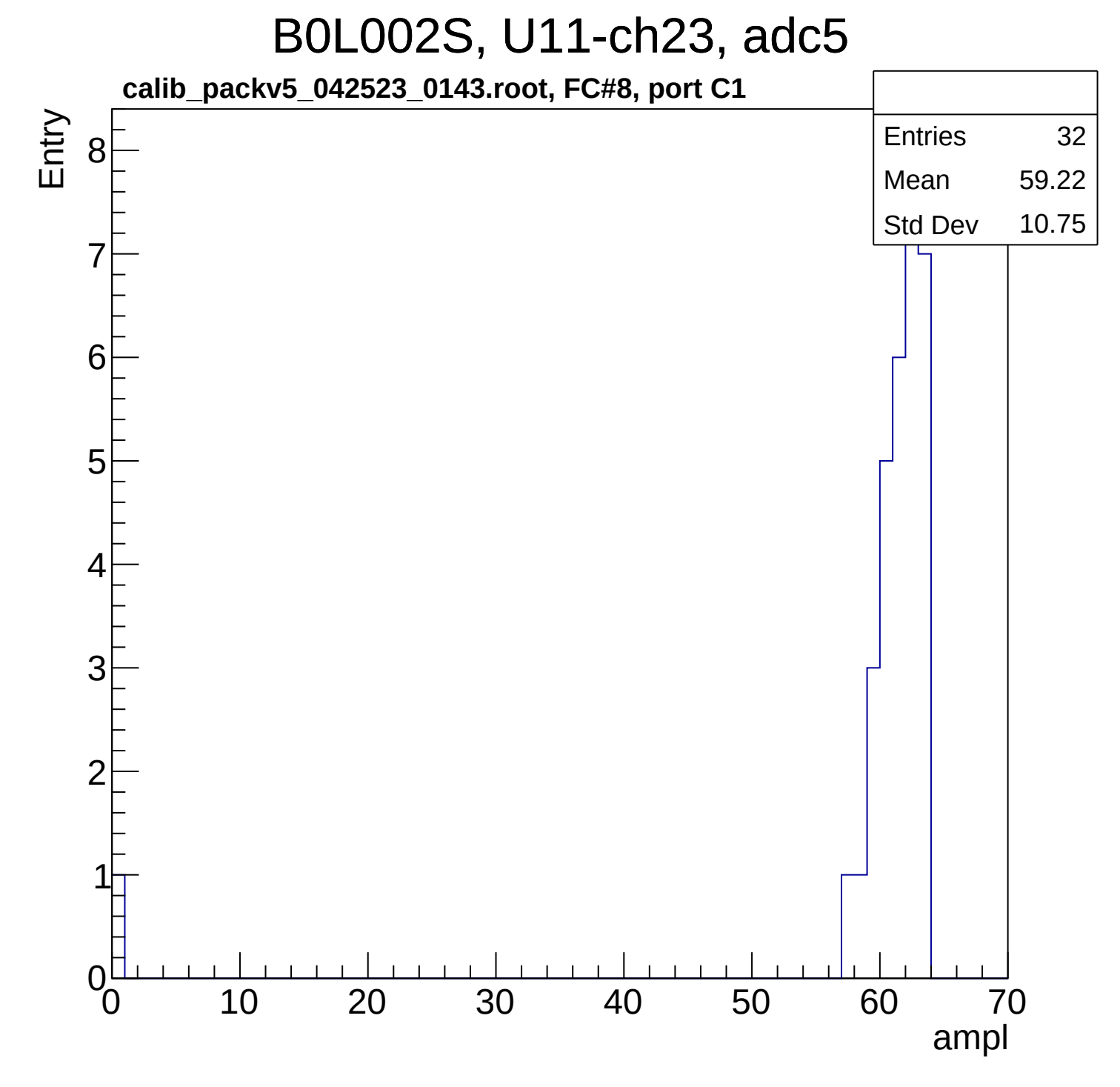
B0L002S, U11-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.22
Std Dev 10.75
Entry
ampl
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1
2
3
4
5
6
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0
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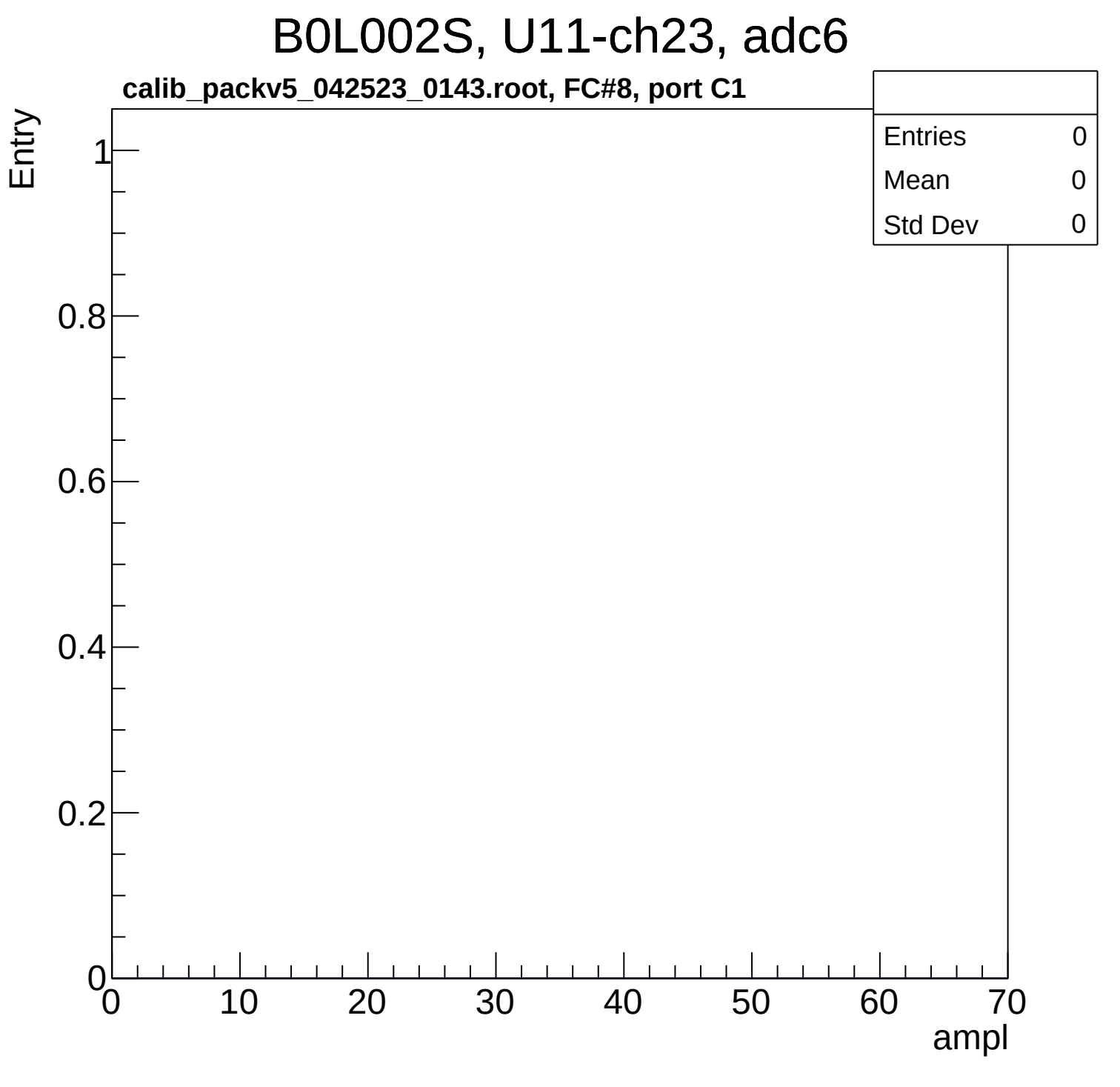
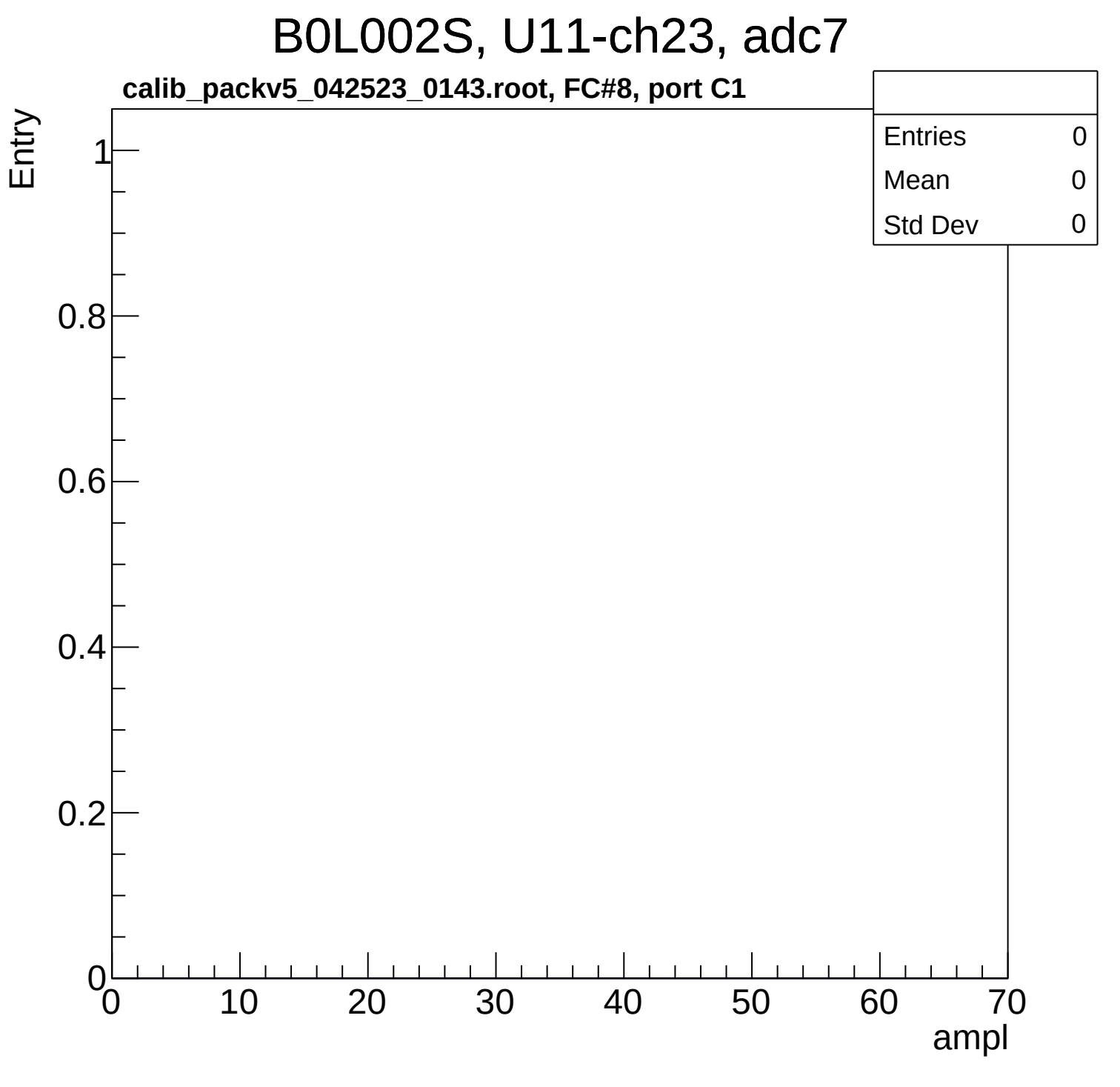
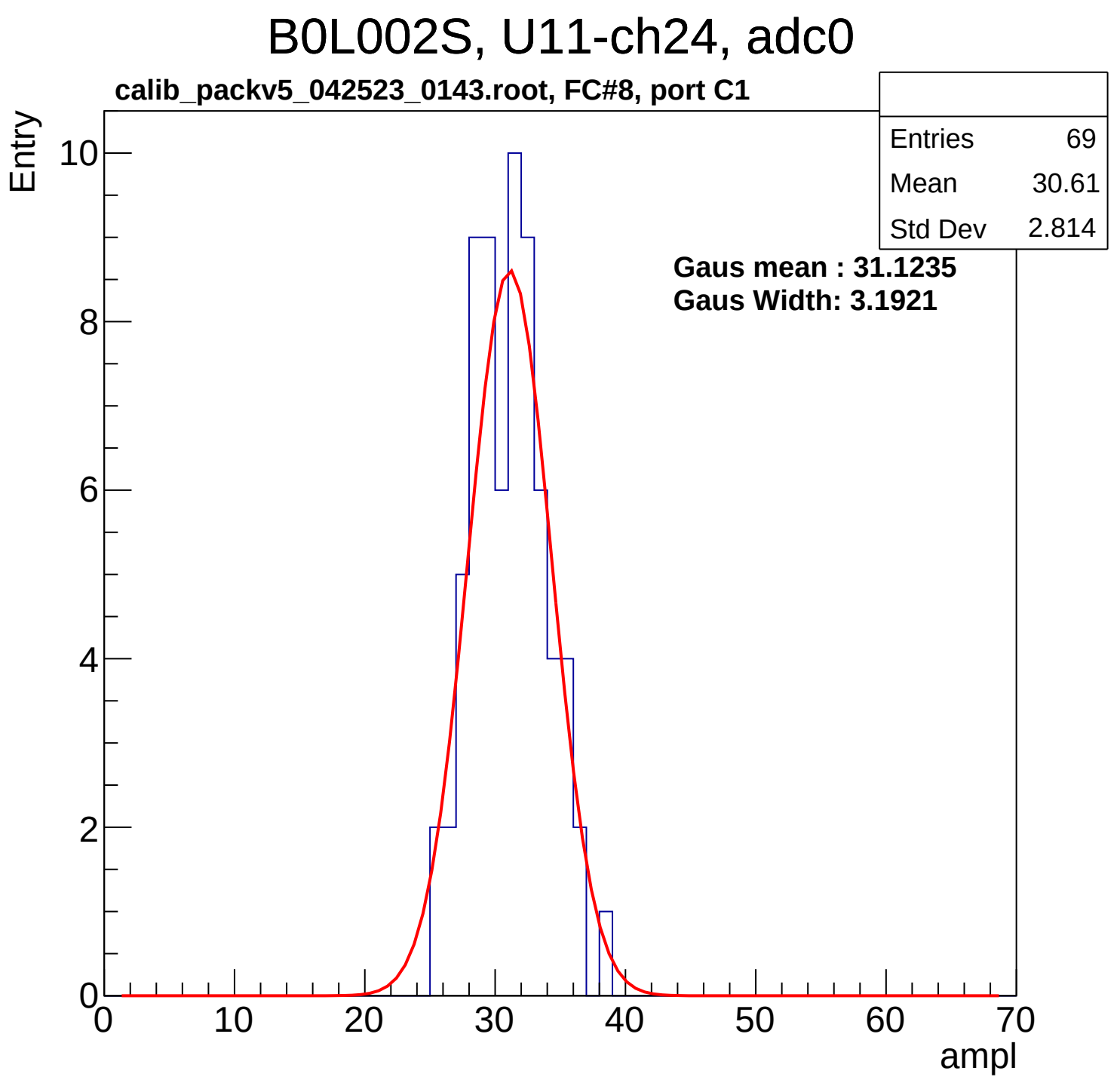

B0L002S, U11-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.61
Std Dev 2.814
Gaus mean : 31.1235
Gaus Width: 3.1921
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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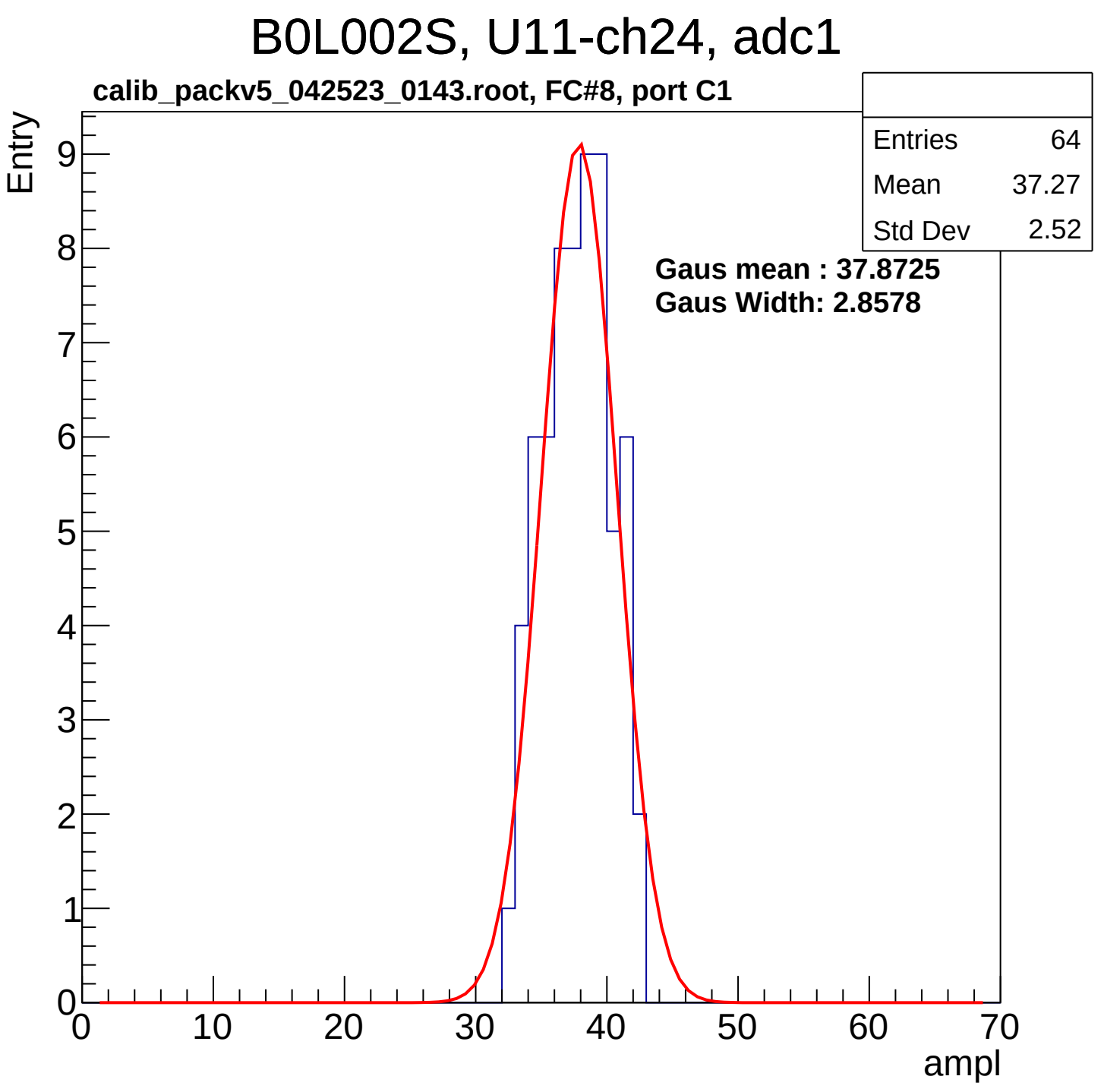
B0L002S, U11-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.27
Std Dev 2.52
Gaus mean : 37.8725
Gaus Width: 2.8578
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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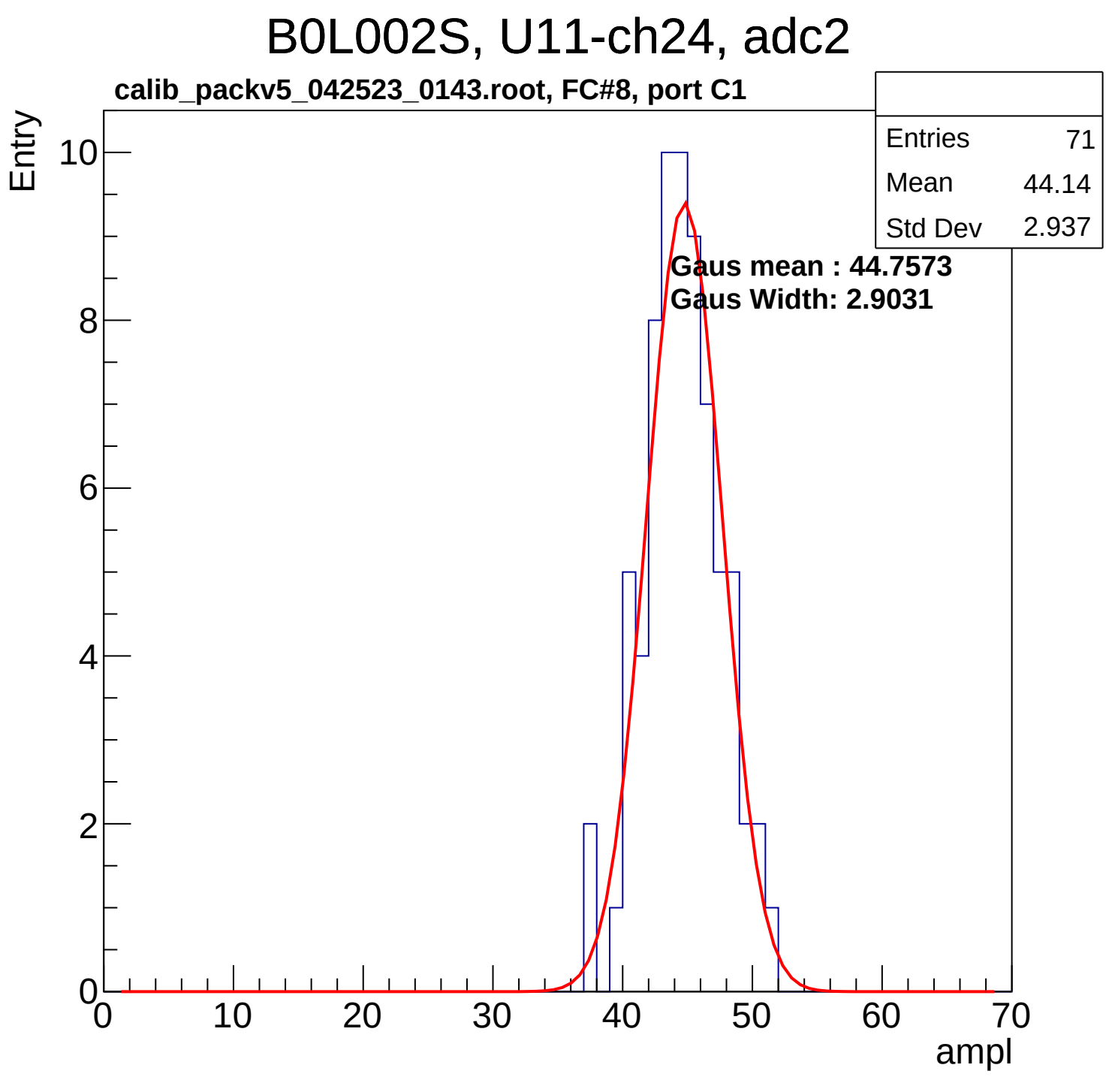
B0L002S, U11-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 44.14
Std Dev 2.937
Gaus mean : 44.7573
Gaus Width: 2.9031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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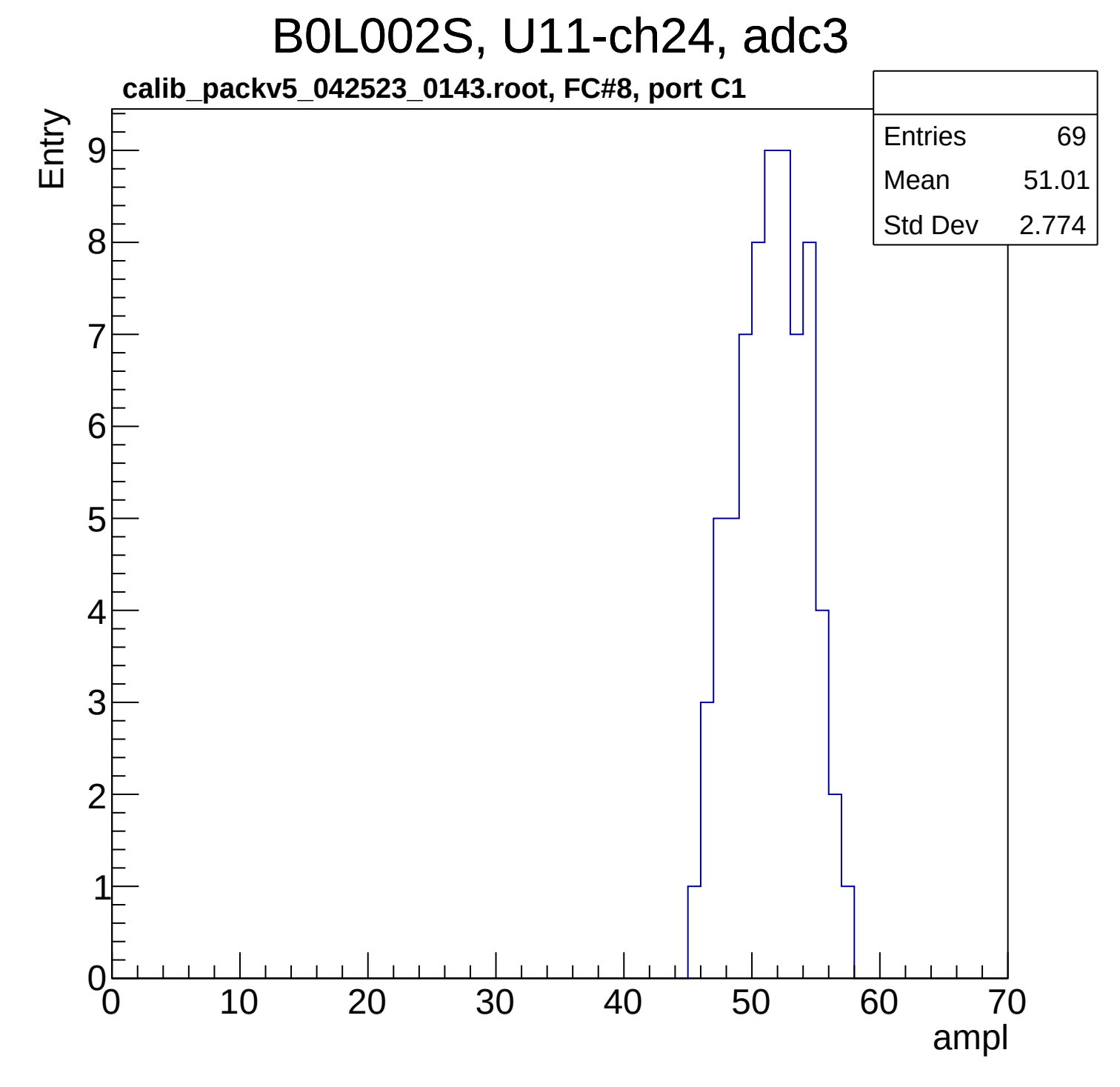
B0L002S, U11-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.01
Std Dev 2.774
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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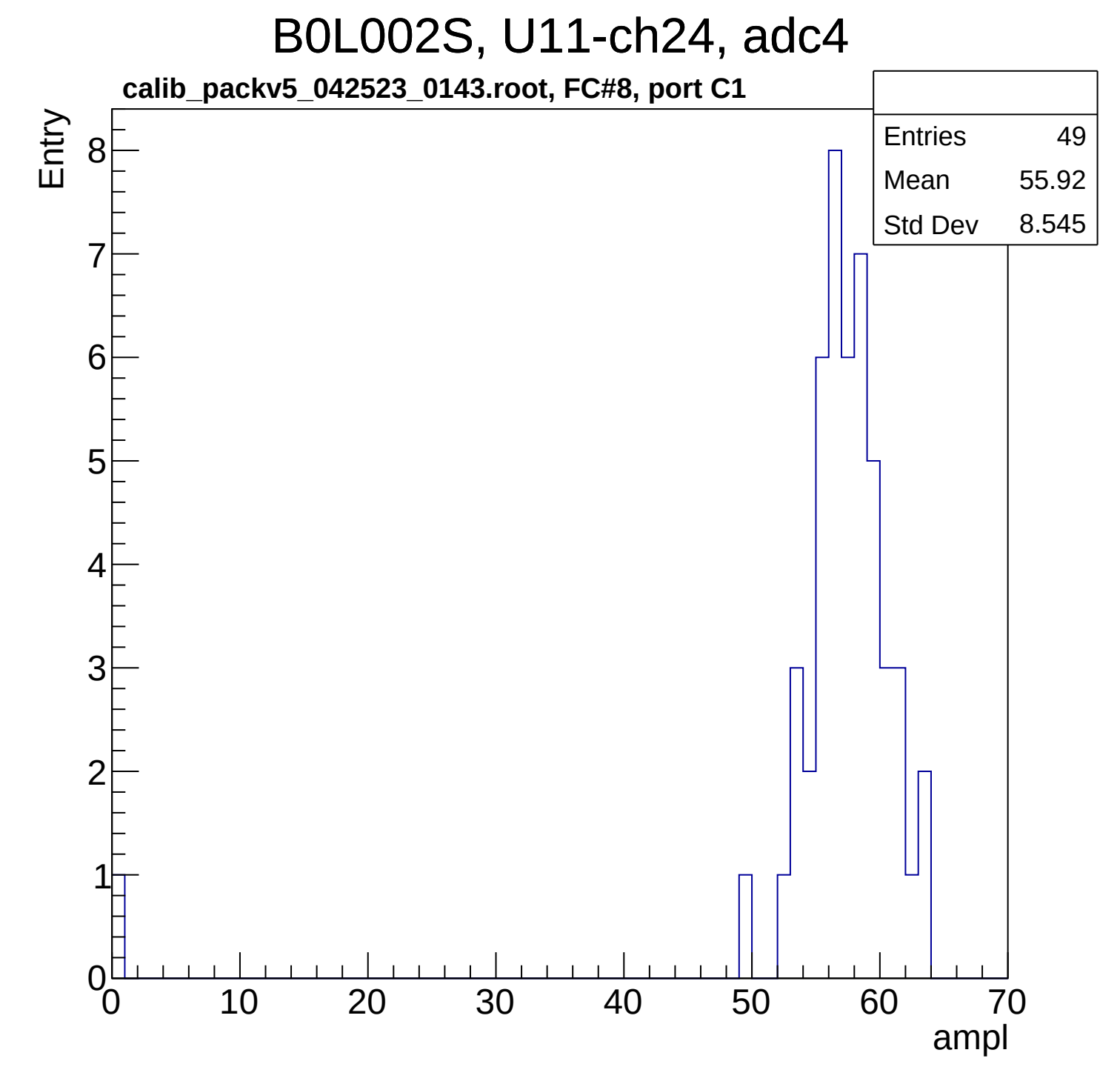
B0L002S, U11-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 55.92
Std Dev 8.545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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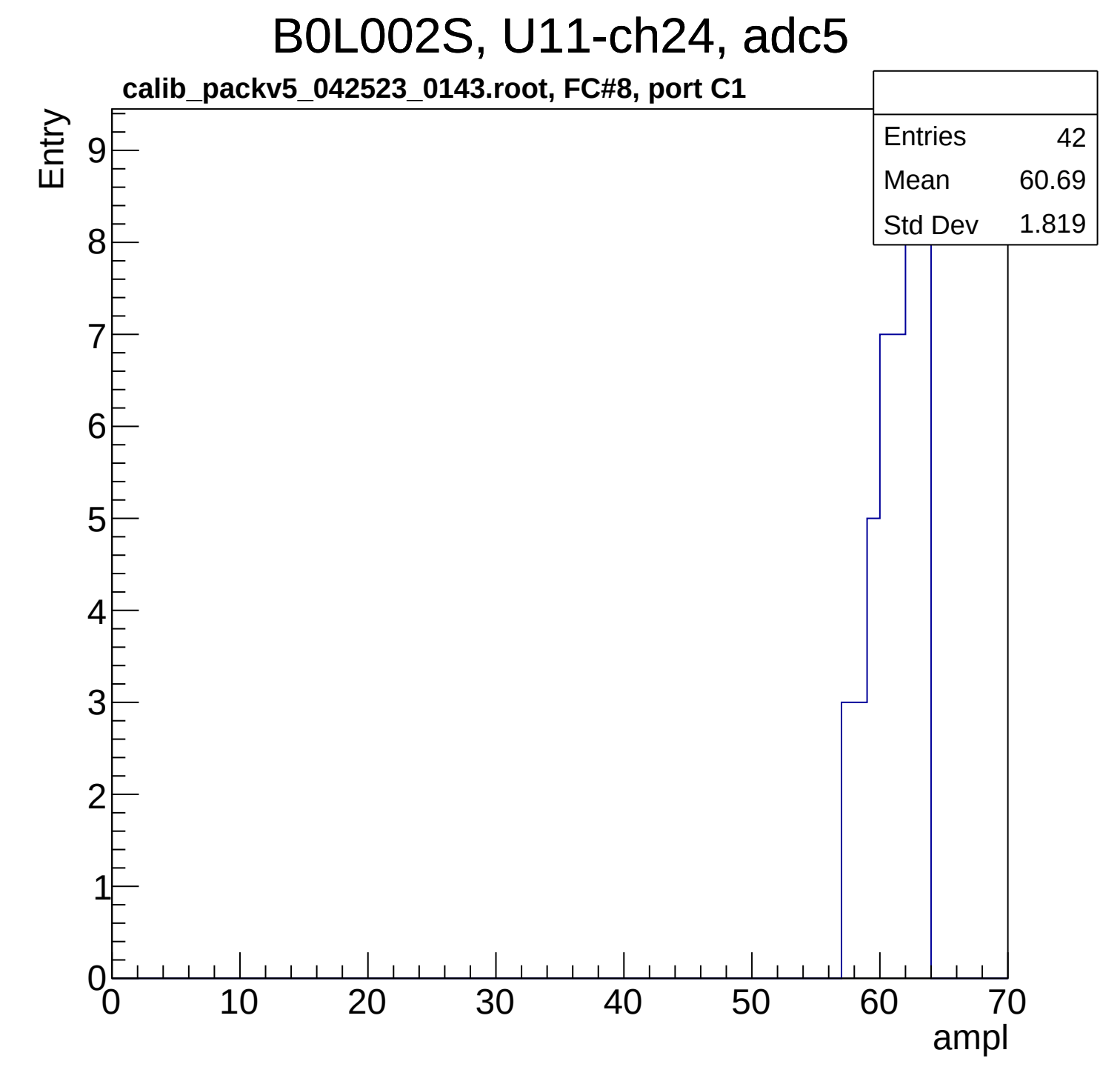
B0L002S, U11-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 60.69
Std Dev 1.819
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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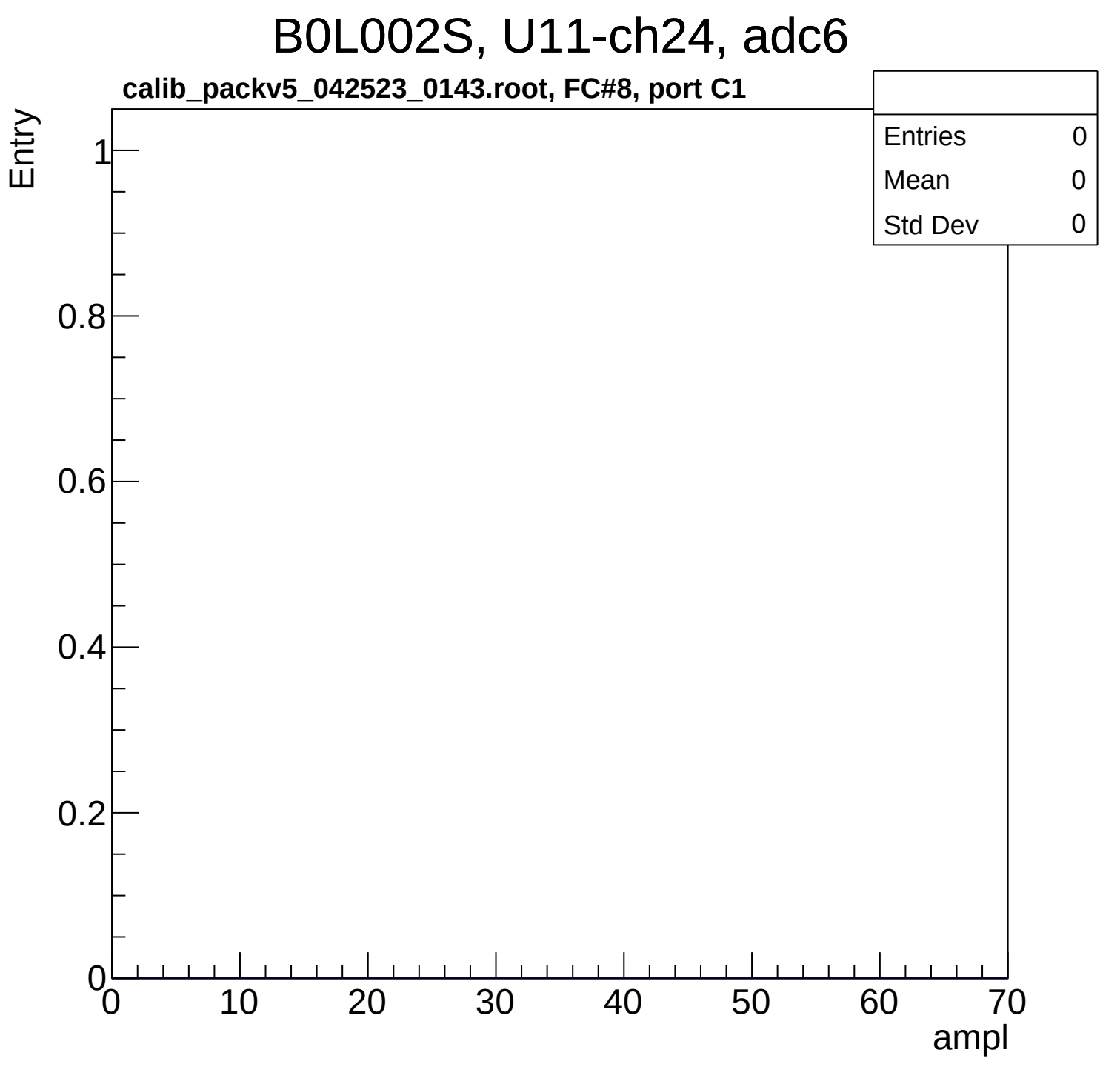
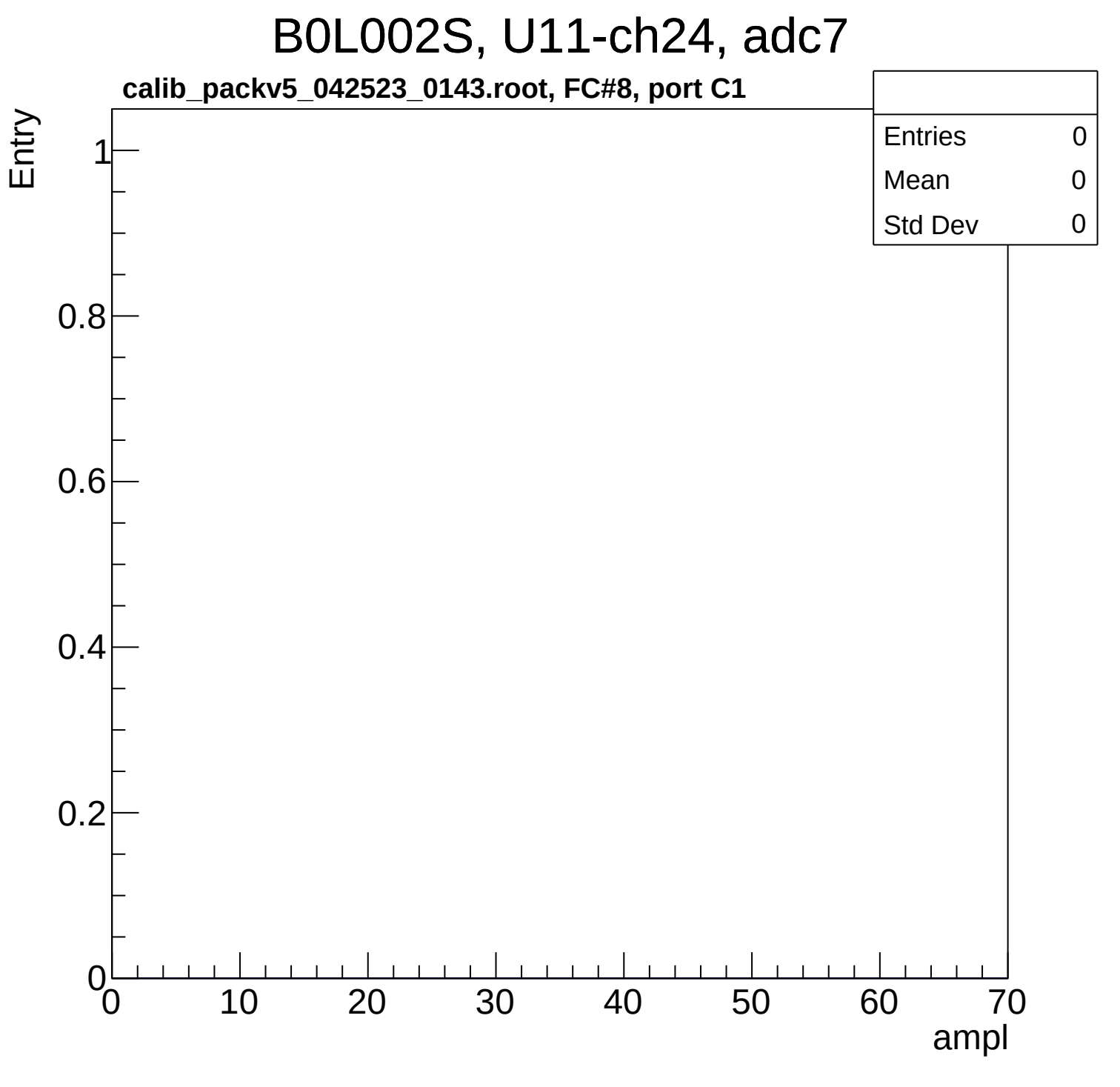

B0L002S, U11-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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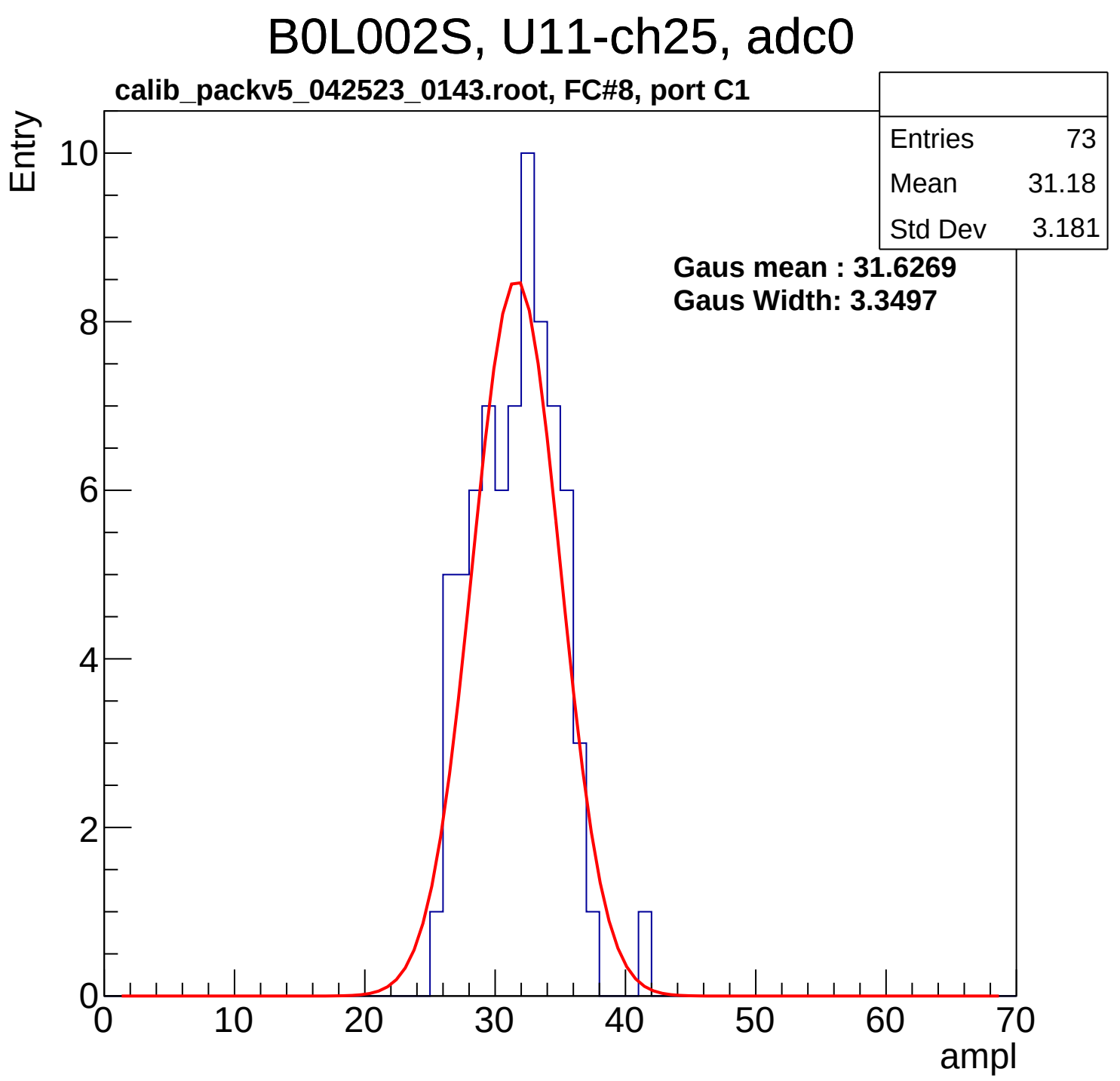
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 31.18
Std Dev 3.181
Gaus mean : 31.6269
Gaus Width: 3.3497
Entry
ampl
0
2
4
6
8
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0
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20
30
40
50
60
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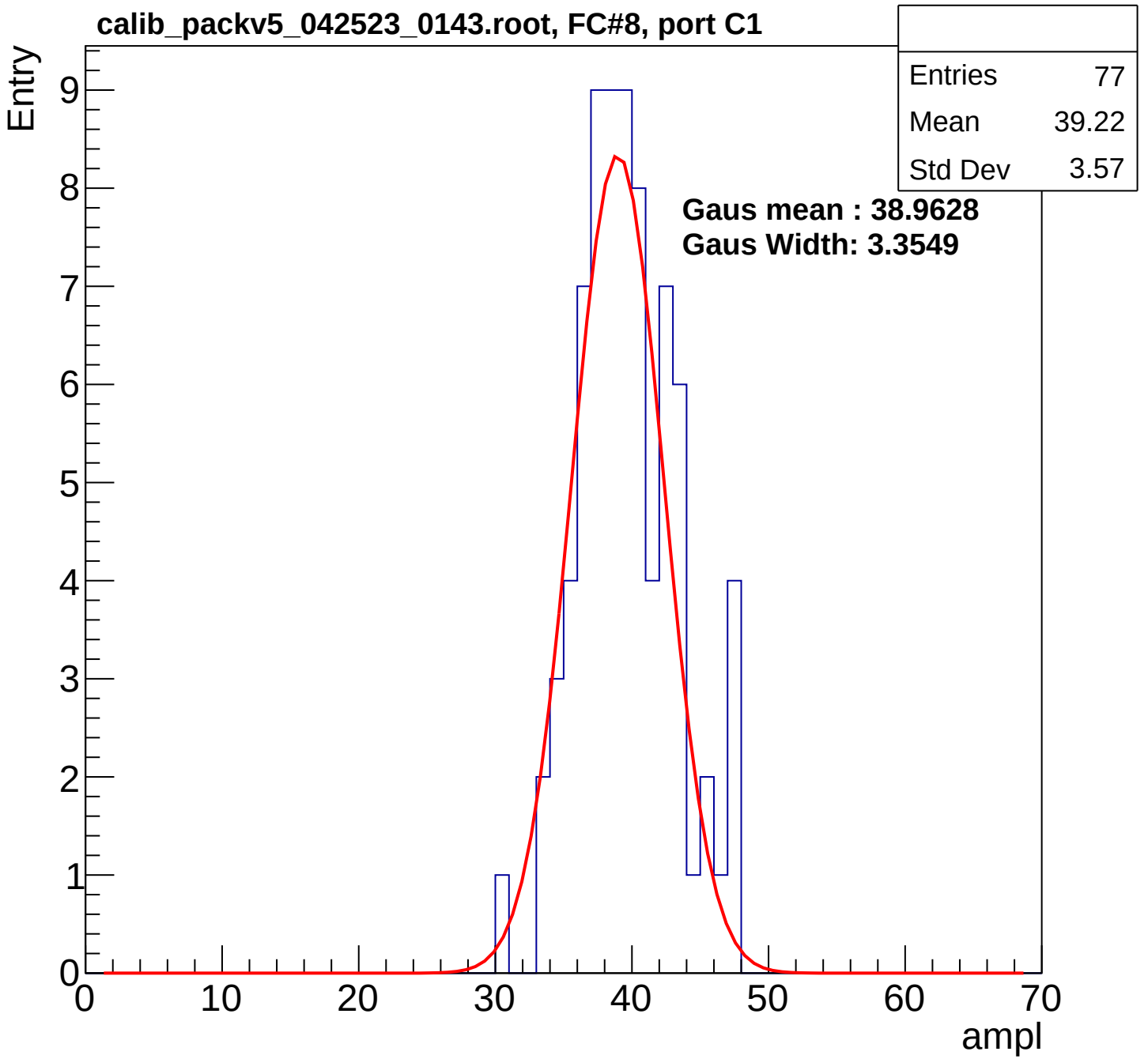
B0L002S, U11-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.22
Std Dev 3.57
Gaus mean : 38.9628
Gaus Width: 3.3549
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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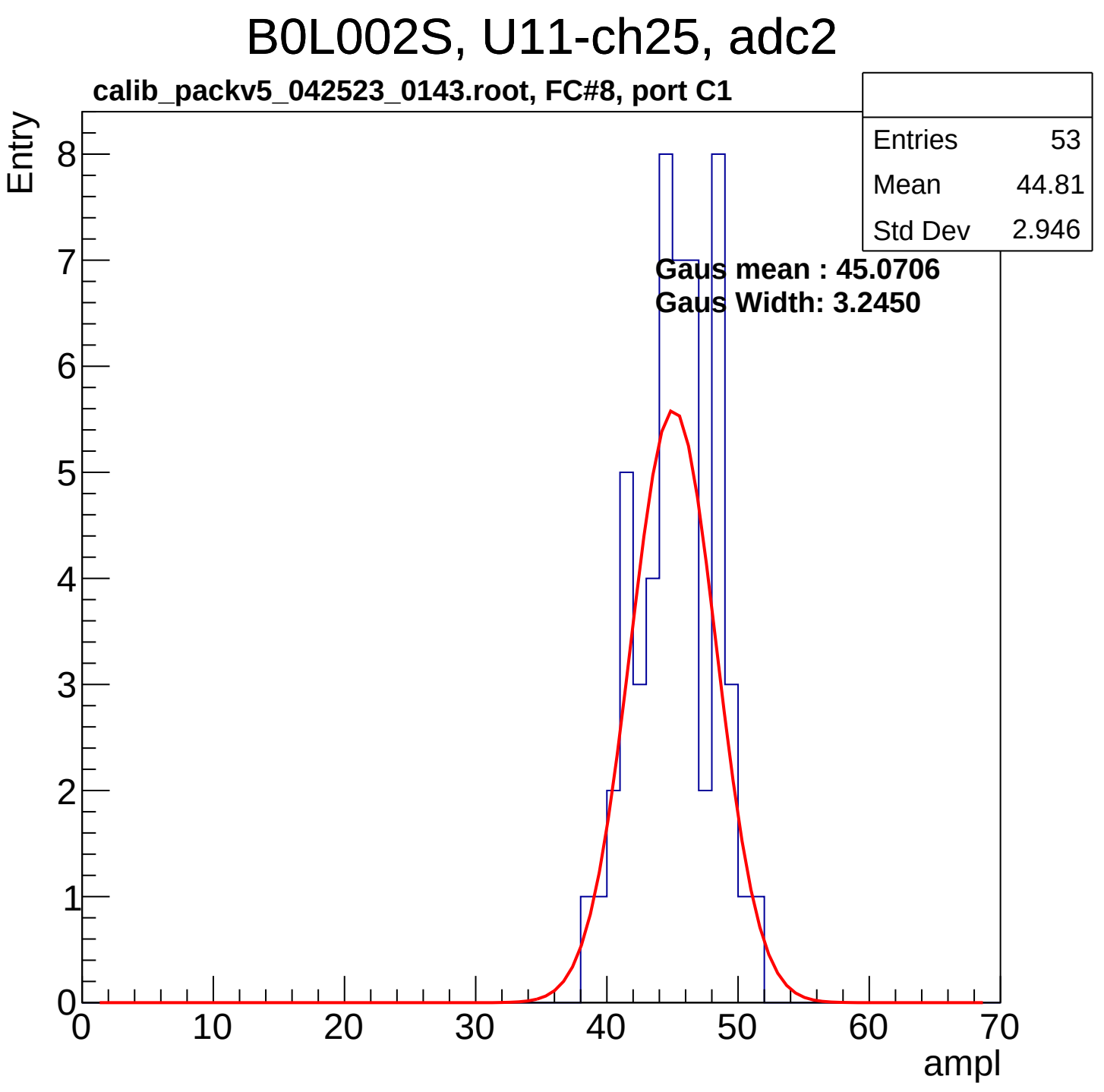
B0L002S, U11-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 44.81
Std Dev 2.946
Gaus mean : 45.0706
Gaus Width: 3.2450
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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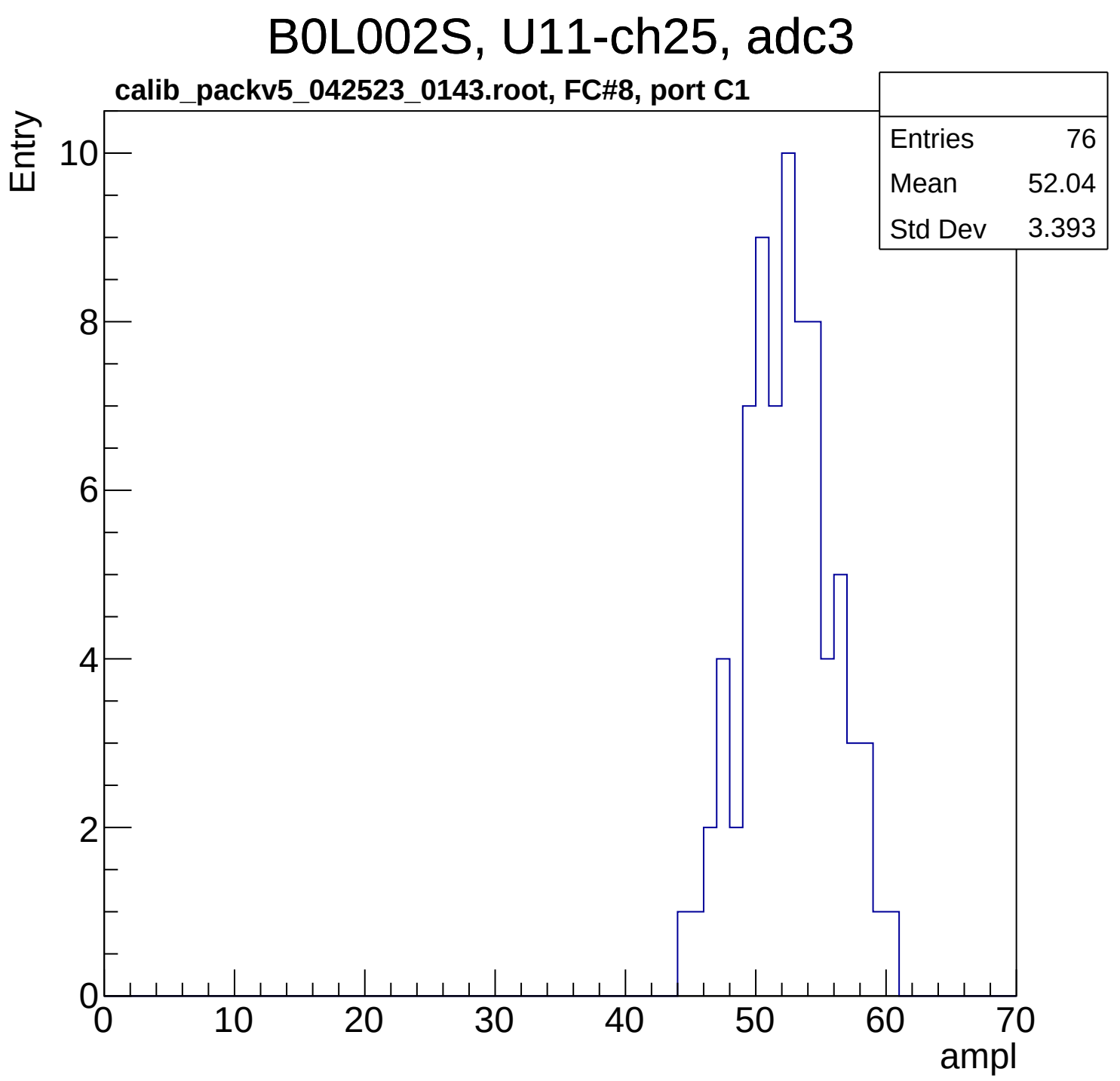
B0L002S, U11-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.04
Std Dev 3.393
Entry
ampl
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0
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20
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40
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60
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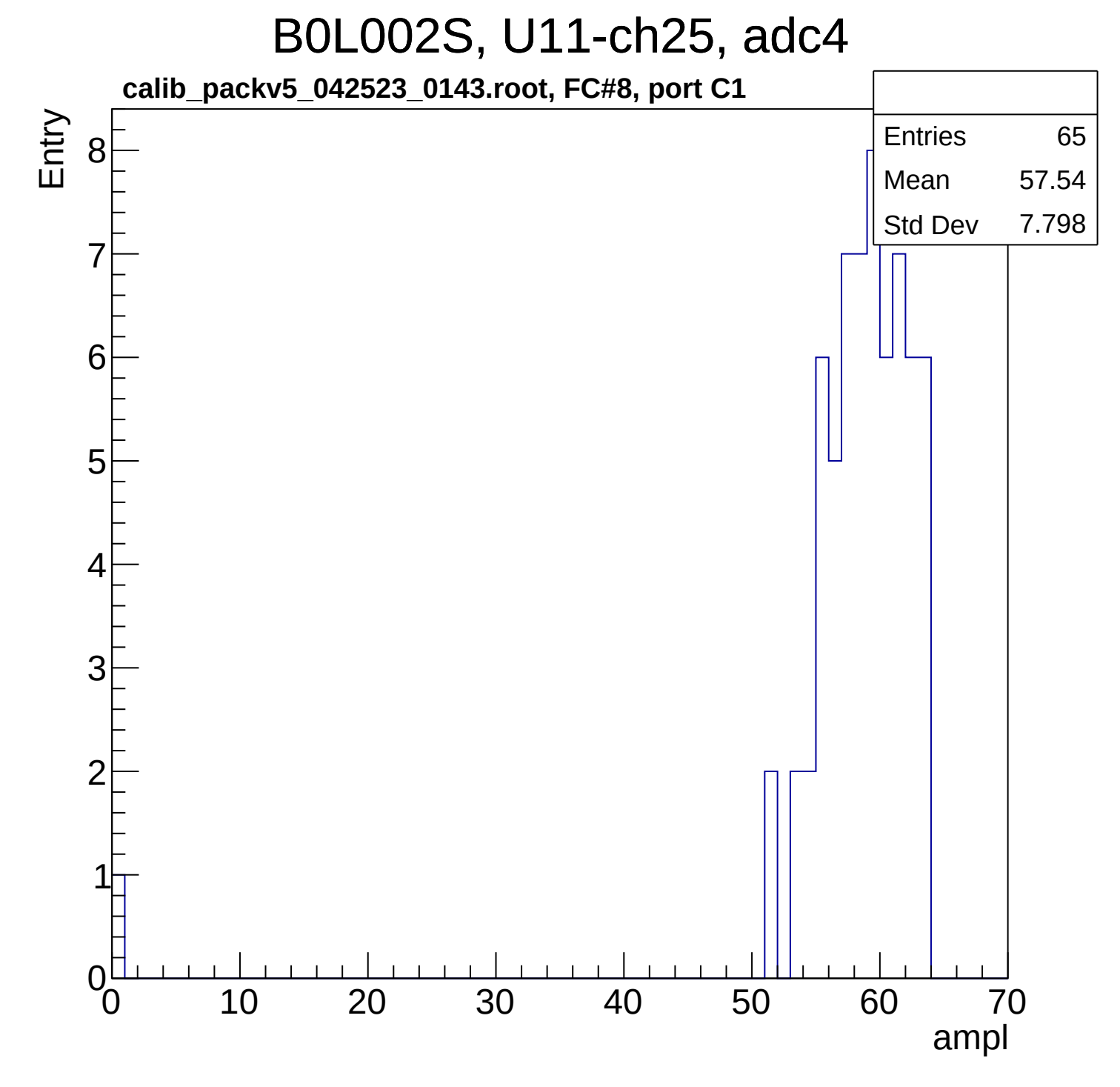
B0L002S, U11-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.54
Std Dev 7.798
Entry
ampl
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4
5
6
7
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0
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20
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40
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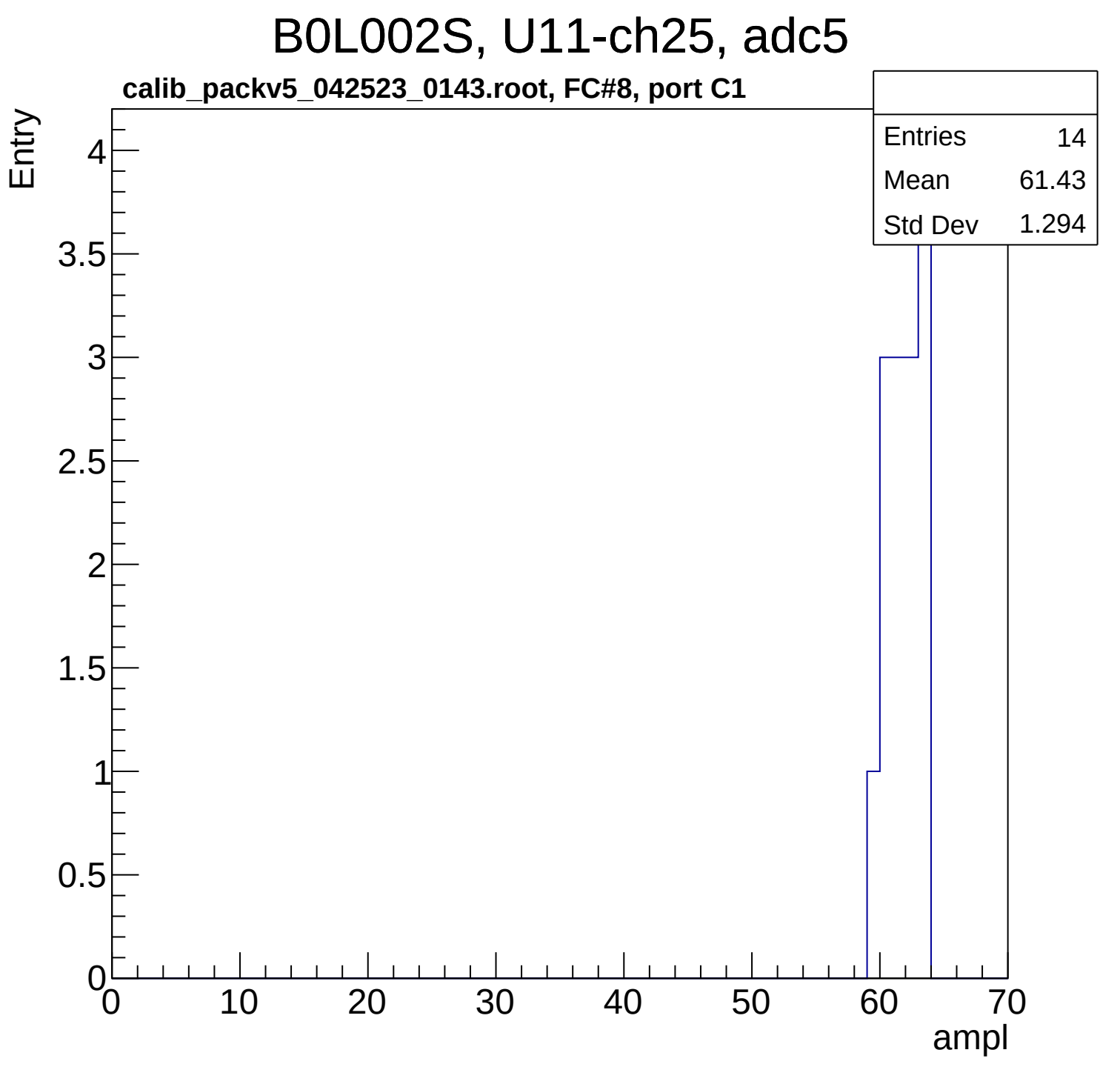
B0L002S, U11-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 61.43
Std Dev 1.294
Entry
ampl
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1.5
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2.5
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3.5
4
0
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20
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60
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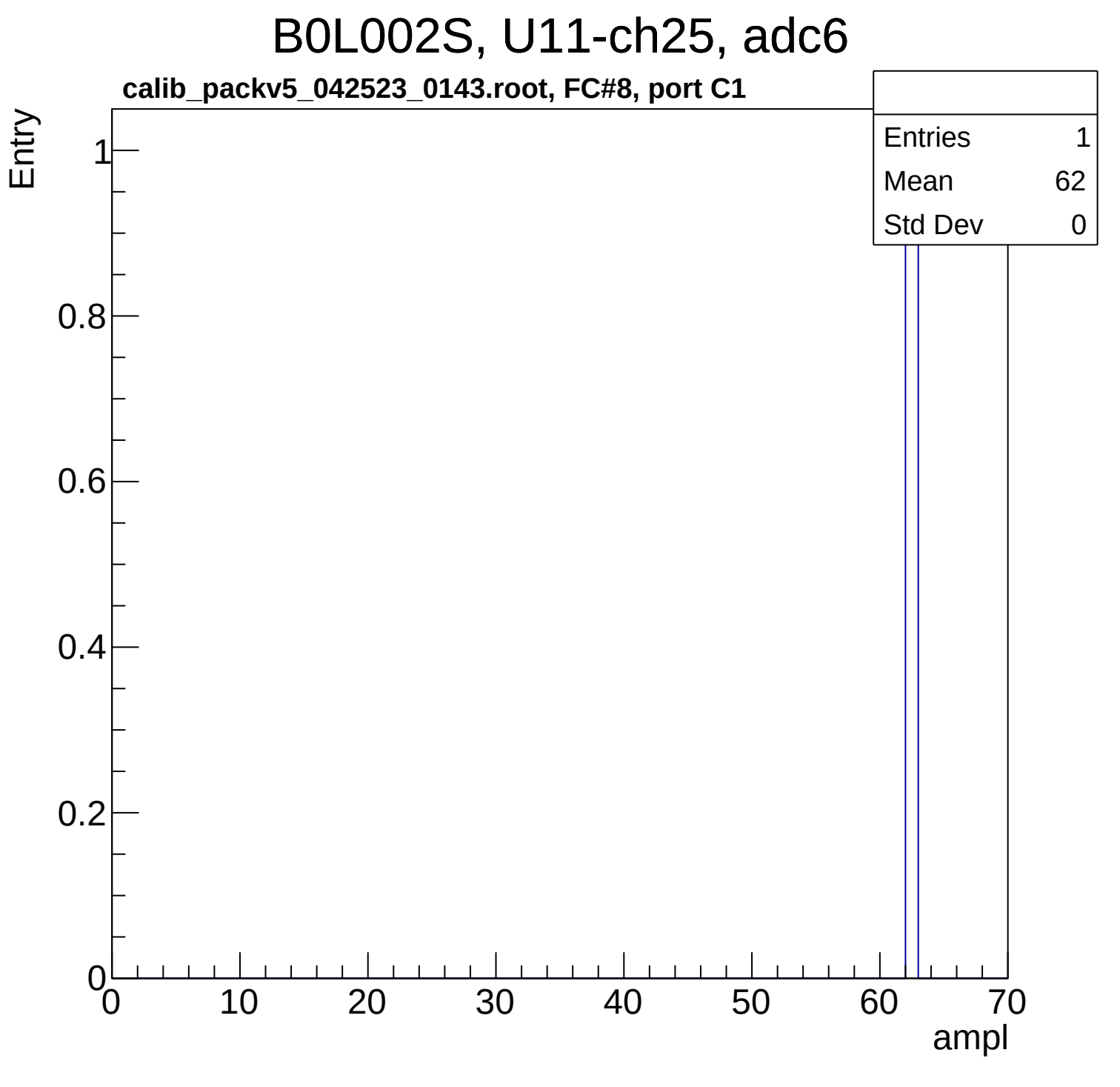
B0L002S, U11-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
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10
20
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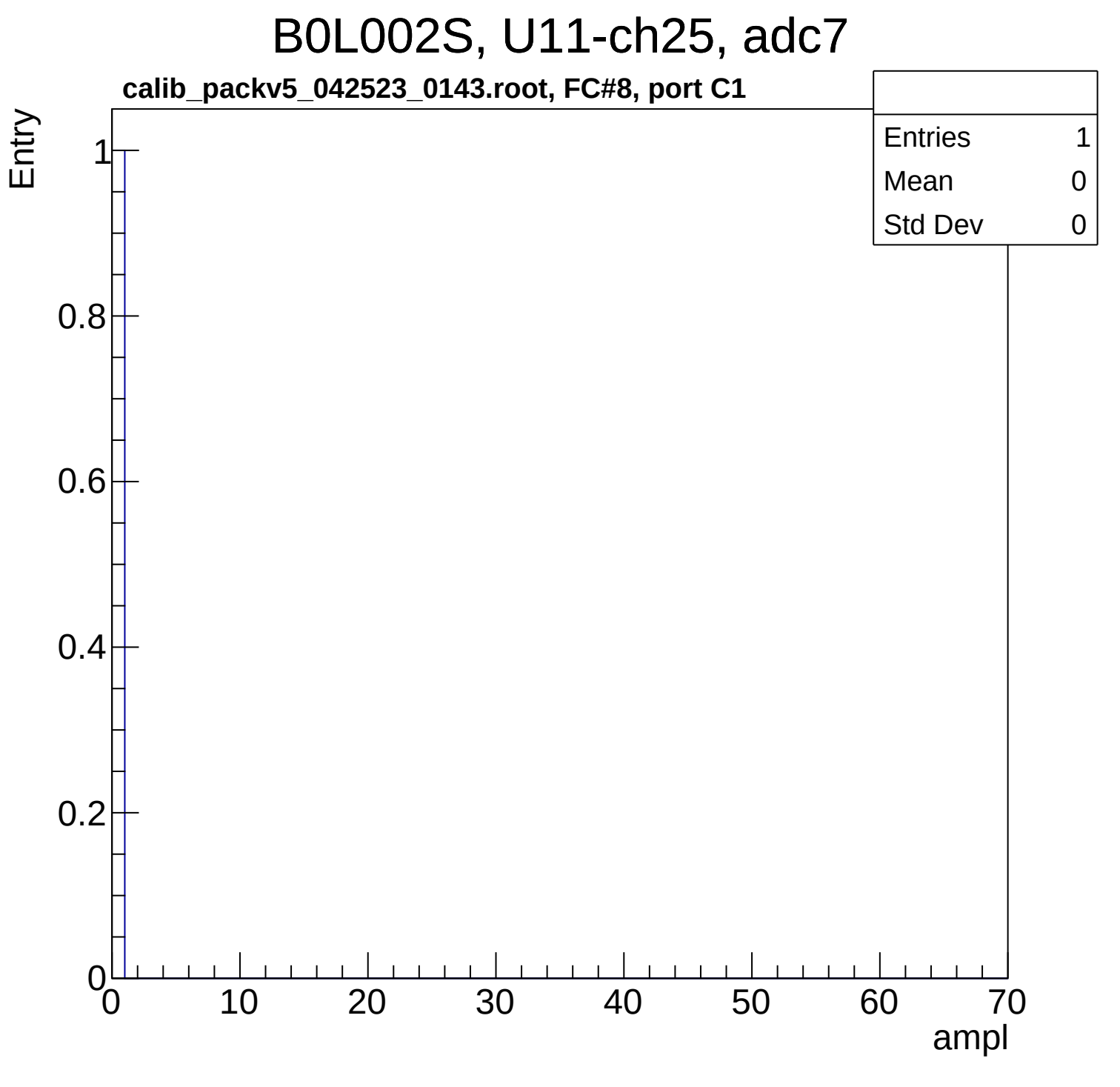
40
50
60
70
ampl


B0L002S, U11-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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30
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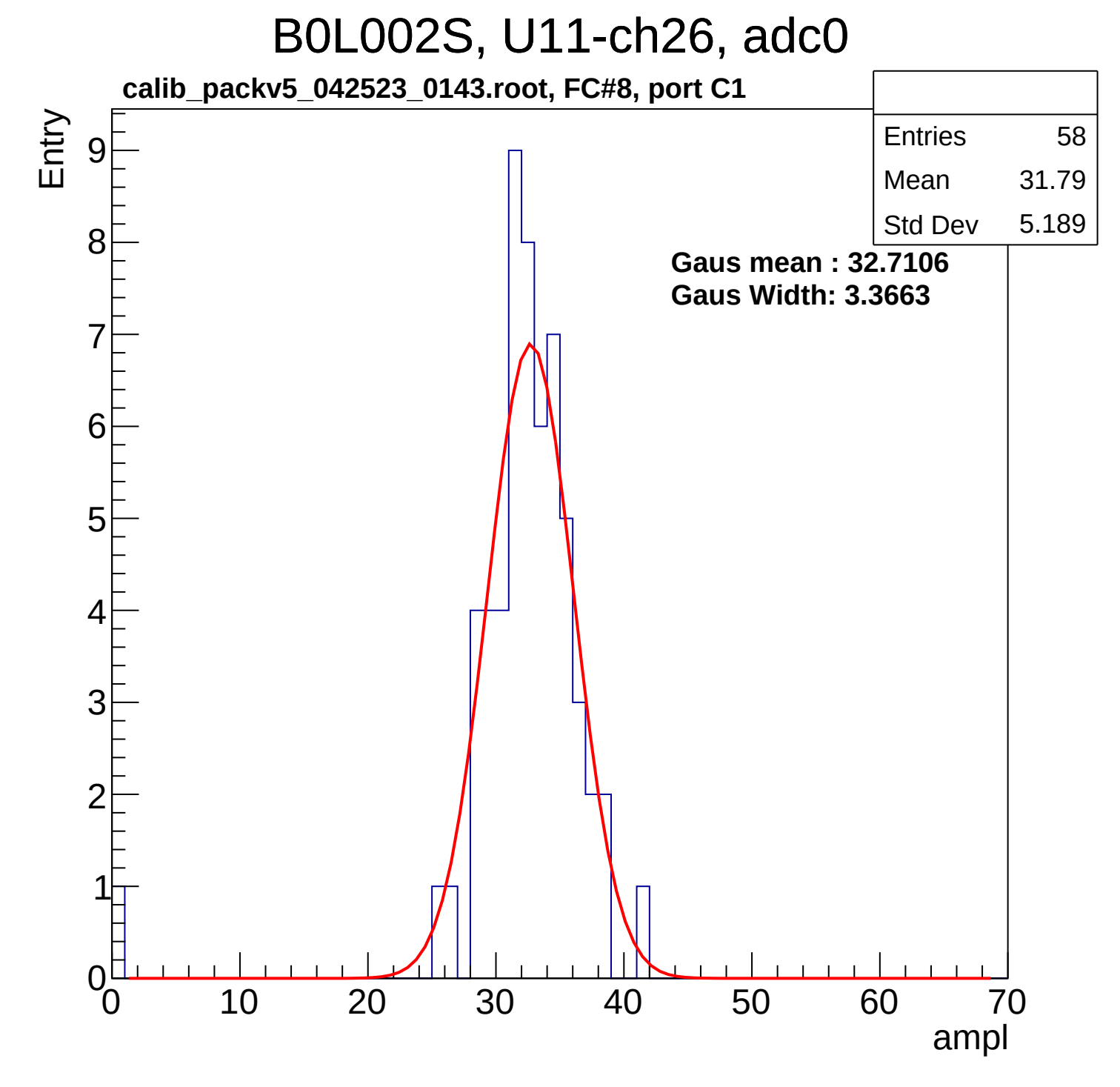
50
60
70
ampl


B0L002S, U11-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.79
Std Dev 5.189
Gaus mean : 32.7106
Gaus Width: 3.3663
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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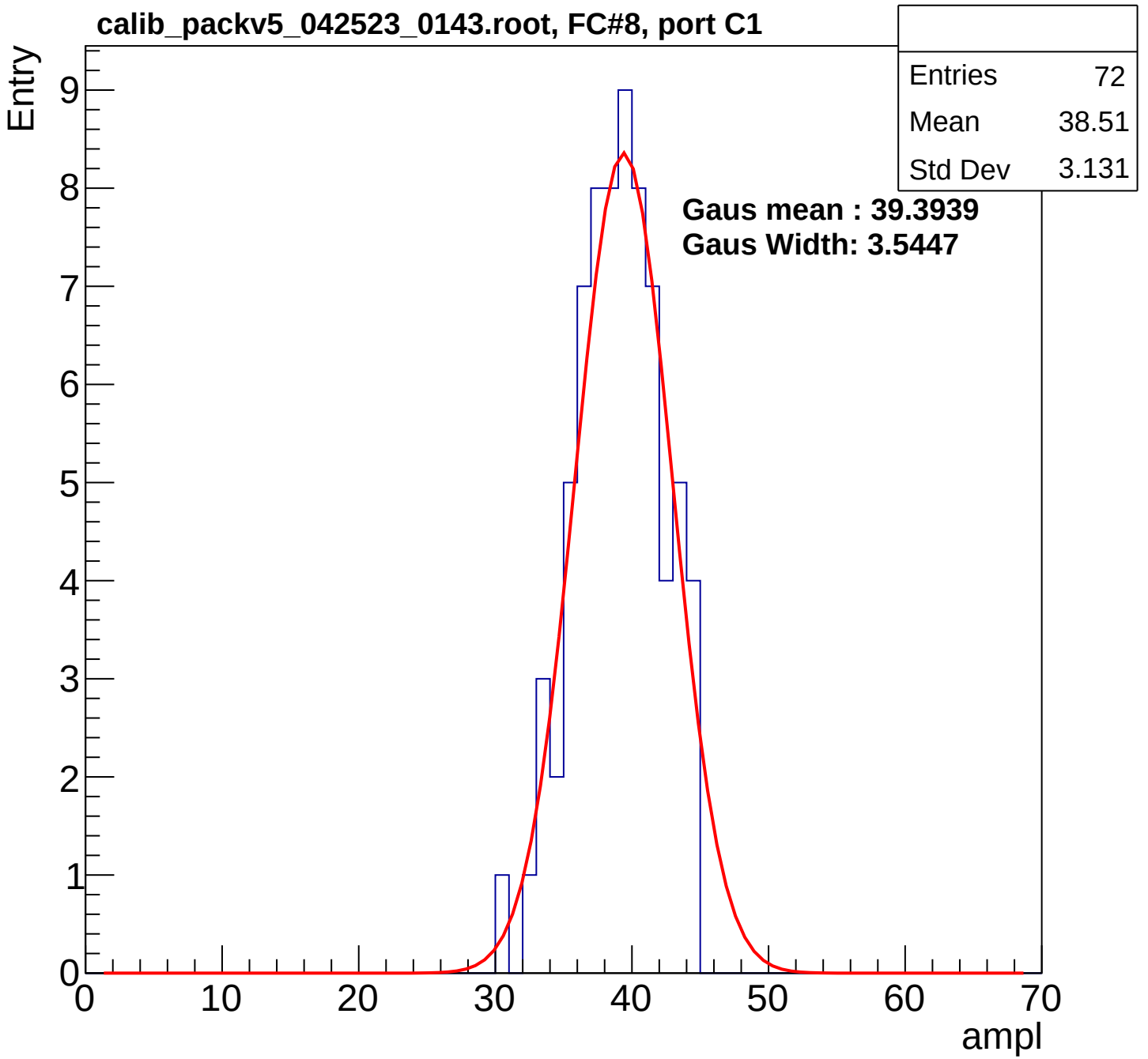
60
70


B0L002S, U11-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.51
Std Dev 3.131
Gaus mean : 39.3939
Gaus Width: 3.5447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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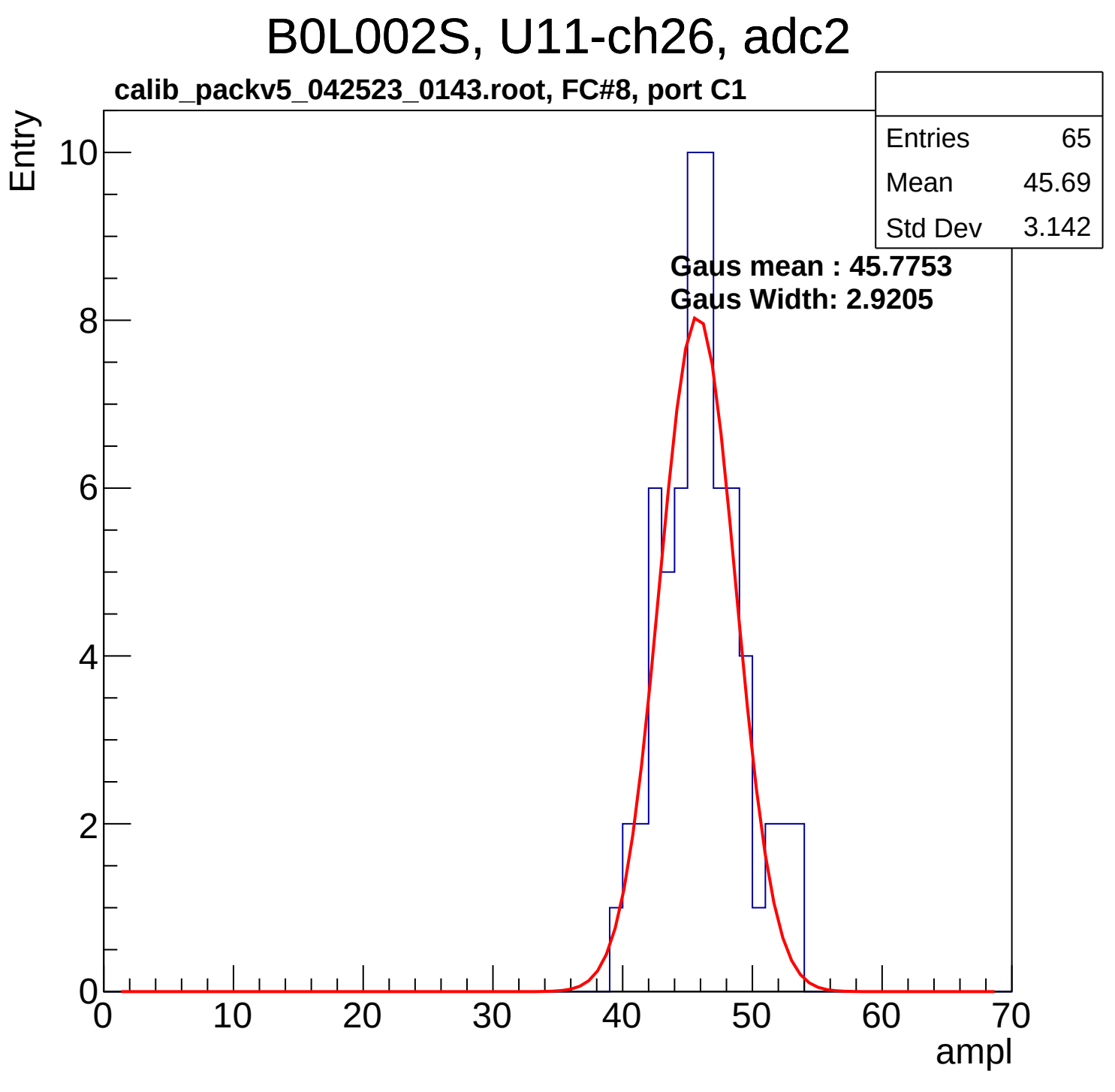
60
70


B0L002S, U11-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.69
Std Dev 3.142
Gaus mean : 45.7753
Gaus Width: 2.9205
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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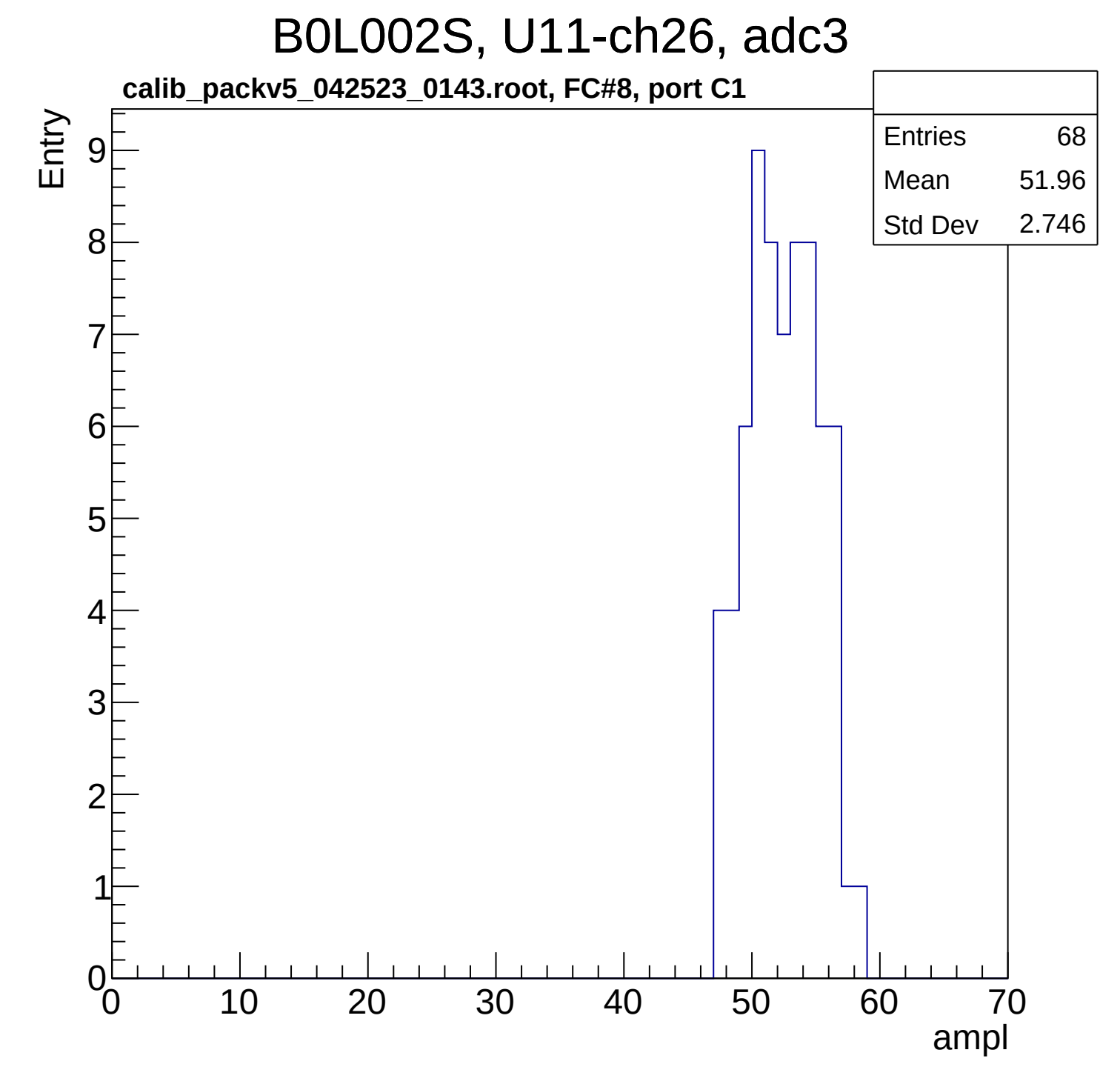
B0L002S, U11-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.96
Std Dev 2.746
Entry
ampl
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4
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6
7
8
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0
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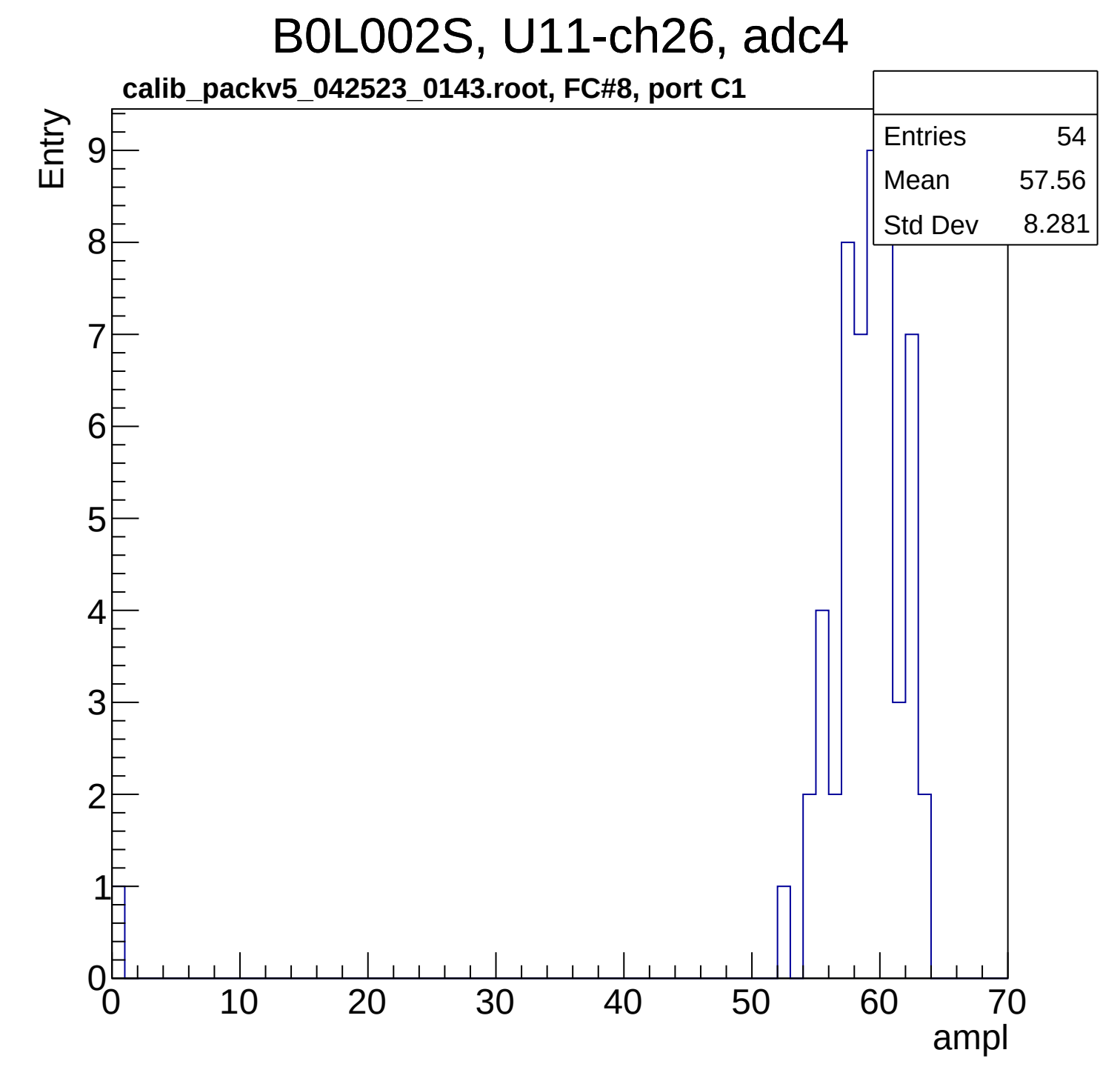
B0L002S, U11-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.56
Std Dev 8.281
Entry
ampl
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3
4
5
6
7
8
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0
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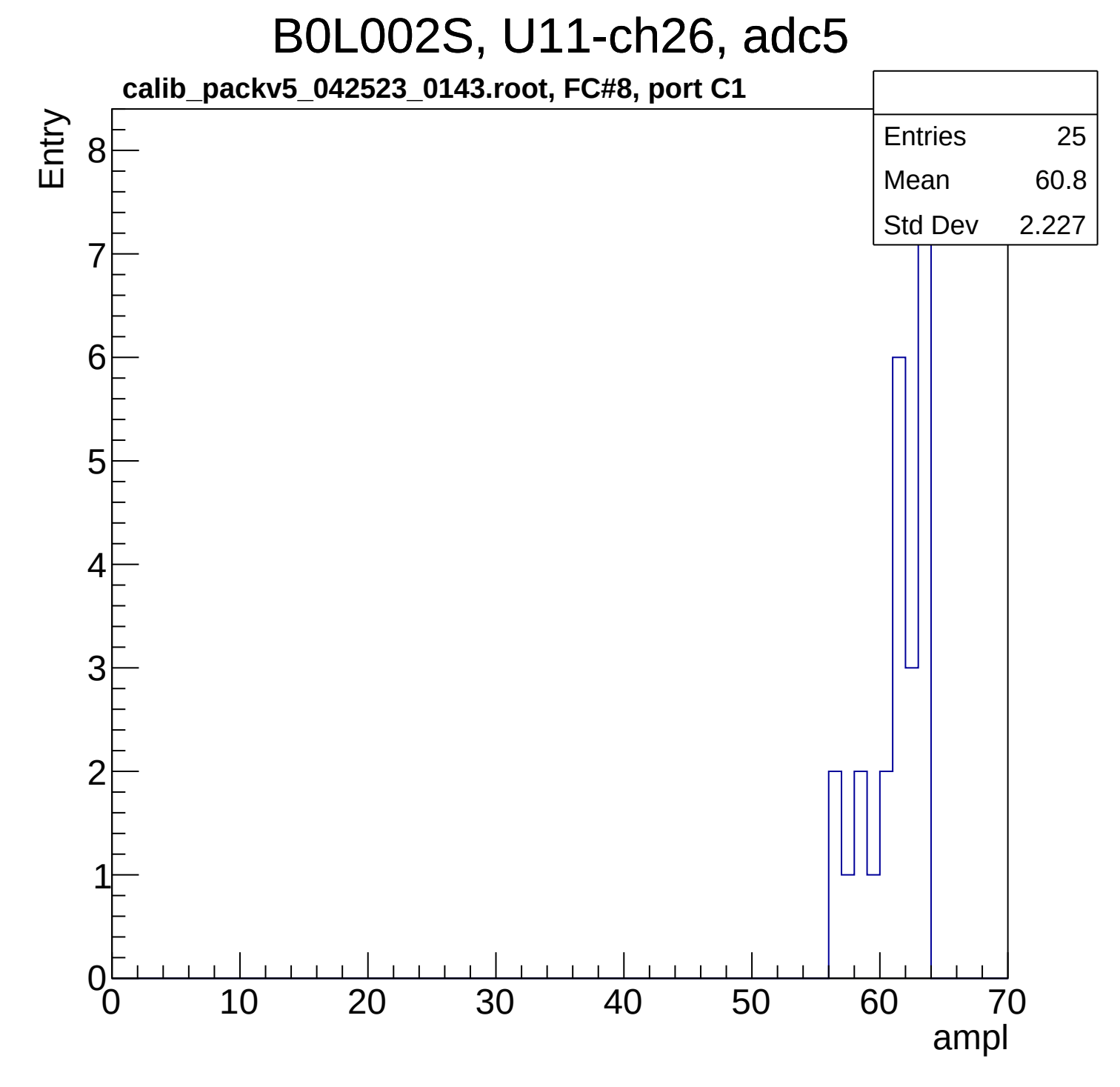
B0L002S, U11-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 60.8
Std Dev 2.227
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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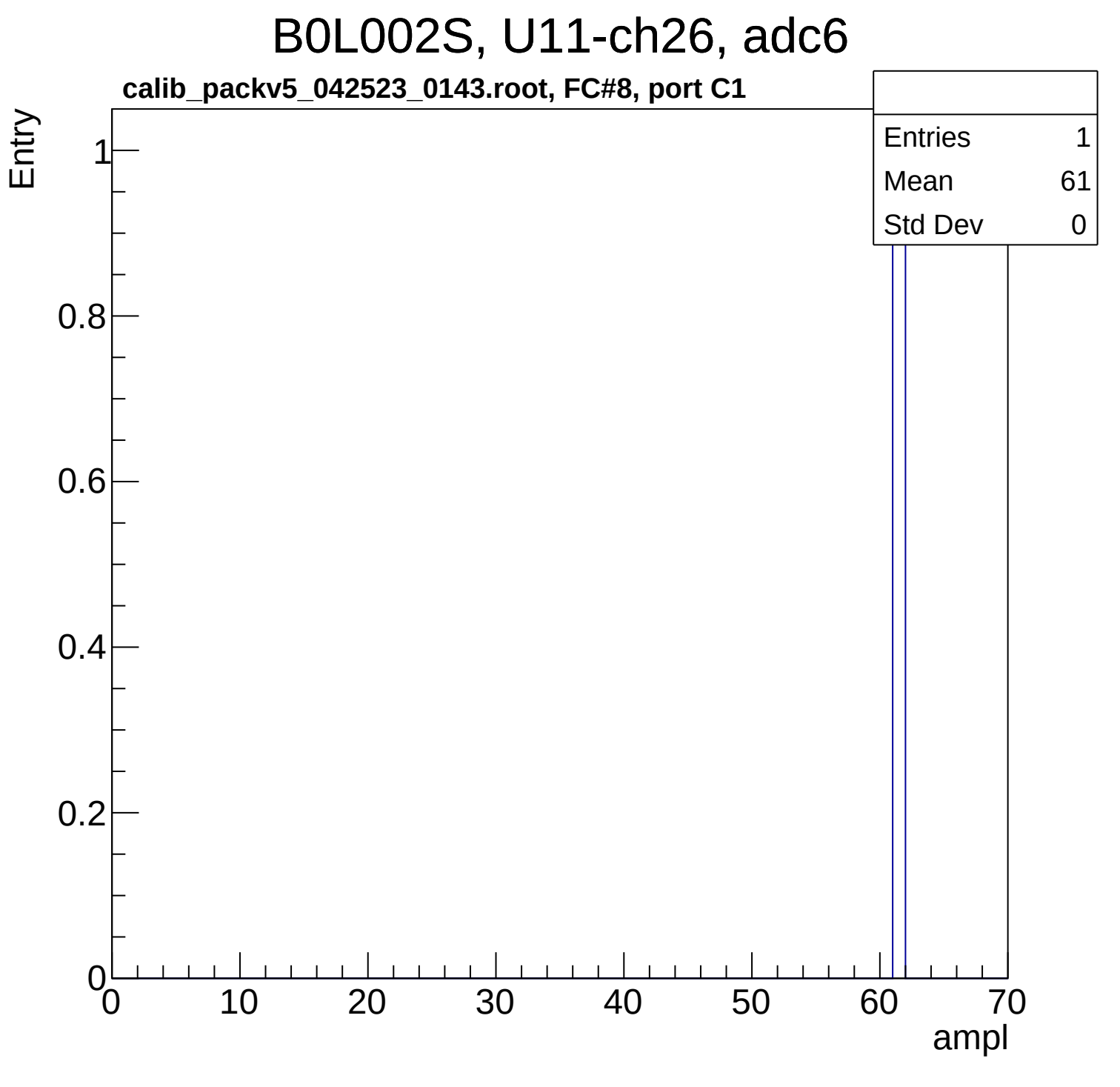
B0L002S, U11-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
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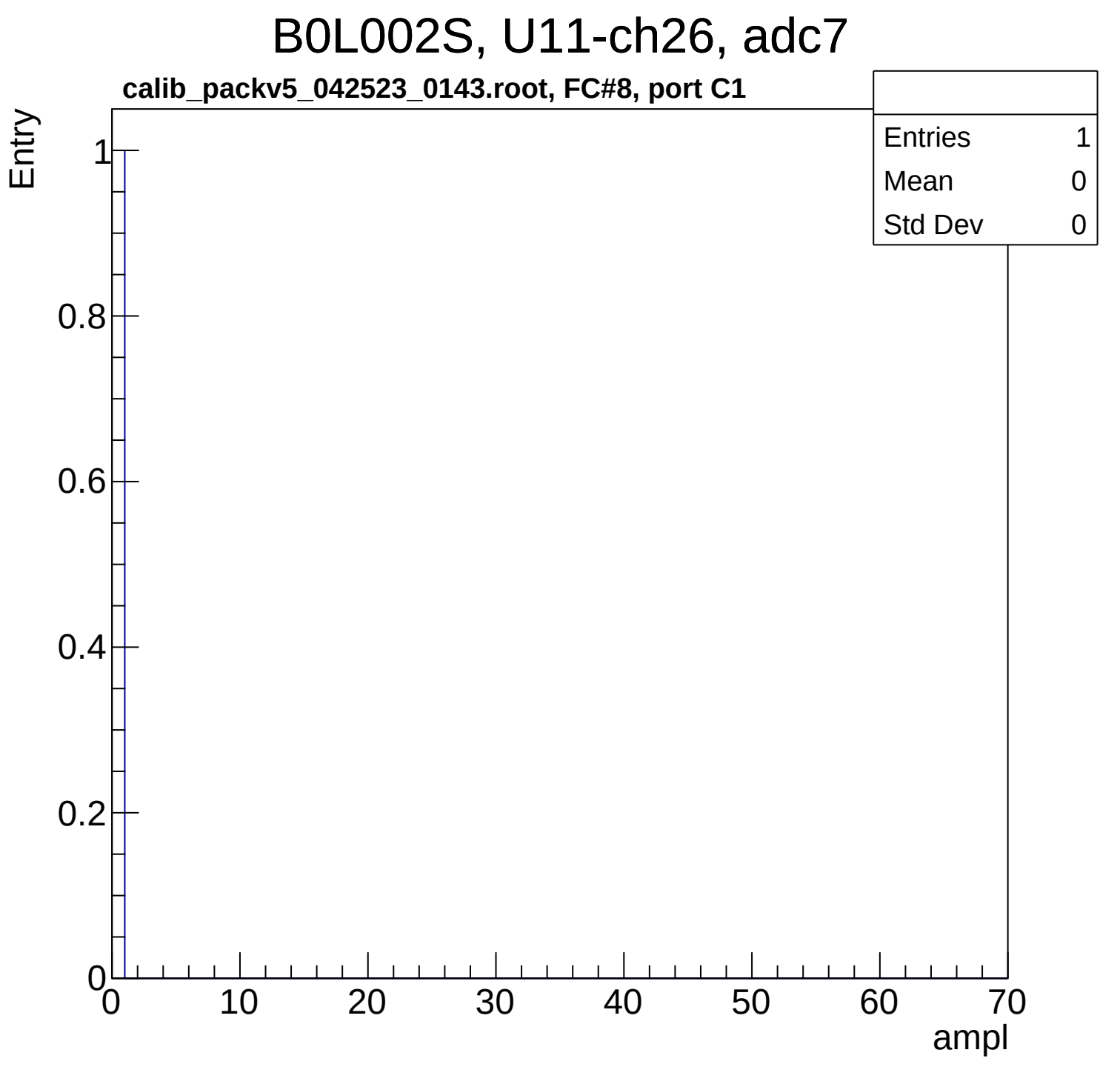
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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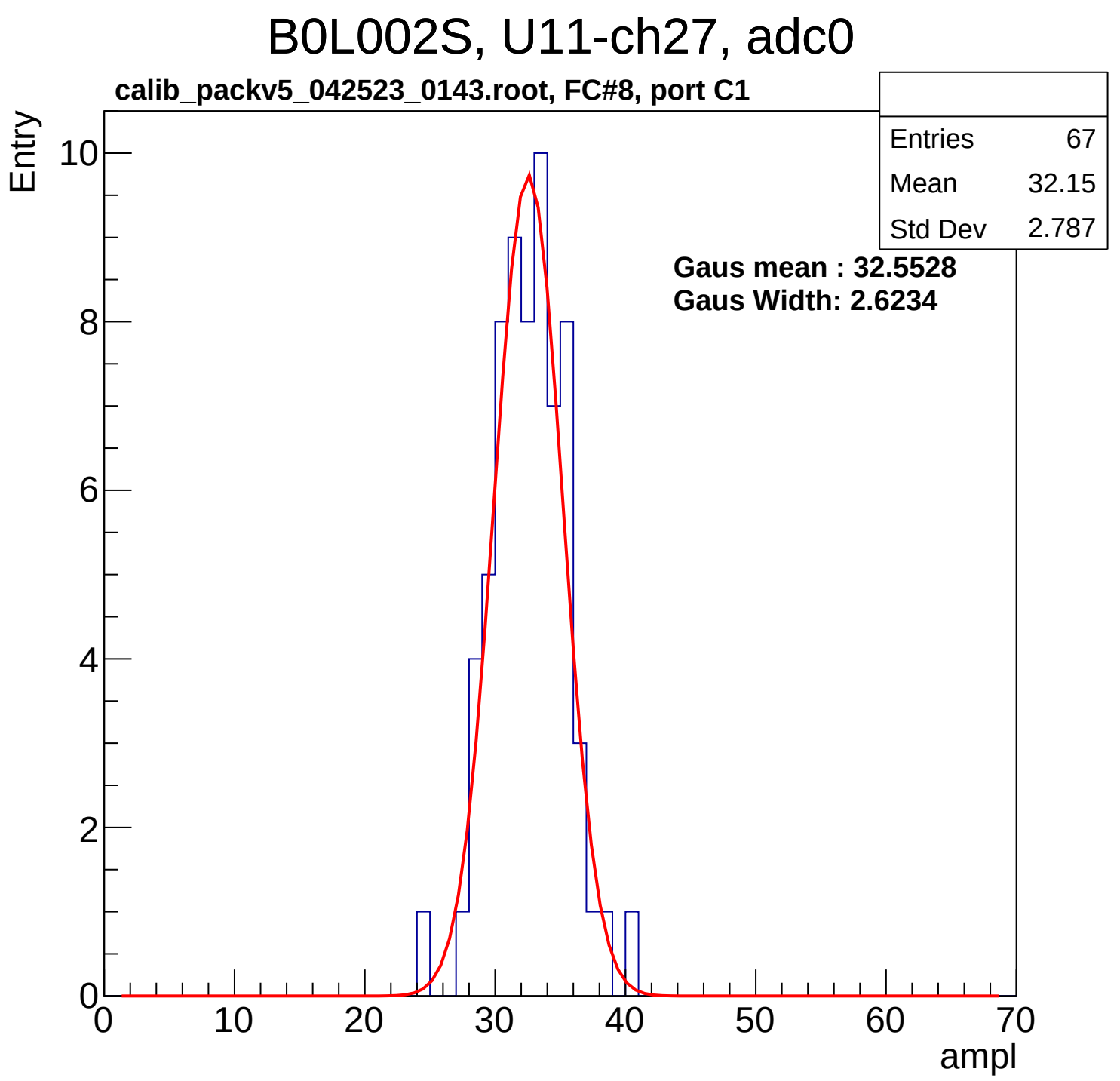
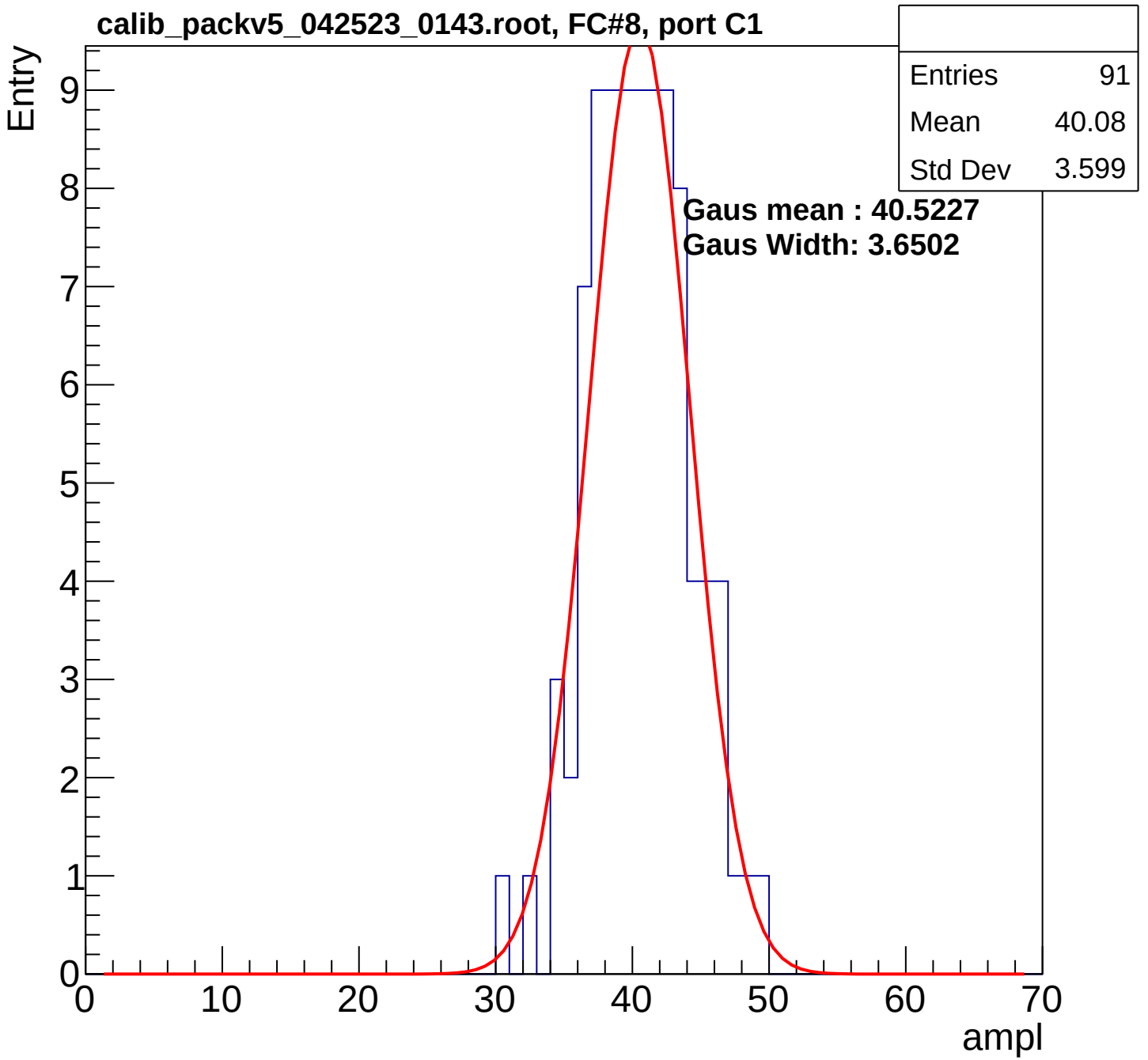
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 32.15
Std Dev 2.787
Gaus mean : 32.5528
Gaus Width: 2.6234
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 40.08
Std Dev 3.599
Gaus mean : 40.5227
Gaus Width: 3.6502
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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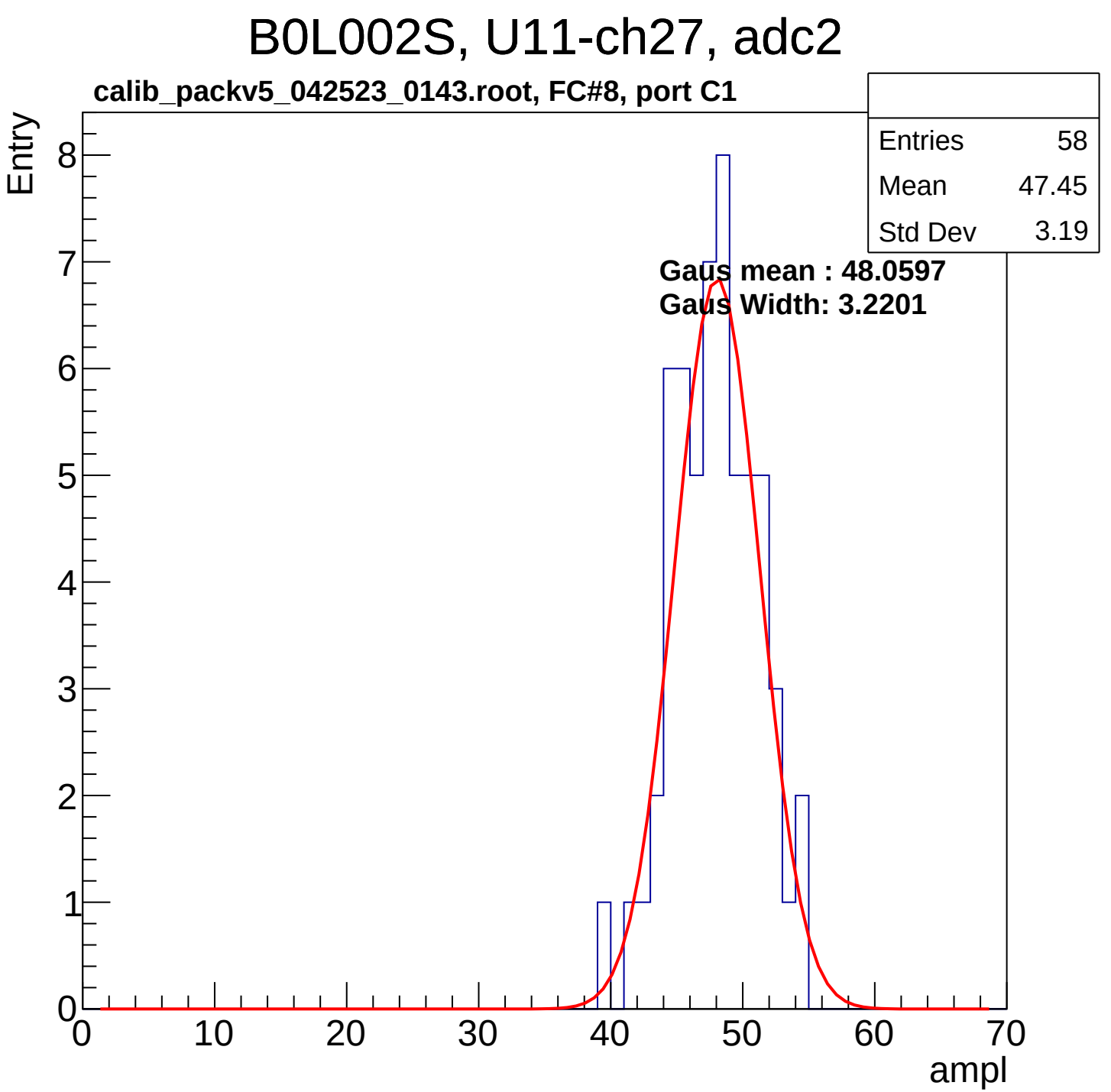
B0L002S, U11-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 47.45
Std Dev 3.19
Gaus mean : 48.0597
Gaus Width: 3.2201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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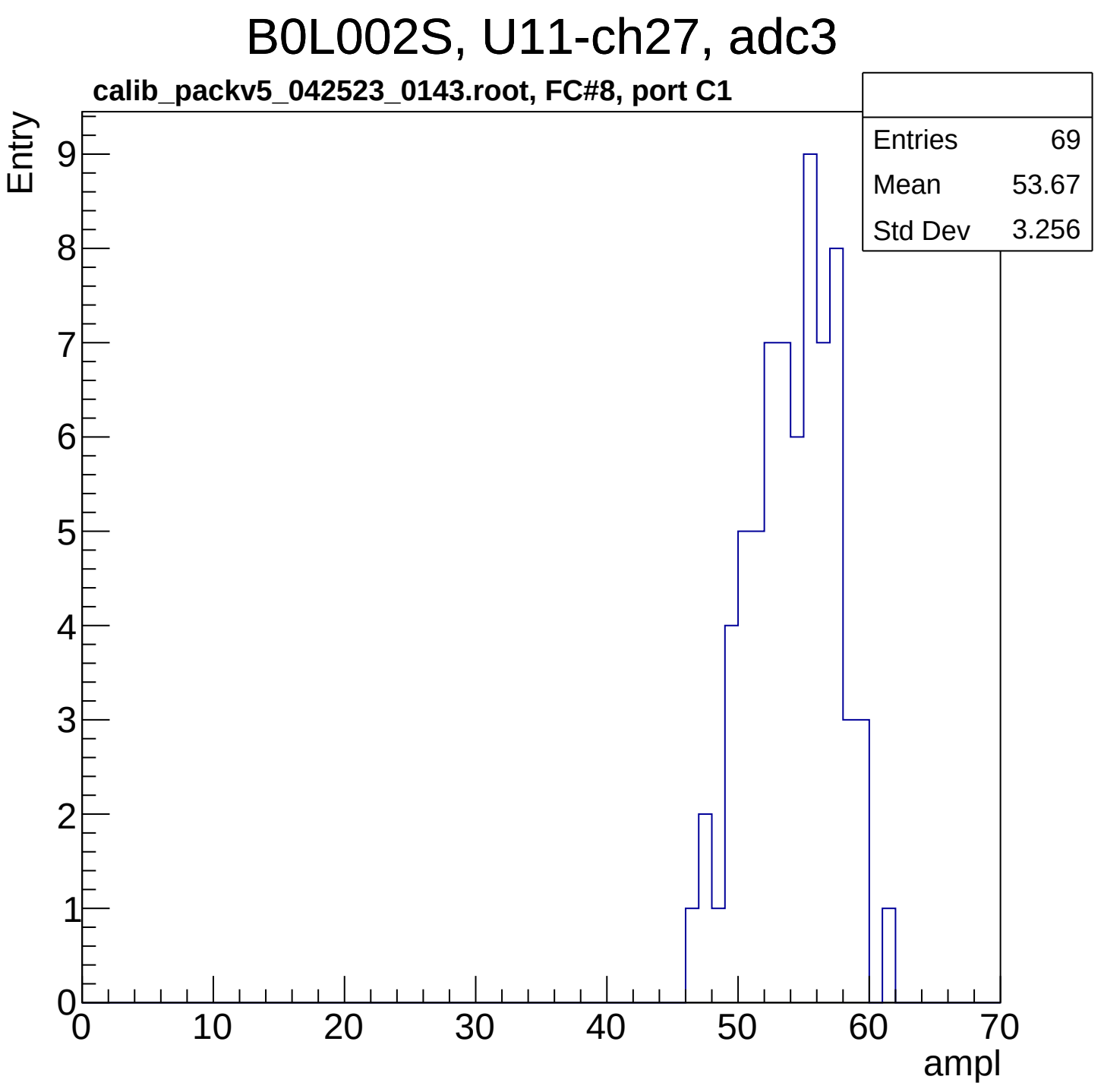
B0L002S, U11-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 53.67
Std Dev 3.256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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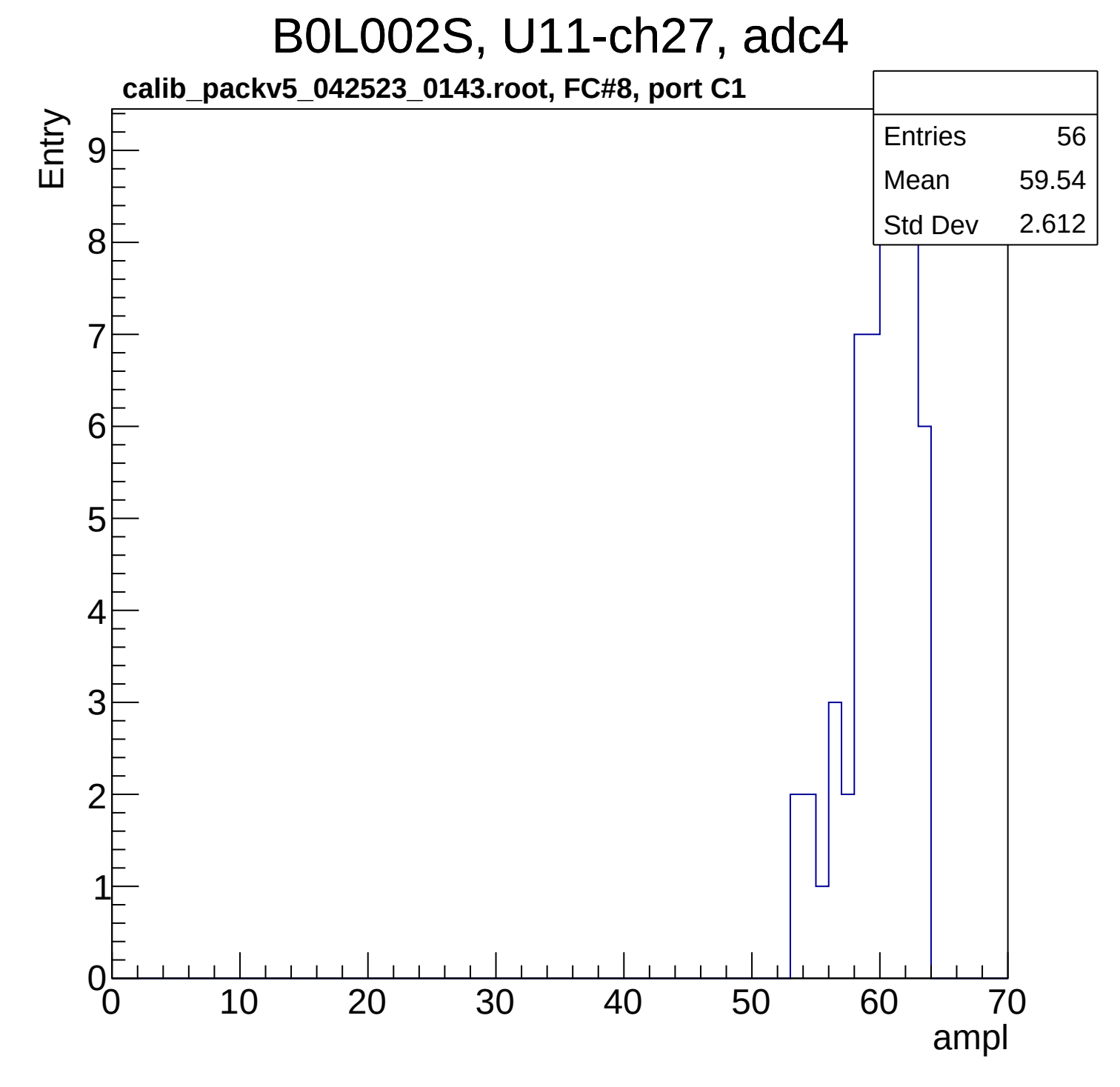
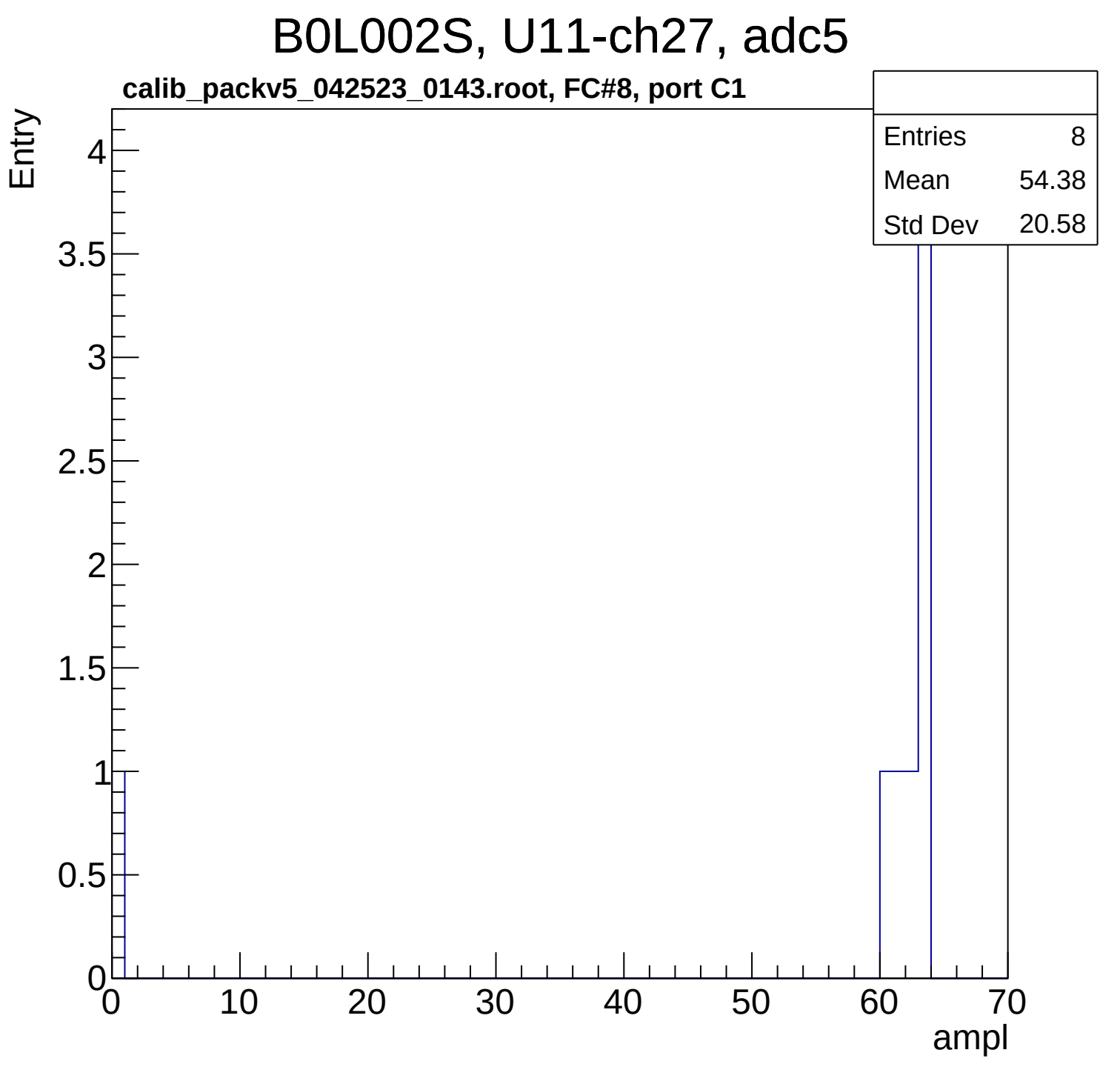

B0L002S, U11-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 59.54
Std Dev 2.612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.38
Std Dev 20.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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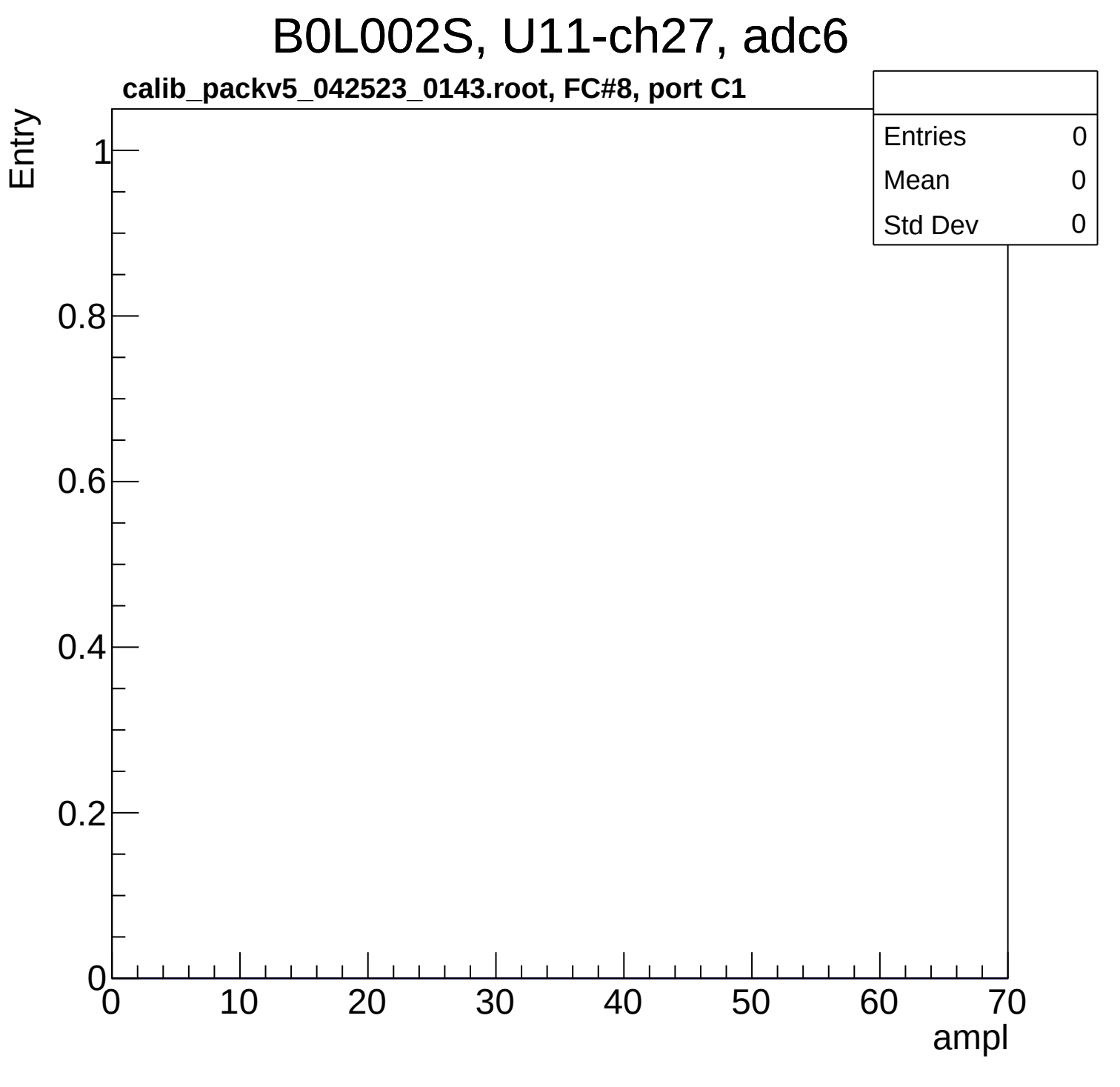
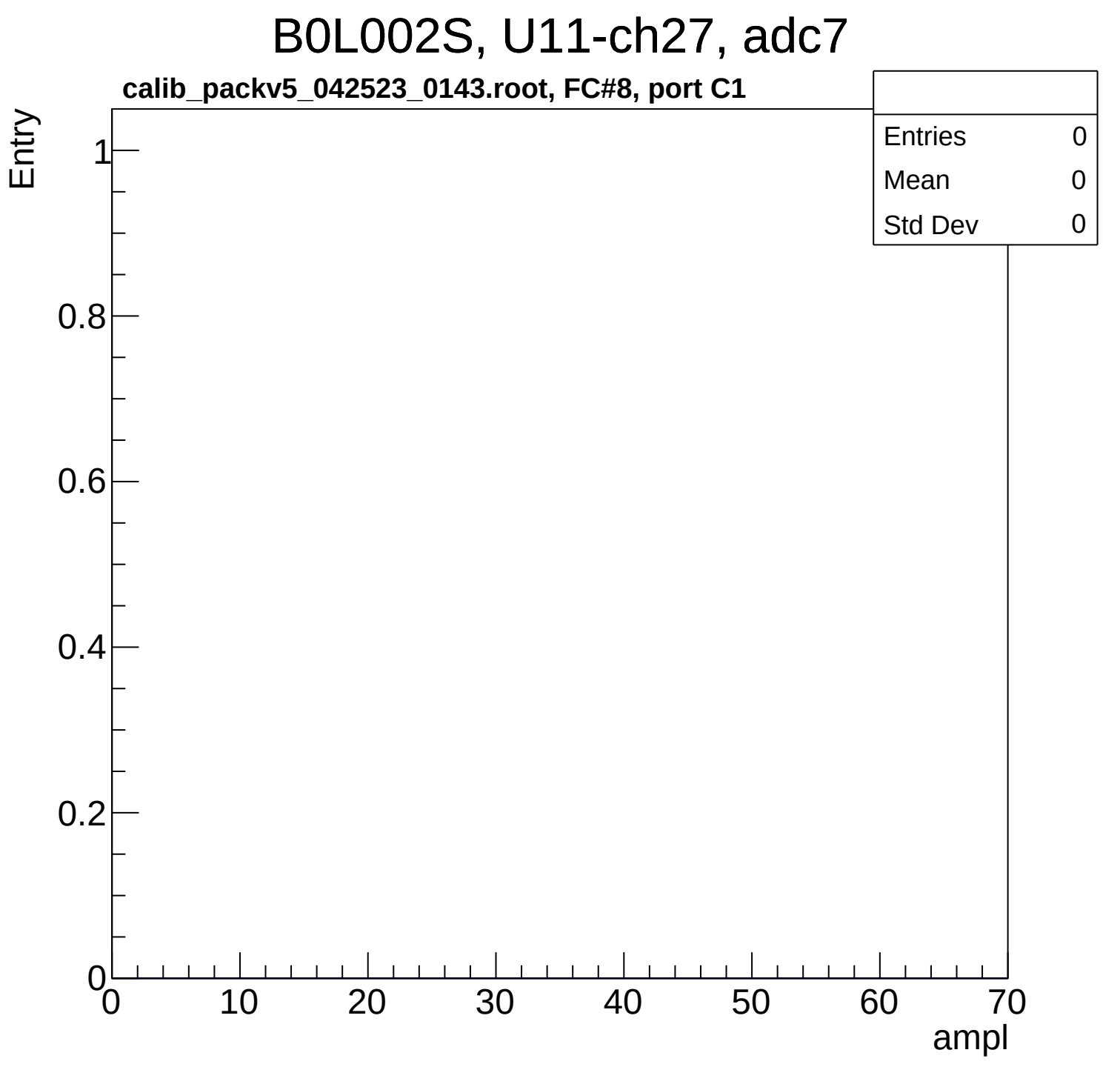

B0L002S, U11-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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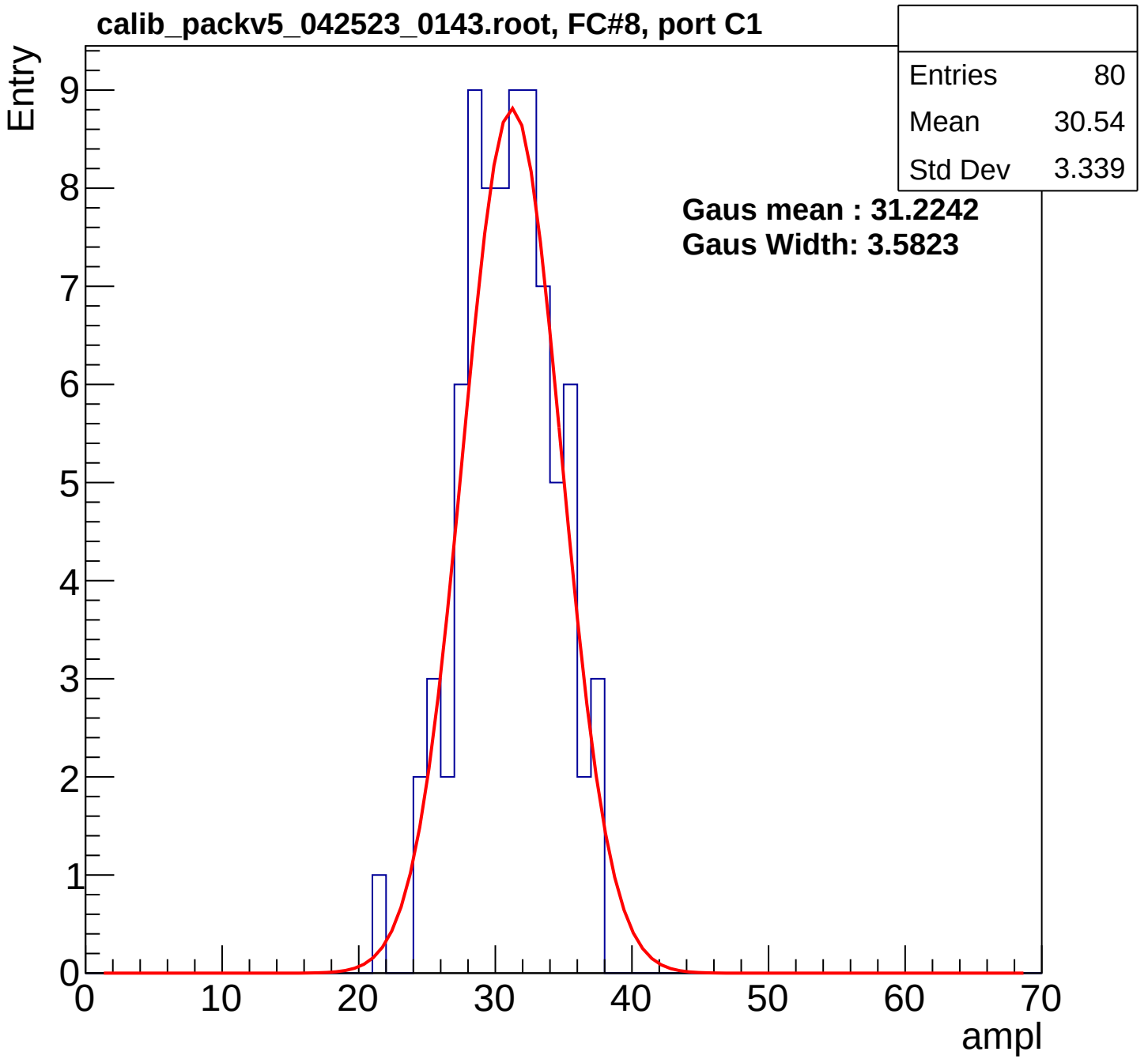
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.54
Std Dev 3.339
Gaus mean : 31.2242
Gaus Width: 3.5823
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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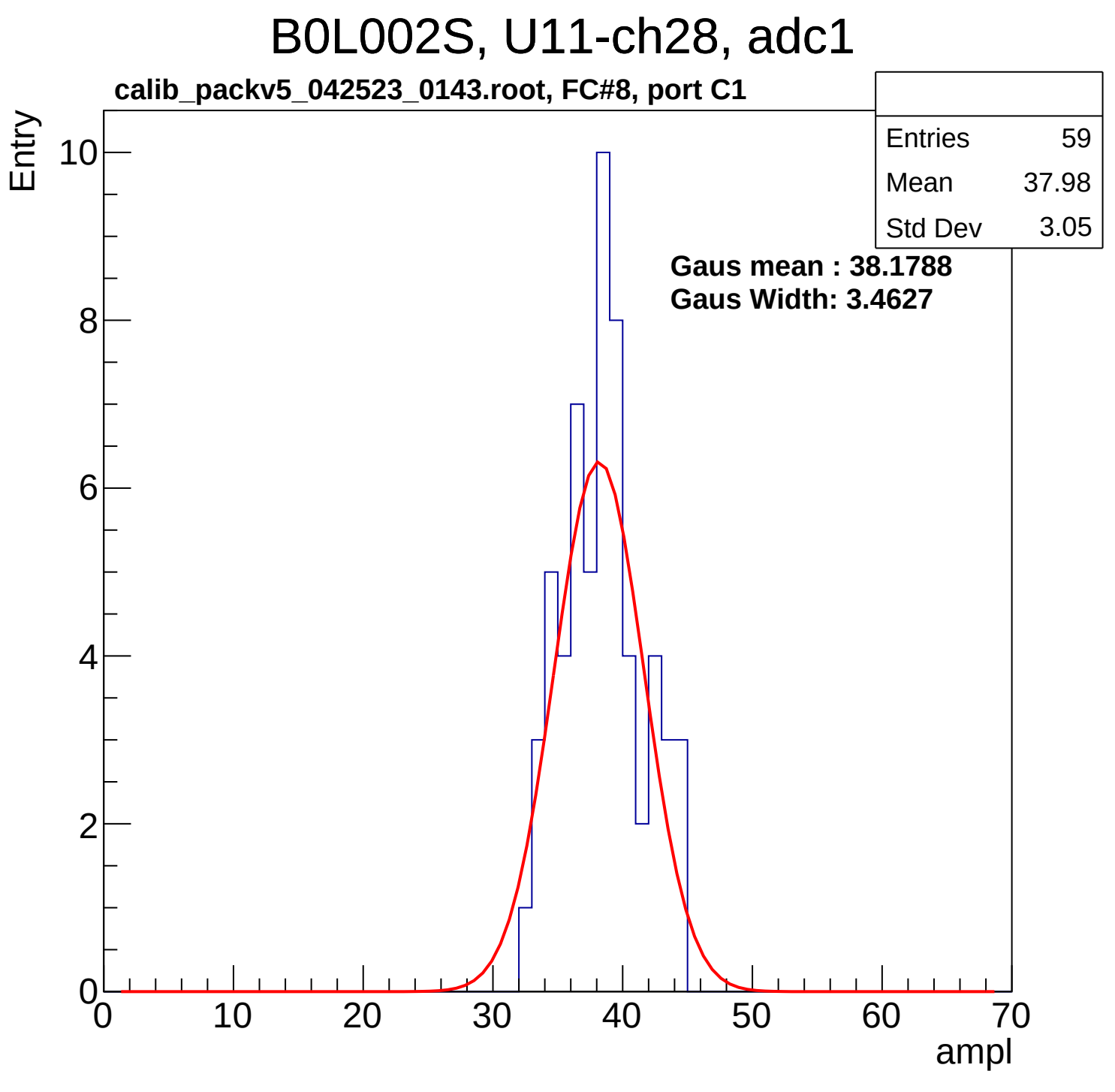
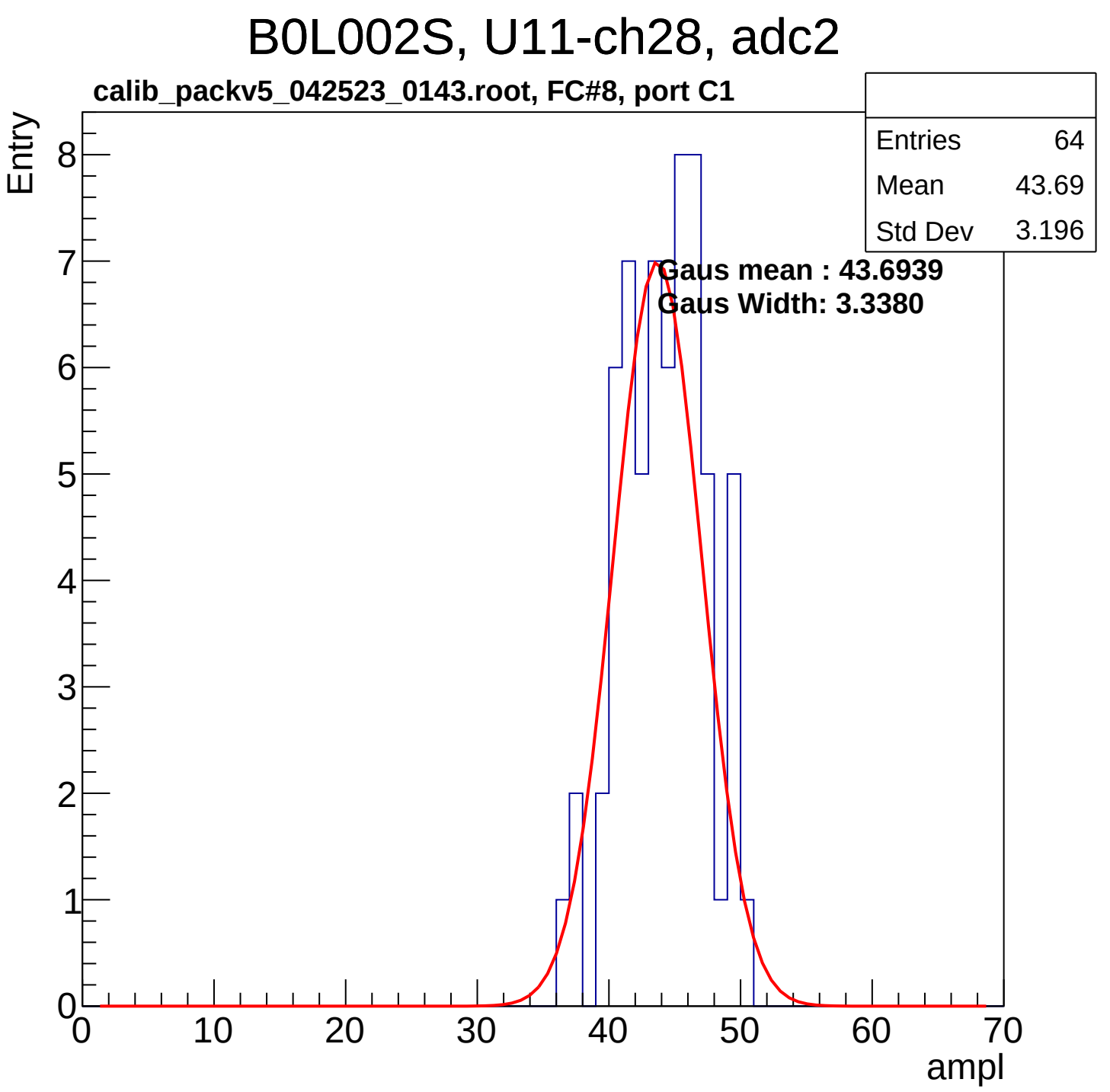

B0L002S, U11-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.98
Std Dev 3.05
Gaus mean : 38.1788
Gaus Width: 3.4627
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.69
Std Dev 3.196
Gaus mean : 43.6939
Gaus Width: 3.3380
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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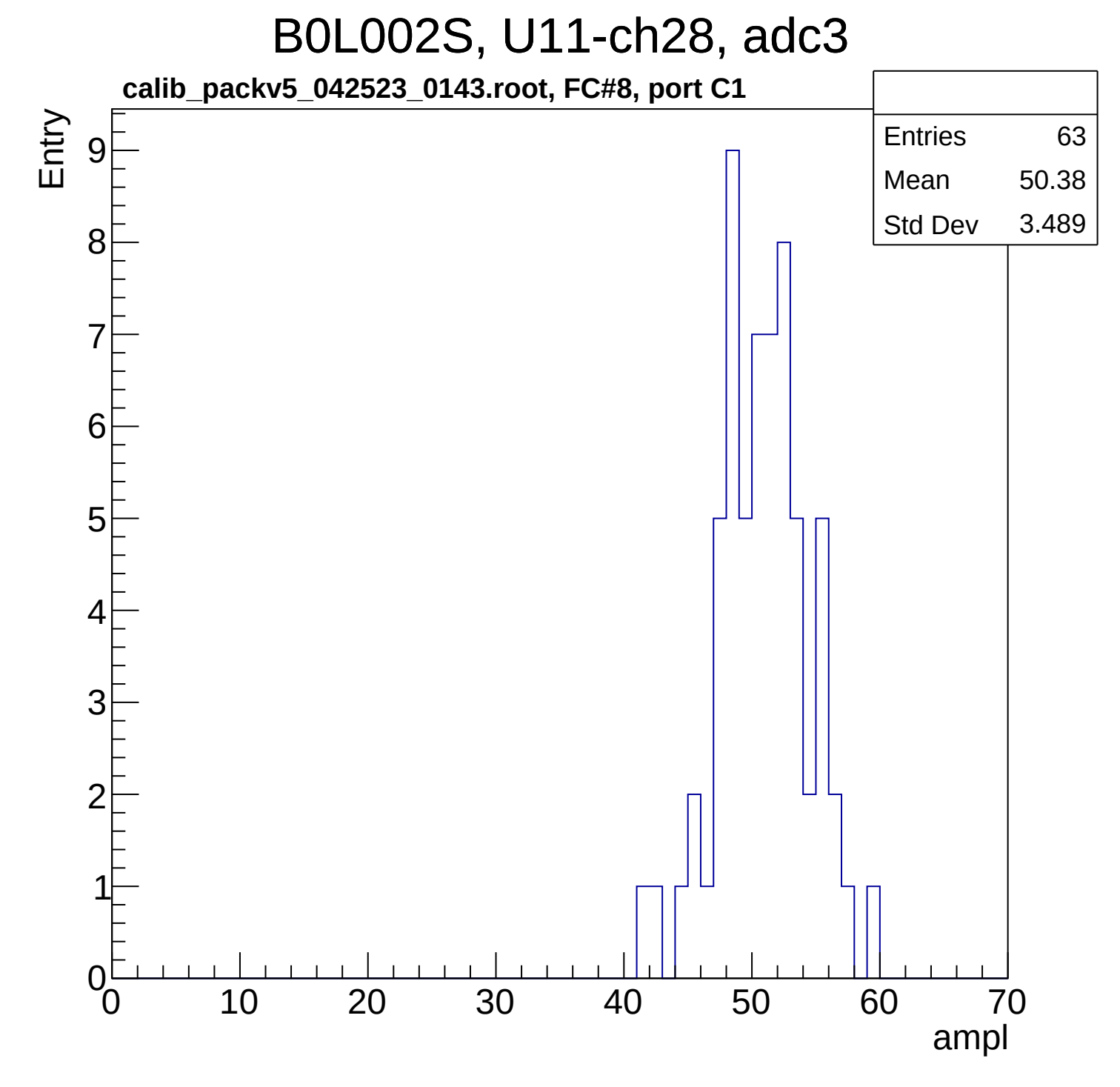
B0L002S, U11-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.38
Std Dev 3.489
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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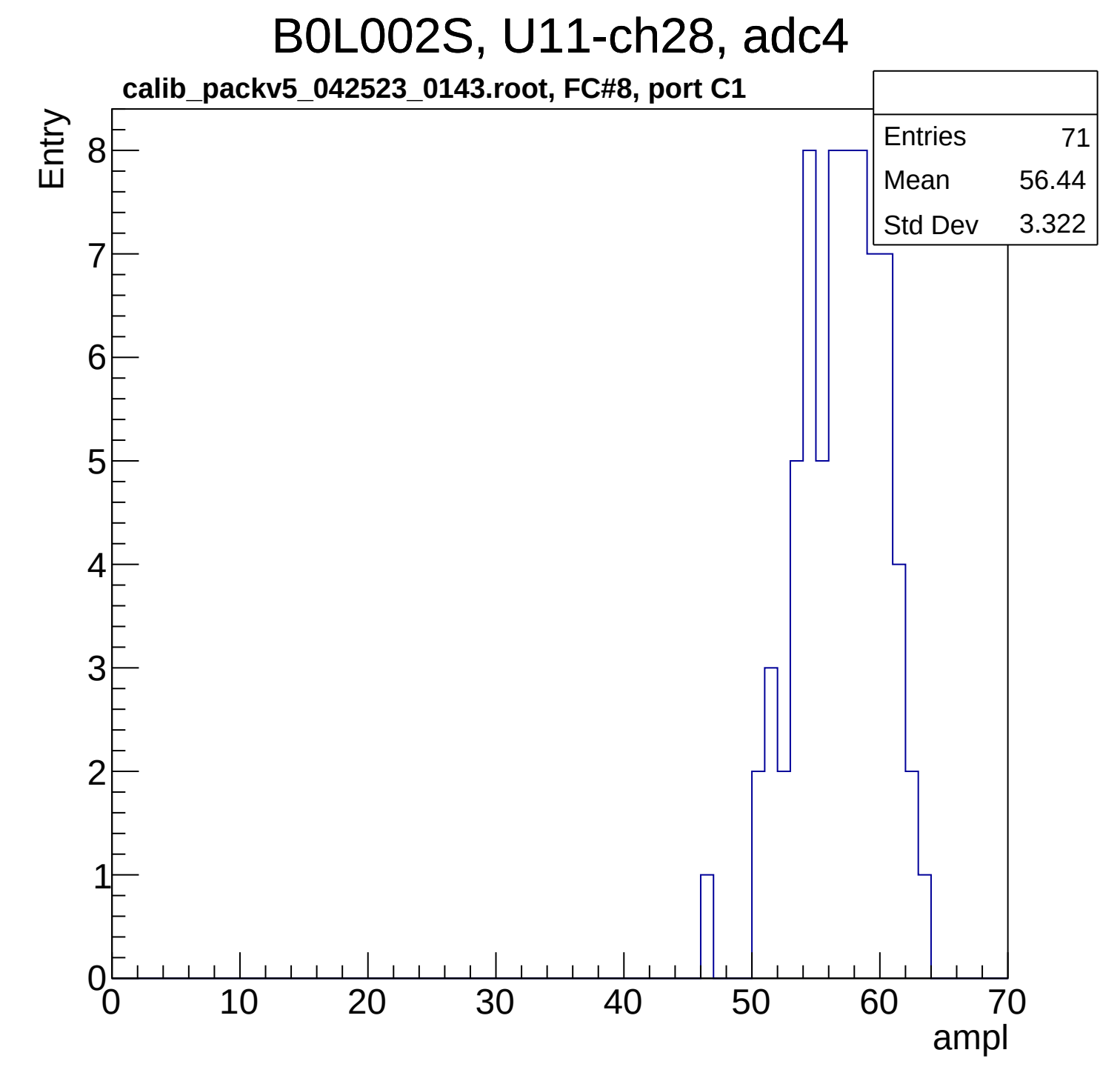
B0L002S, U11-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 56.44
Std Dev 3.322
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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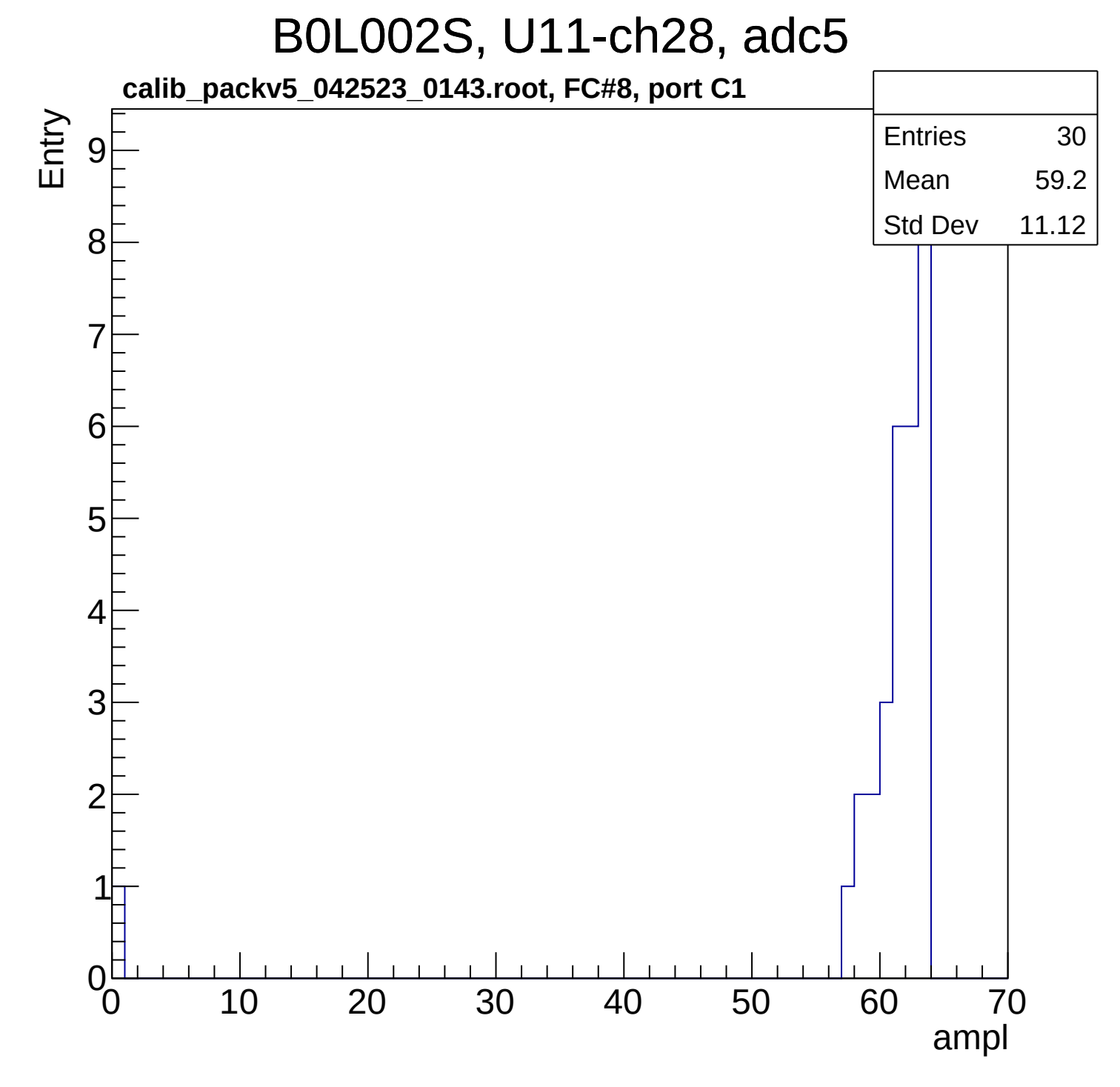
B0L002S, U11-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.2
Std Dev 11.12
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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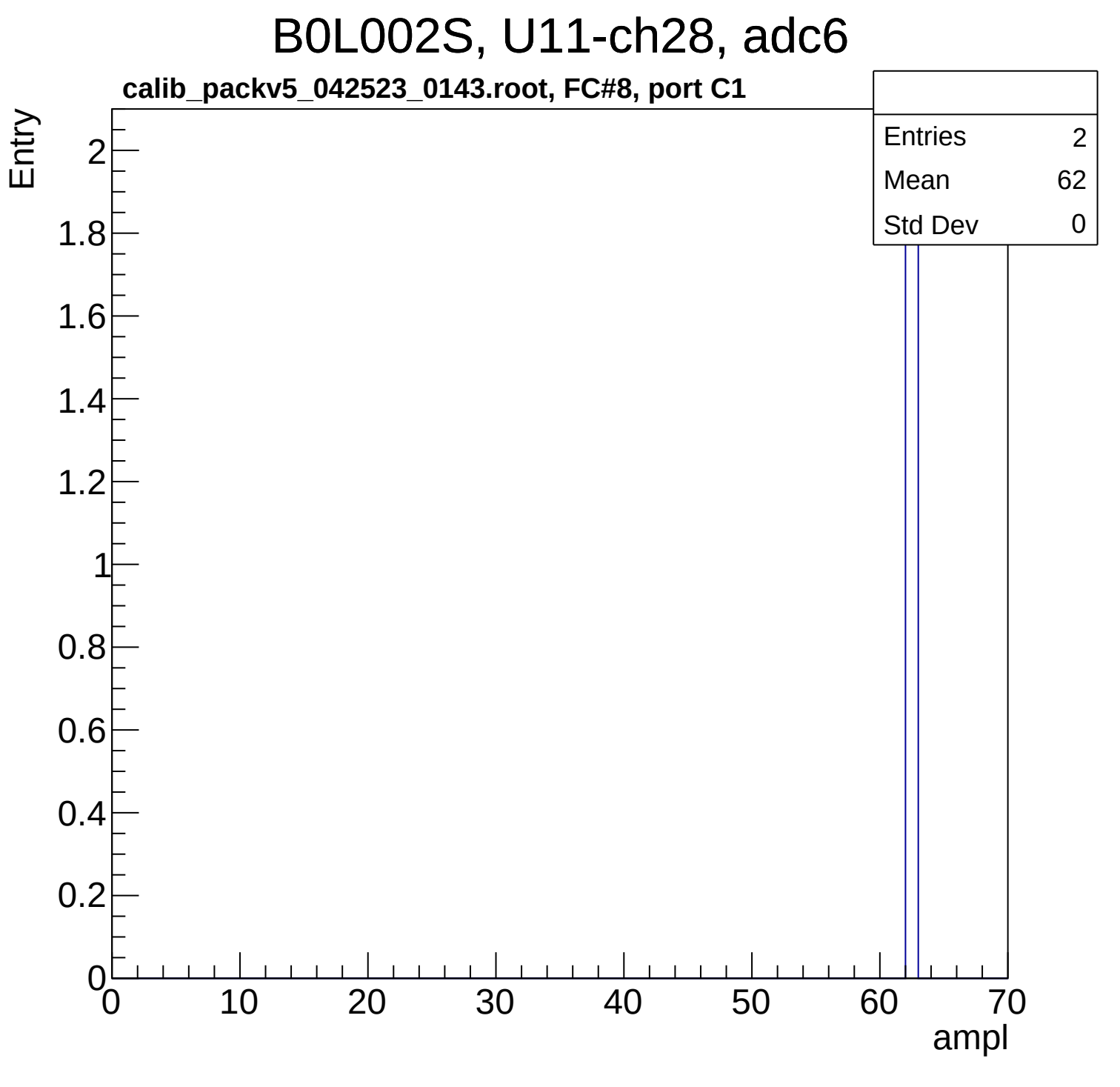
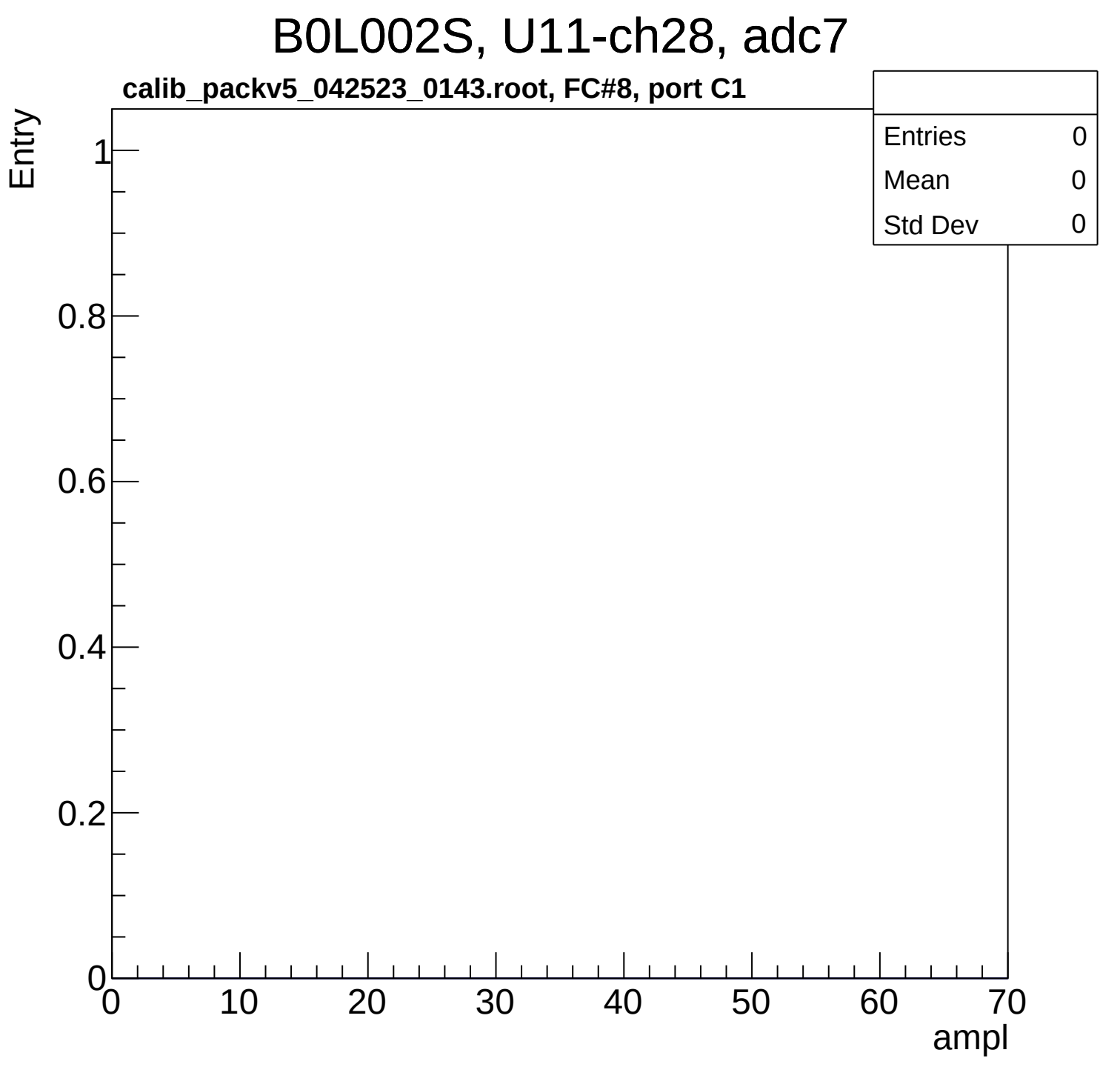

B0L002S, U11-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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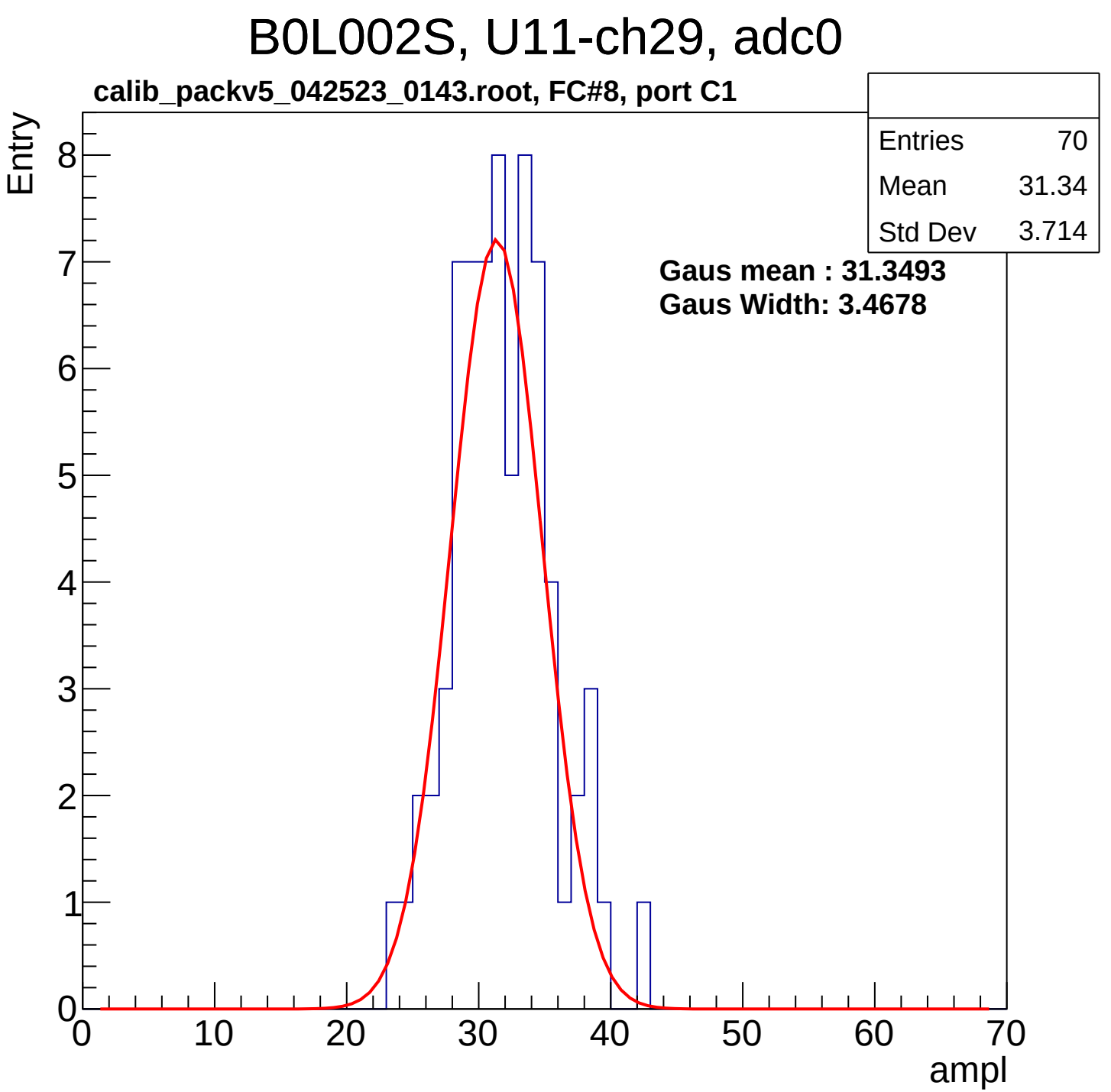
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.34
Std Dev 3.714
Gaus mean : 31.3493
Gaus Width: 3.4678
Entry
ampl
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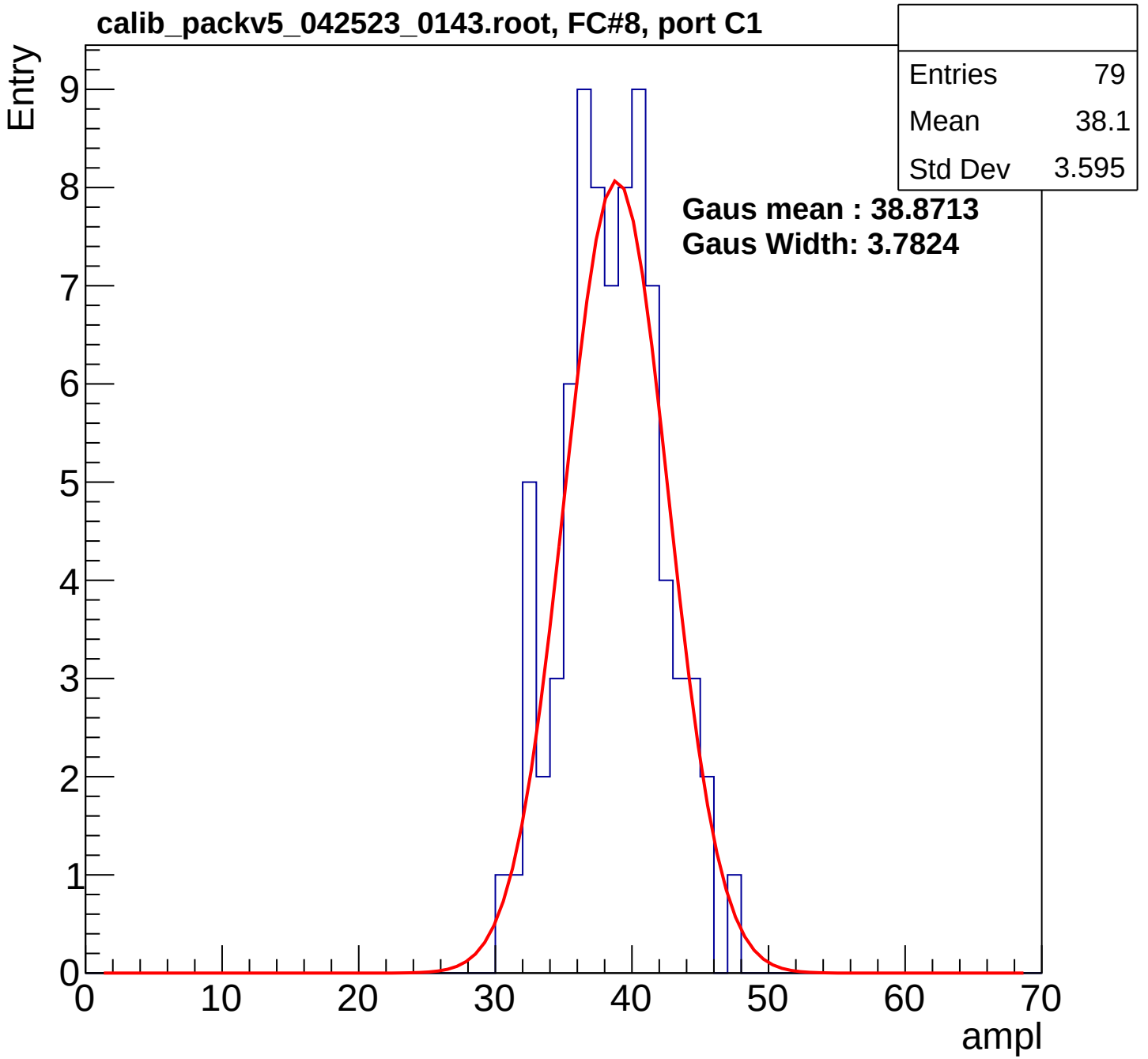
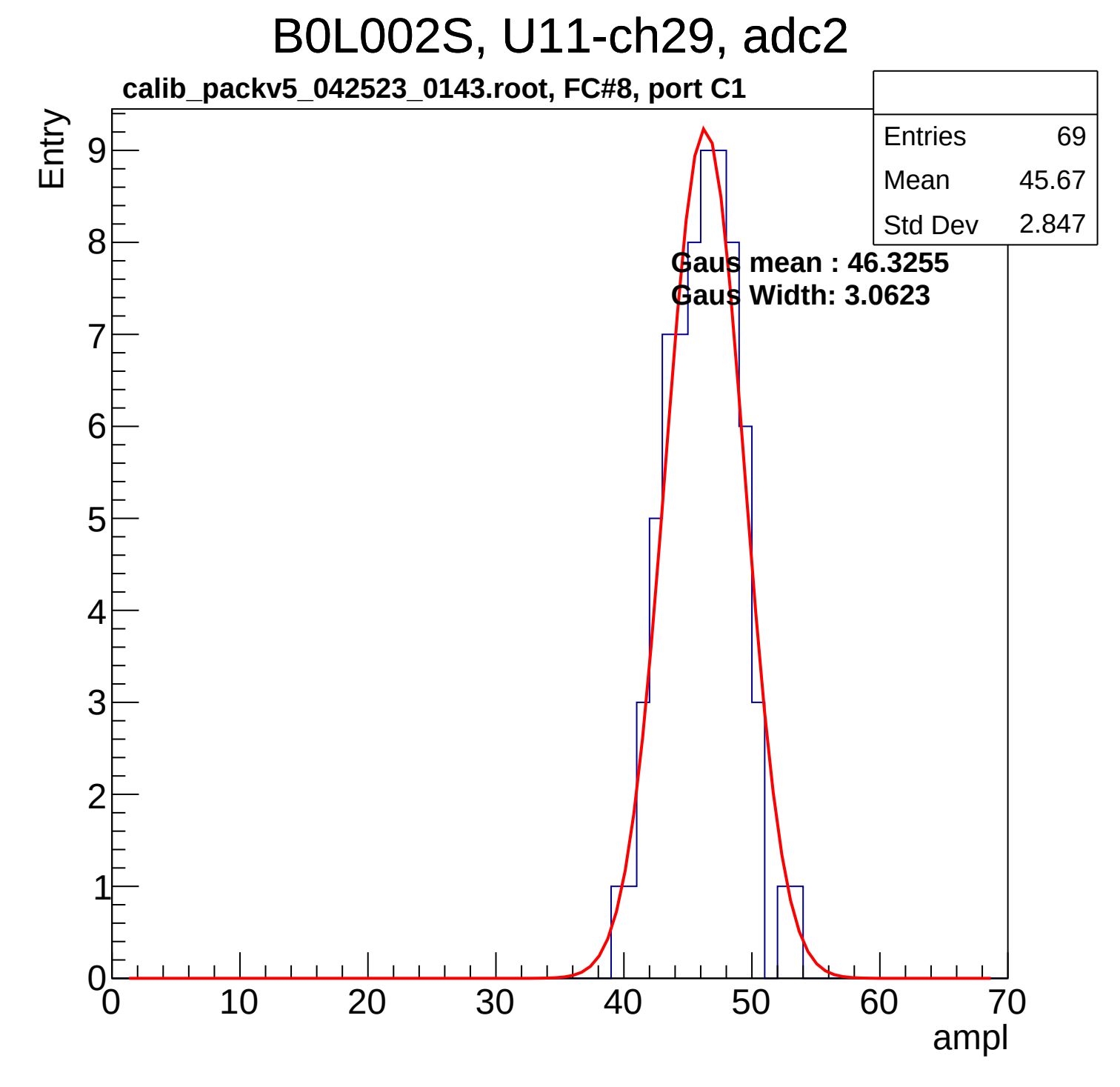
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 38.1
Std Dev 3.595
Gaus mean : 38.8713
Gaus Width: 3.7824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.67
Std Dev 2.847
Gaus mean : 46.3255
Gaus Width: 3.0623
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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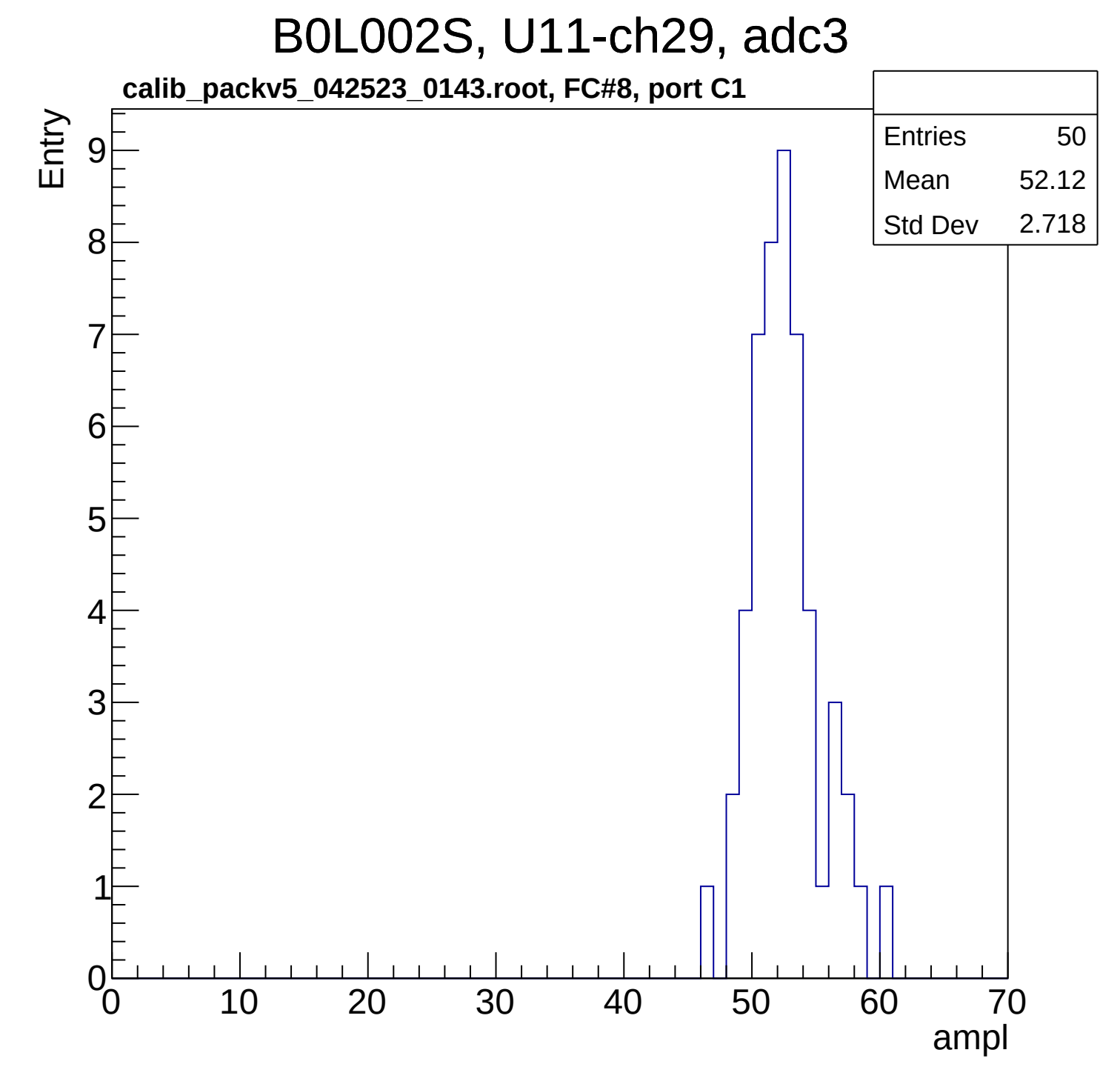
B0L002S, U11-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 52.12
Std Dev 2.718
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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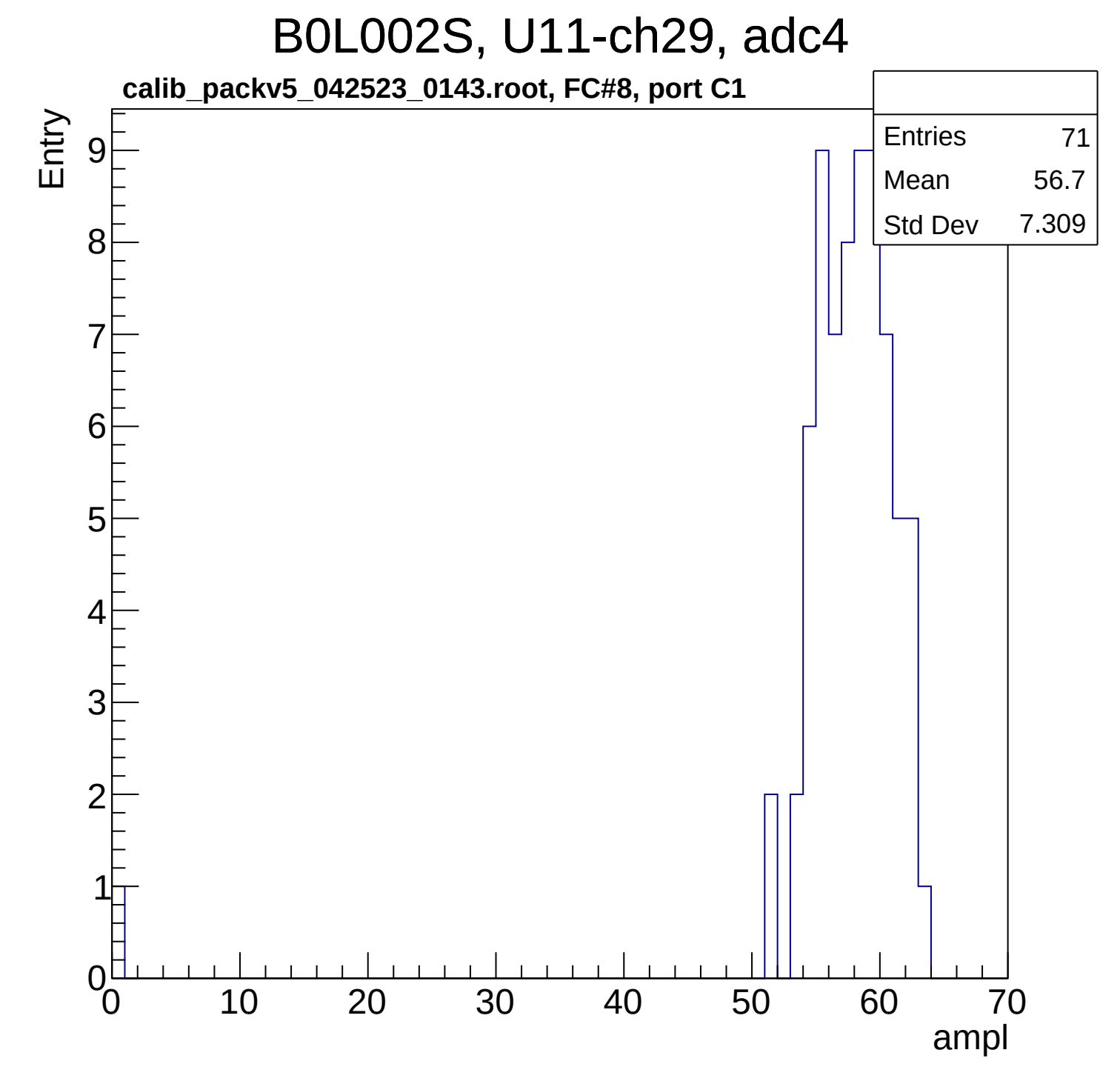
B0L002S, U11-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 56.7
Std Dev 7.309
Entry
ampl
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3
4
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6
7
8
9
0
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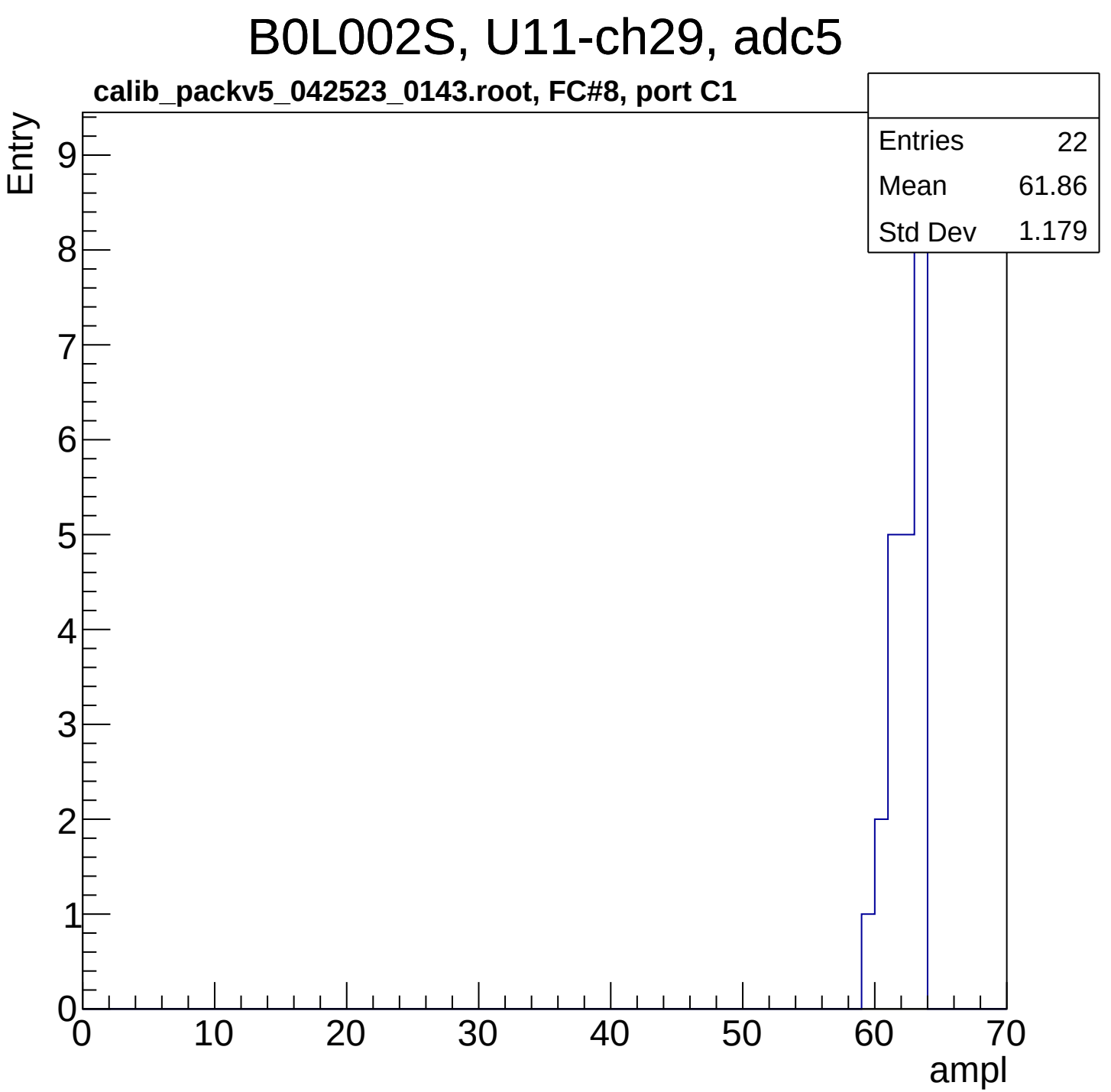
B0L002S, U11-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.86
Std Dev 1.179
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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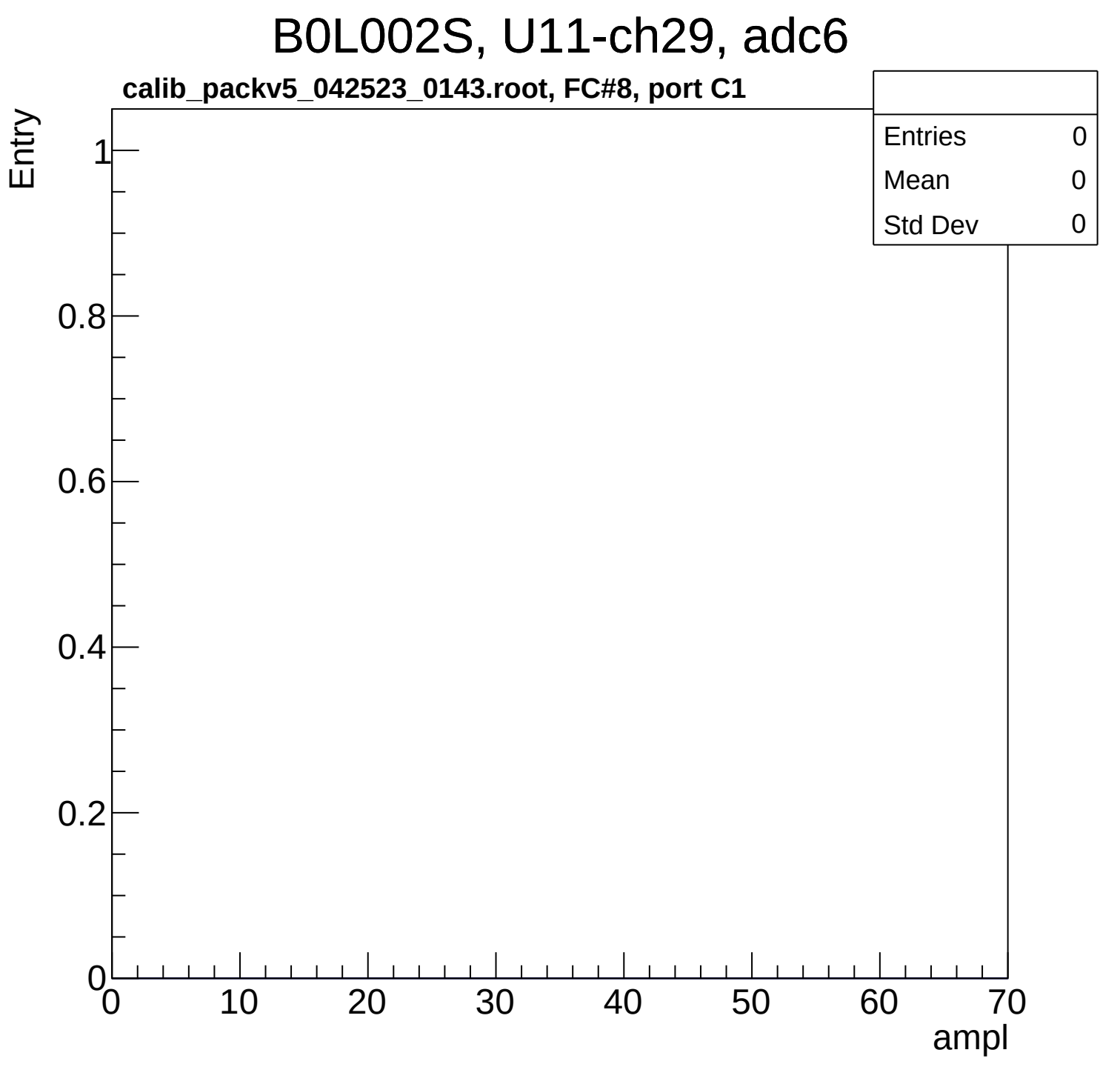
B0L002S, U11-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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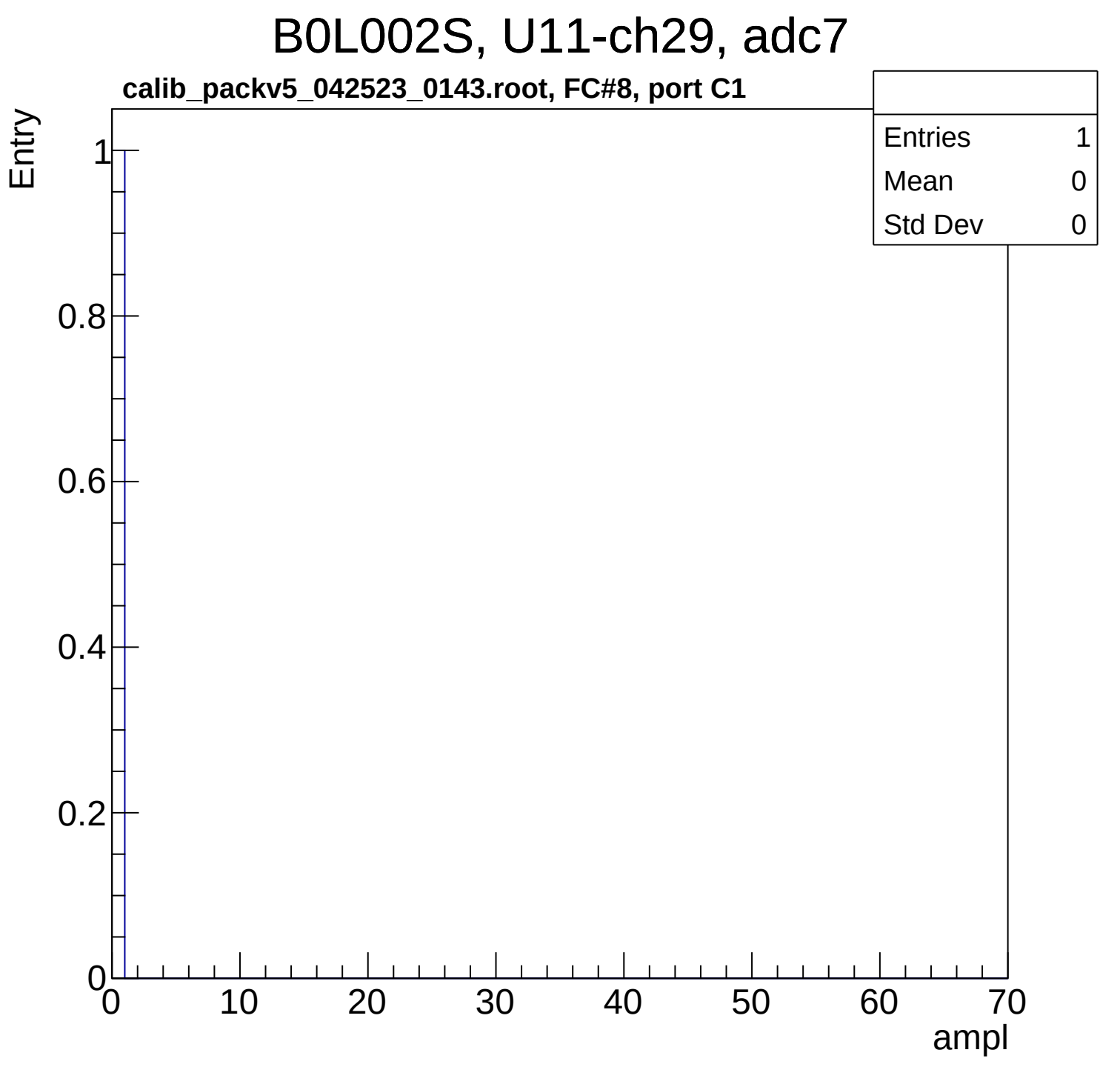
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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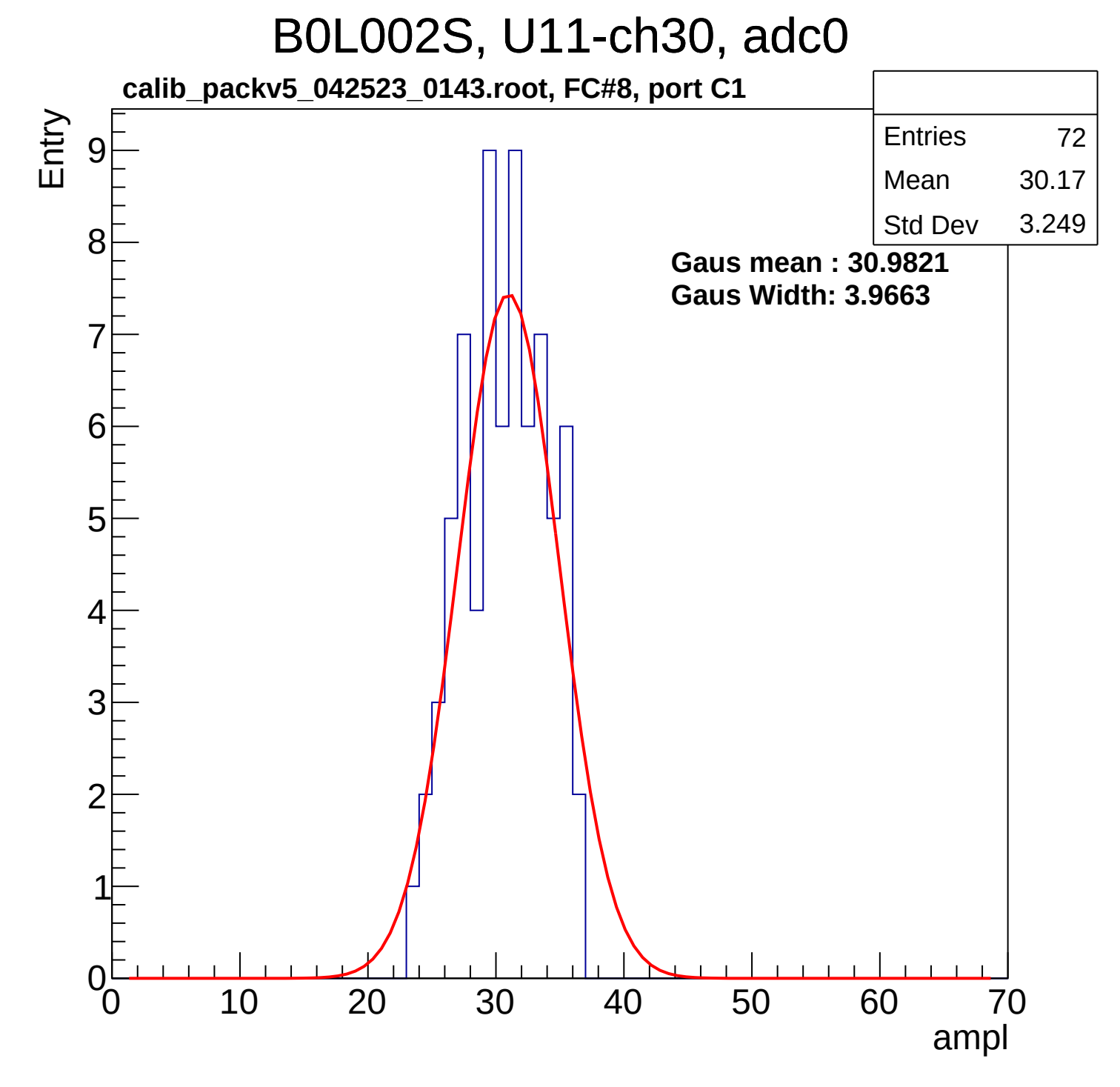
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.17
Std Dev 3.249
Gaus mean : 30.9821
Gaus Width: 3.9663
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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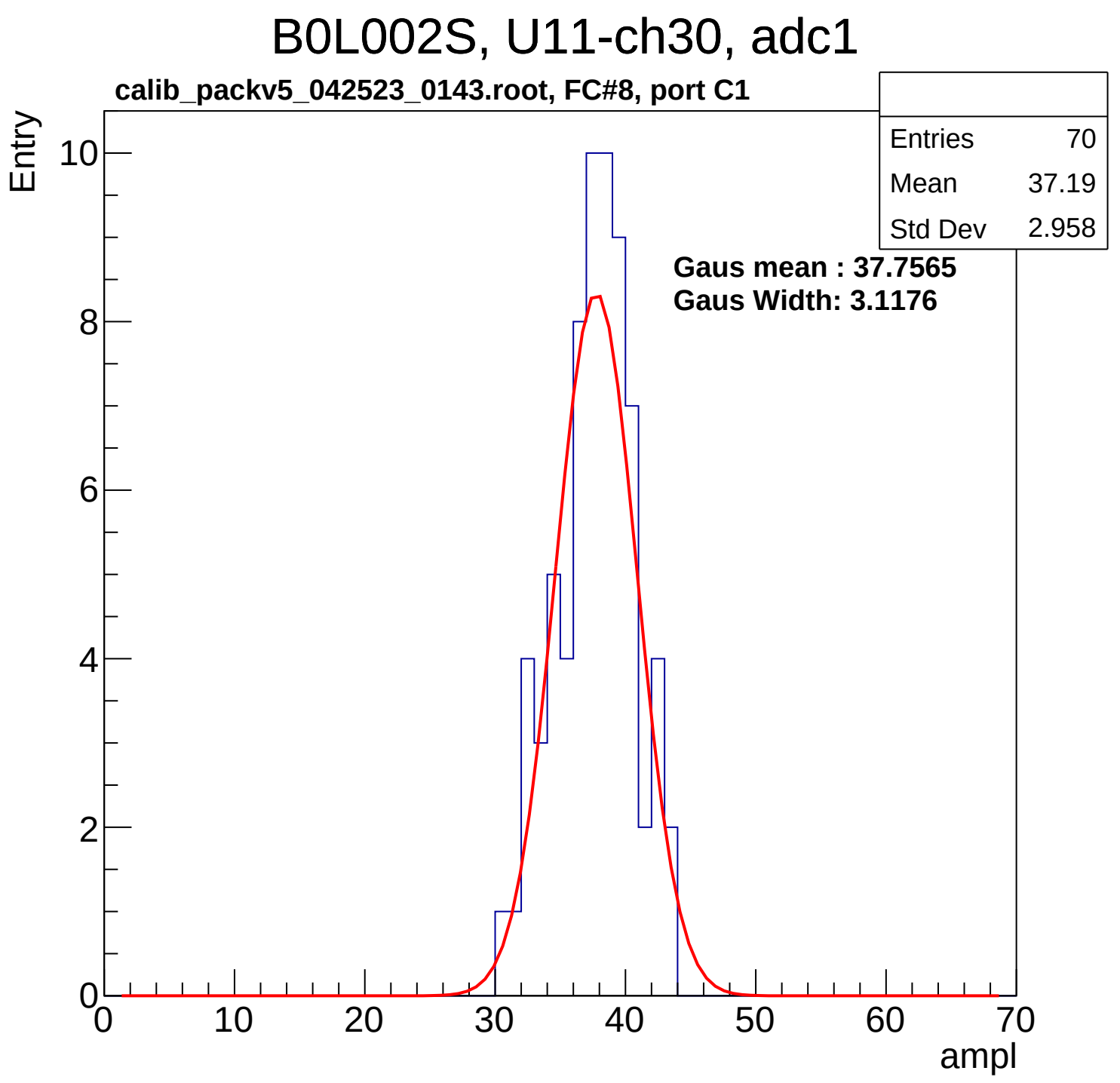
B0L002S, U11-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.19
Std Dev 2.958
Gaus mean : 37.7565
Gaus Width: 3.1176
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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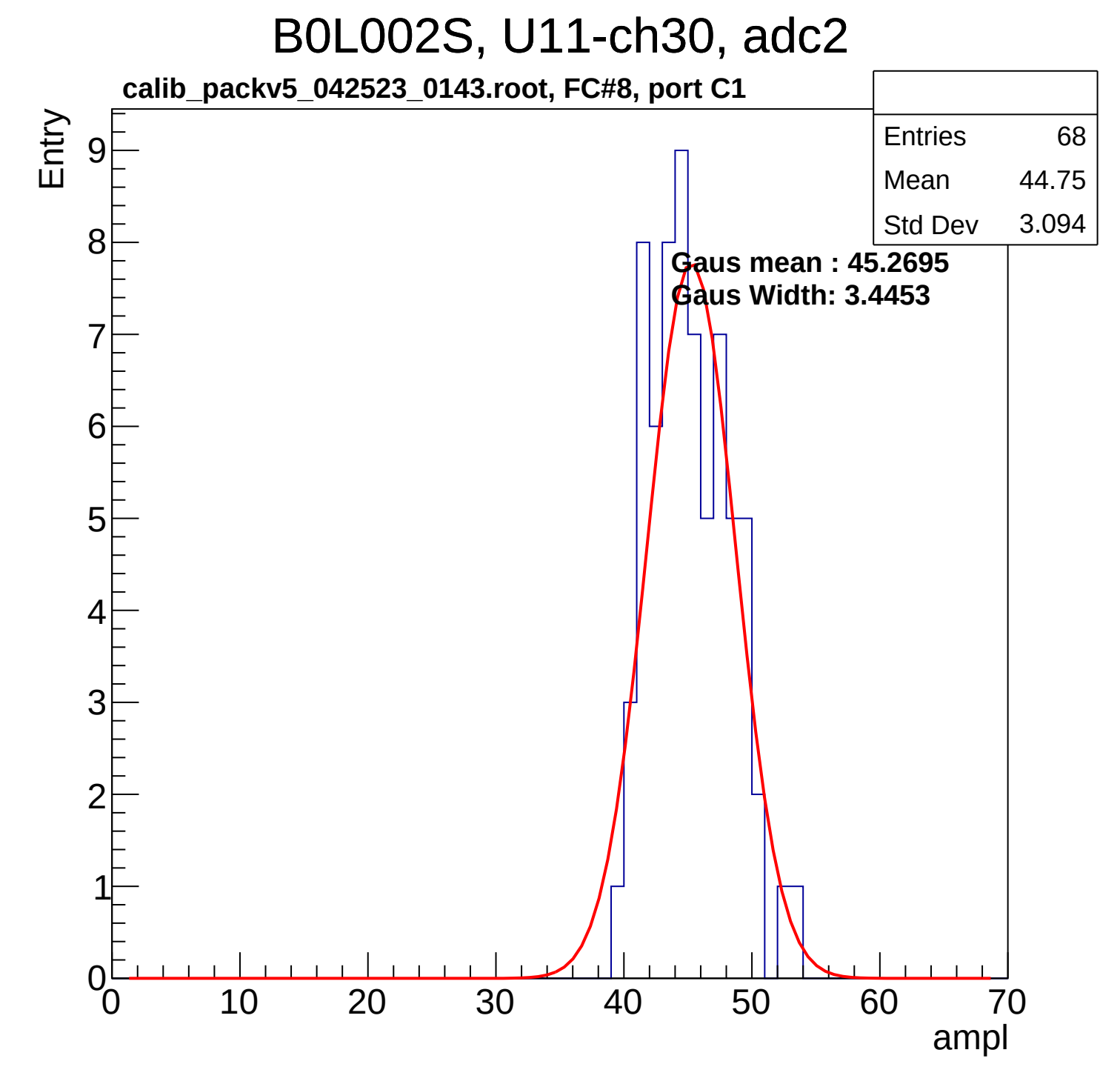
B0L002S, U11-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.75
Std Dev 3.094
Gaus mean : 45.2695
Gaus Width: 3.4453
Entry
ampl
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2
3
4
5
6
7
8
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0
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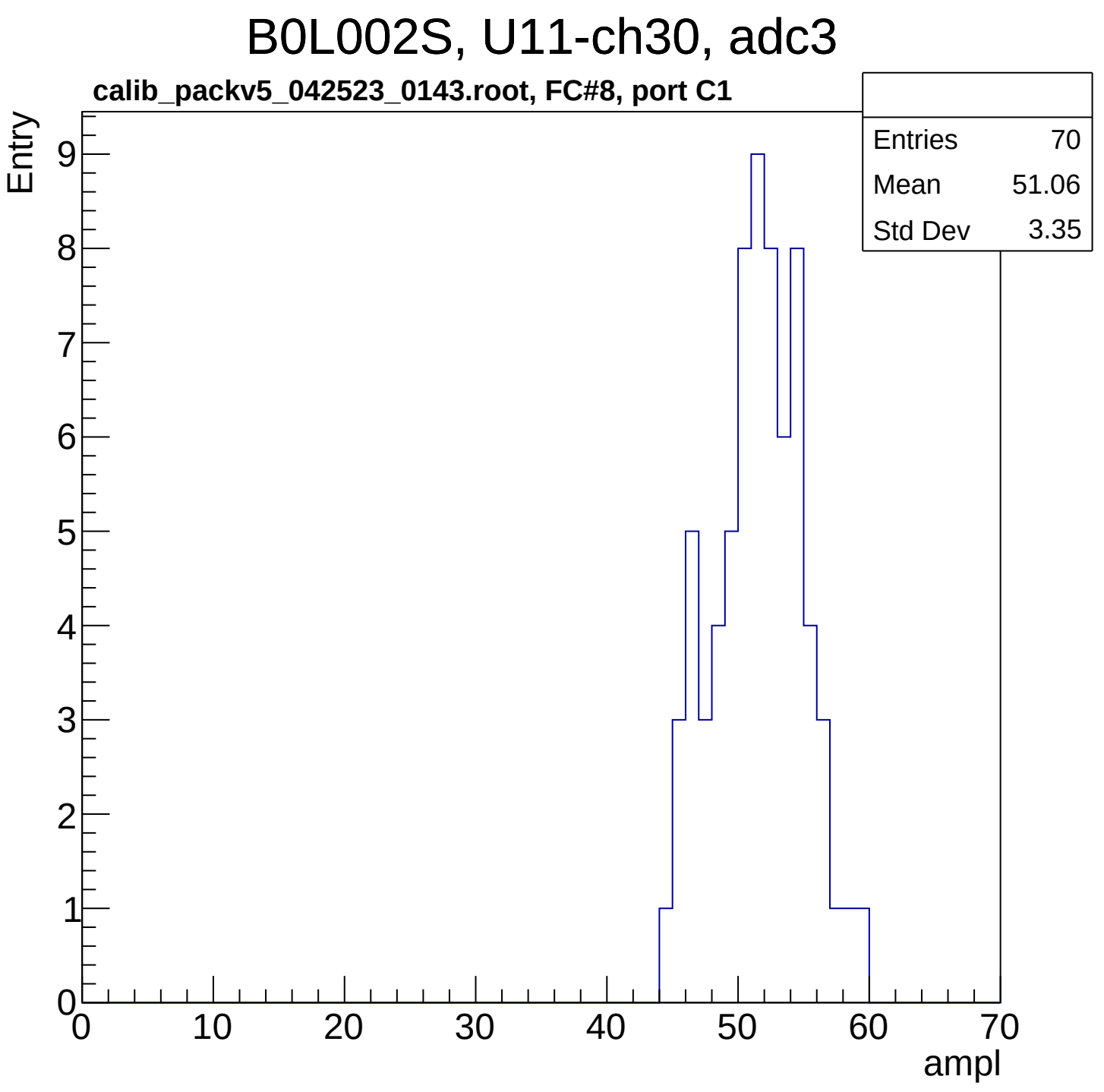
B0L002S, U11-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.06
Std Dev 3.35
Entry
ampl
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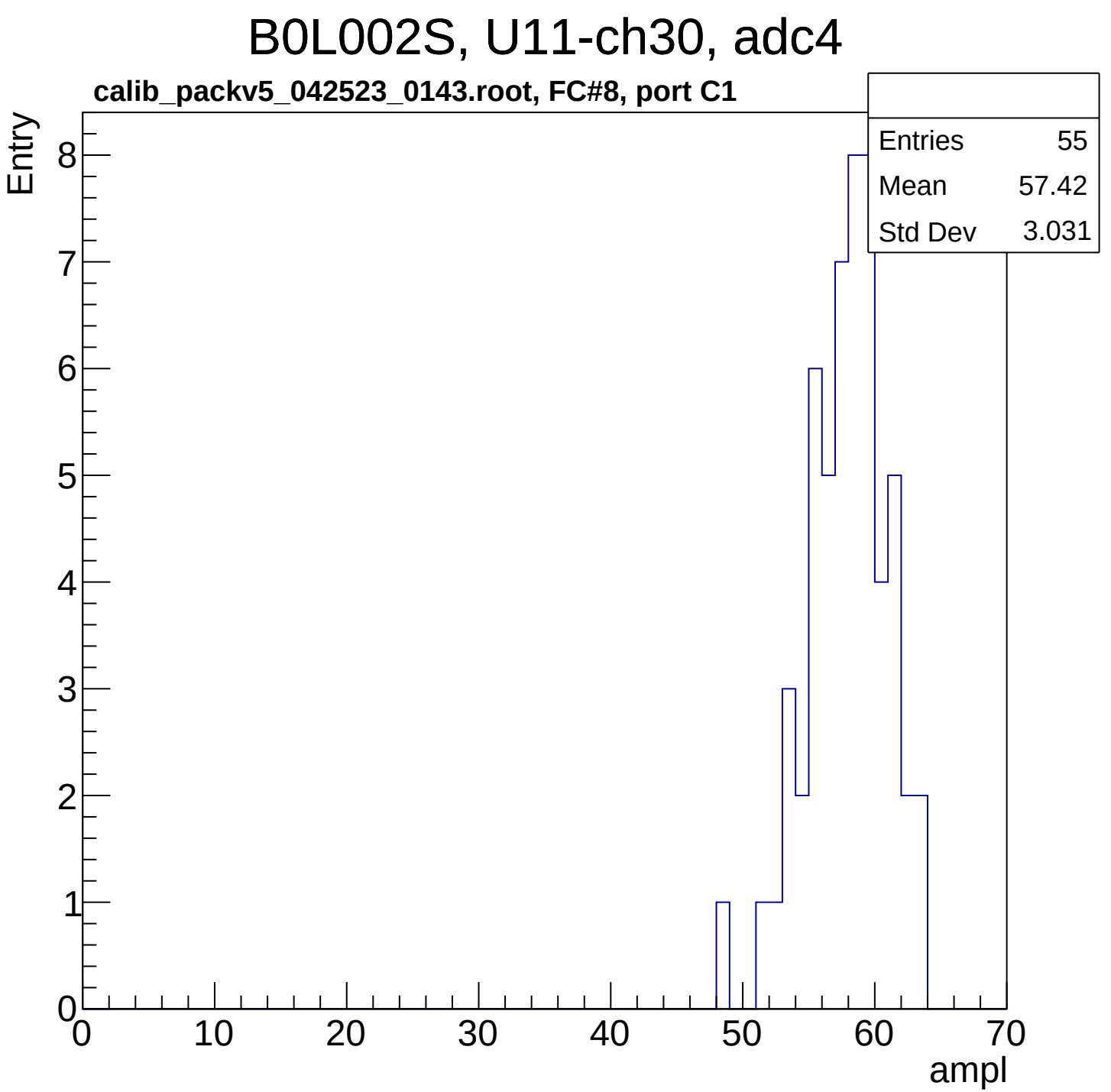
B0L002S, U11-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.42
Std Dev 3.031
Entry
ampl
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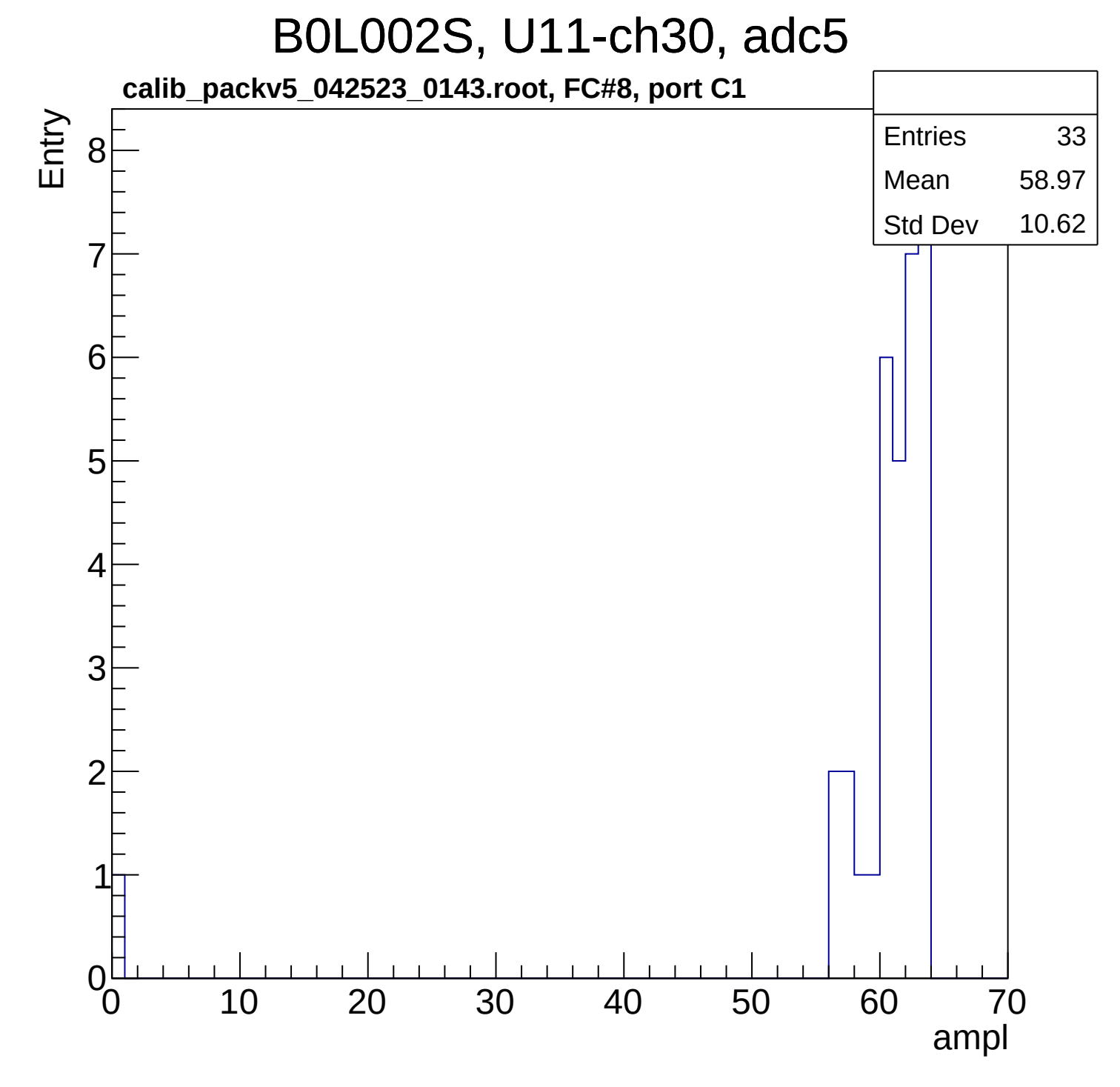
B0L002S, U11-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.97
Std Dev 10.62
Entry
ampl
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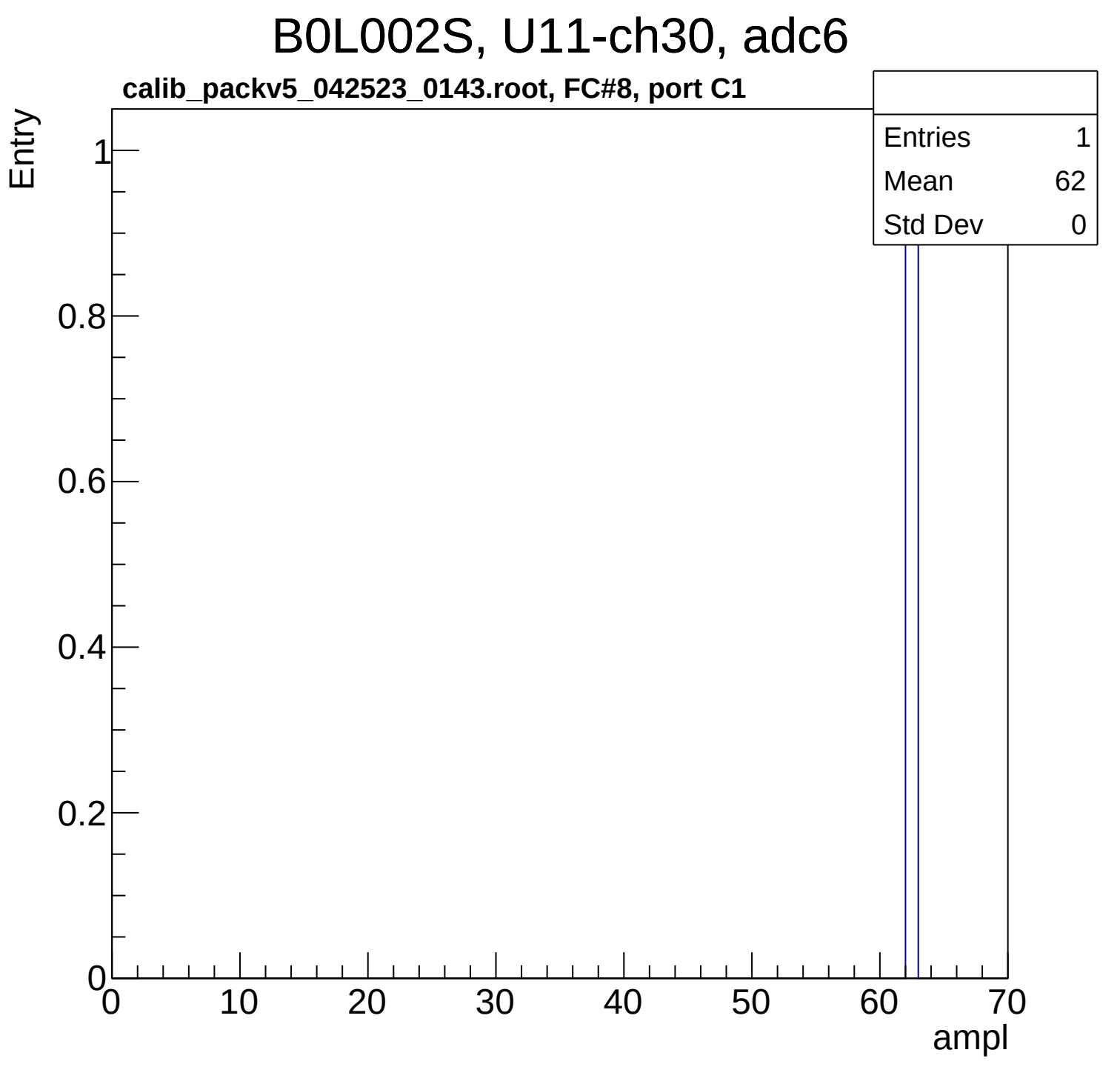
B0L002S, U11-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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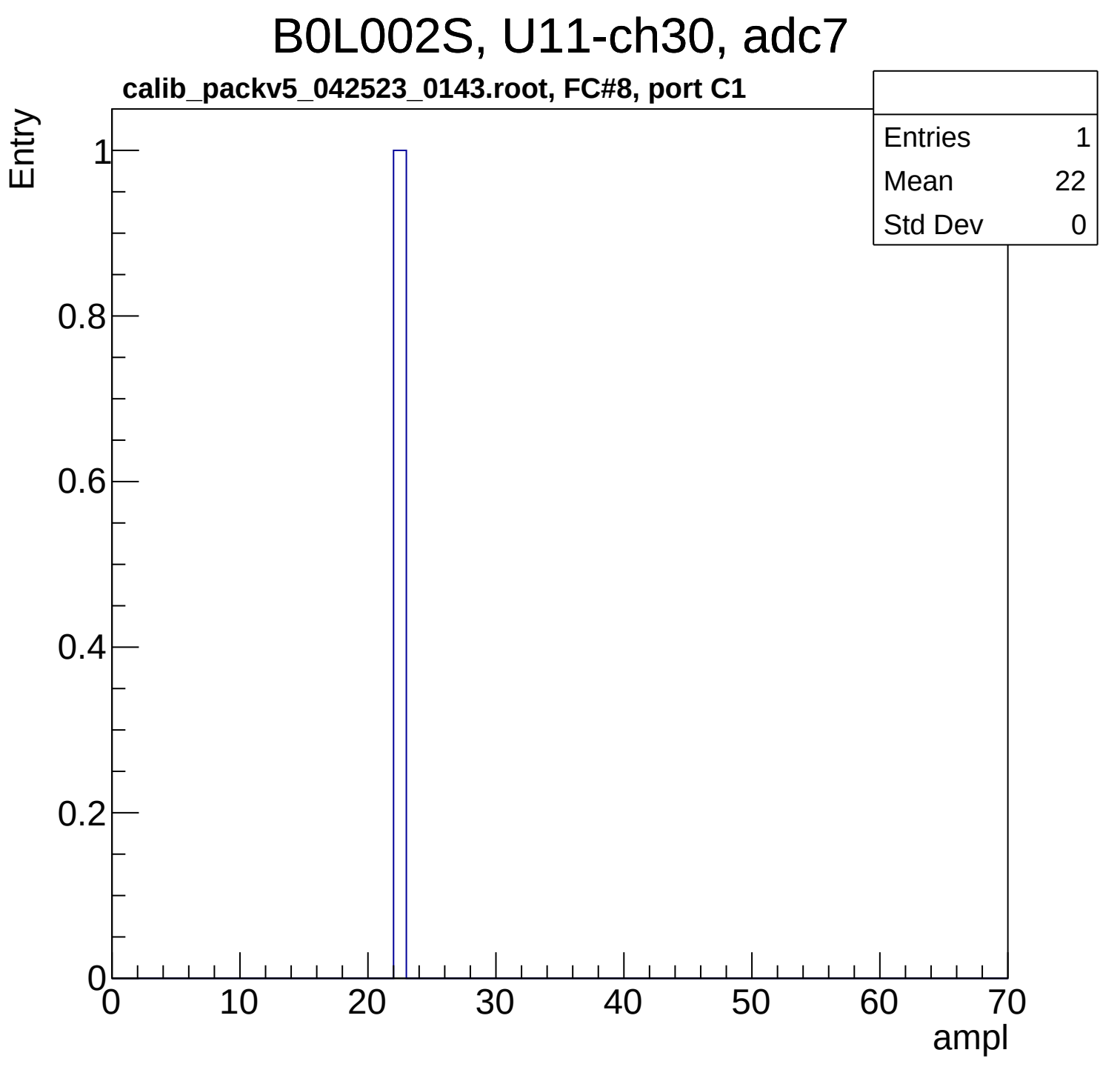
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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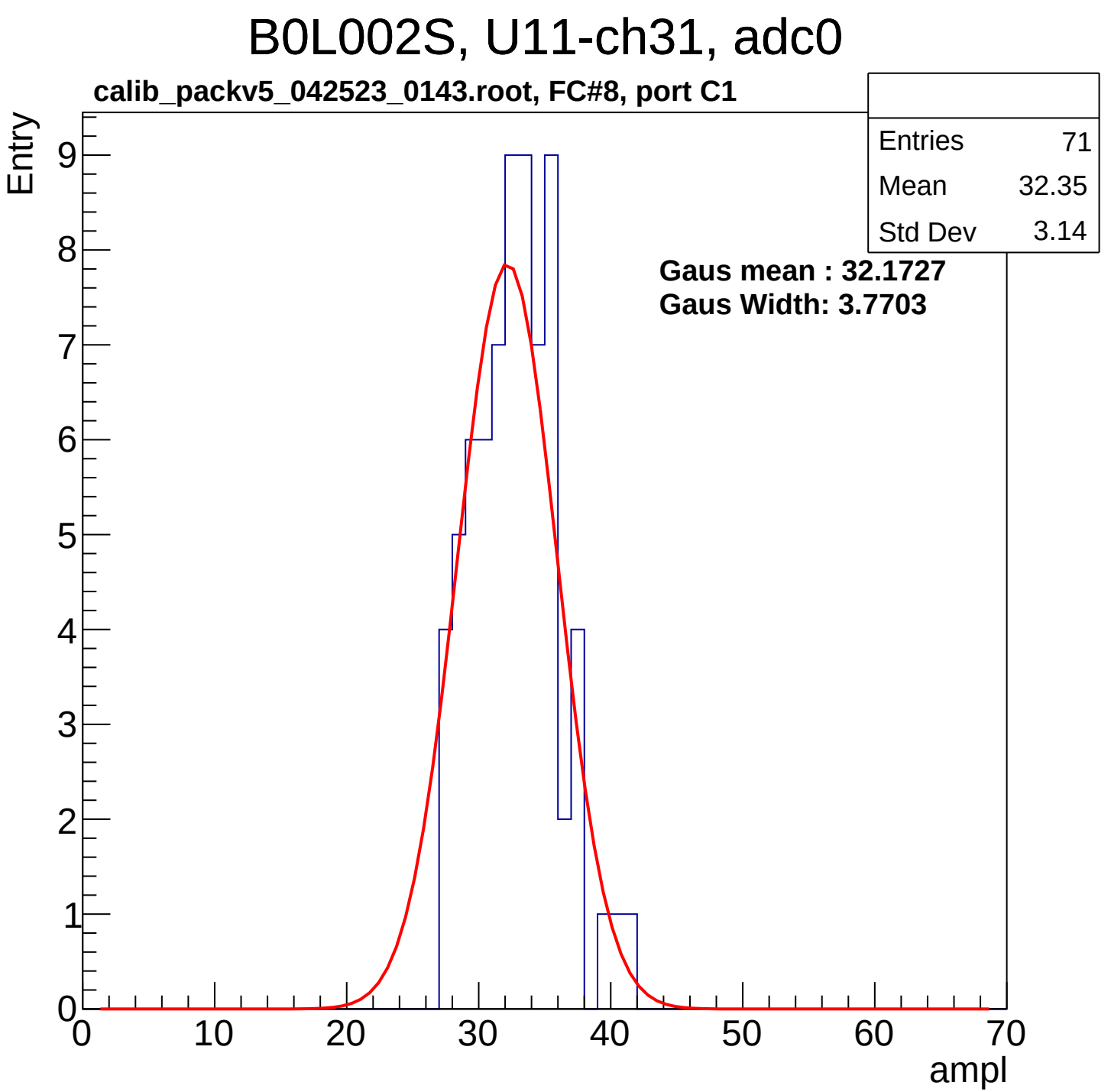
50
60
70
ampl


B0L002S, U11-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 32.35
Std Dev 3.14
Gaus mean : 32.1727
Gaus Width: 3.7703
Entry
ampl
0
1
2
3
4
5
6
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8
9
0
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60
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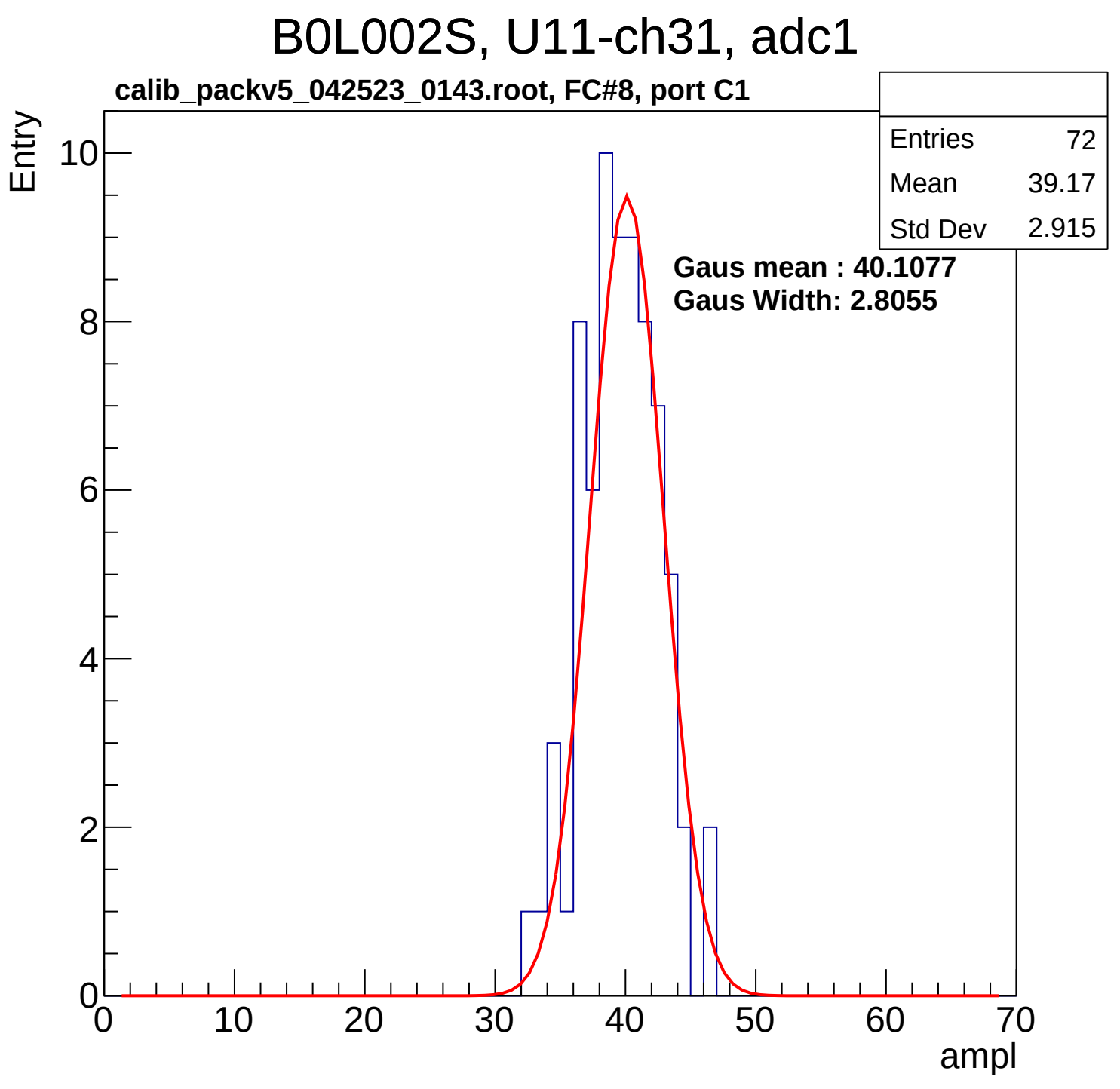
B0L002S, U11-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 39.17
Std Dev 2.915
Gaus mean : 40.1077
Gaus Width: 2.8055
Entry
ampl
0
2
4
6
8
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0
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40
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60
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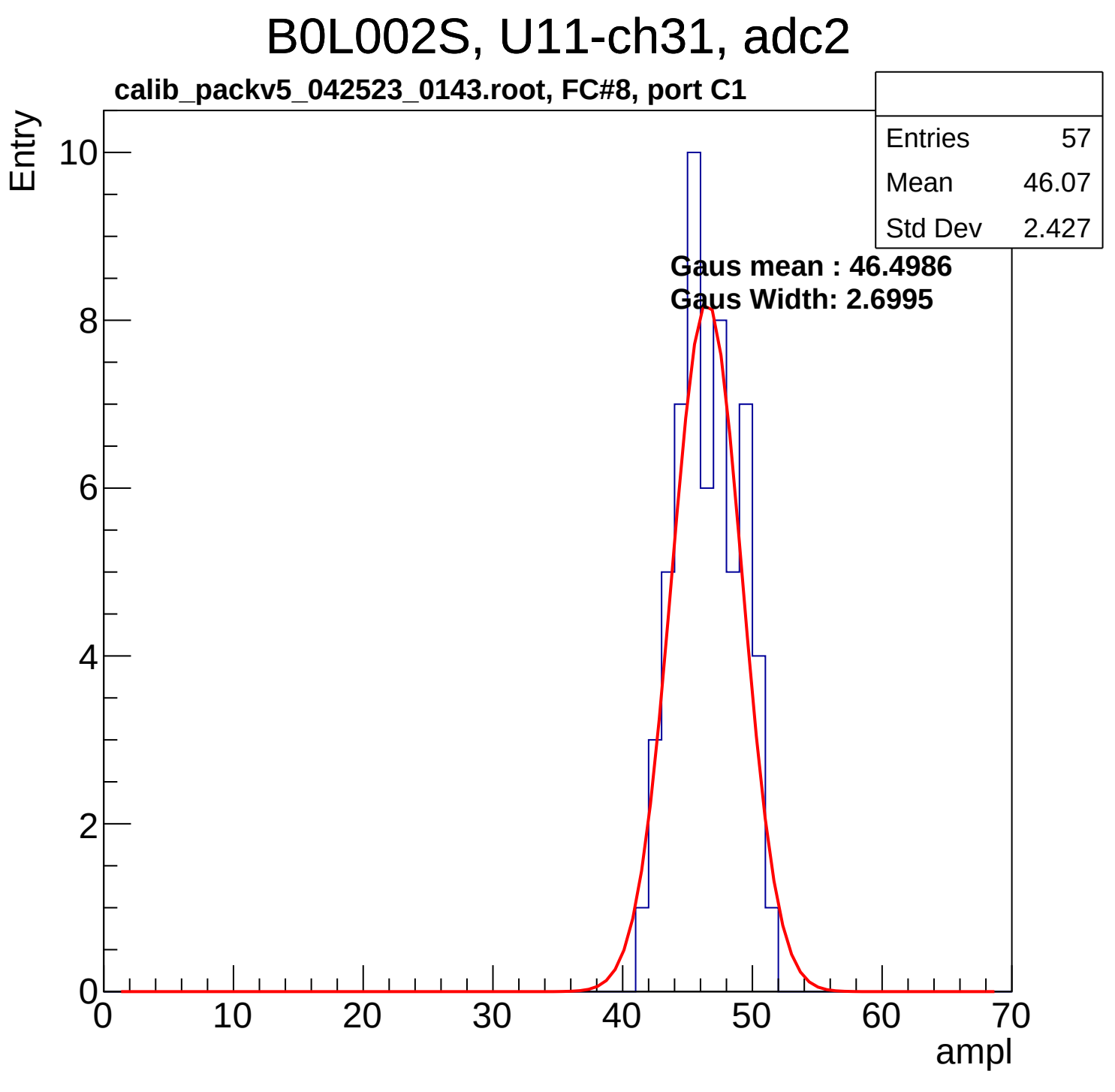
B0L002S, U11-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 46.07
Std Dev 2.427
Gaus mean : 46.4986
Gaus Width: 2.6995
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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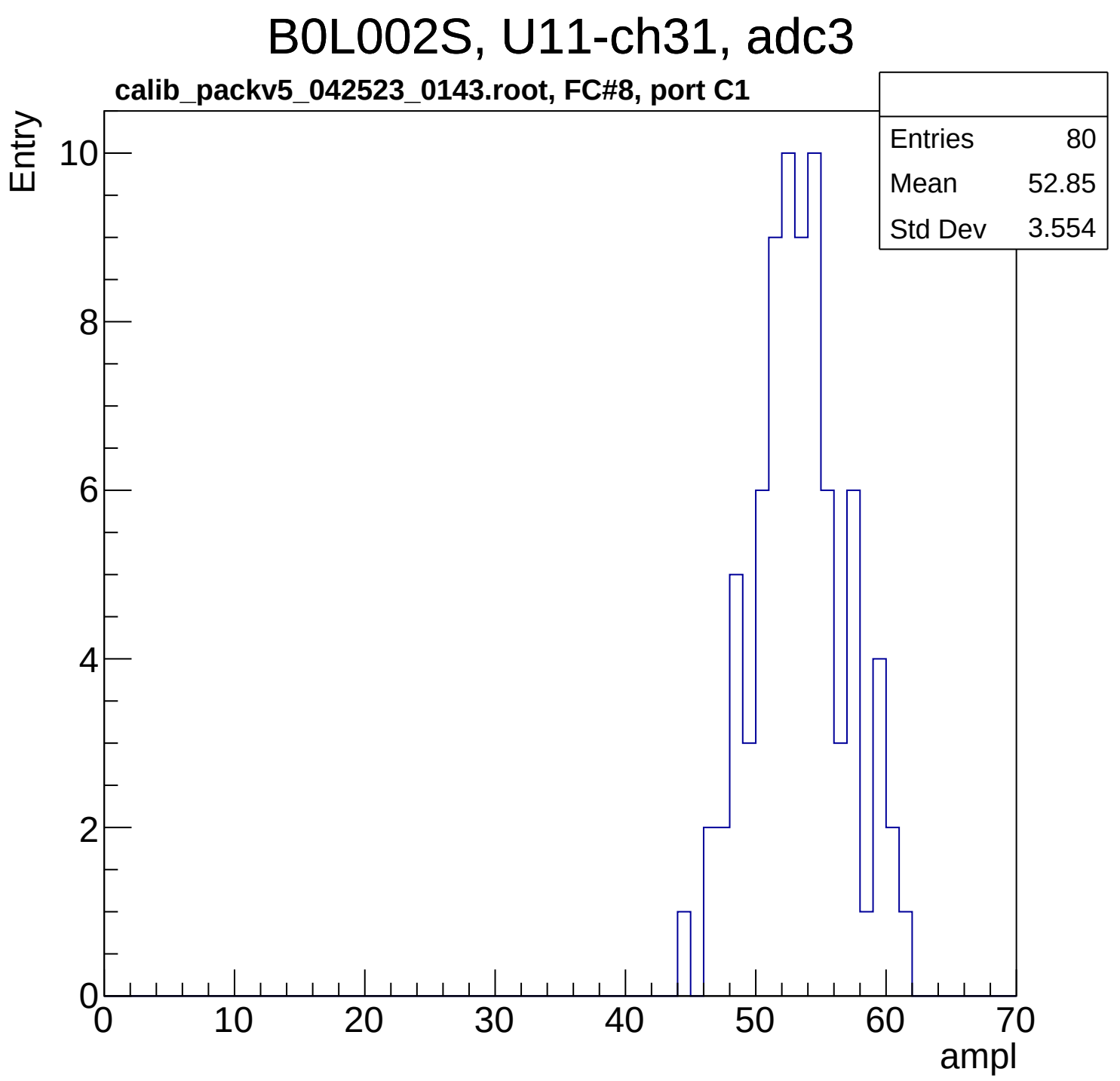
B0L002S, U11-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 52.85
Std Dev 3.554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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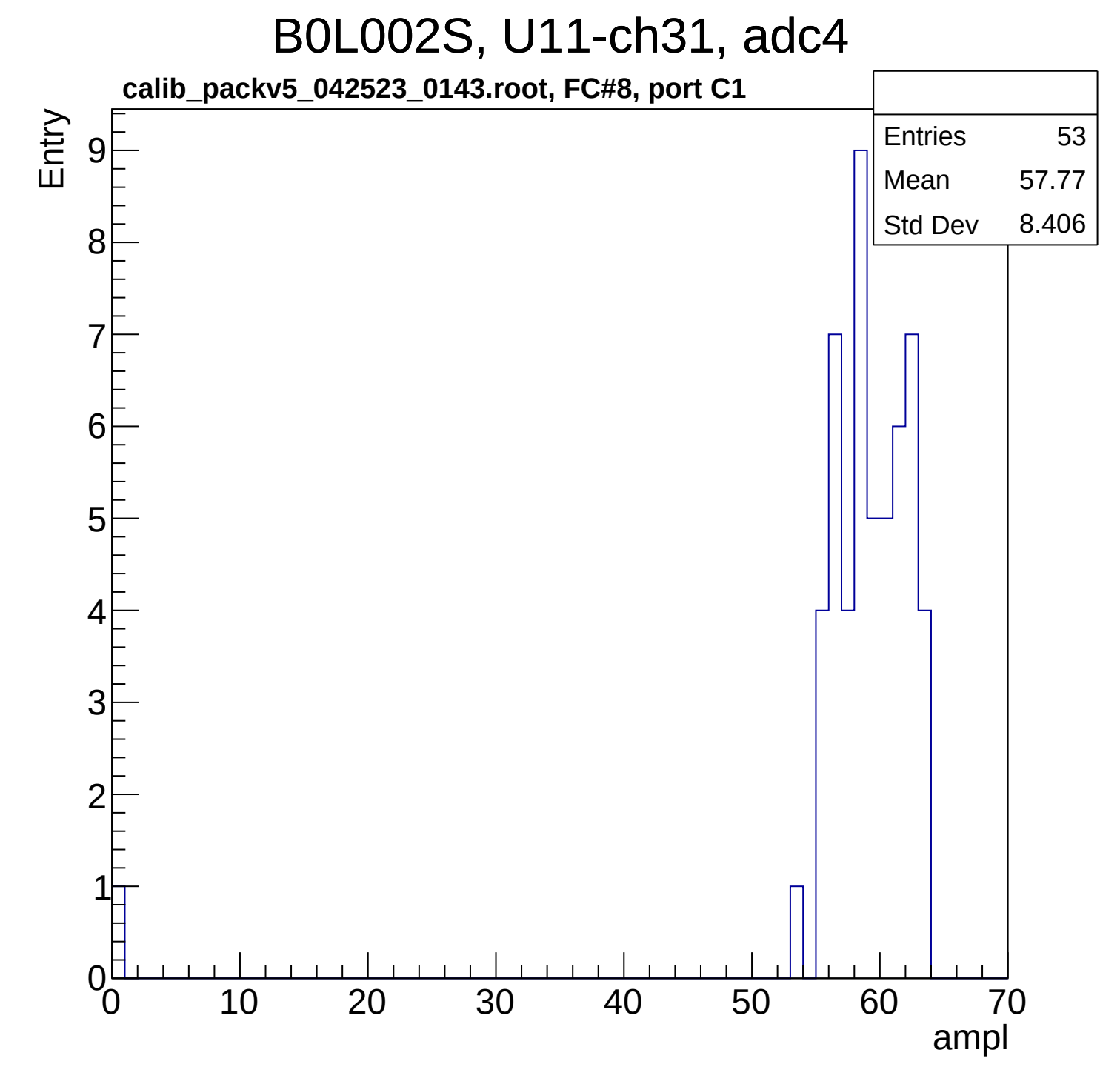
B0L002S, U11-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.77
Std Dev 8.406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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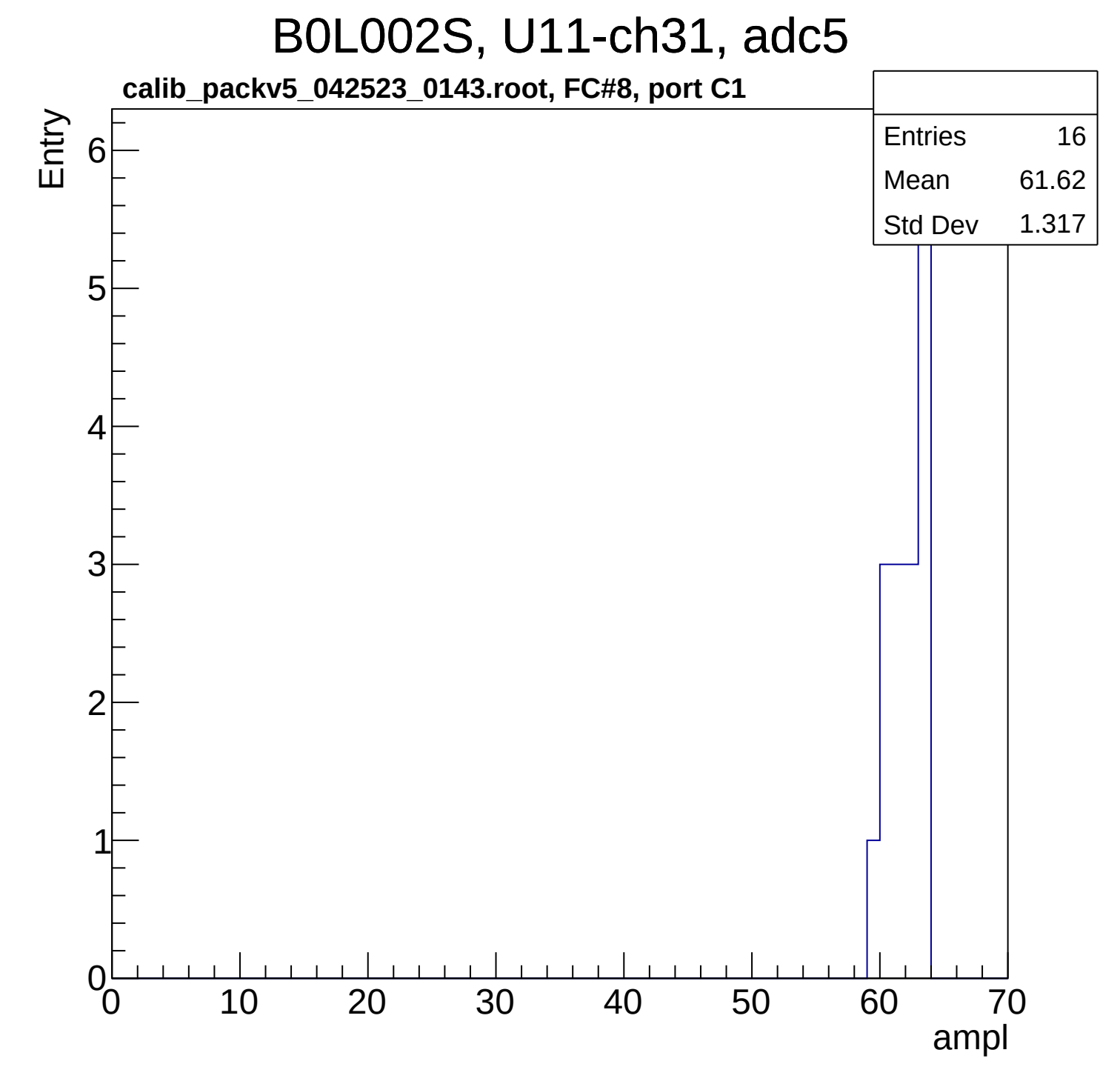
B0L002S, U11-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 61.62
Std Dev 1.317
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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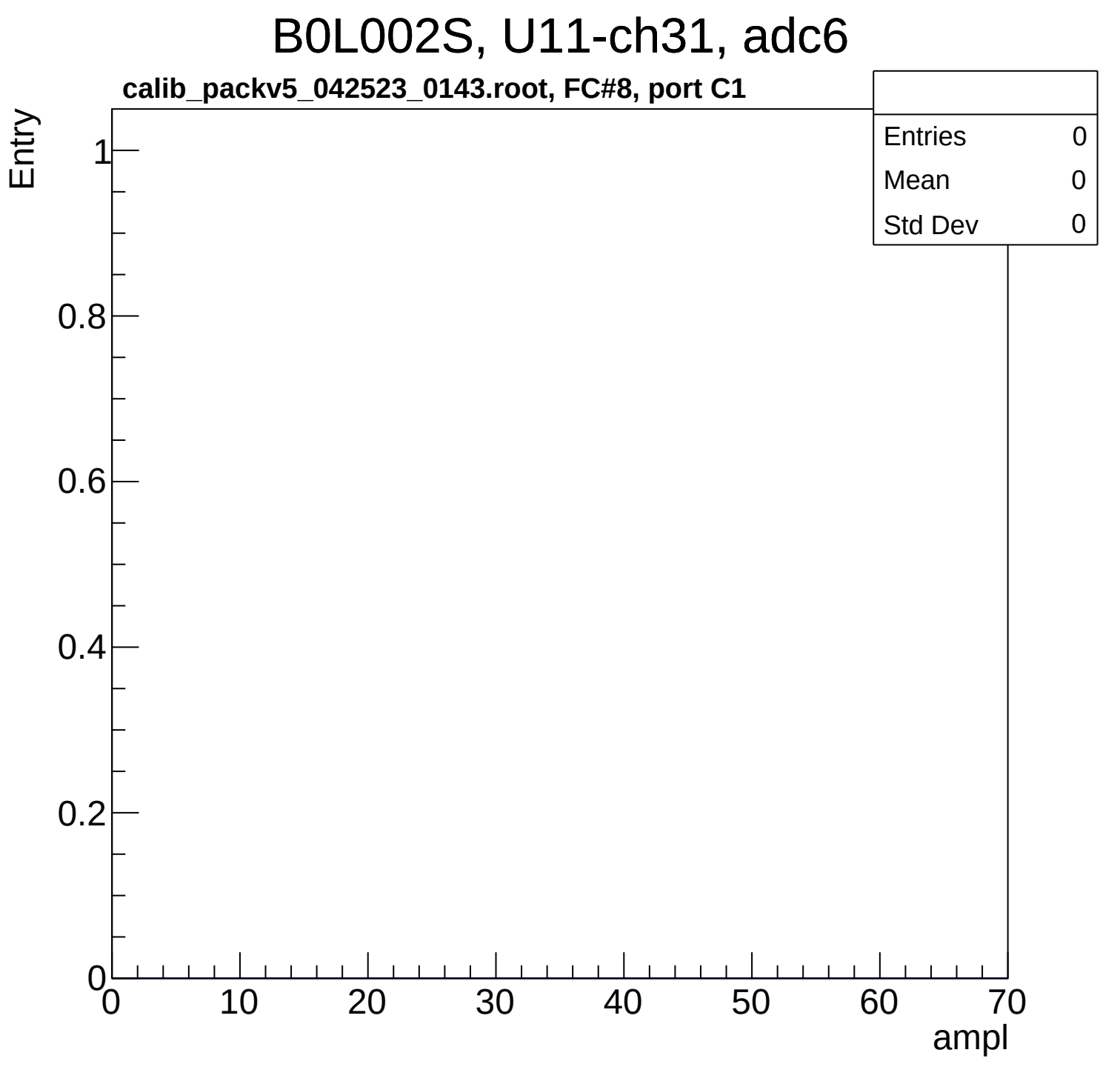
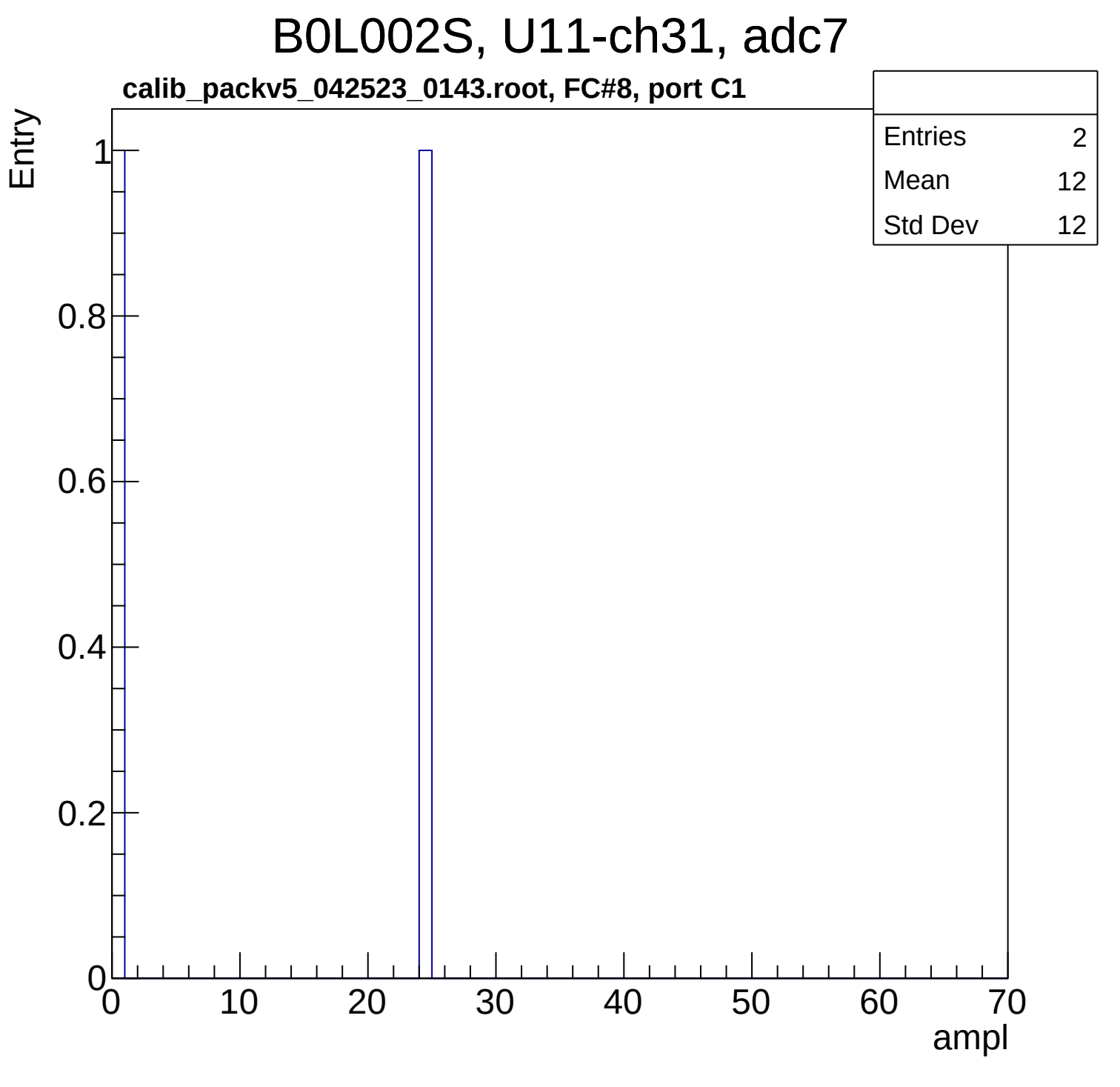

B0L002S, U11-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
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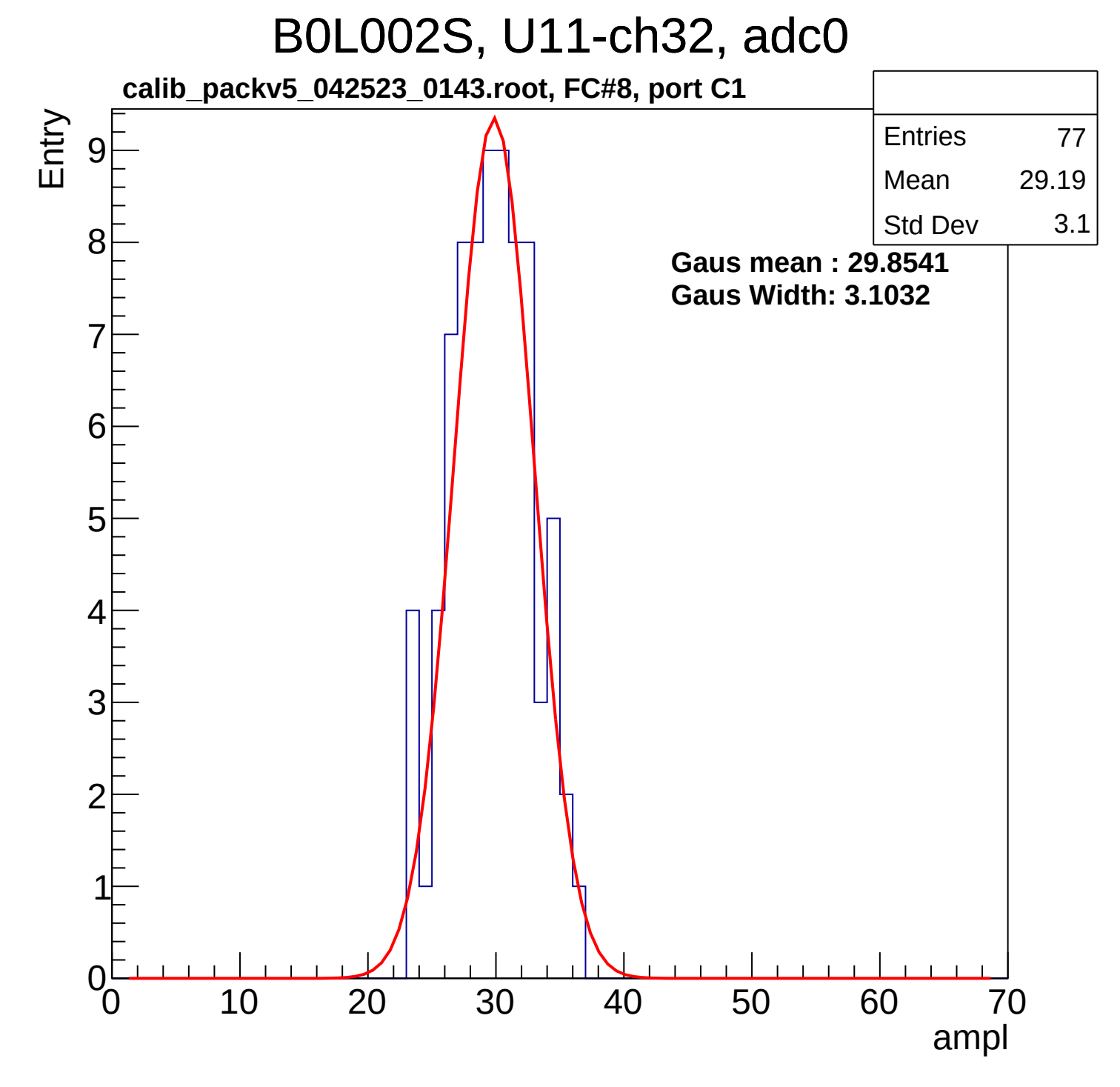
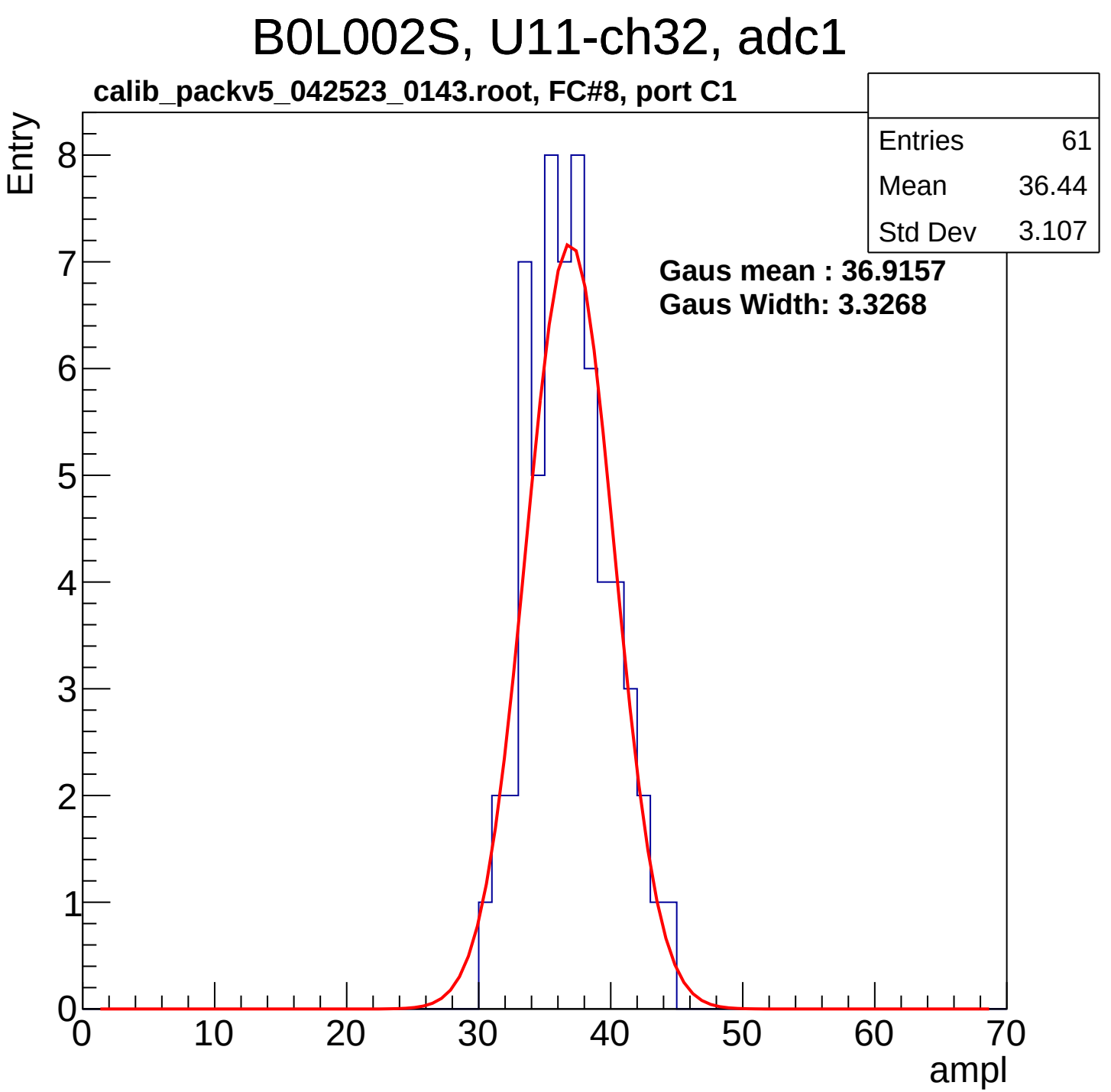
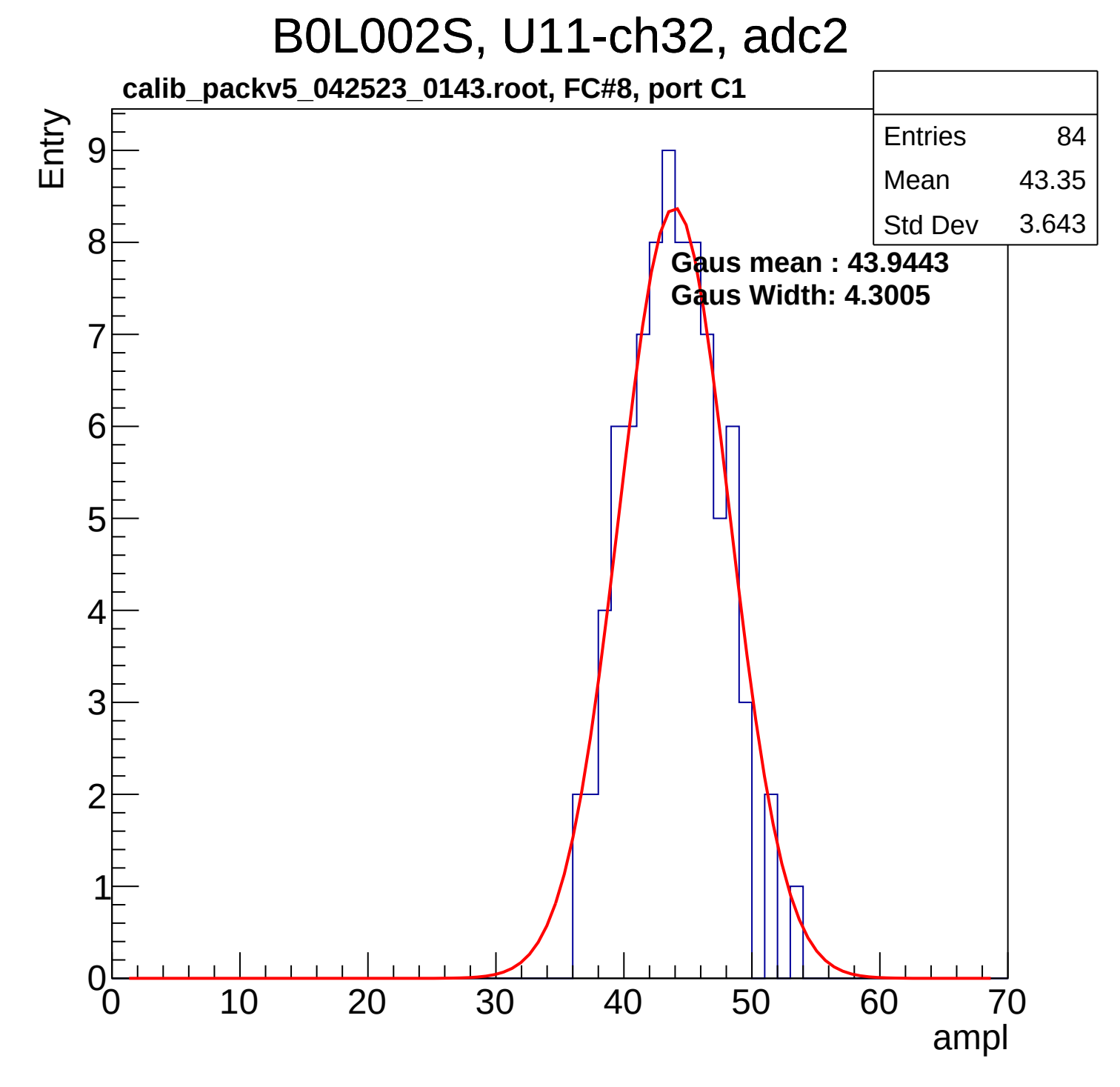
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 29.19
Std Dev 3.1
Gaus mean : 29.8541
Gaus Width: 3.1032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.44
Std Dev 3.107
Gaus mean : 36.9157
Gaus Width: 3.3268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 43.35
Std Dev 3.643
Gaus mean : 43.9443
Gaus Width: 4.3005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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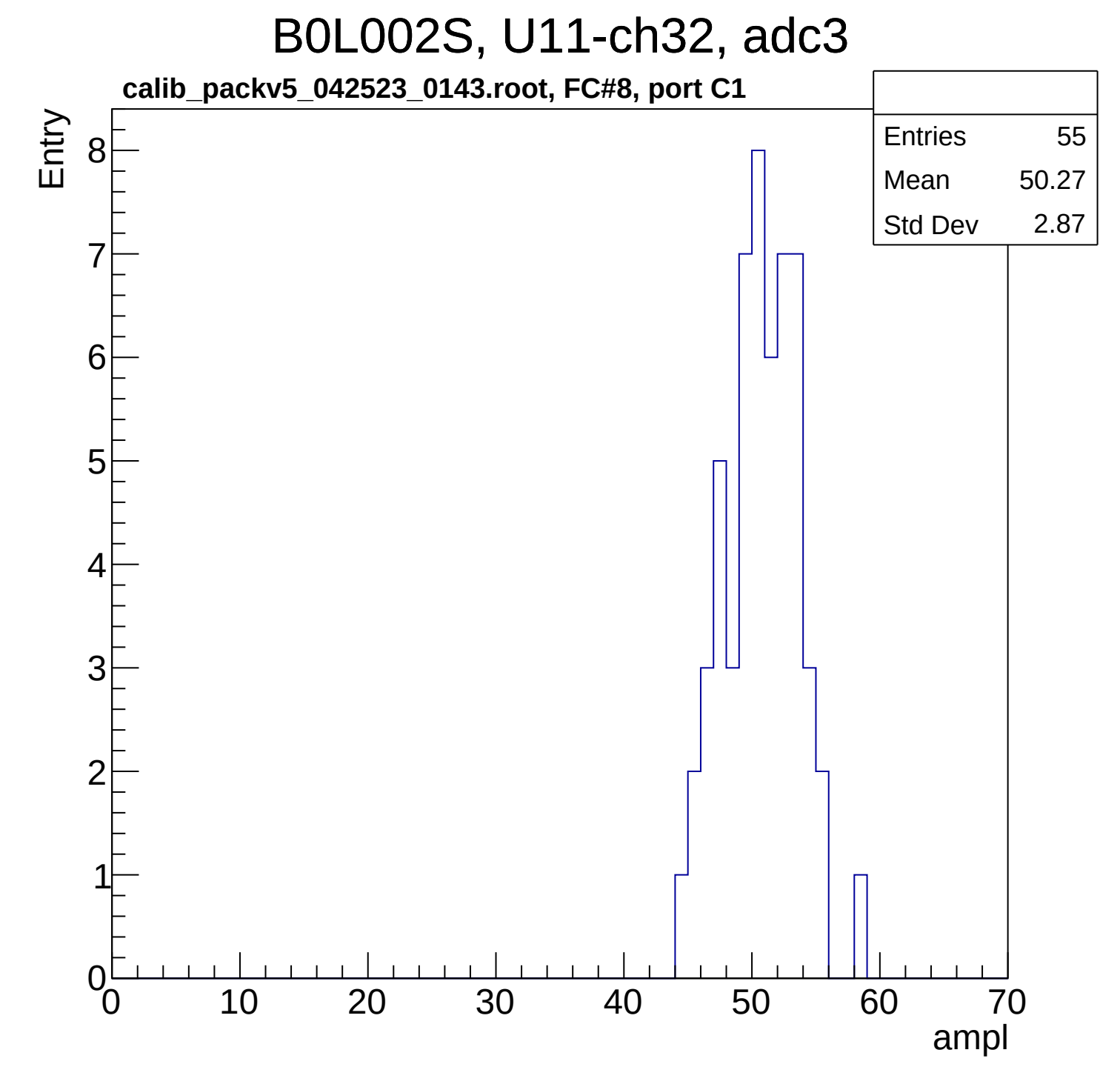
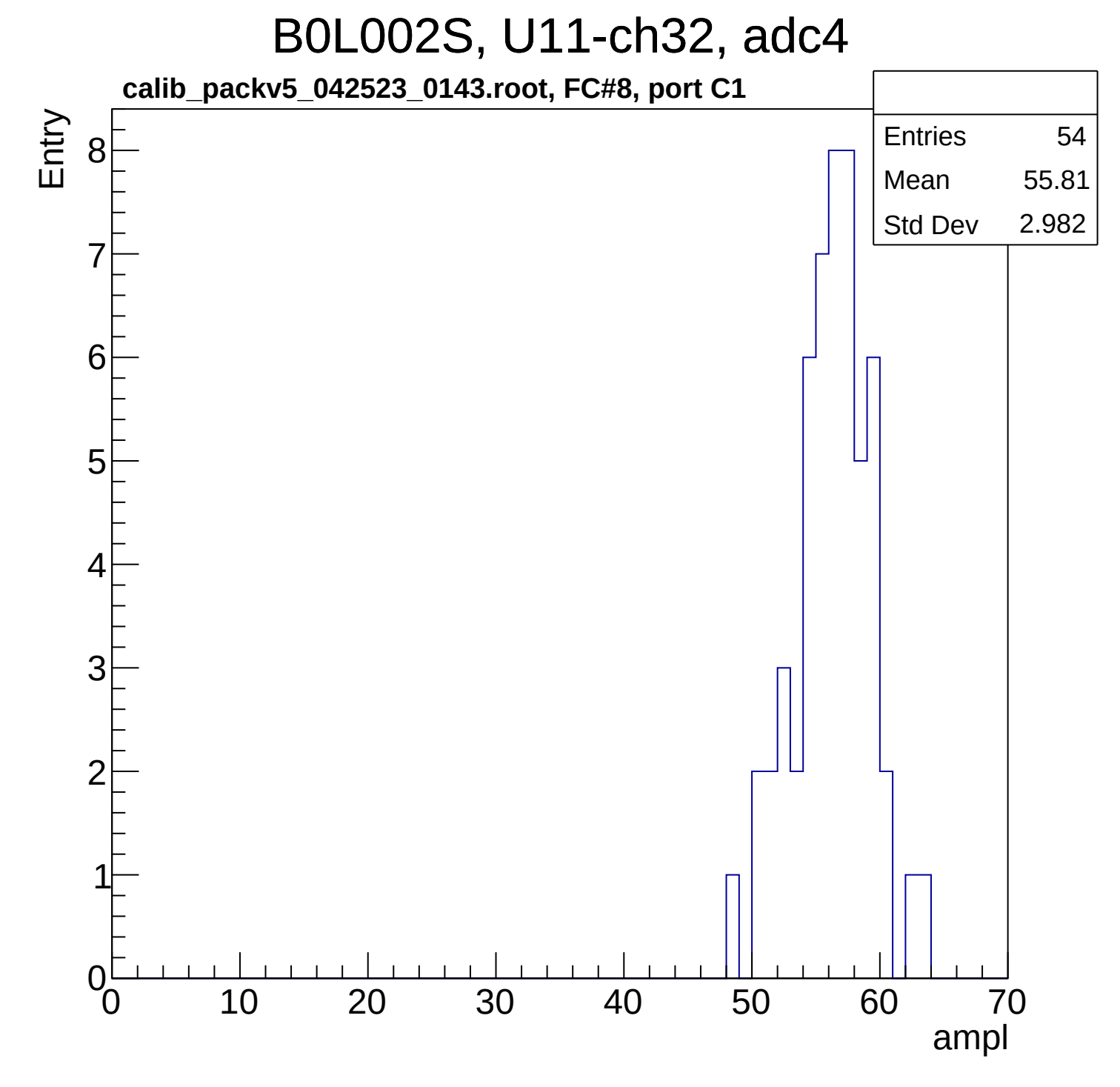

B0L002S, U11-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 50.27
Std Dev 2.87
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 55.81
Std Dev 2.982
Entry
ampl
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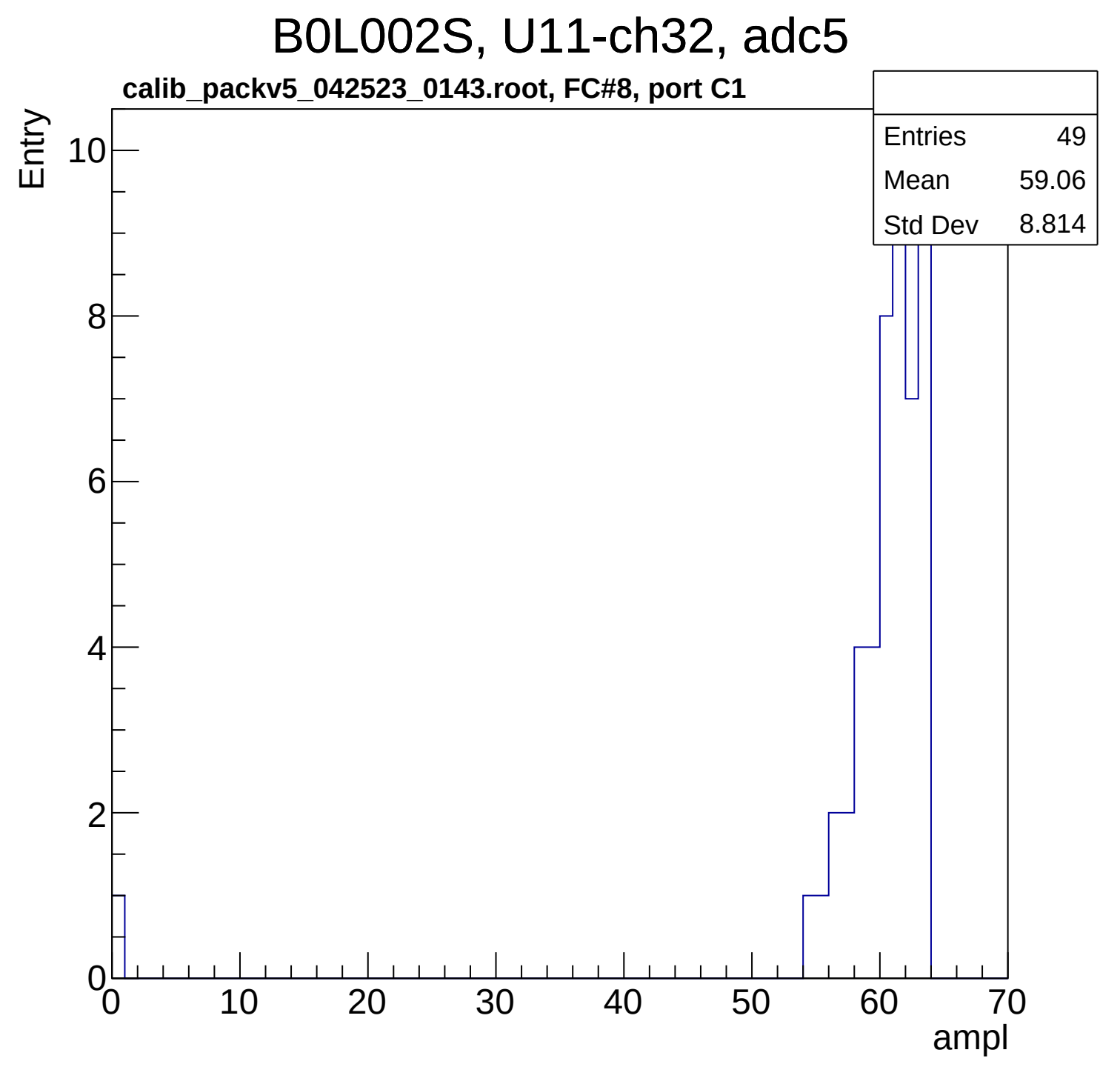
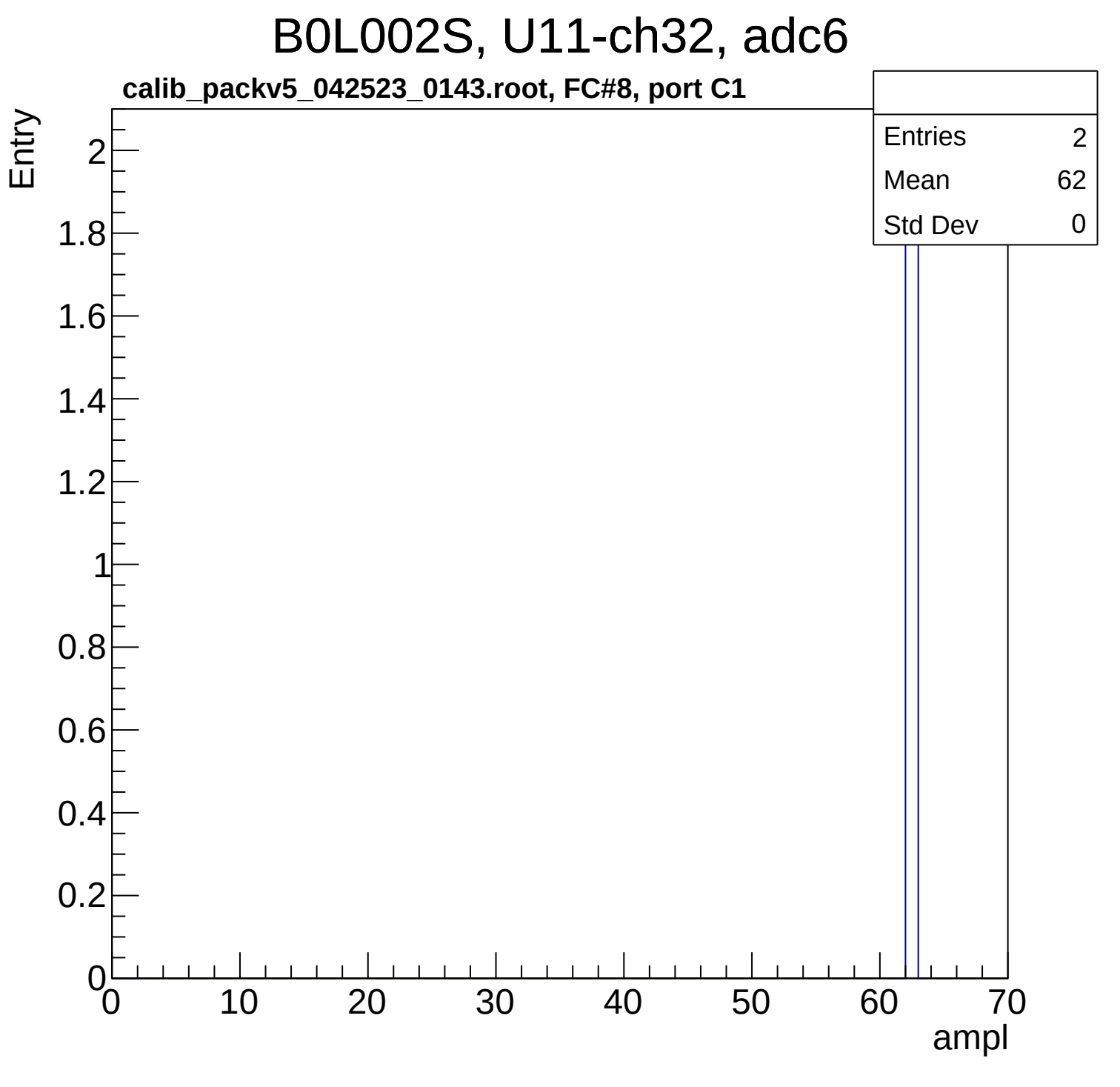
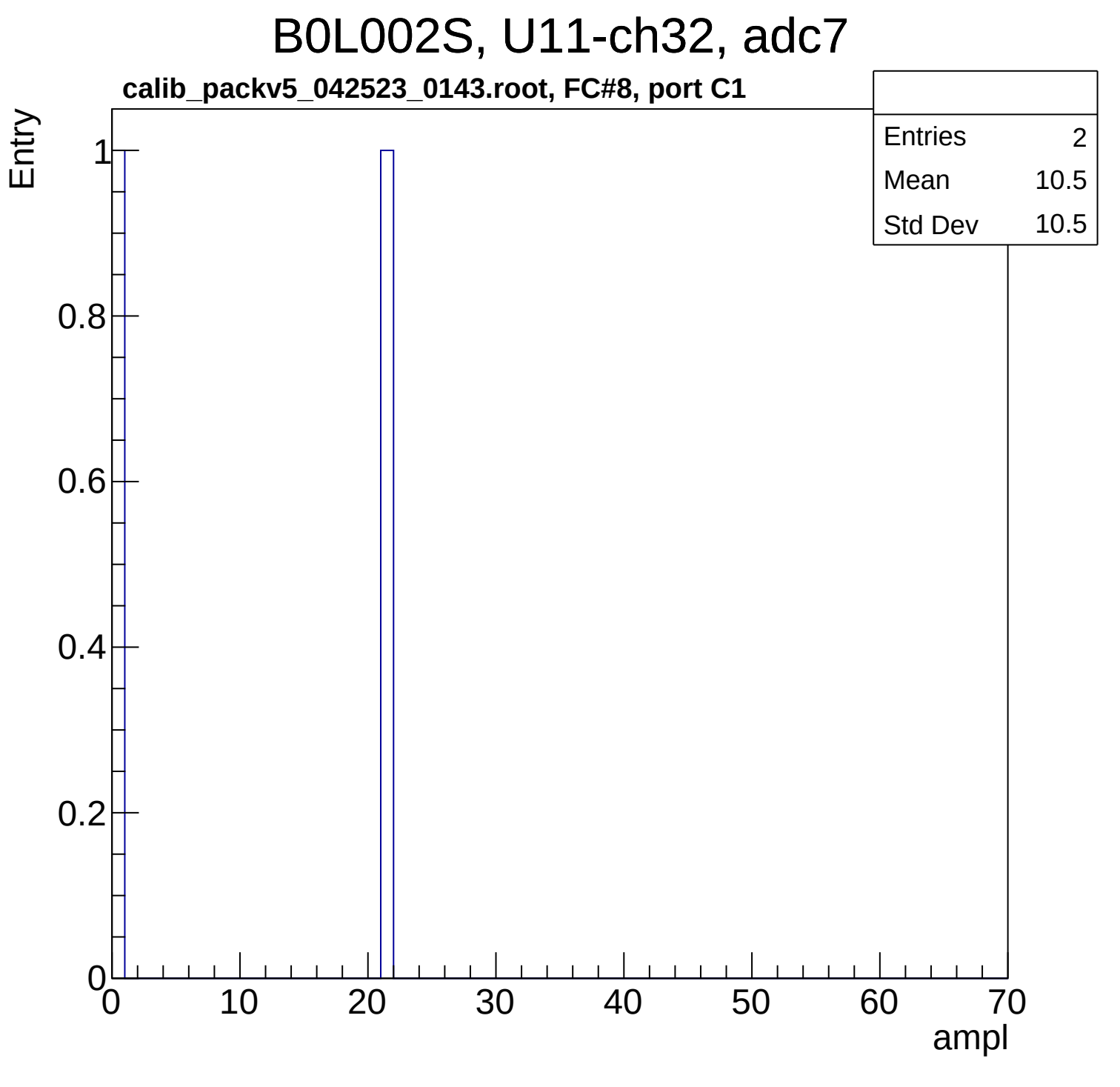
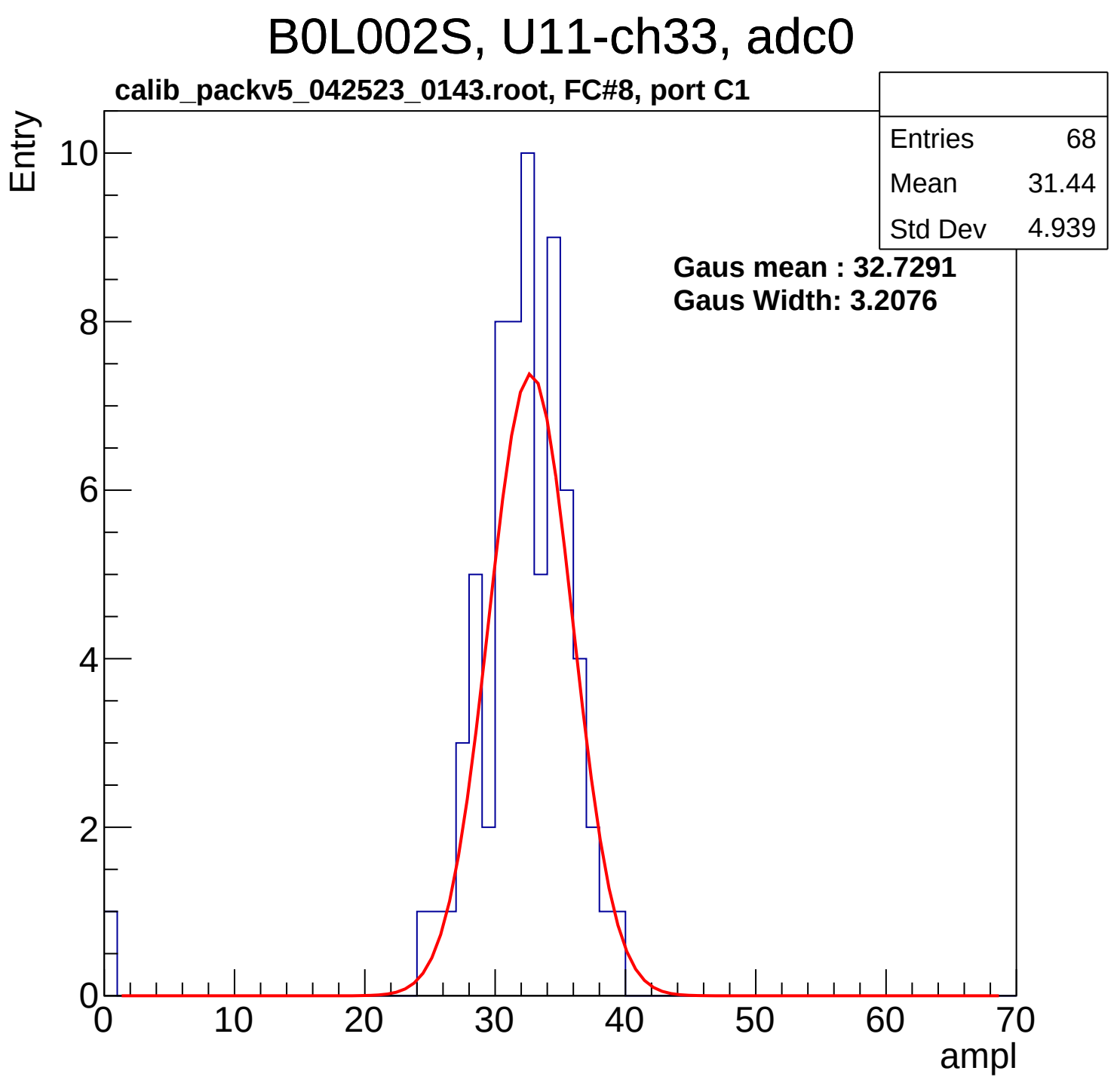
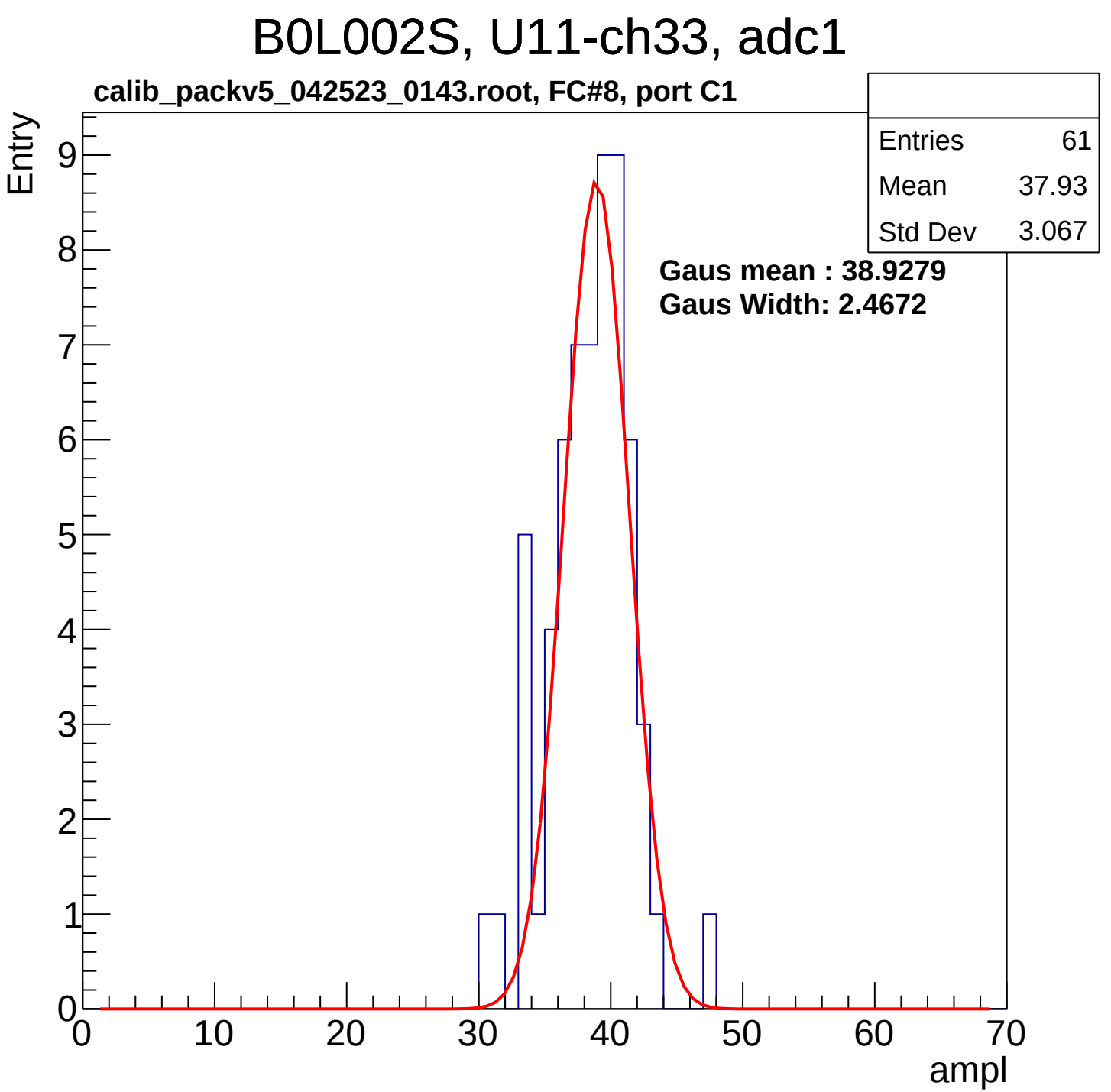
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.06
Std Dev 8.814
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.44
Std Dev 4.939
Gaus mean : 32.7291
Gaus Width: 3.2076
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.93
Std Dev 3.067
Gaus mean : 38.9279
Gaus Width: 2.4672
Entry
ampl
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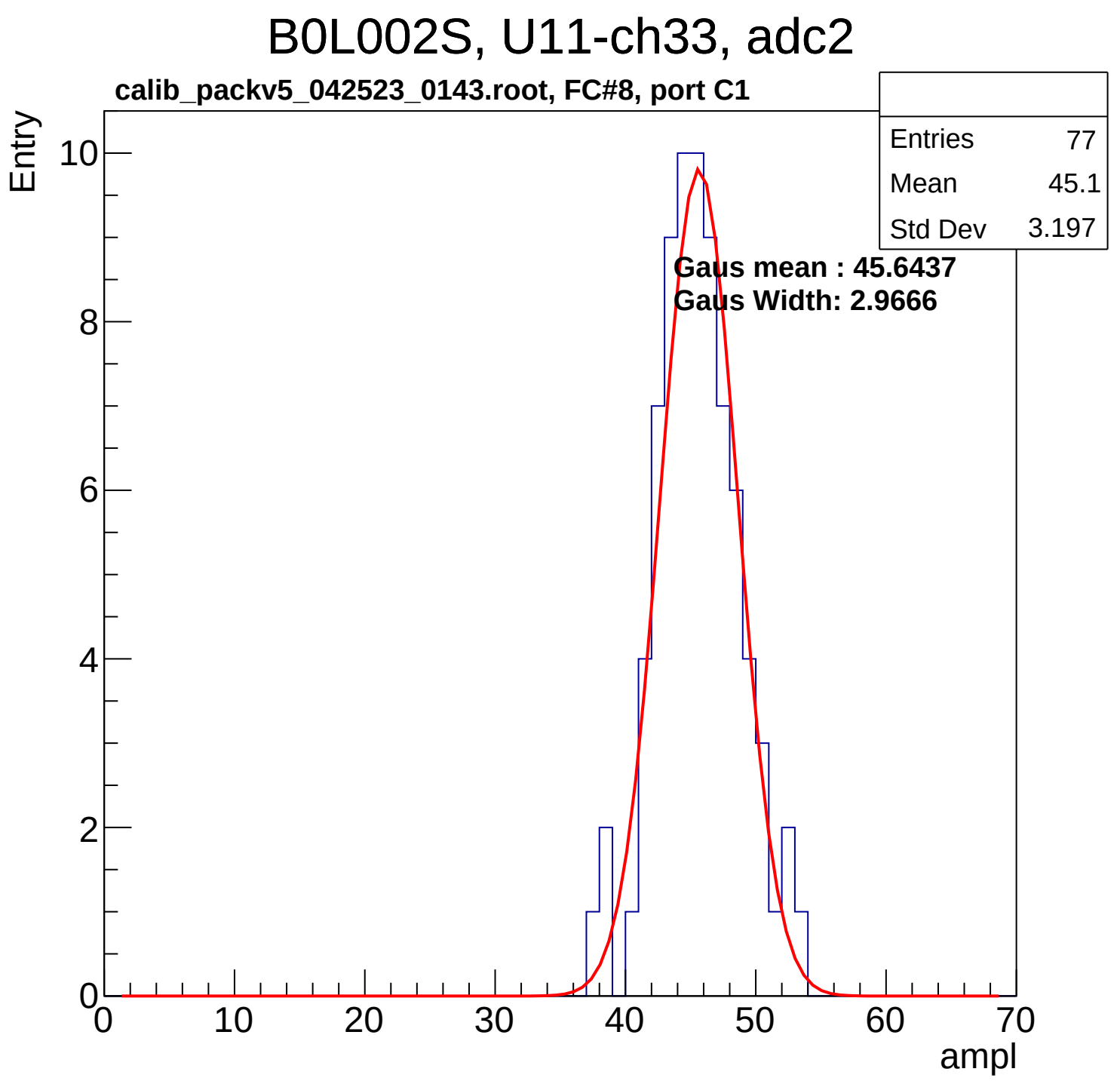
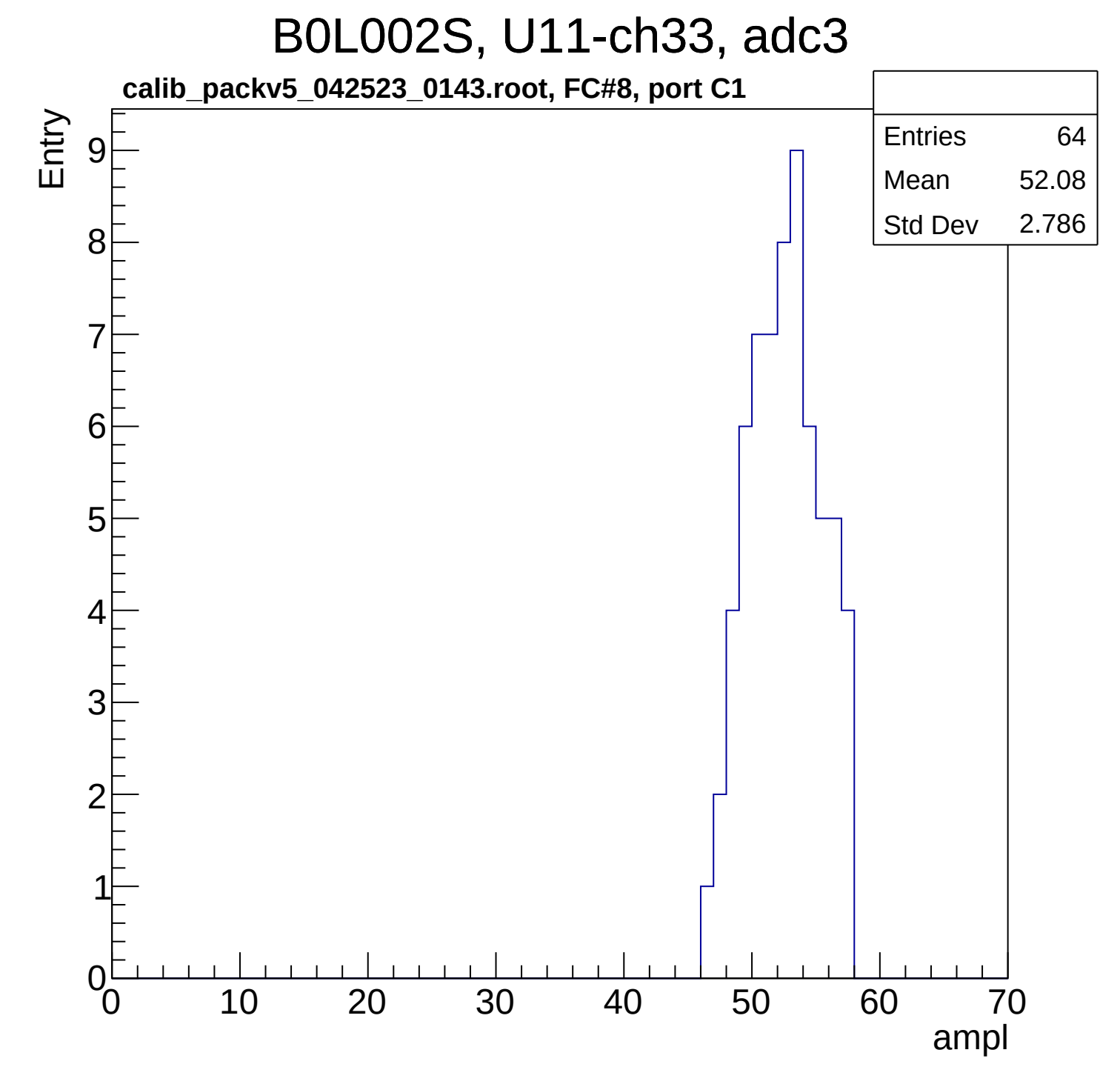
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 45.1
Std Dev 3.197
Gaus mean : 45.6437
Gaus Width: 2.9666
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 52.08
Std Dev 2.786
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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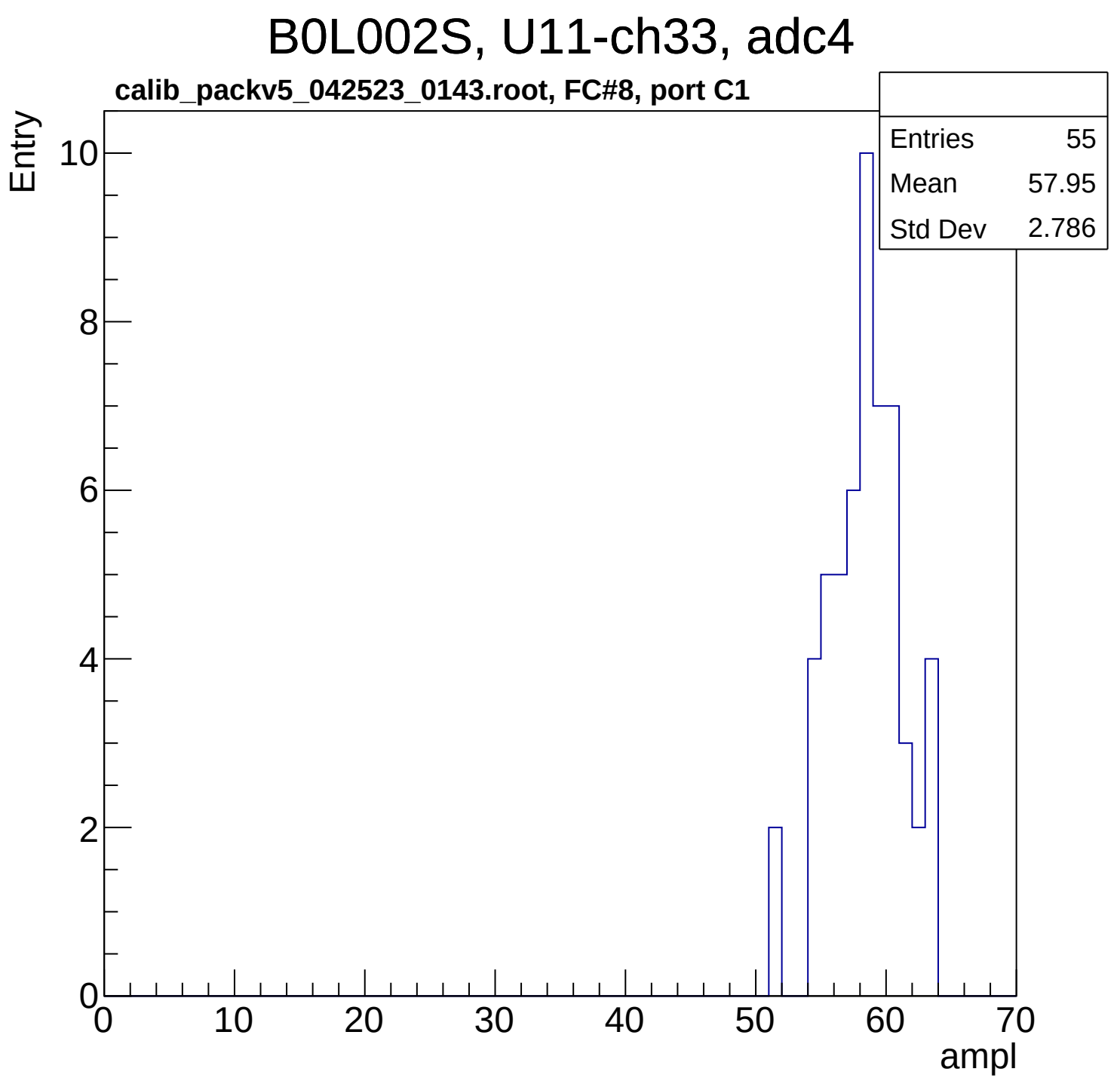
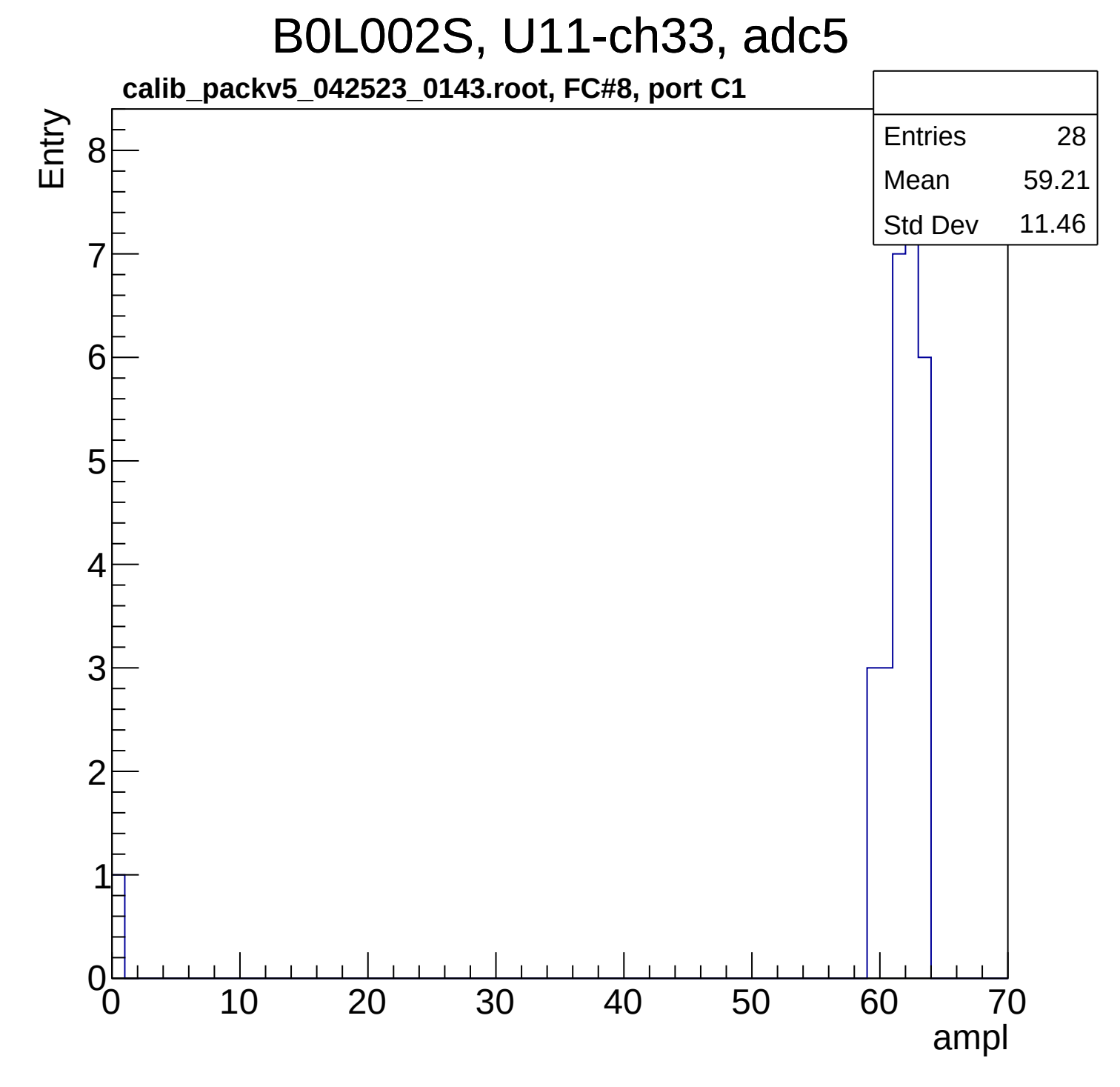

B0L002S, U11-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.95
Std Dev 2.786
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 59.21
Std Dev 11.46
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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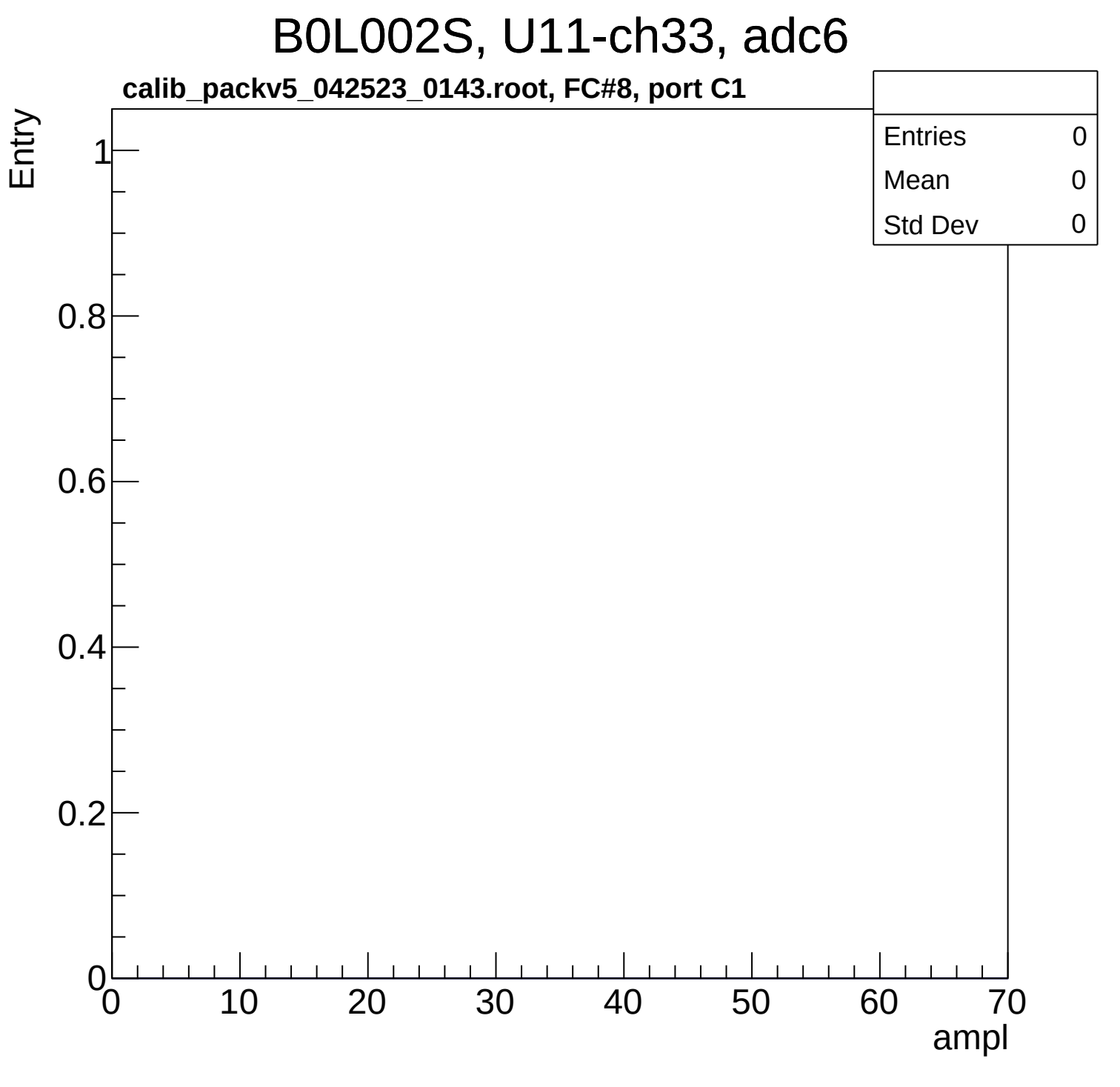
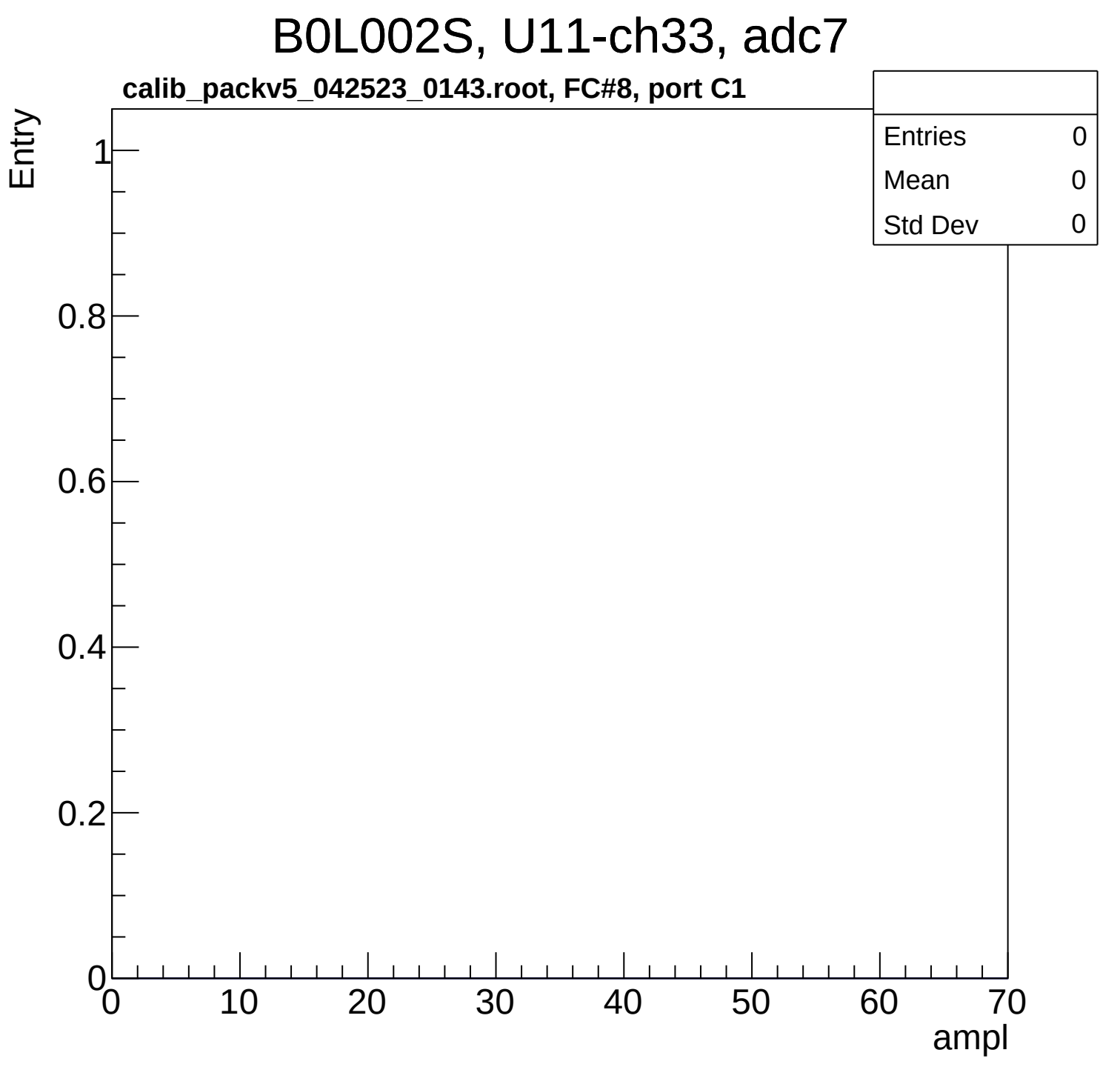

B0L002S, U11-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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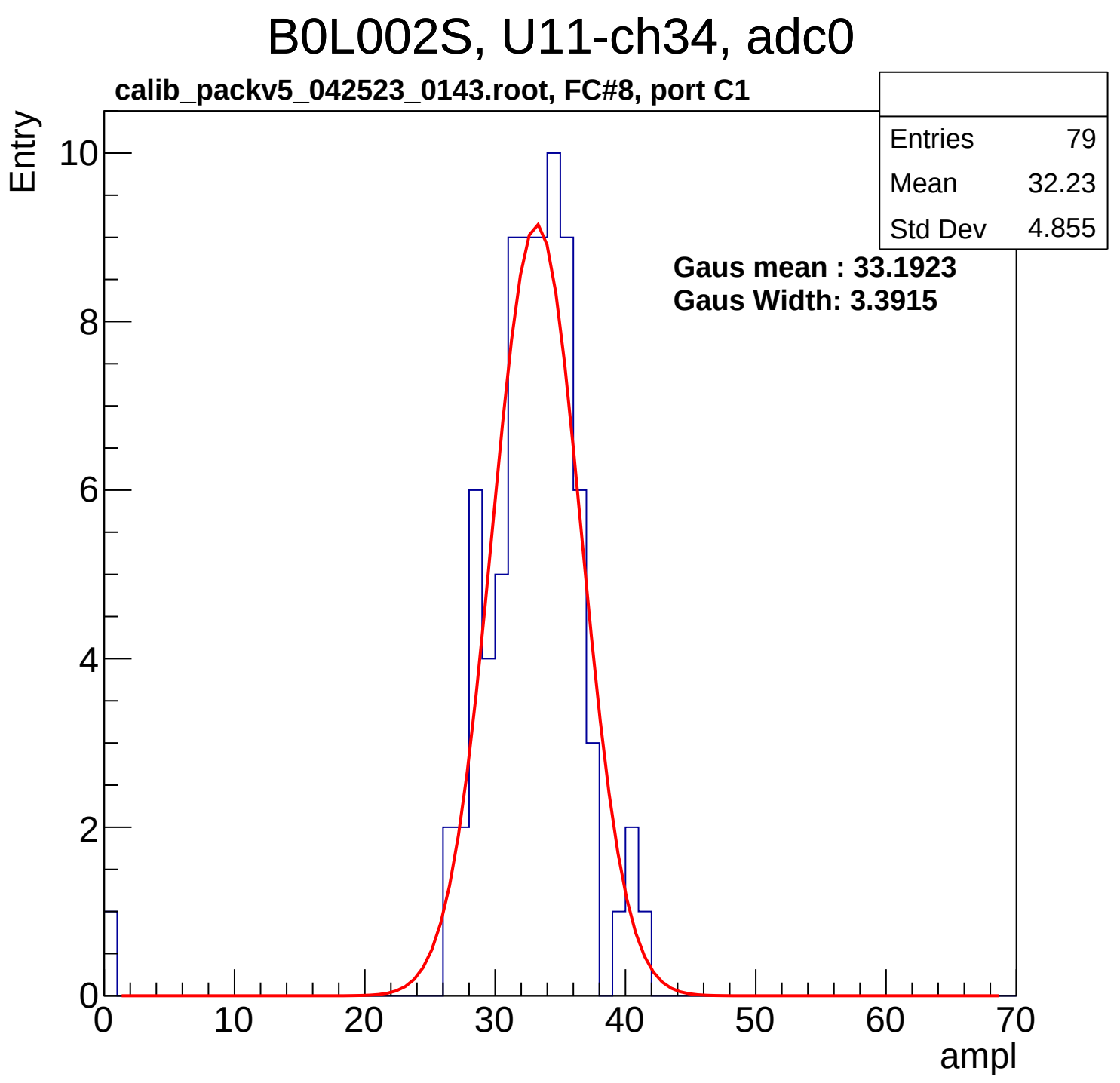
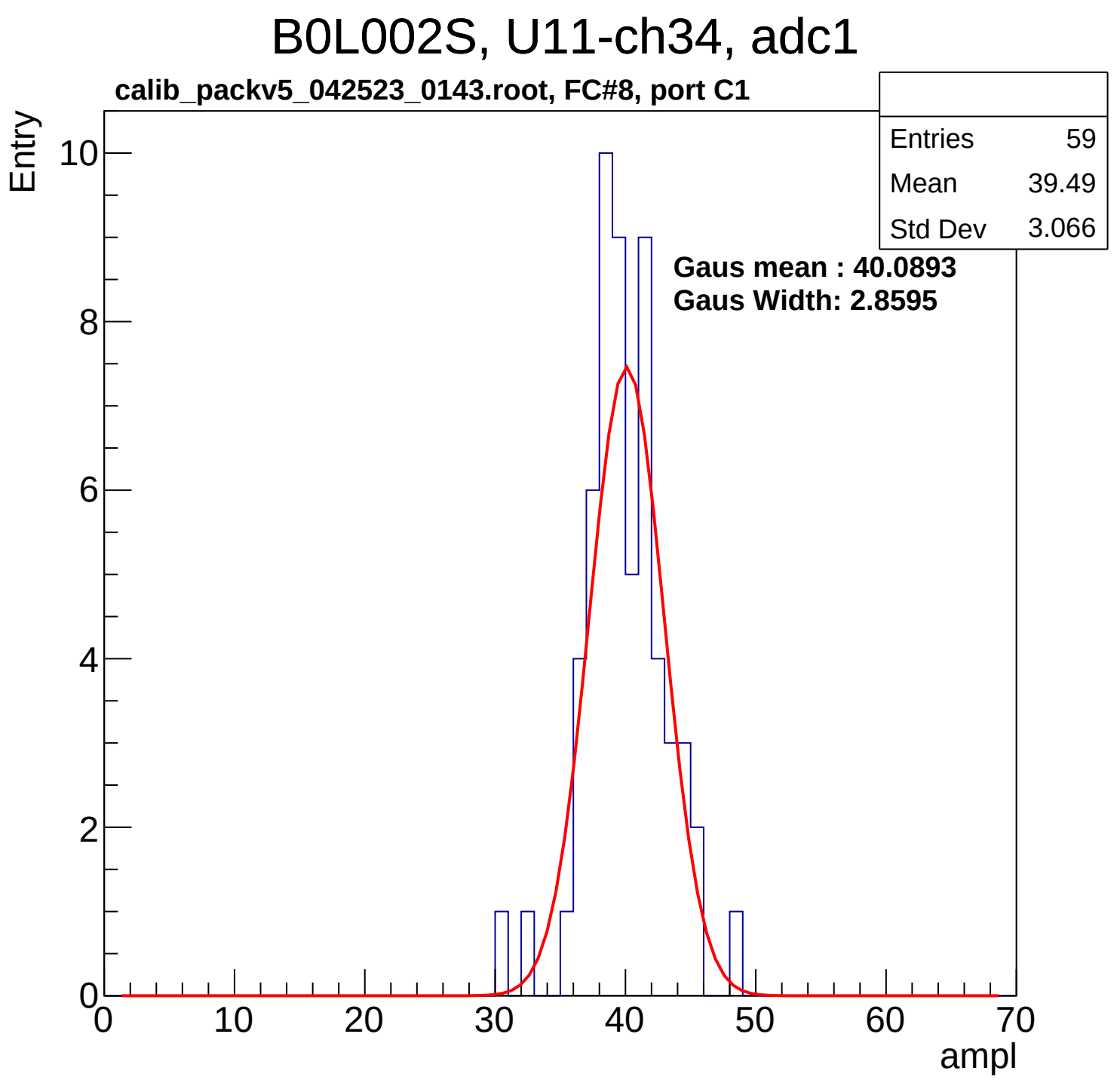
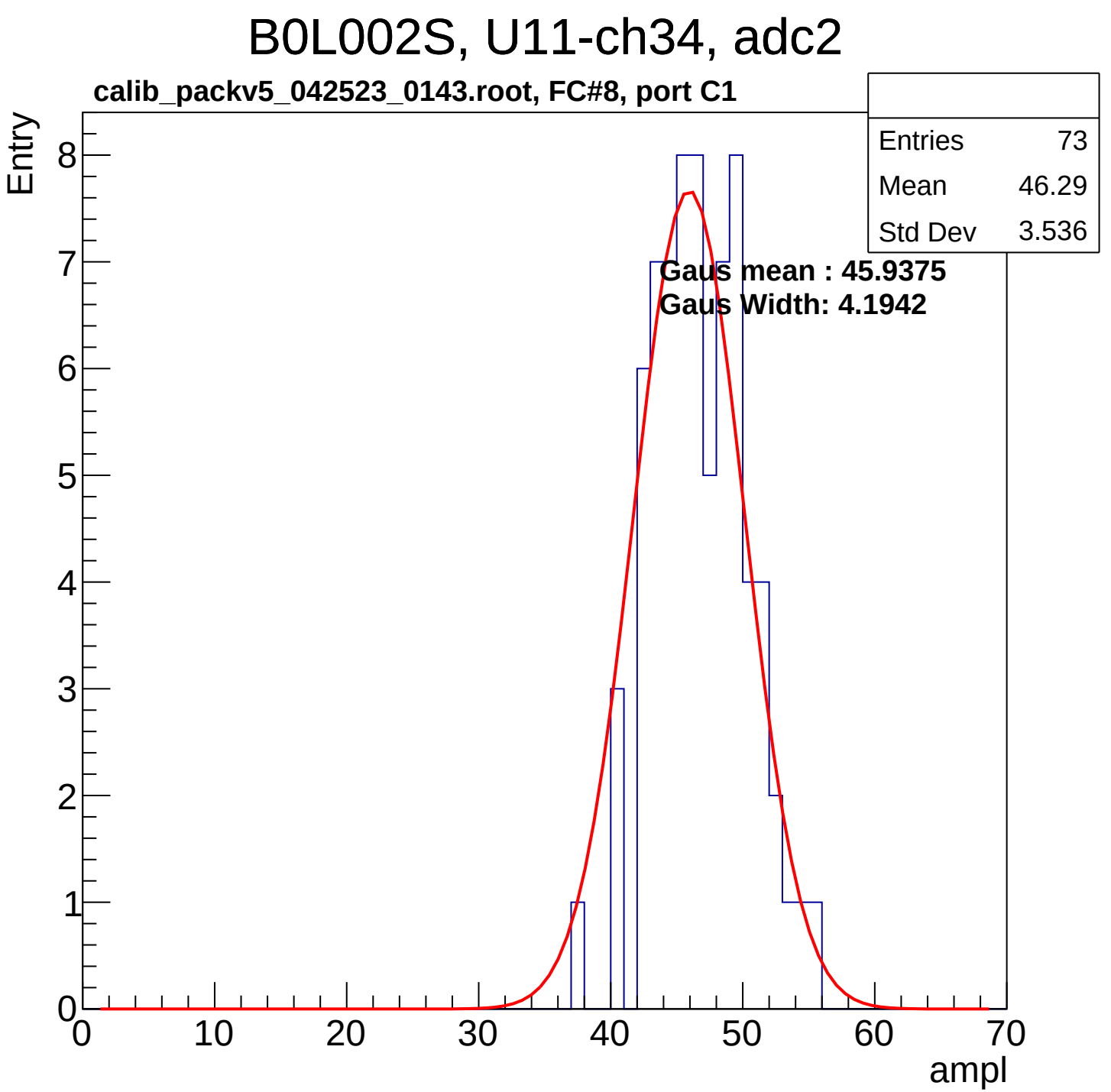
50
60
70
ampl


B0L002S, U11-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 32.23
Std Dev 4.855
Gaus mean : 33.1923
Gaus Width: 3.3915
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 39.49
Std Dev 3.066
Gaus mean : 40.0893
Gaus Width: 2.8595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 46.29
Std Dev 3.536
Gaus mean : 45.9375
Gaus Width: 4.1942
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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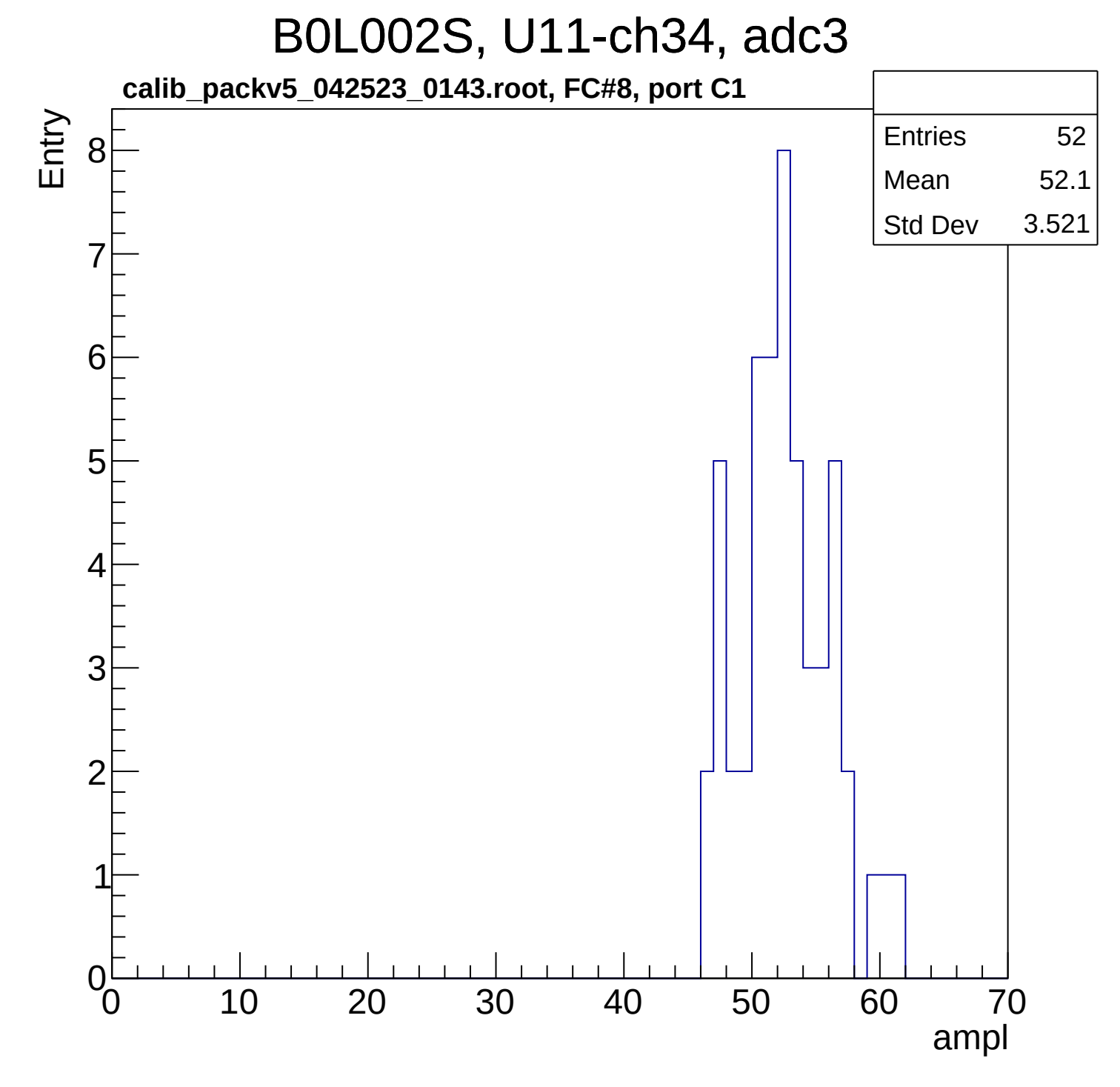
B0L002S, U11-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 52.1
Std Dev 3.521
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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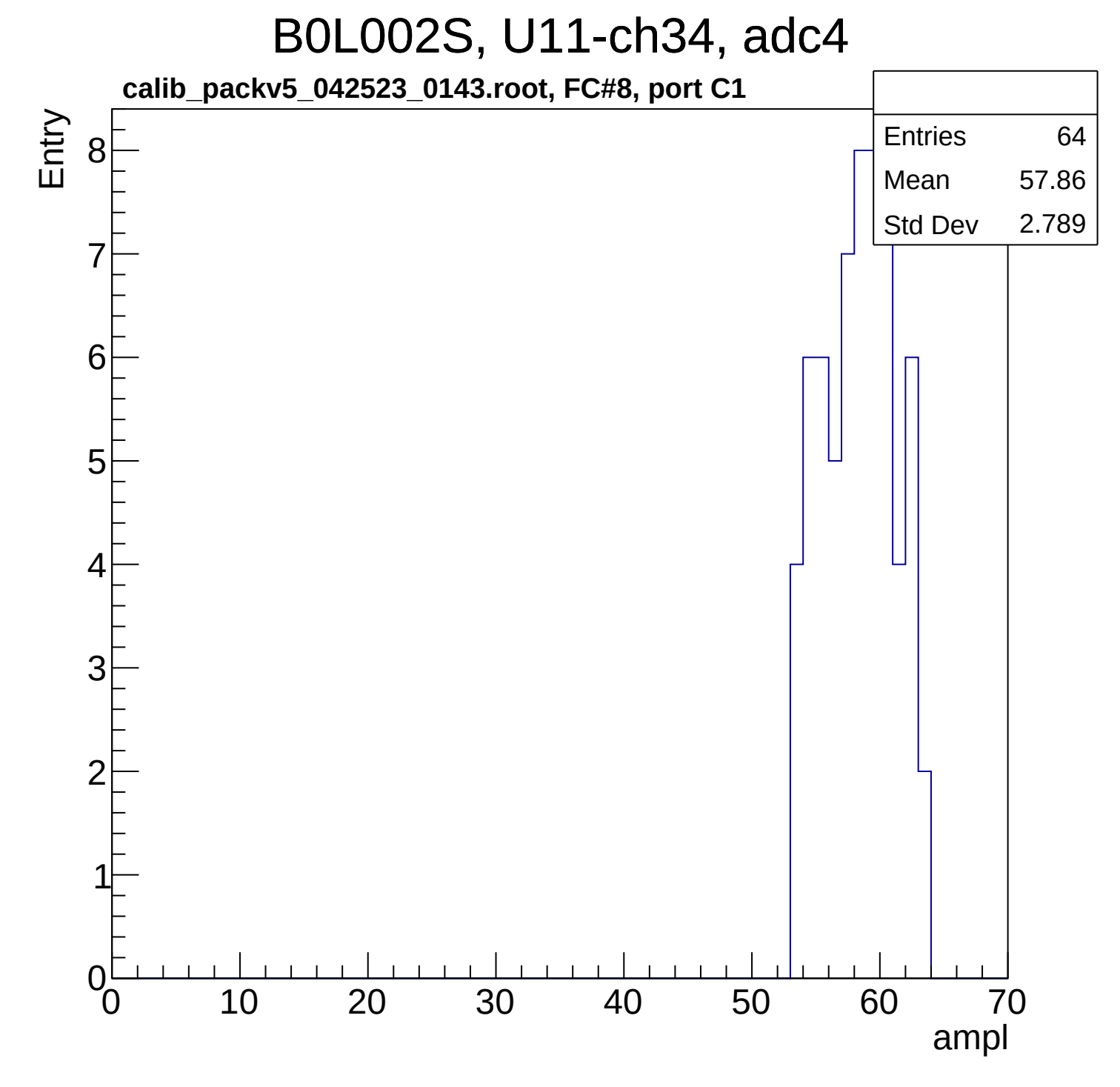
B0L002S, U11-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 57.86
Std Dev 2.789
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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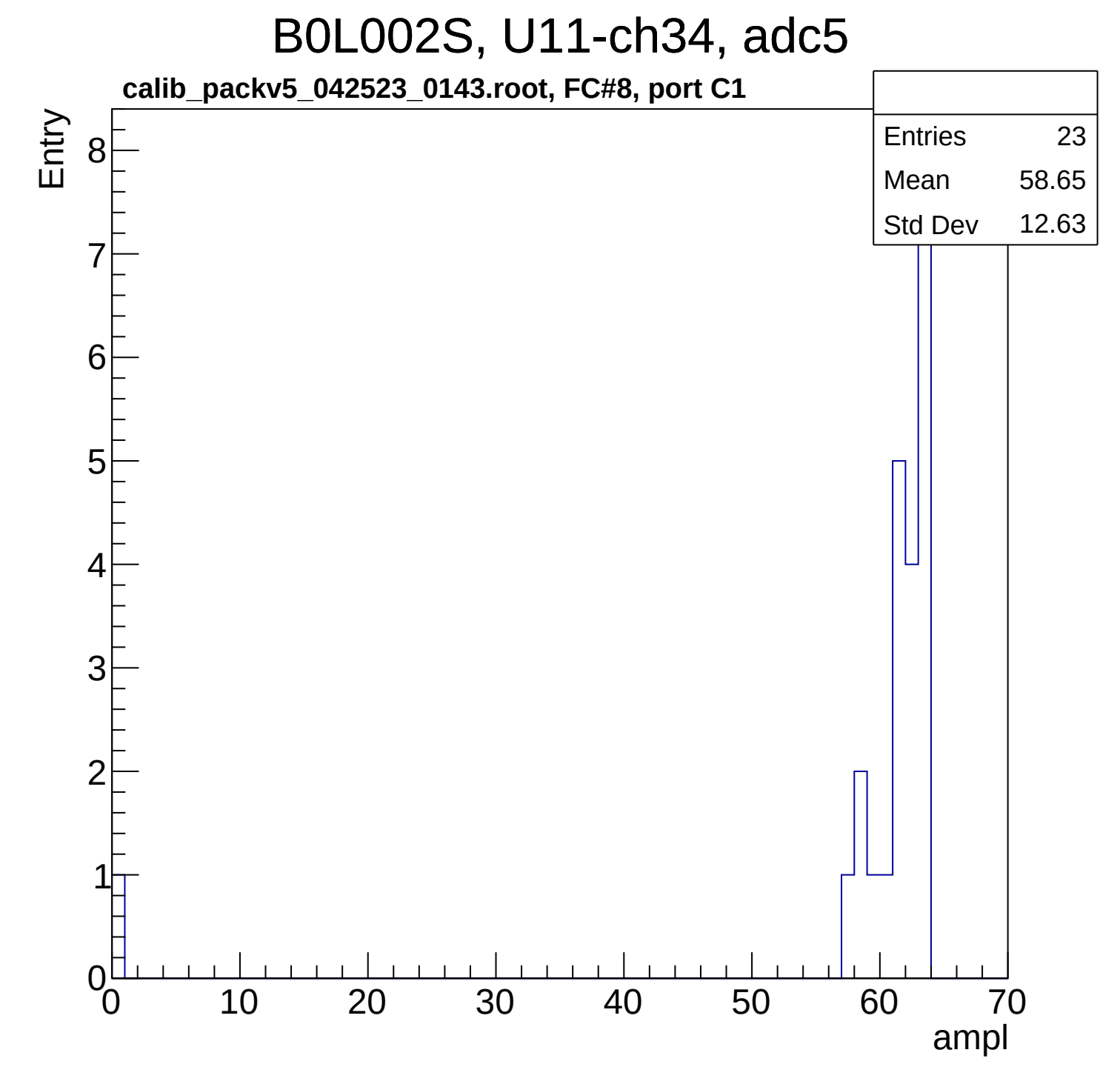
B0L002S, U11-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.65
Std Dev 12.63
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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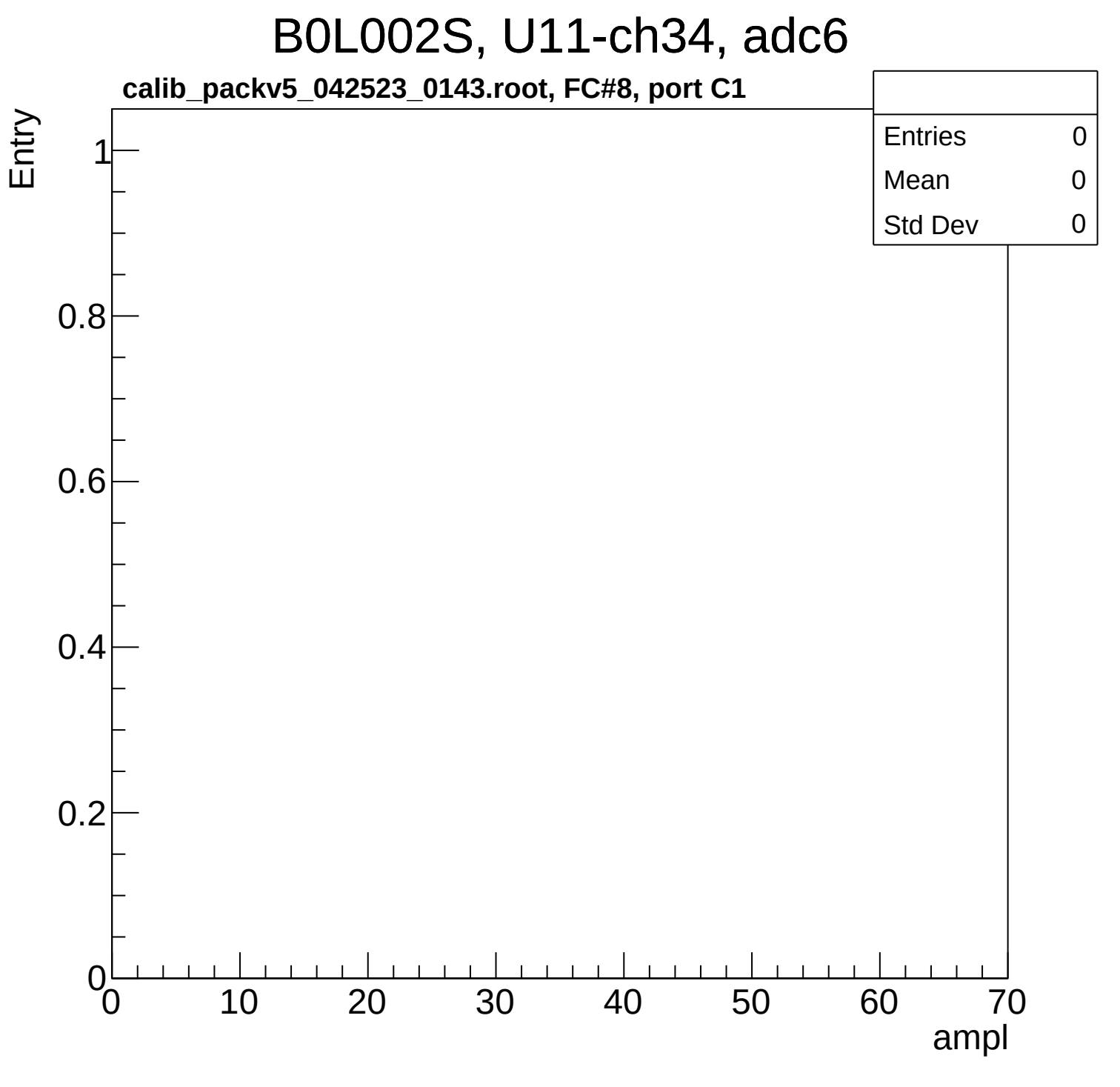
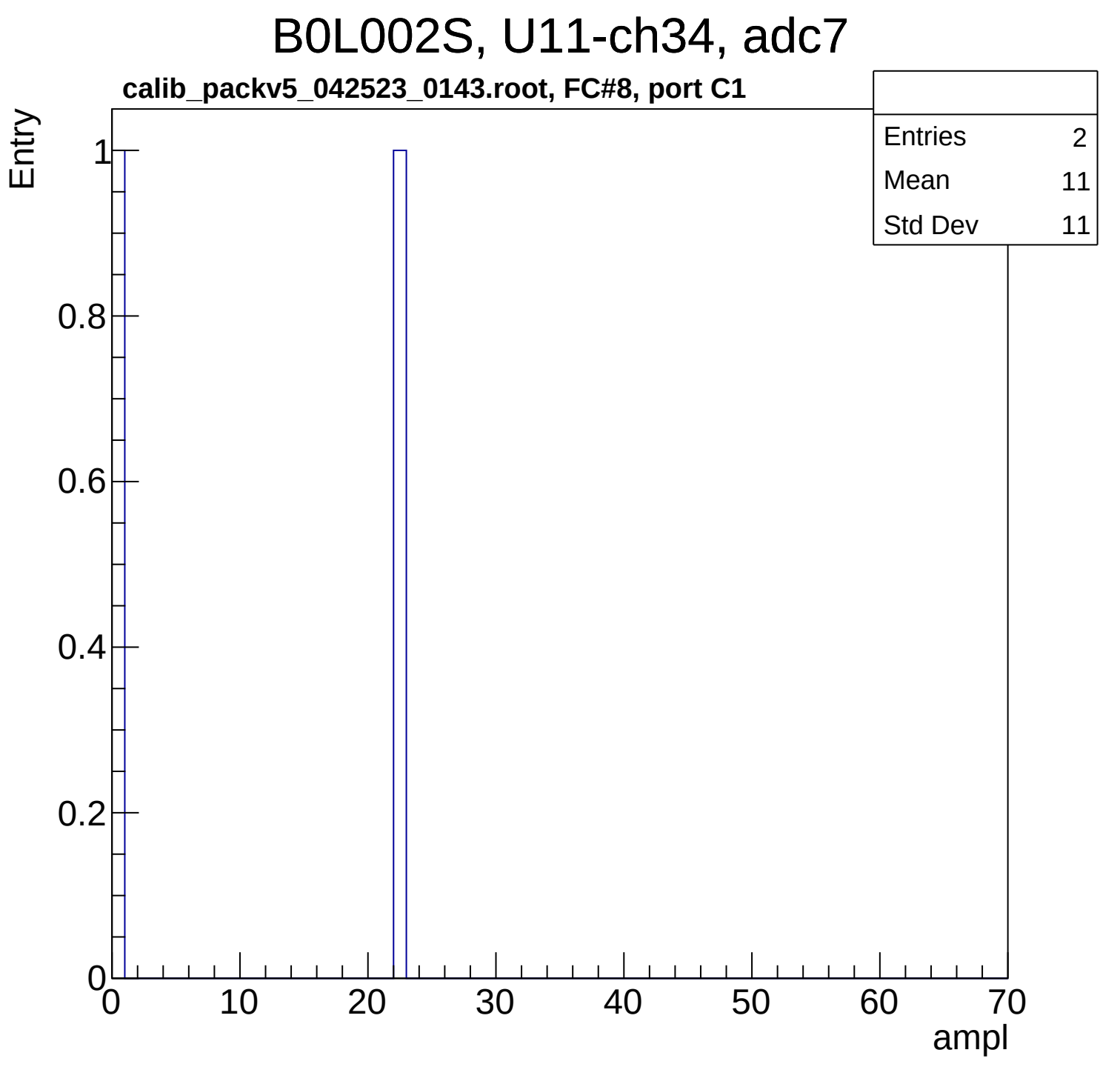

B0L002S, U11-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
0
0
10
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50
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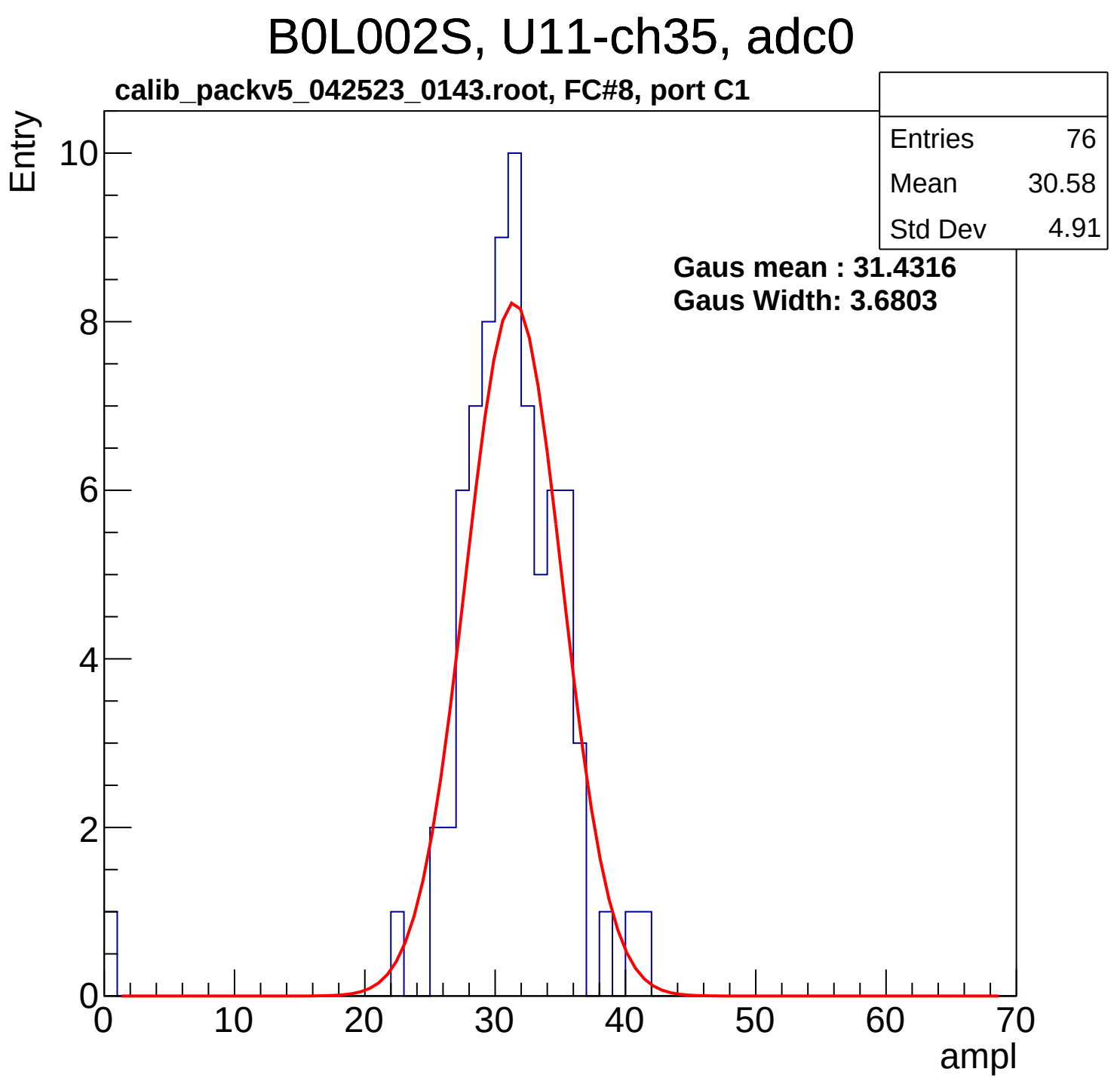
70
ampl


B0L002S, U11-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.58
Std Dev 4.91
Gaus mean : 31.4316
Gaus Width: 3.6803
Entry
ampl
0
2
4
6
8
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0
10
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40
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60
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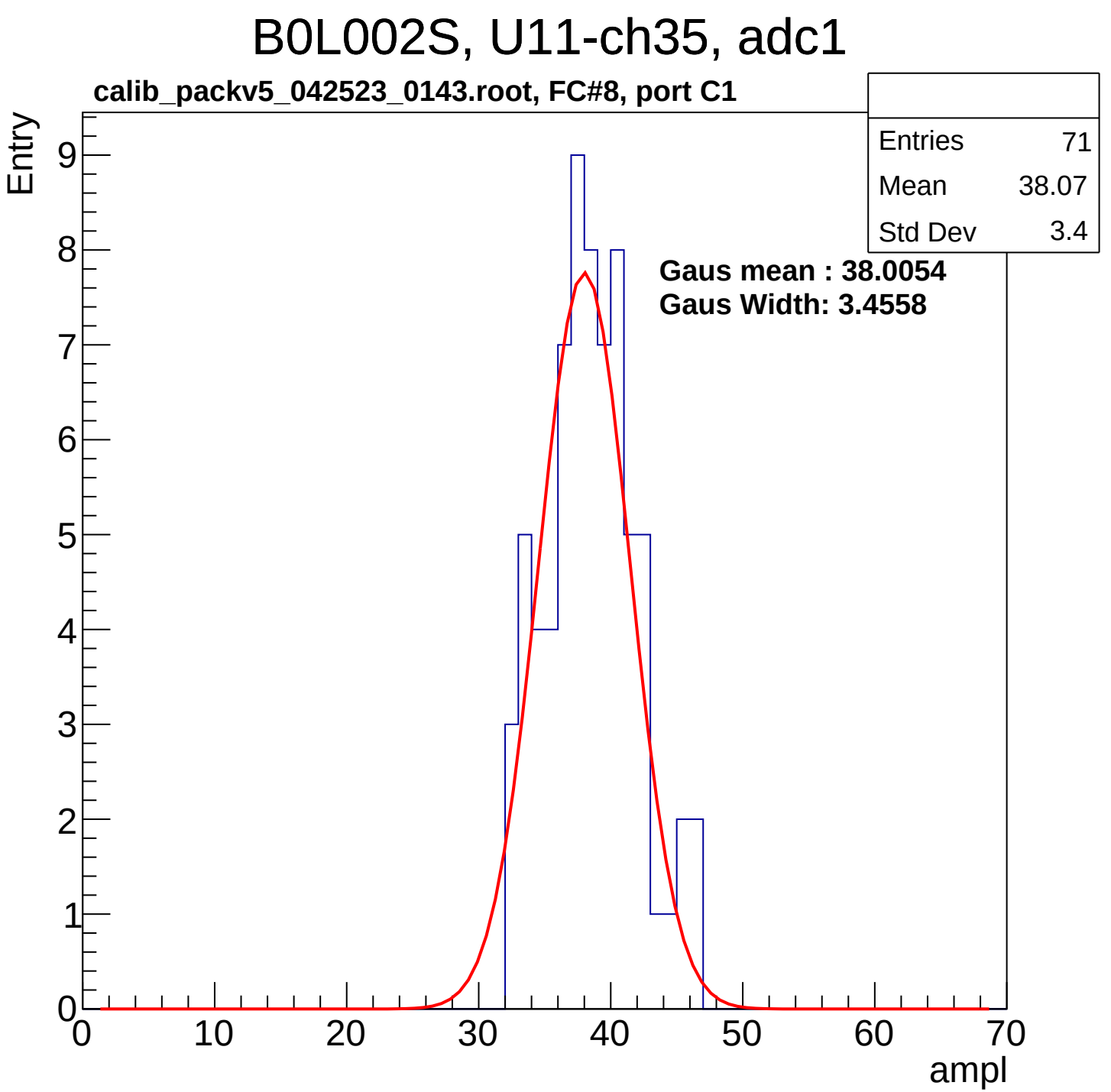
B0L002S, U11-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.07
Std Dev 3.4
Gaus mean : 38.0054
Gaus Width: 3.4558
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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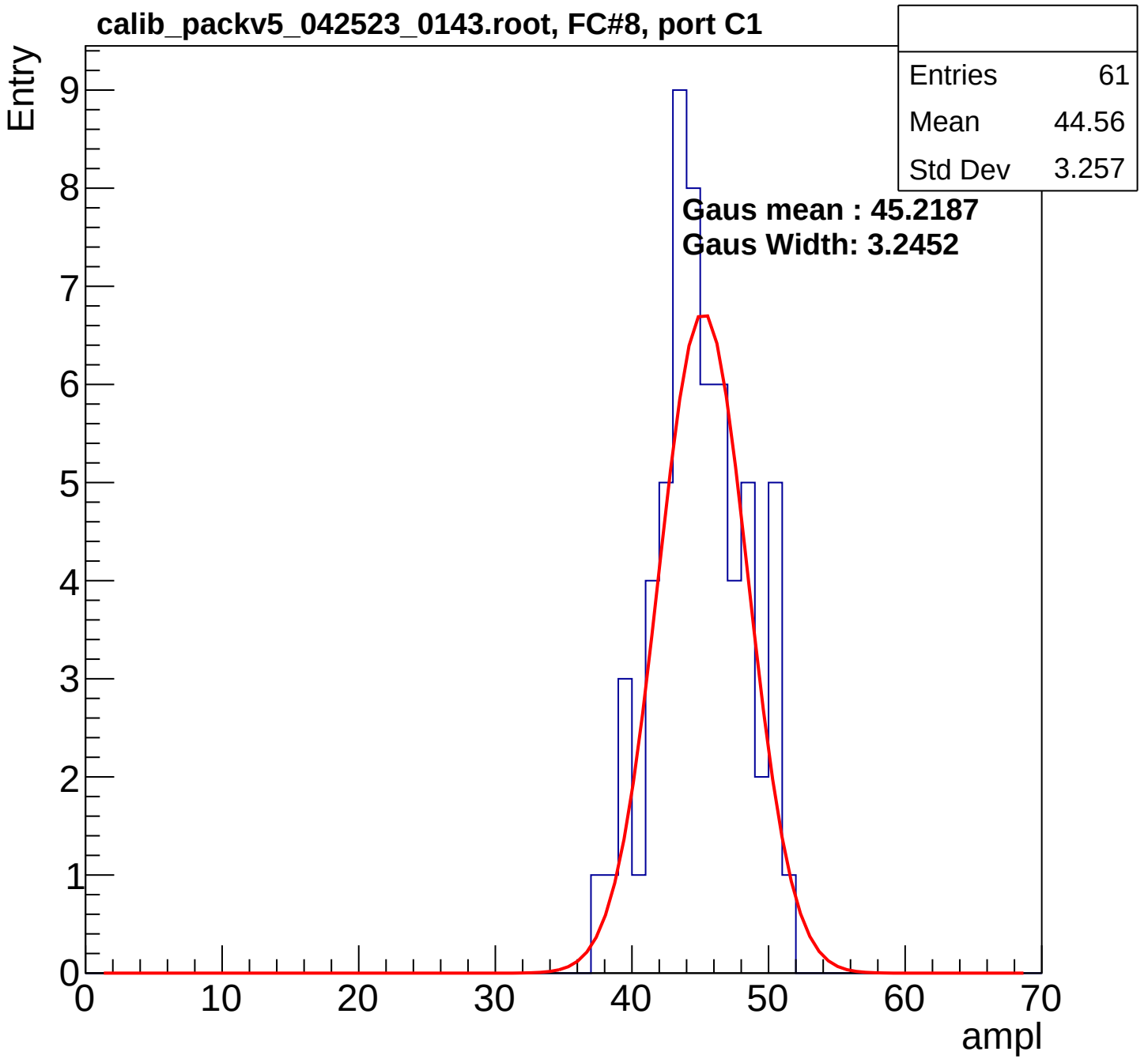
B0L002S, U11-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.56
Std Dev 3.257
Gaus mean : 45.2187
Gaus Width: 3.2452
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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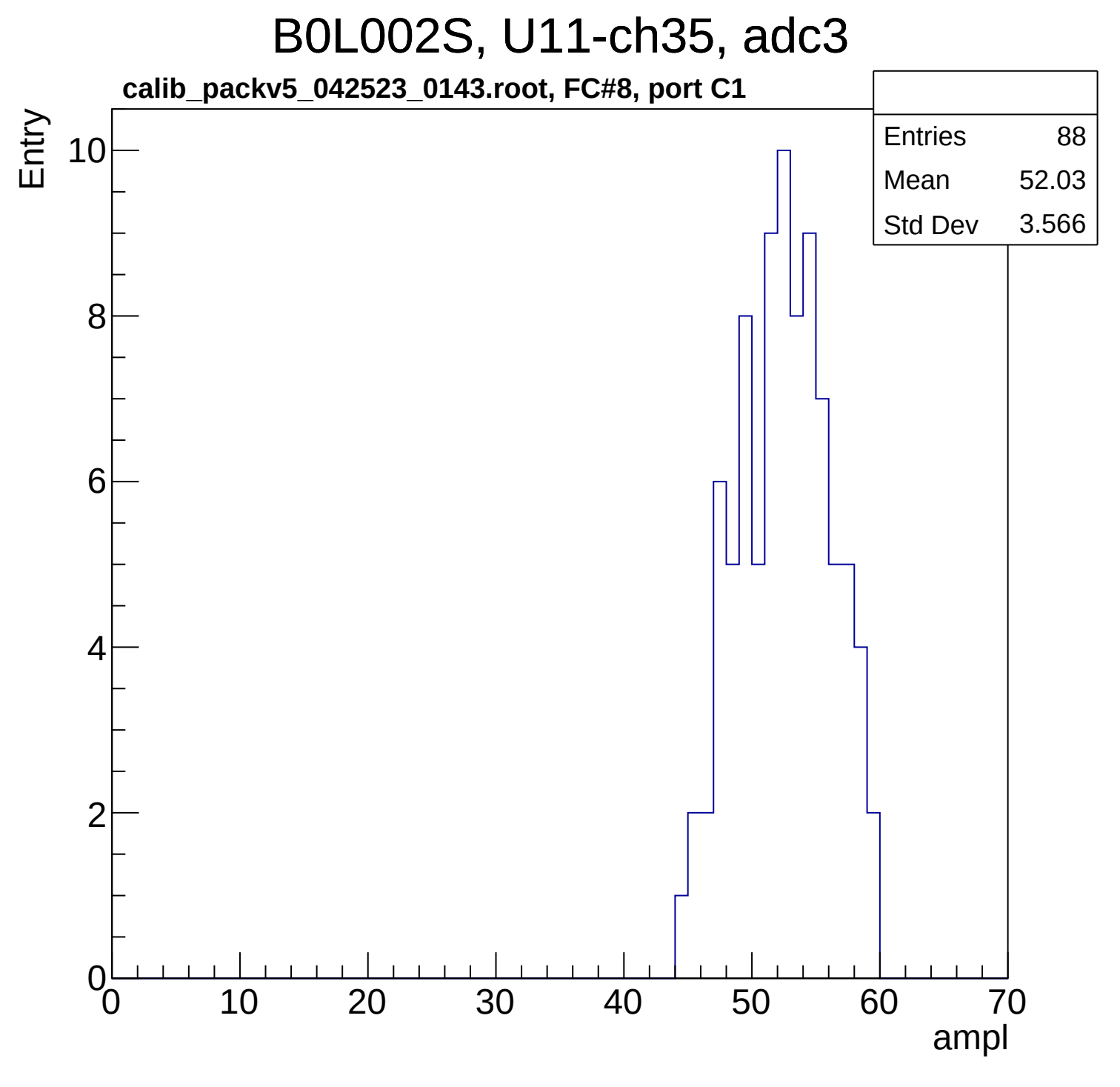
B0L002S, U11-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 52.03
Std Dev 3.566
Entry
ampl
0
2
4
6
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0
10
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40
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60
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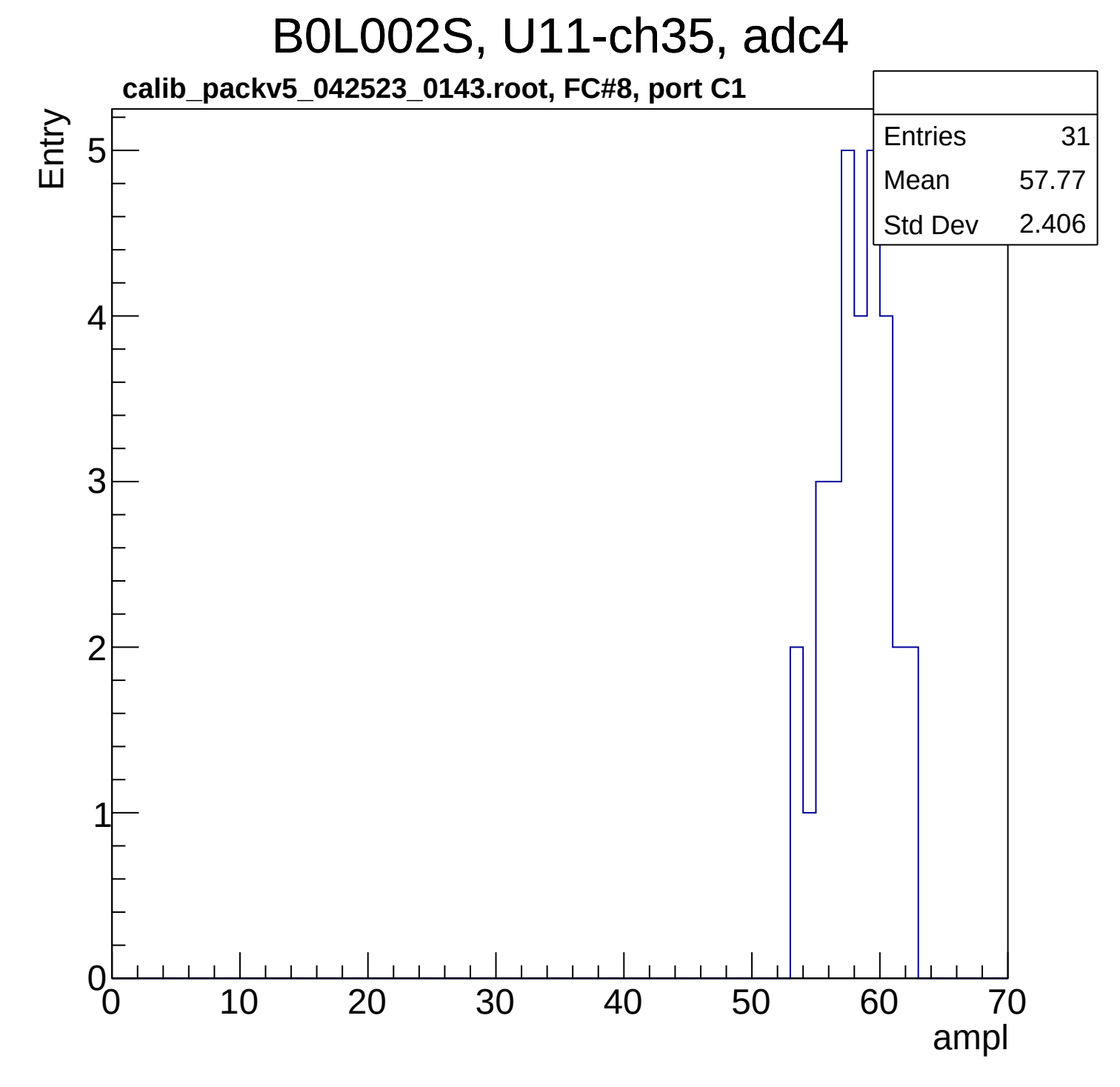
B0L002S, U11-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 57.77
Std Dev 2.406
Entry
ampl
0
1
2
3
4
5
0
10
20
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40
50
60
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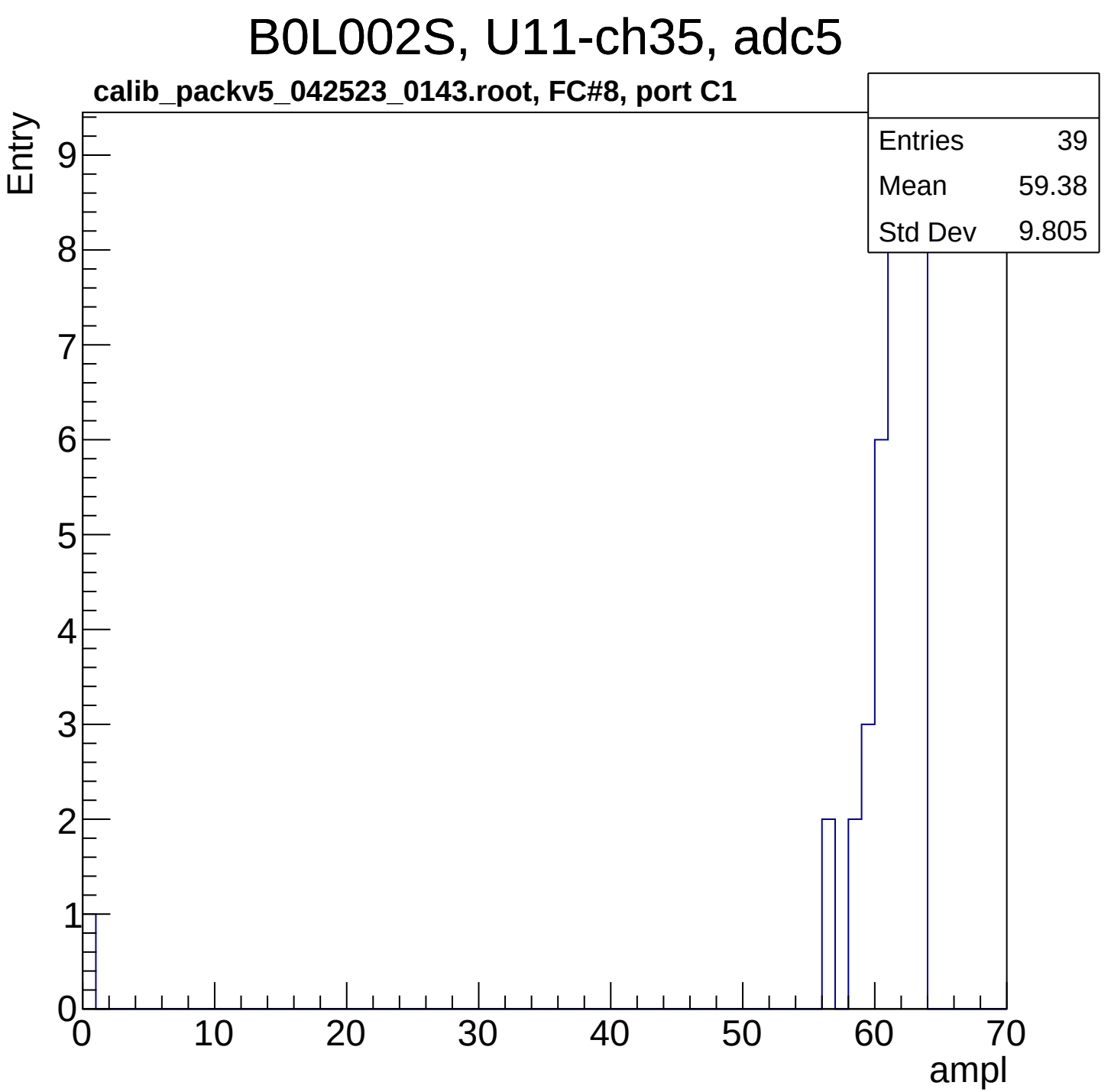
B0L002S, U11-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.38
Std Dev 9.805
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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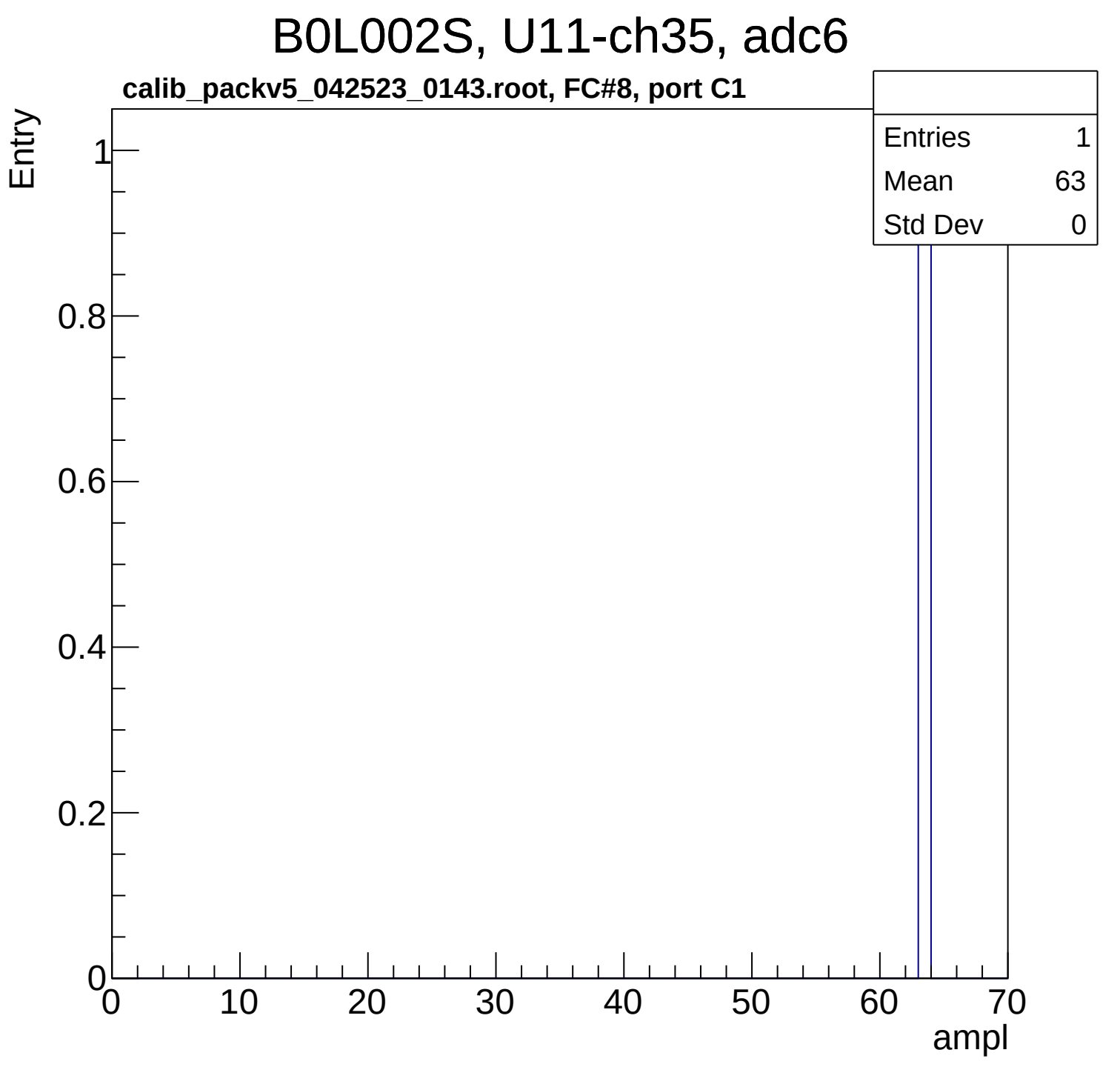
B0L002S, U11-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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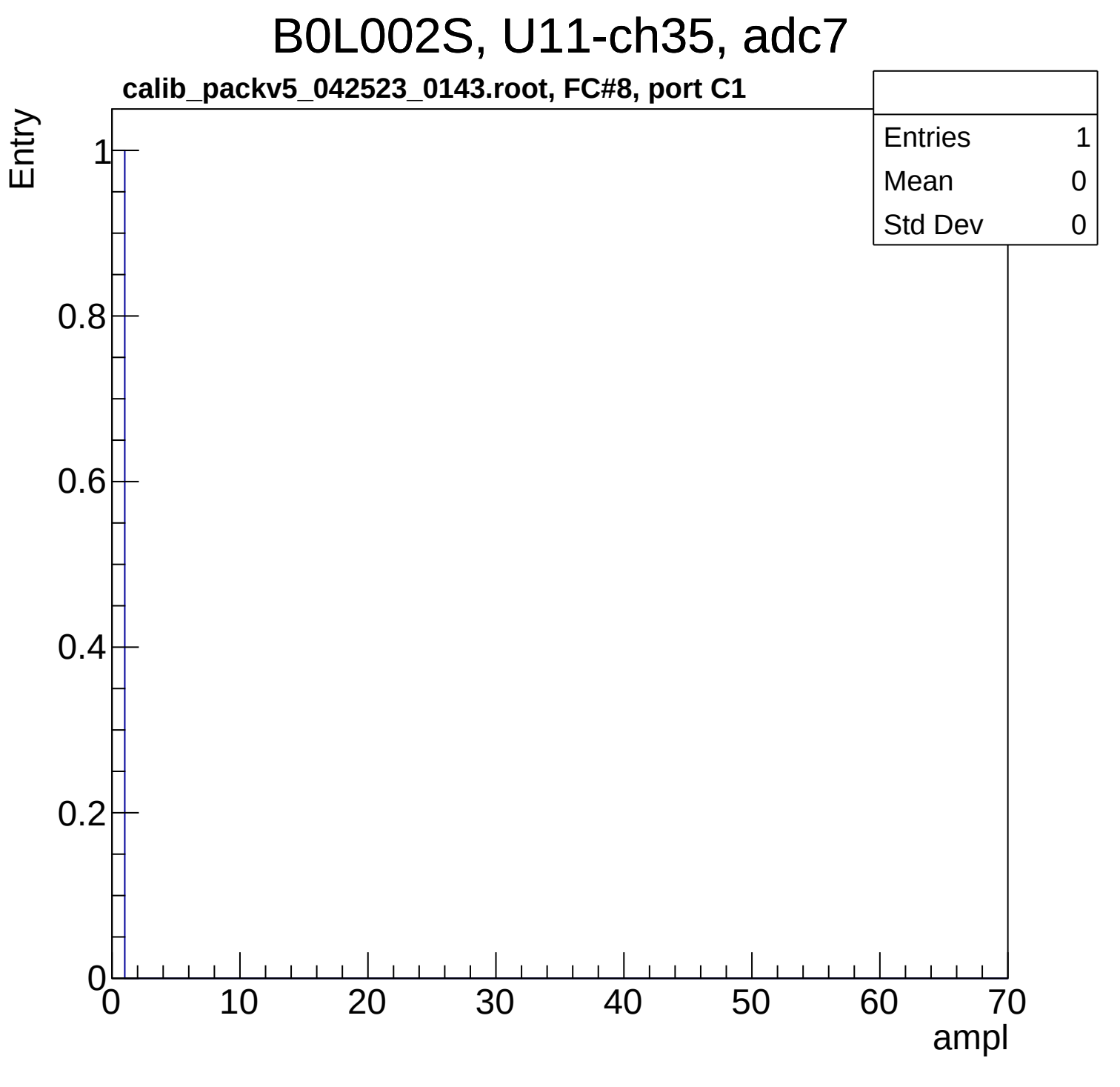
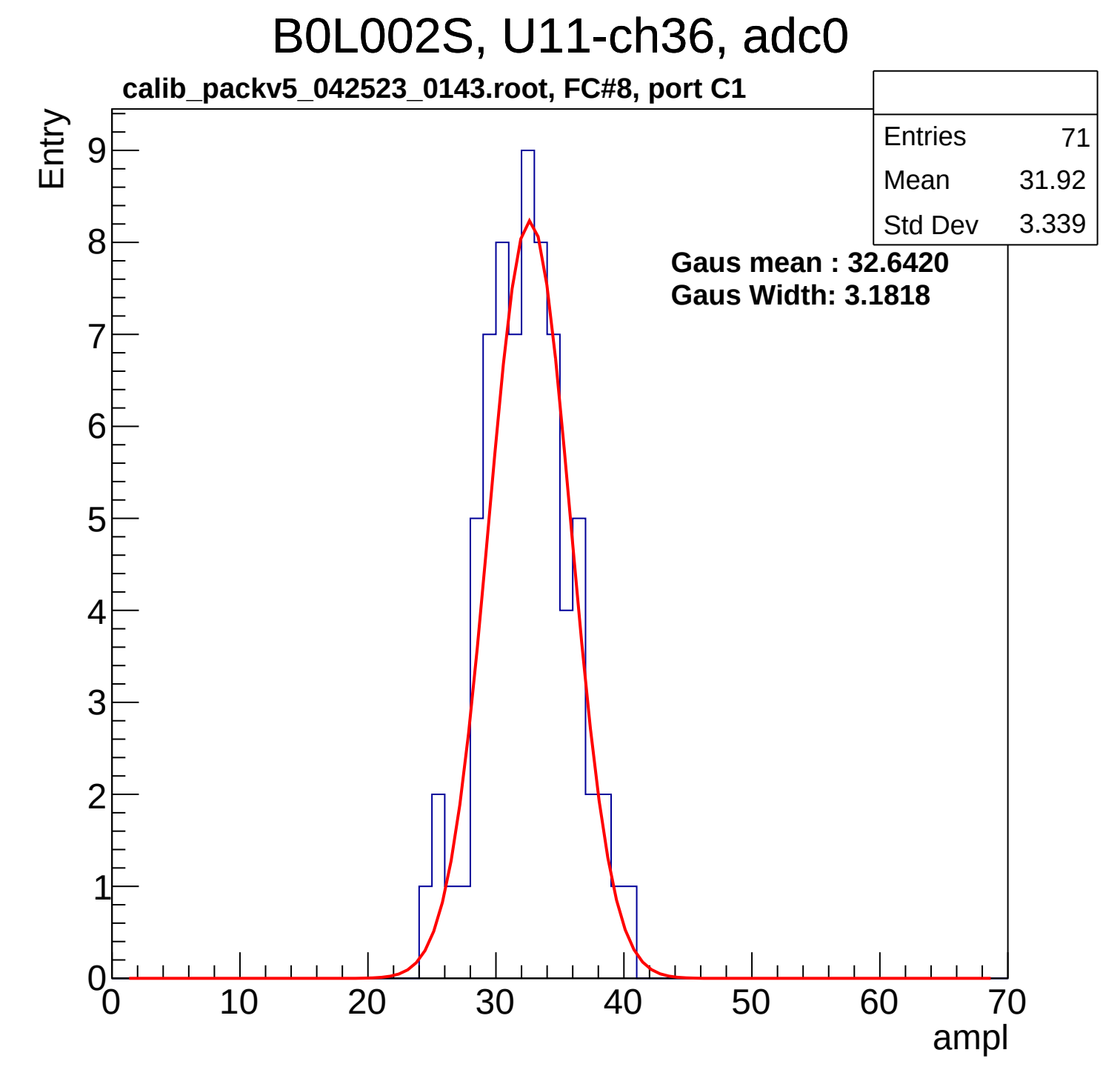
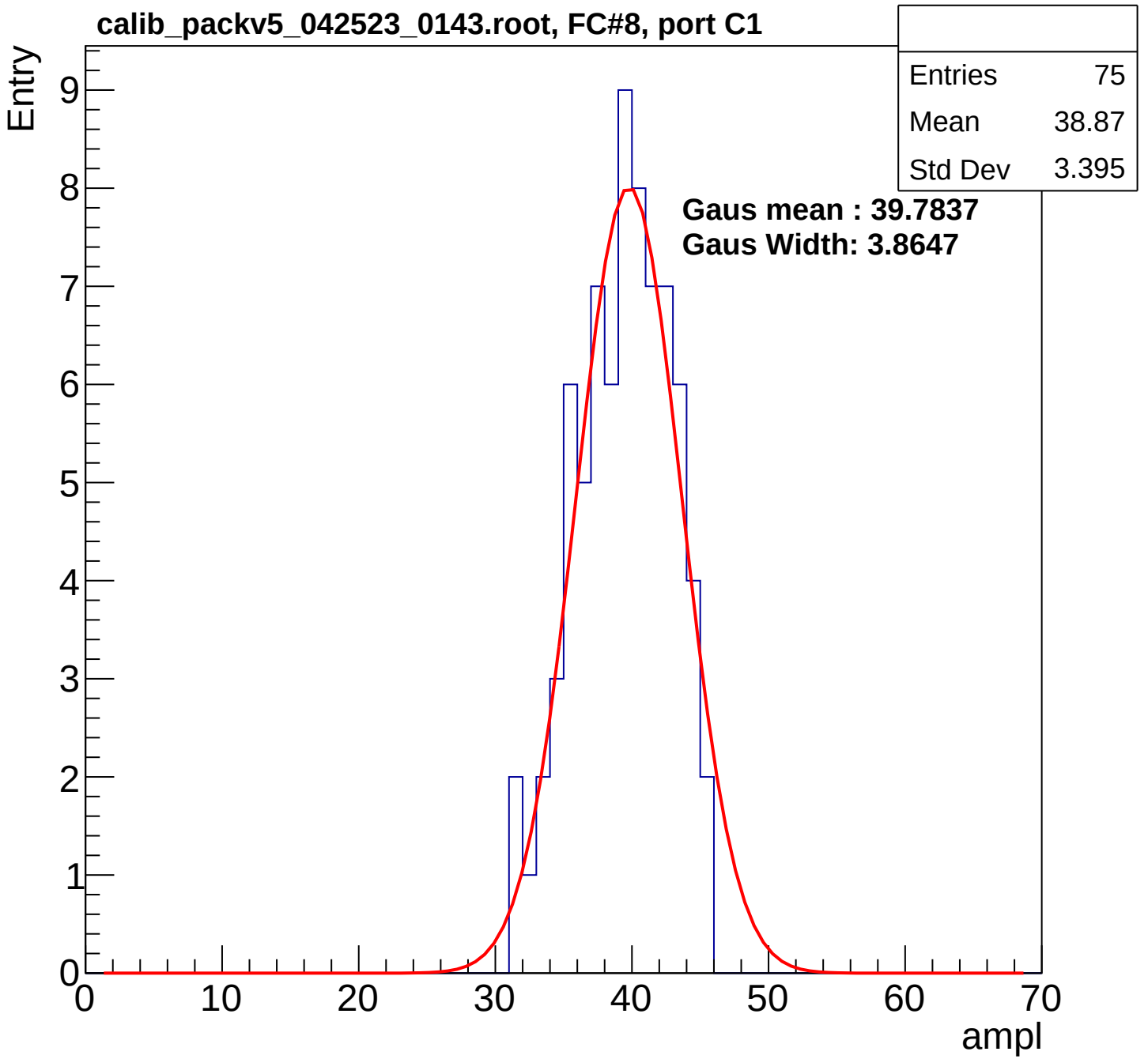
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.92
Std Dev 3.339
Gaus mean : 32.6420
Gaus Width: 3.1818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 38.87
Std Dev 3.395
Gaus mean : 39.7837
Gaus Width: 3.8647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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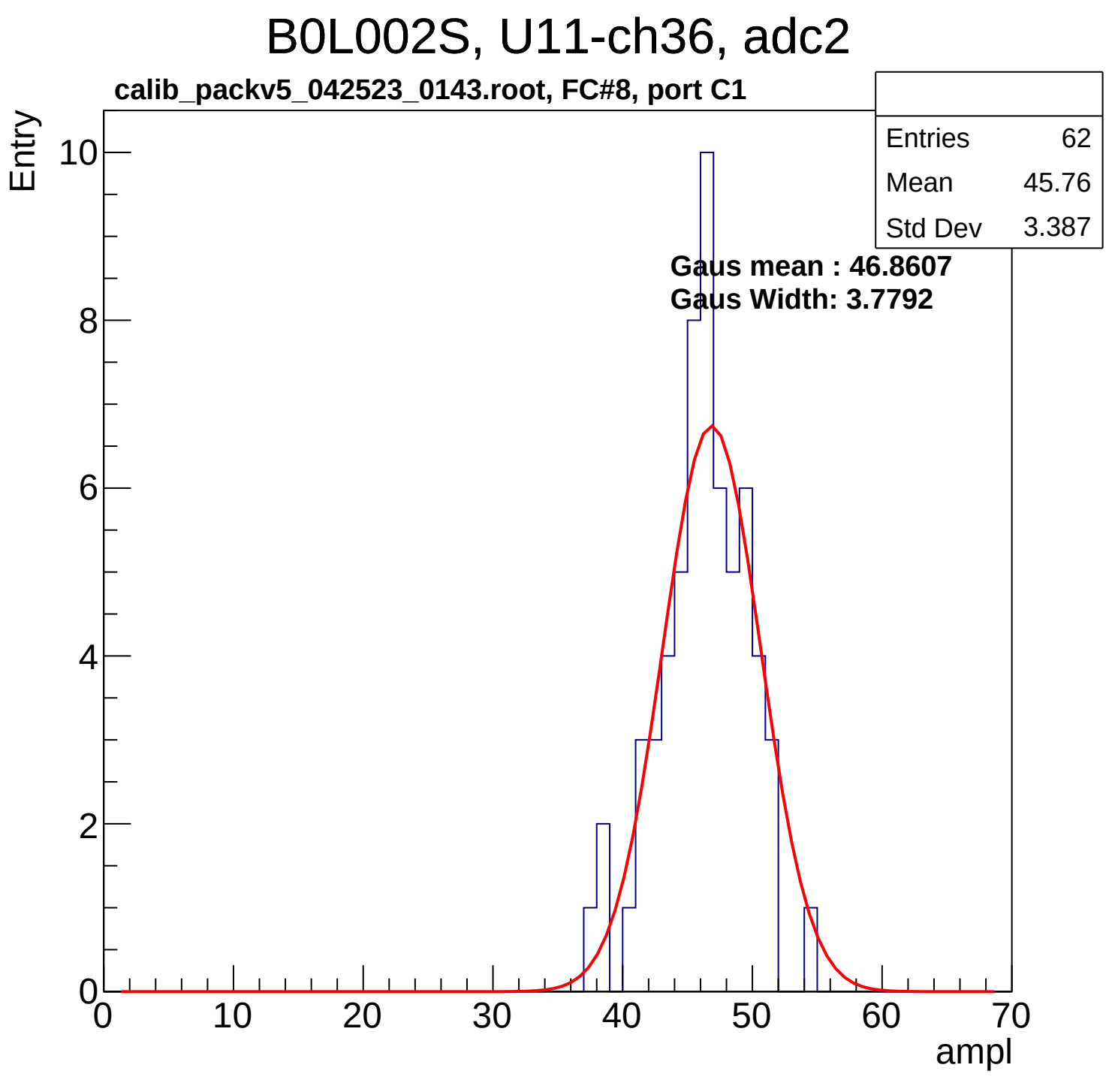
B0L002S, U11-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.76
Std Dev 3.387
Gaus mean : 46.8607
Gaus Width: 3.7792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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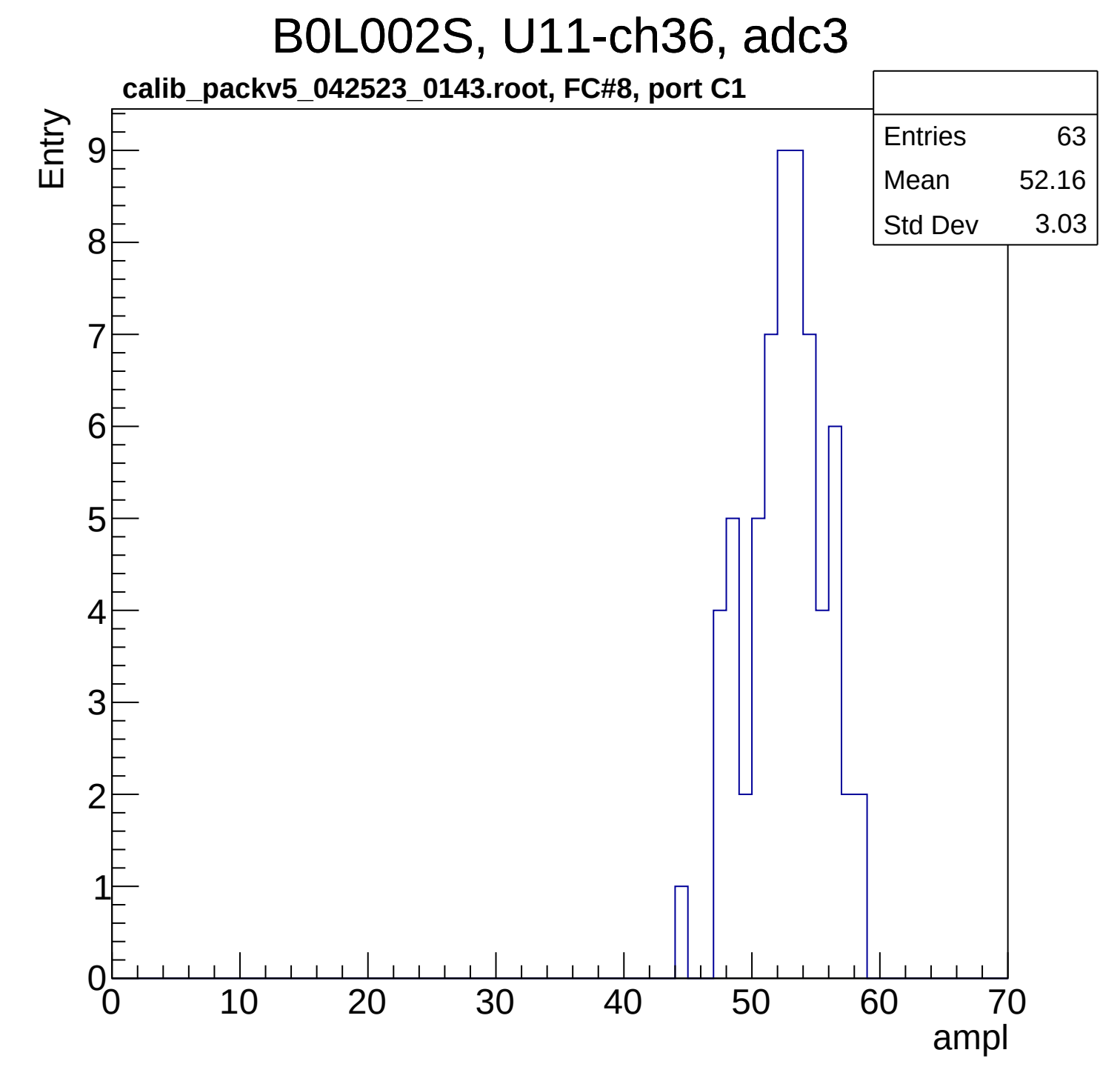
B0L002S, U11-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52.16
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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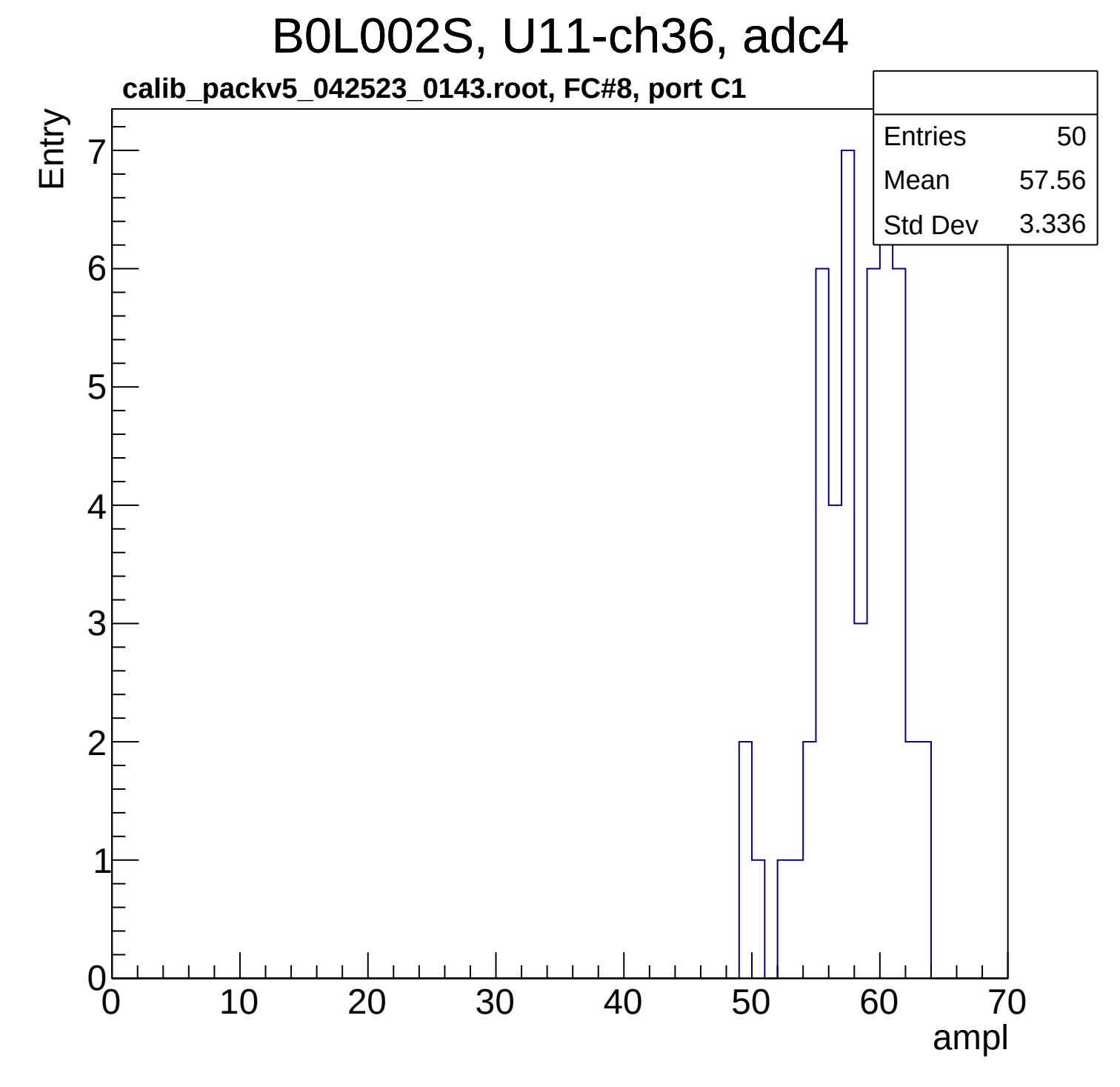
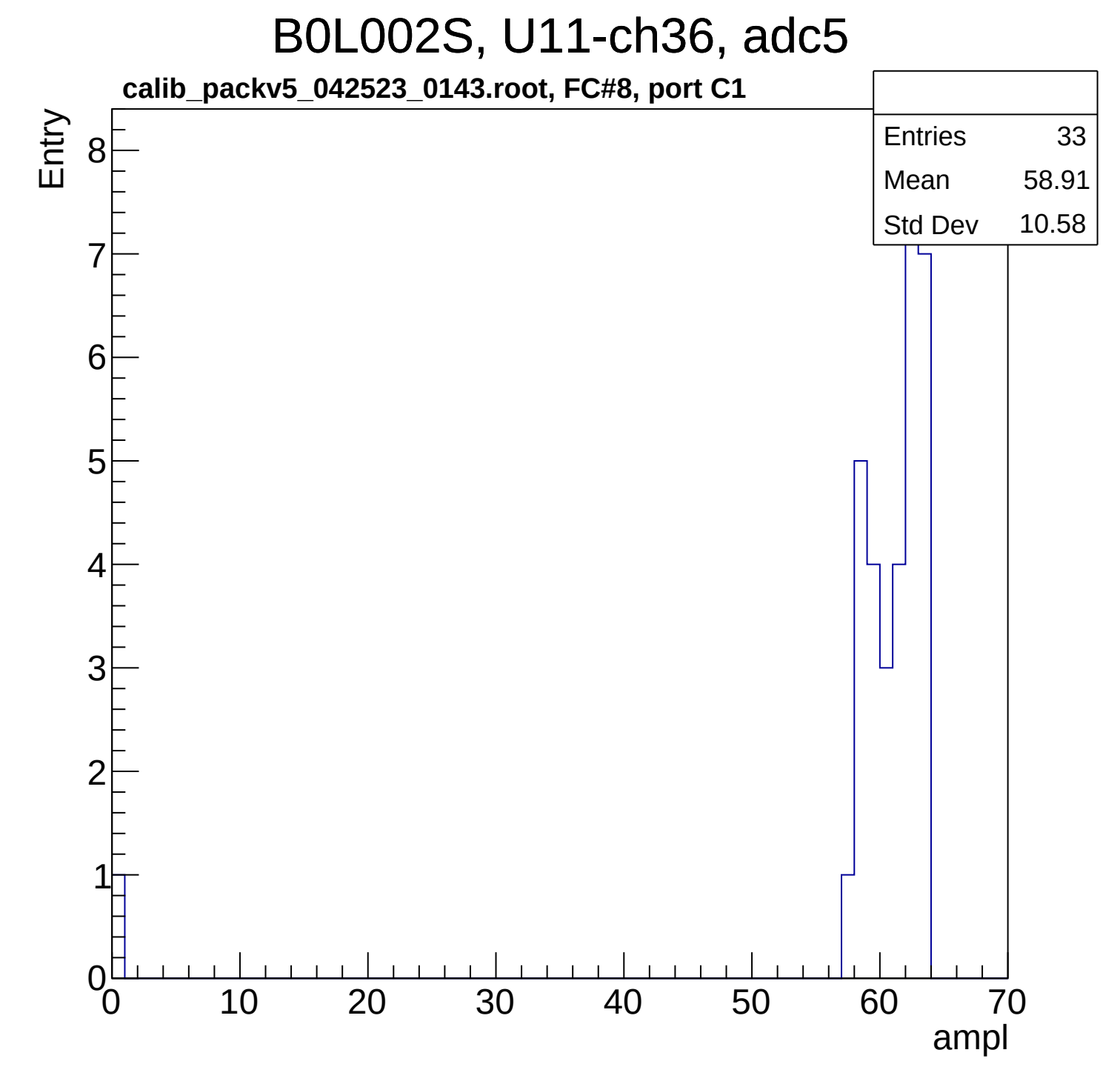

B0L002S, U11-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.56
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.91
Std Dev 10.58
Entry
ampl
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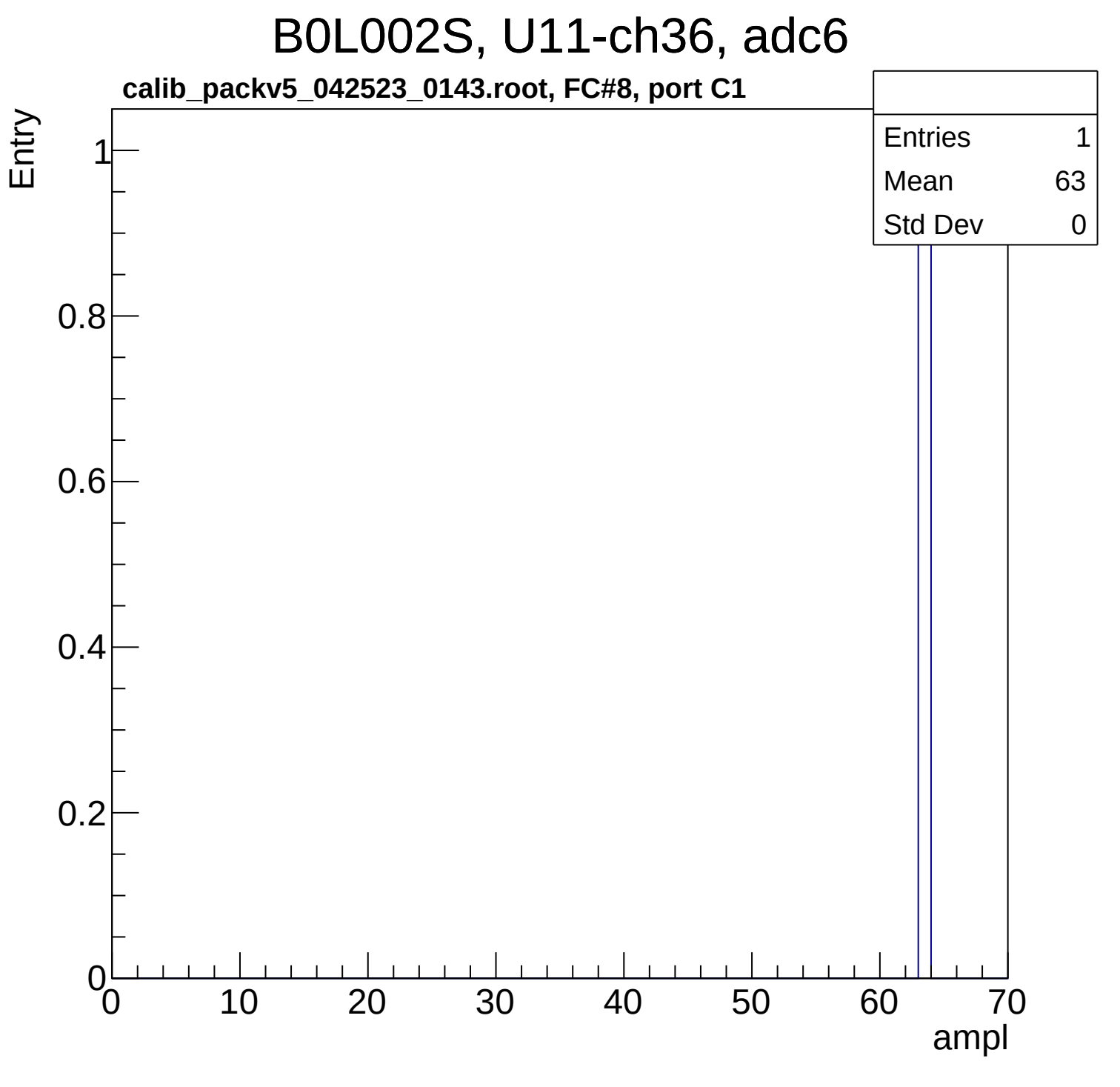
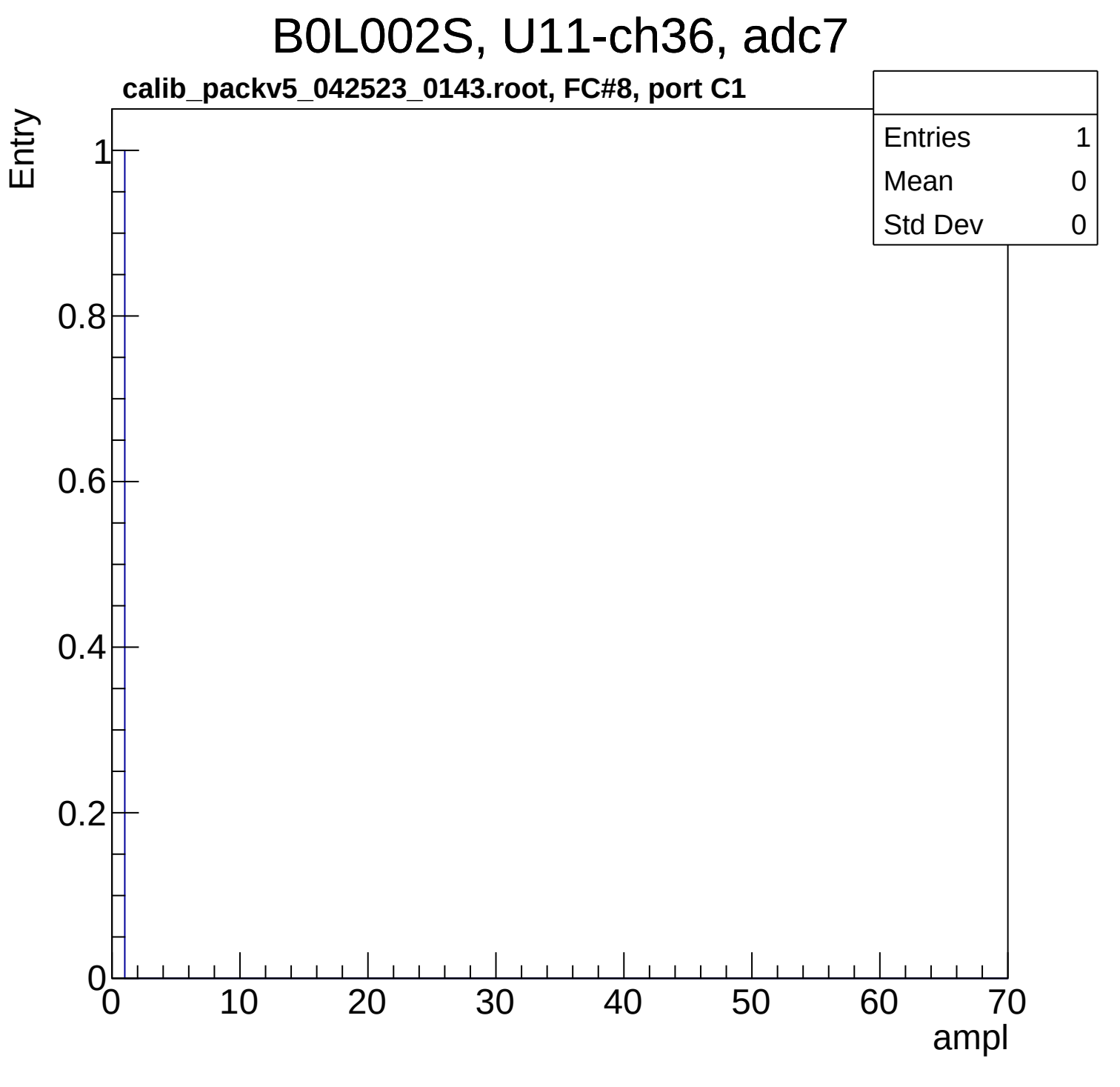
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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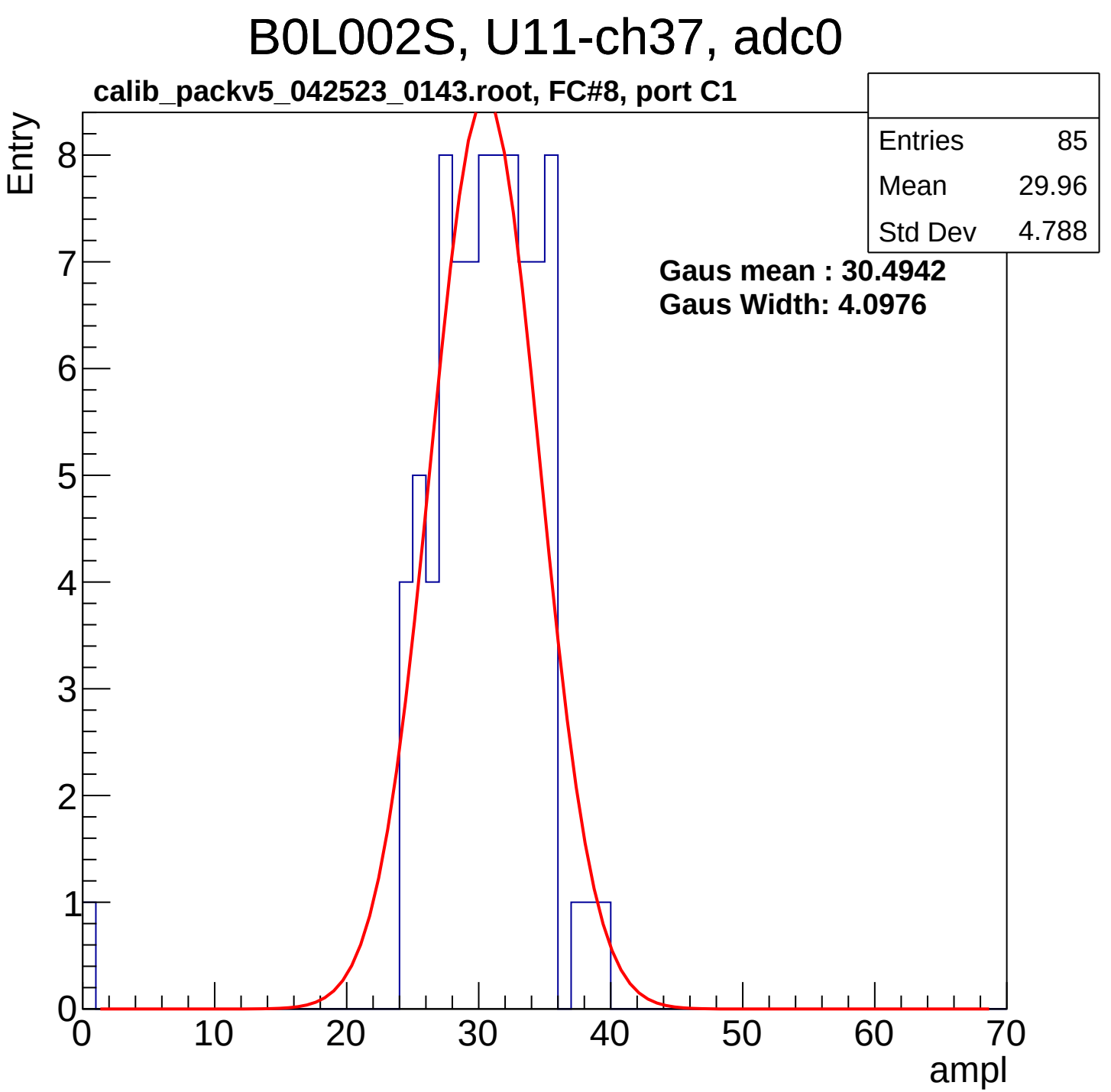
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 29.96
Std Dev 4.788
Gaus mean : 30.4942
Gaus Width: 4.0976
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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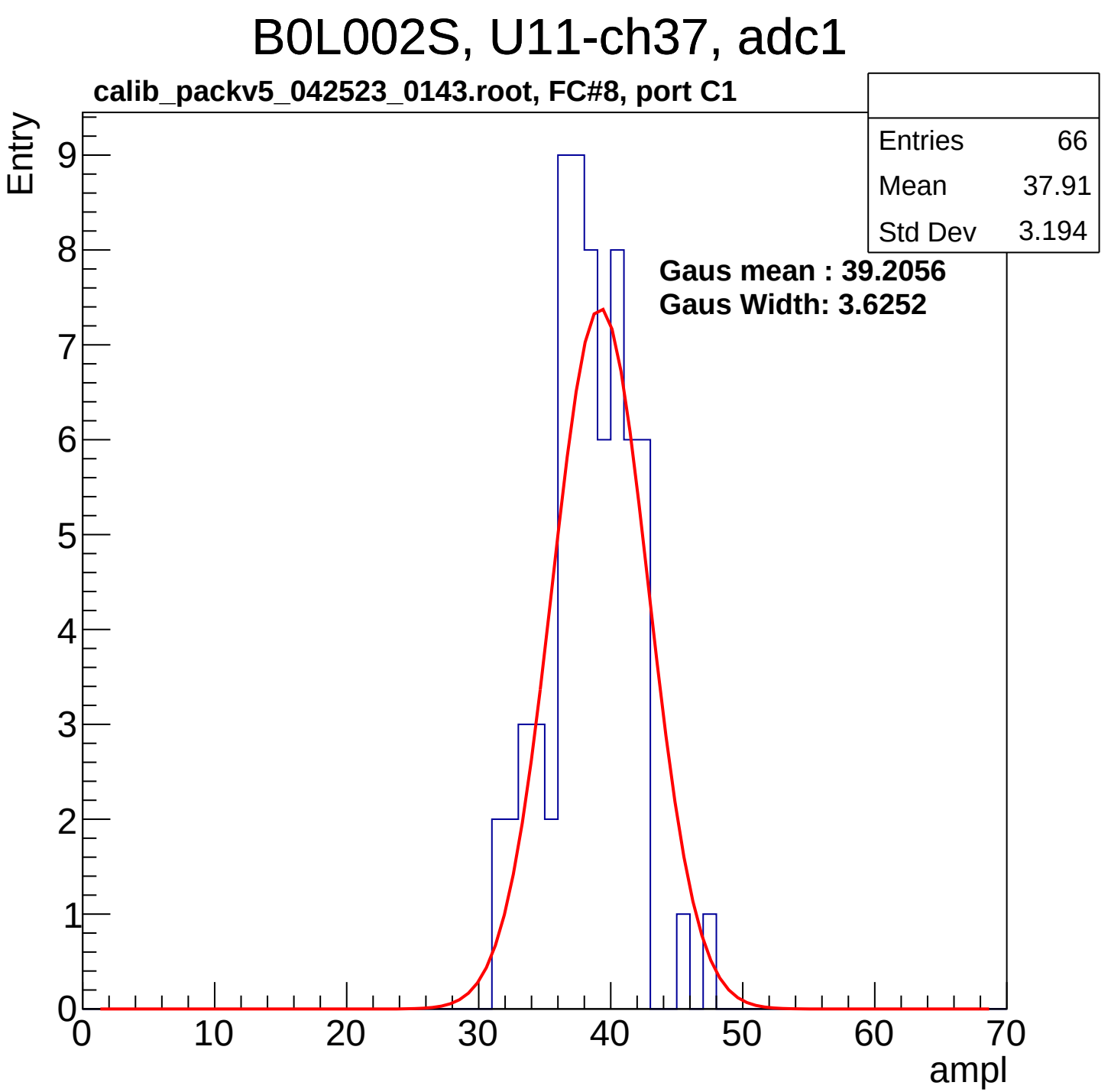
B0L002S, U11-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.91
Std Dev 3.194
Gaus mean : 39.2056
Gaus Width: 3.6252
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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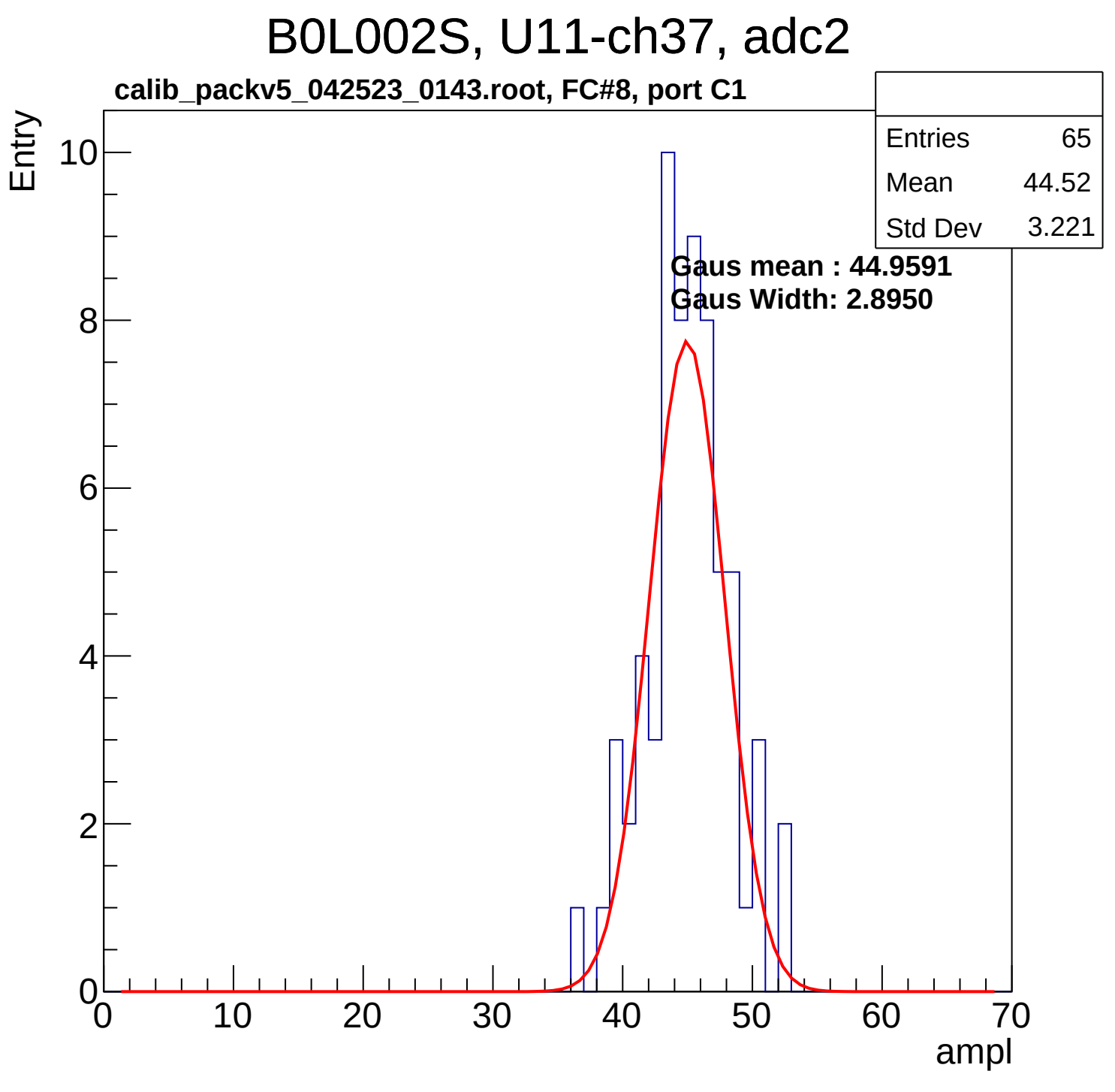
40
50
60
70


B0L002S, U11-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.52
Std Dev 3.221
Gaus mean : 44.9591
Gaus Width: 2.8950
Entry
ampl
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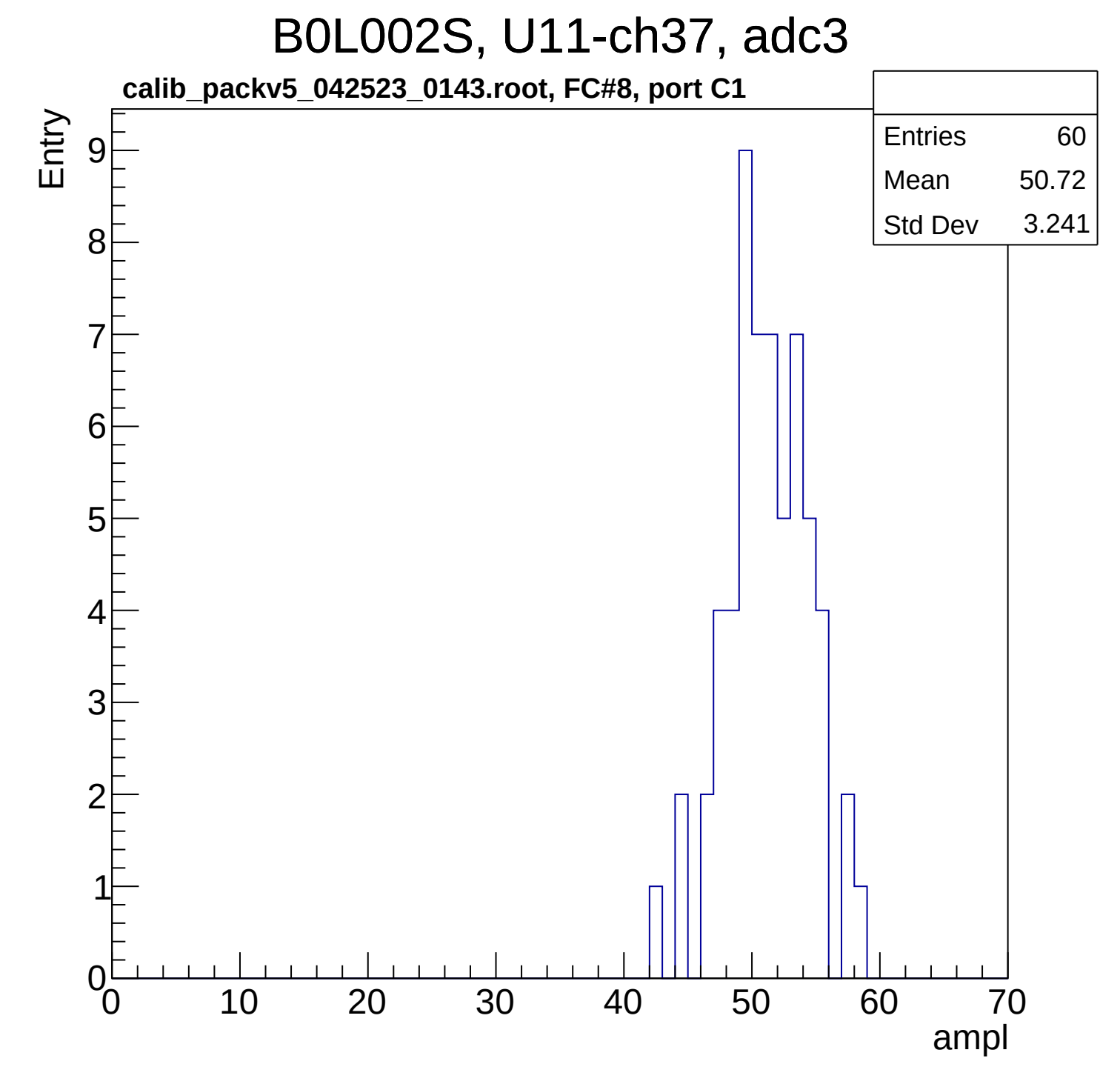
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.72
Std Dev 3.241
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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50
60
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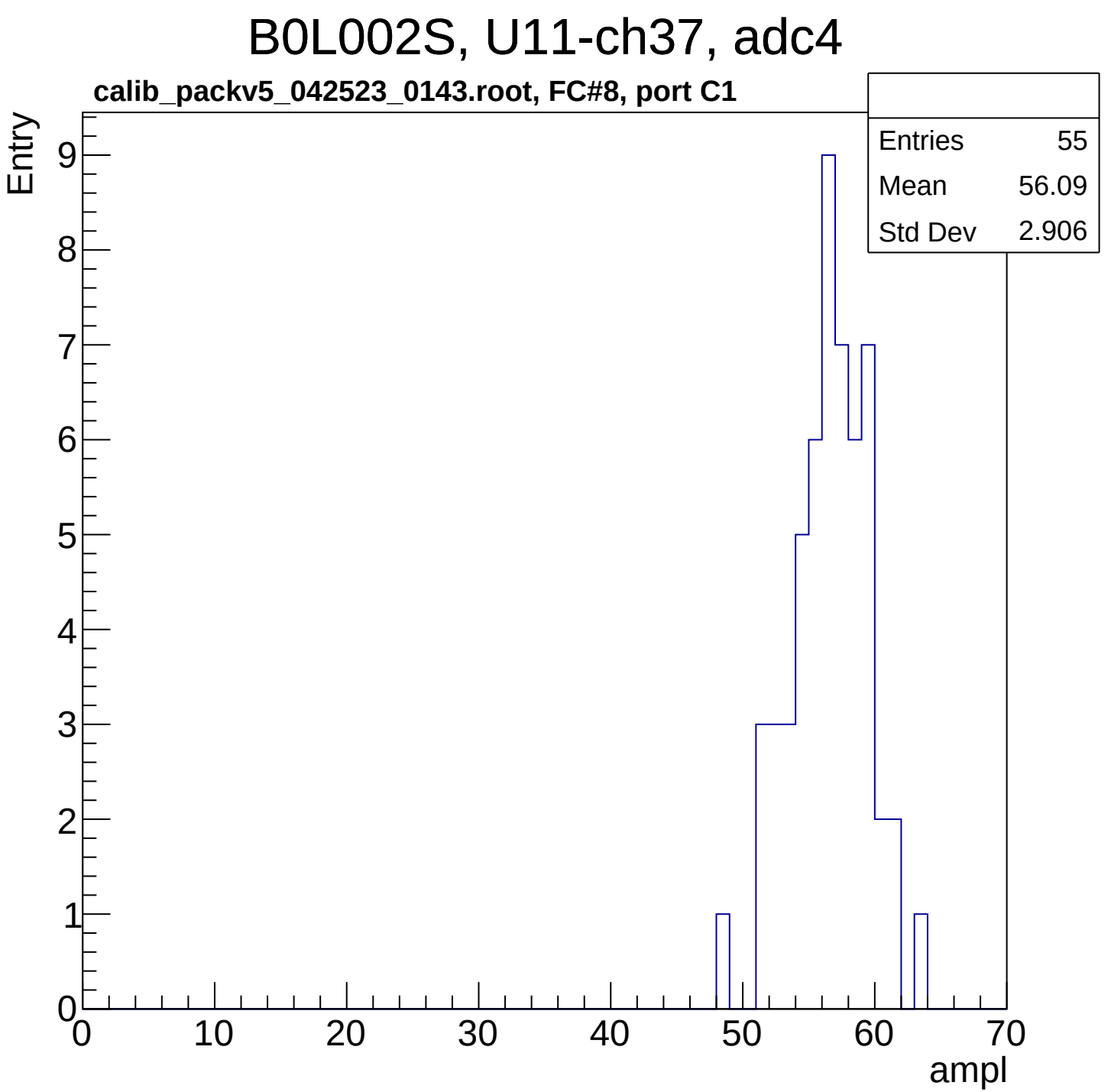
B0L002S, U11-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.09
Std Dev 2.906
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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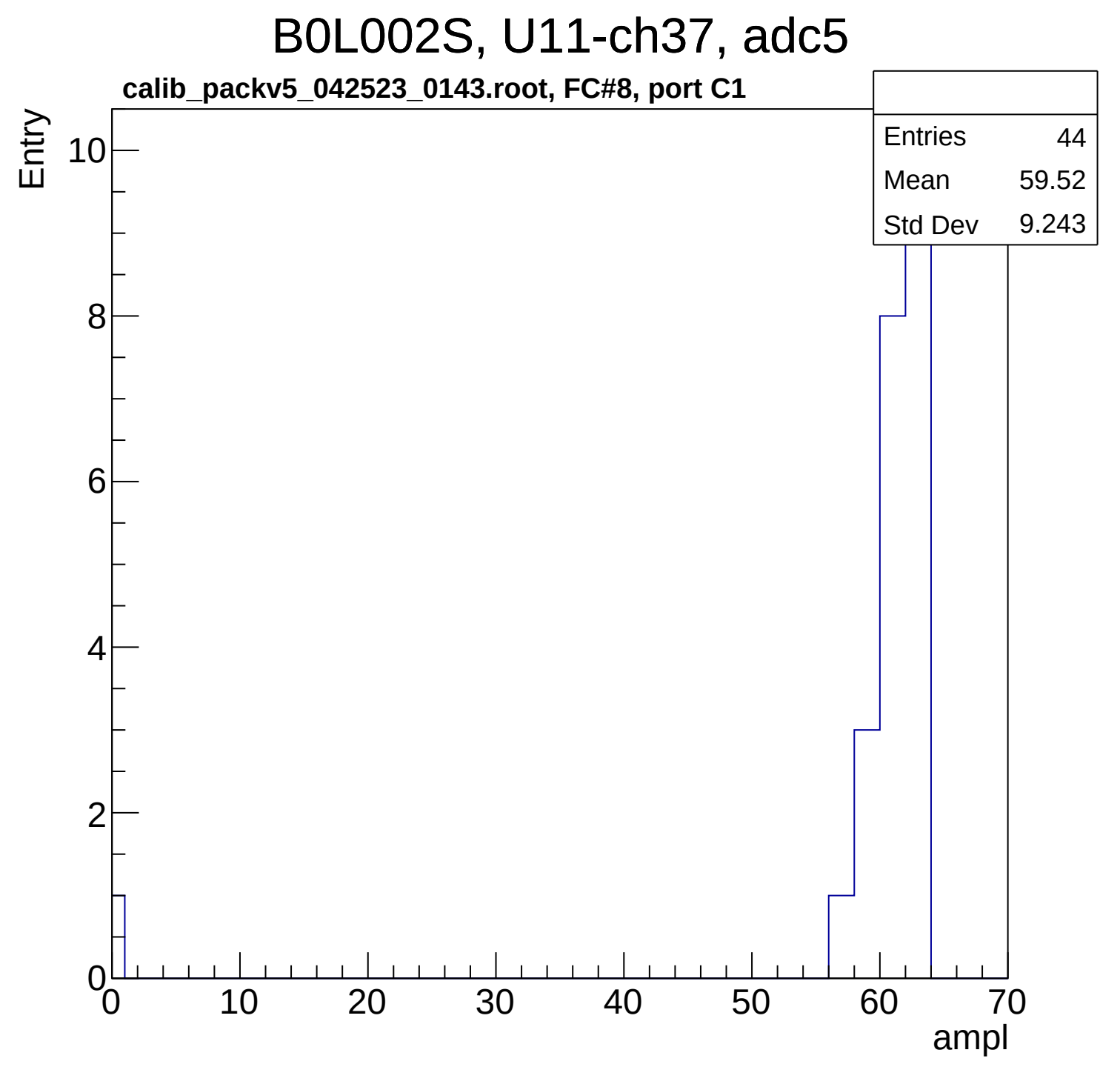
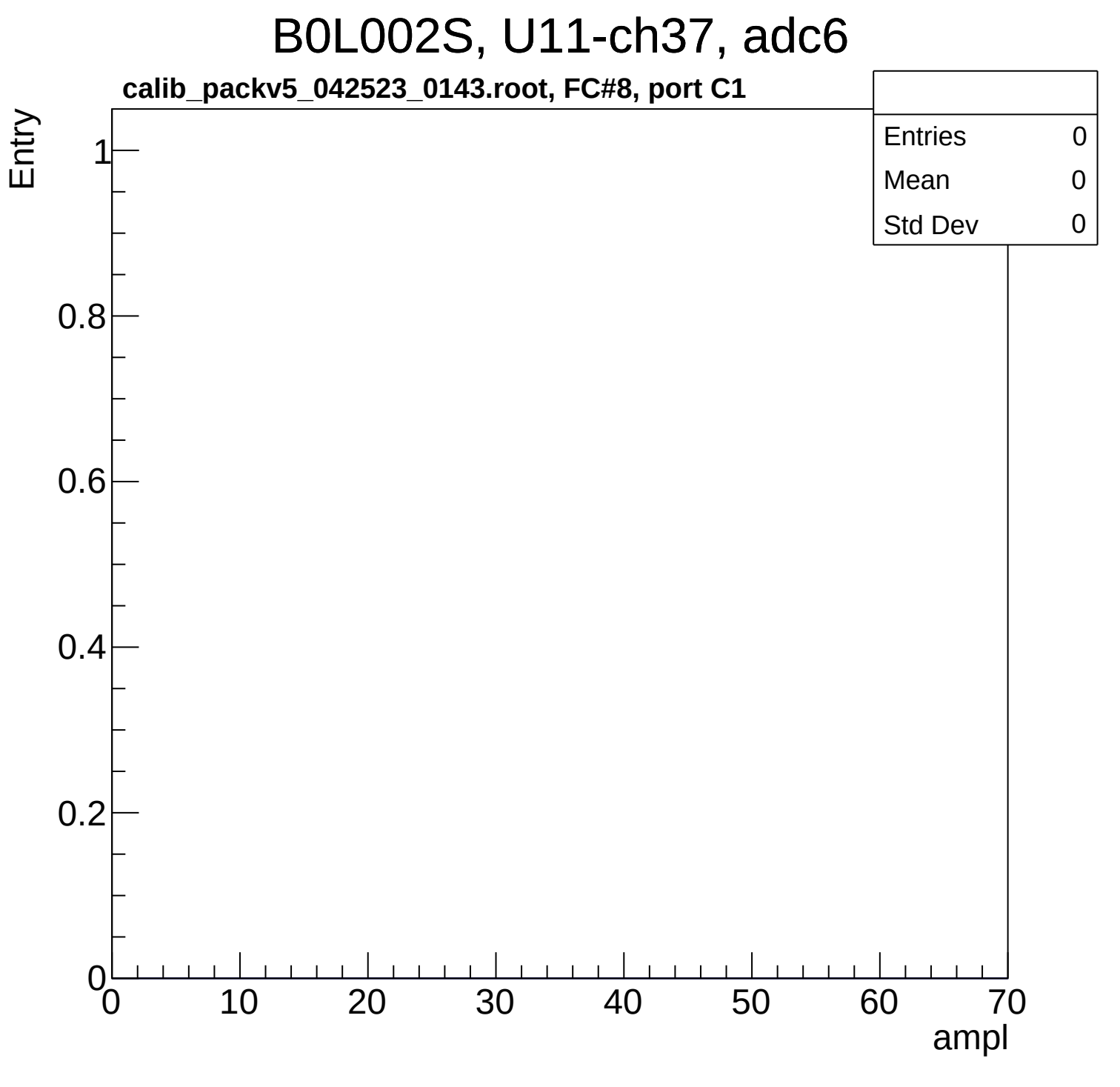

B0L002S, U11-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.52
Std Dev 9.243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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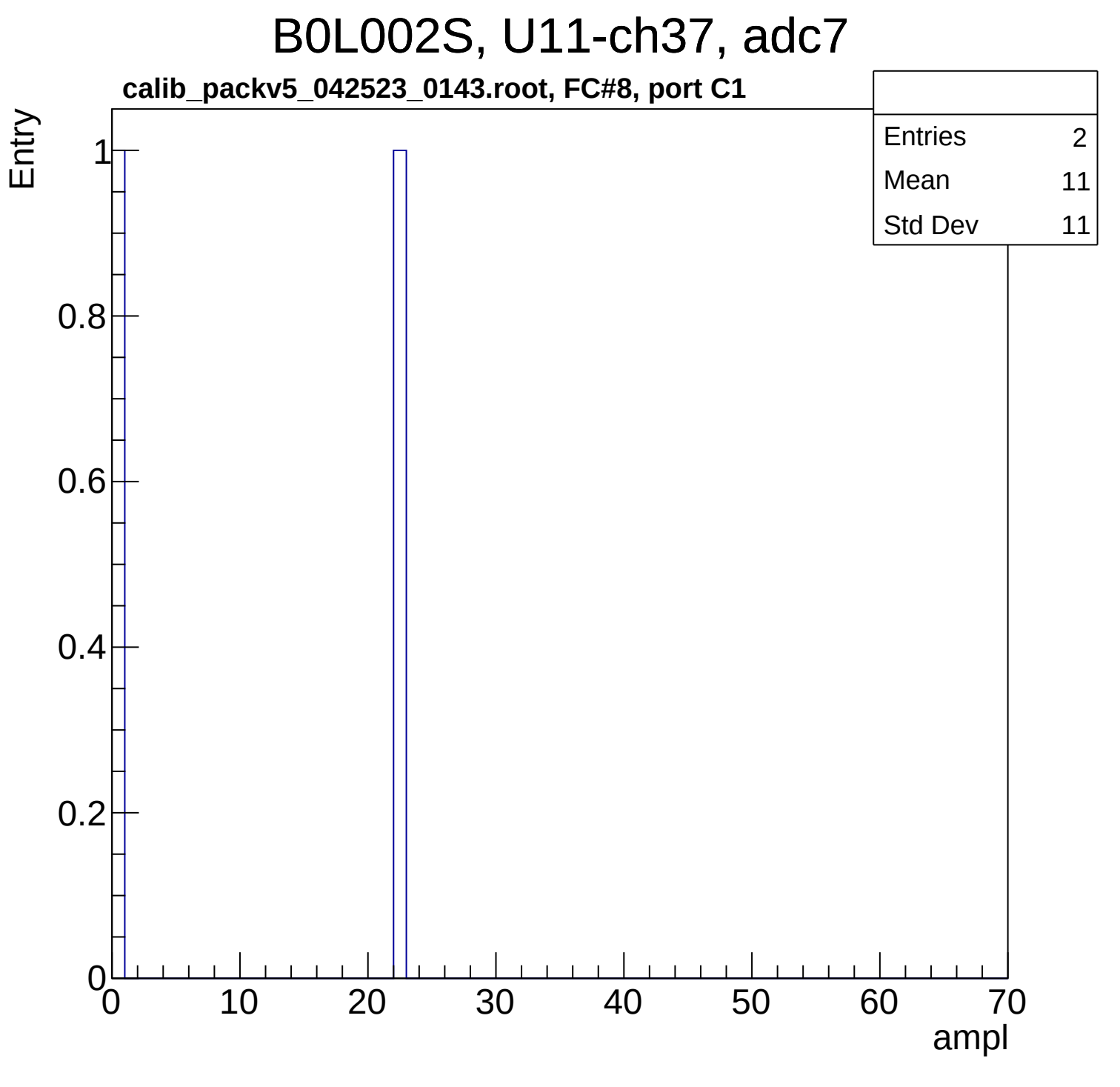
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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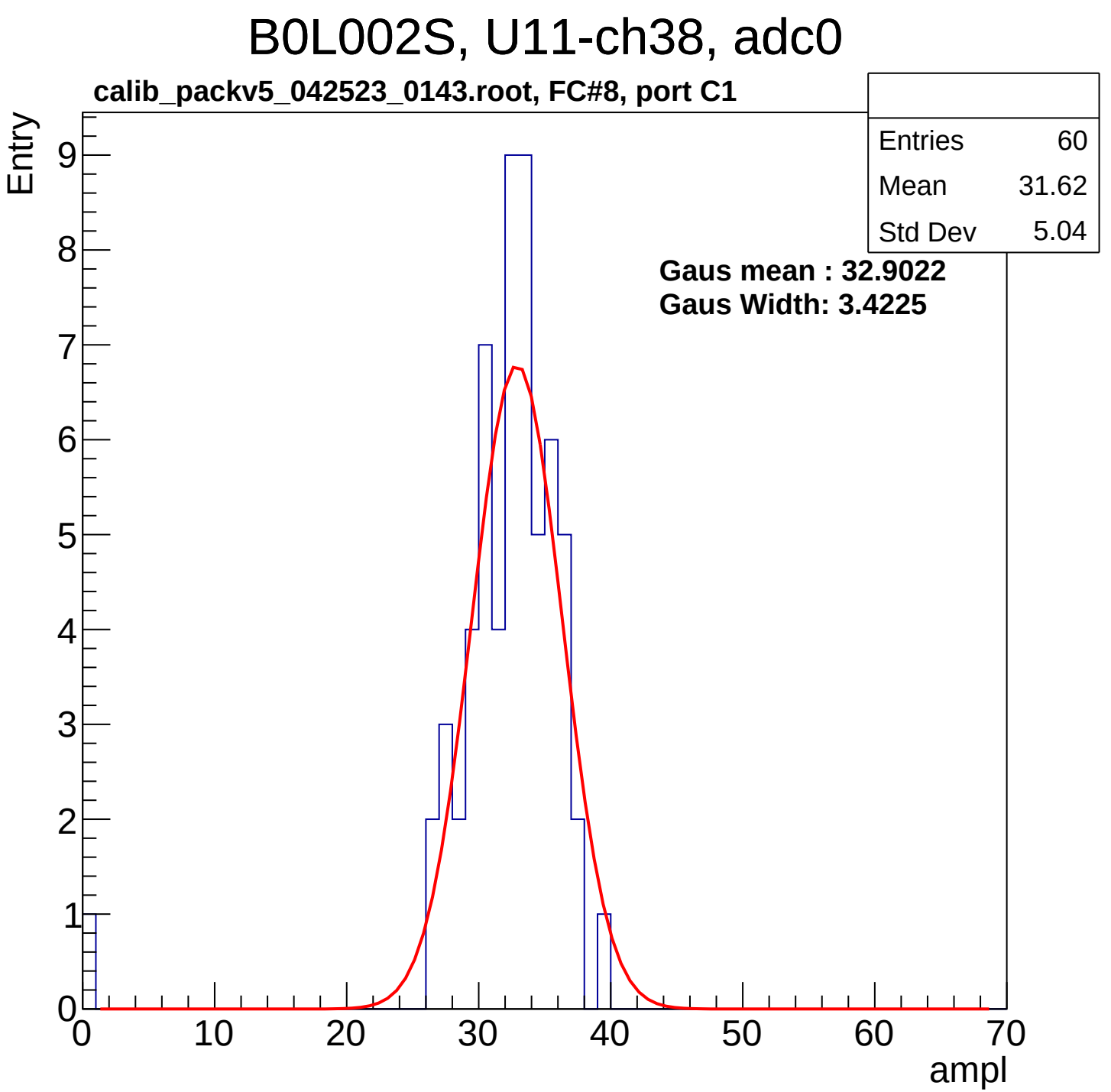
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.62
Std Dev 5.04
Gaus mean : 32.9022
Gaus Width: 3.4225
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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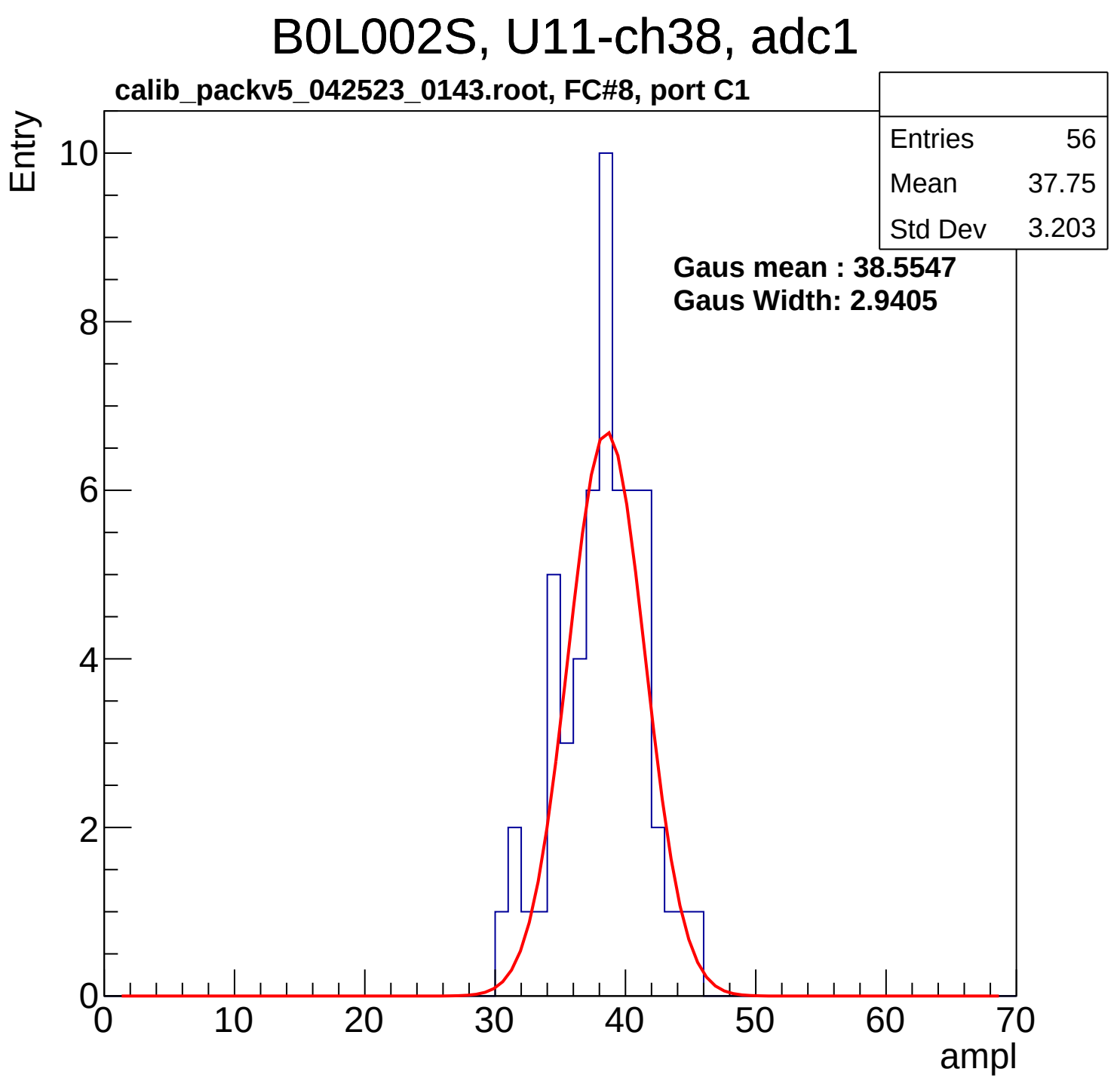
B0L002S, U11-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 37.75
Std Dev 3.203
Gaus mean : 38.5547
Gaus Width: 2.9405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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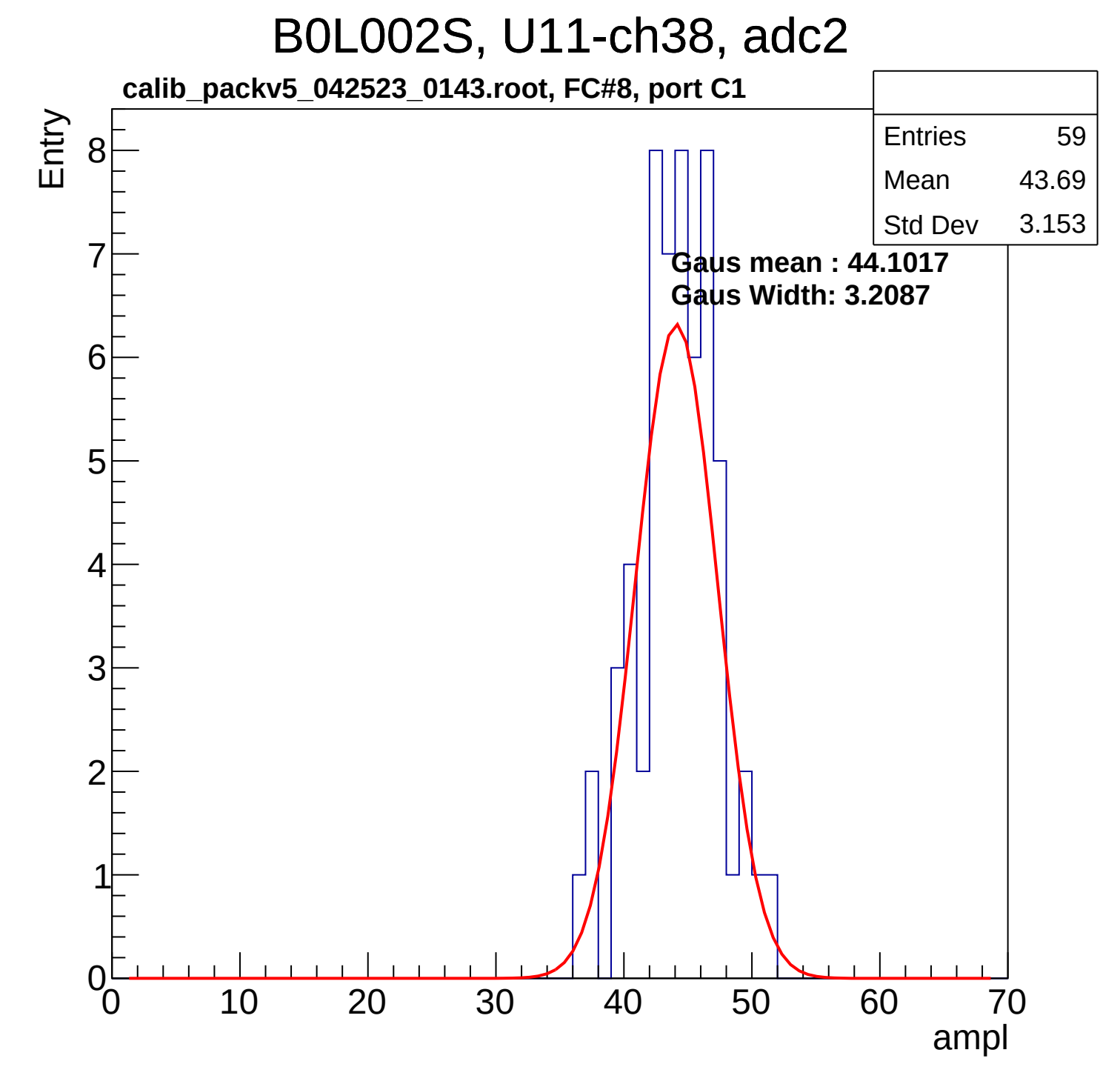
B0L002S, U11-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.69
Std Dev 3.153
Gaus mean : 44.1017
Gaus Width: 3.2087
Entry
ampl
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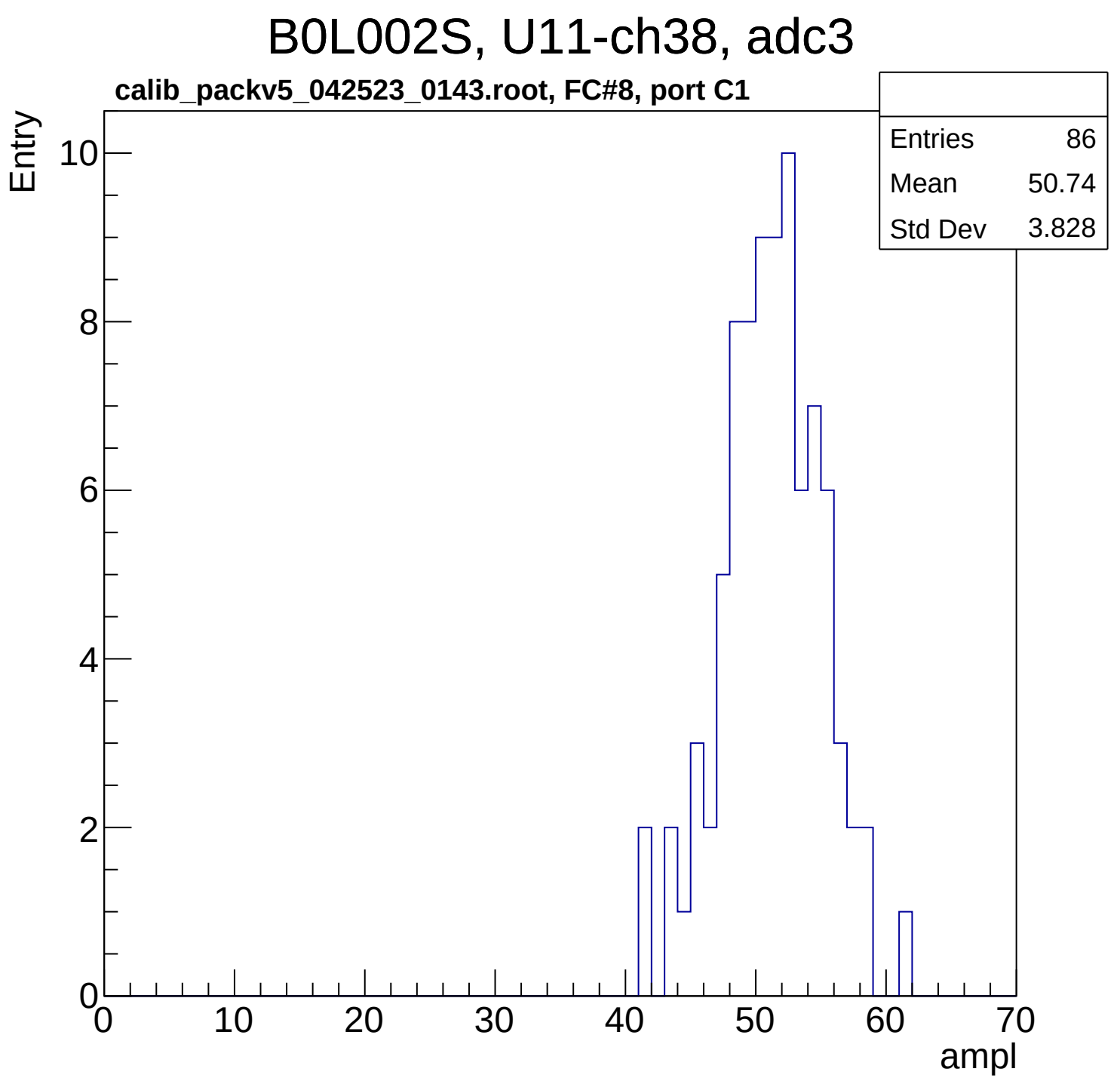
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 50.74
Std Dev 3.828
Entry
ampl
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2
4
6
8
10
0
10
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40
50
60
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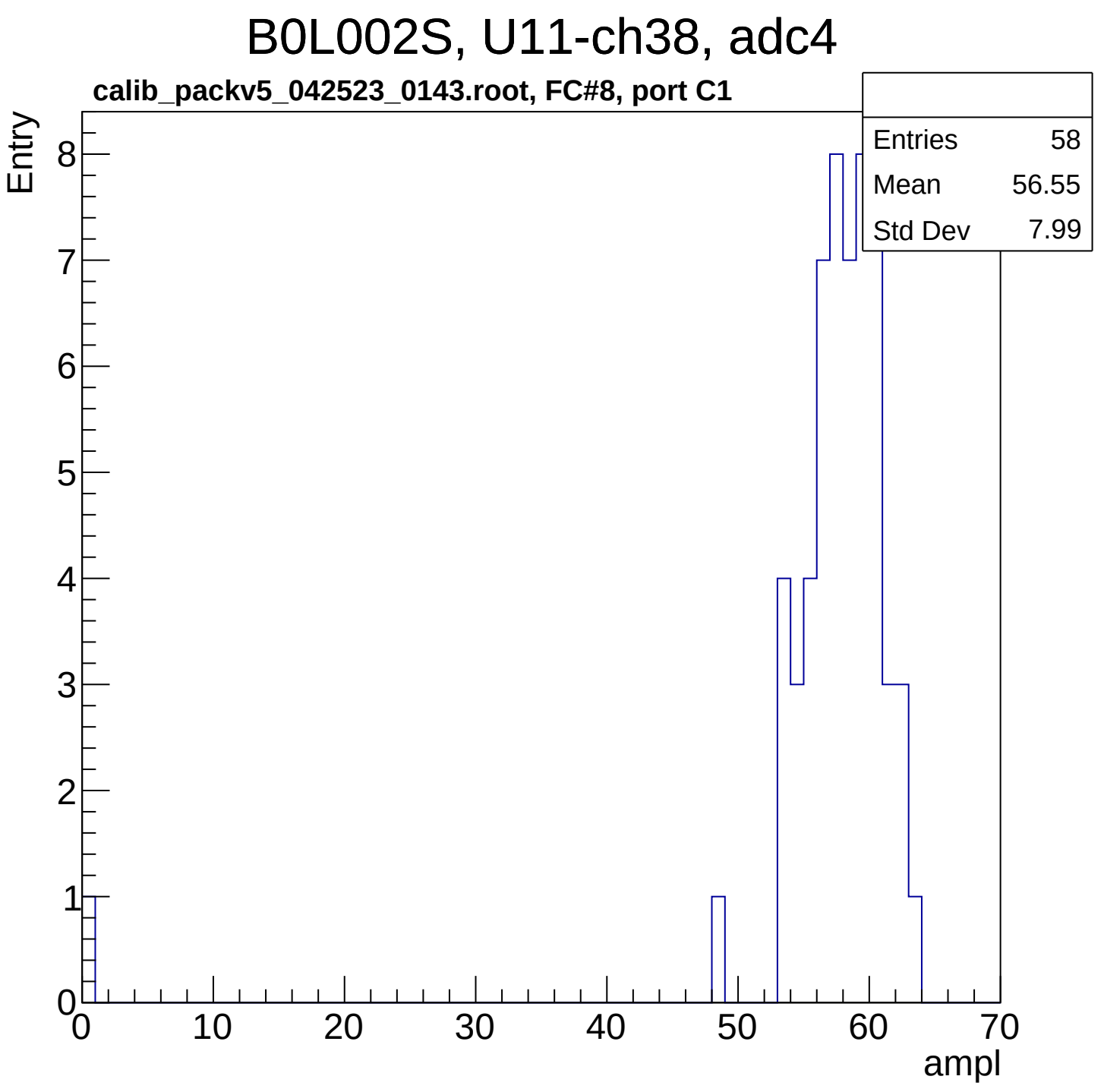
B0L002S, U11-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.55
Std Dev 7.99
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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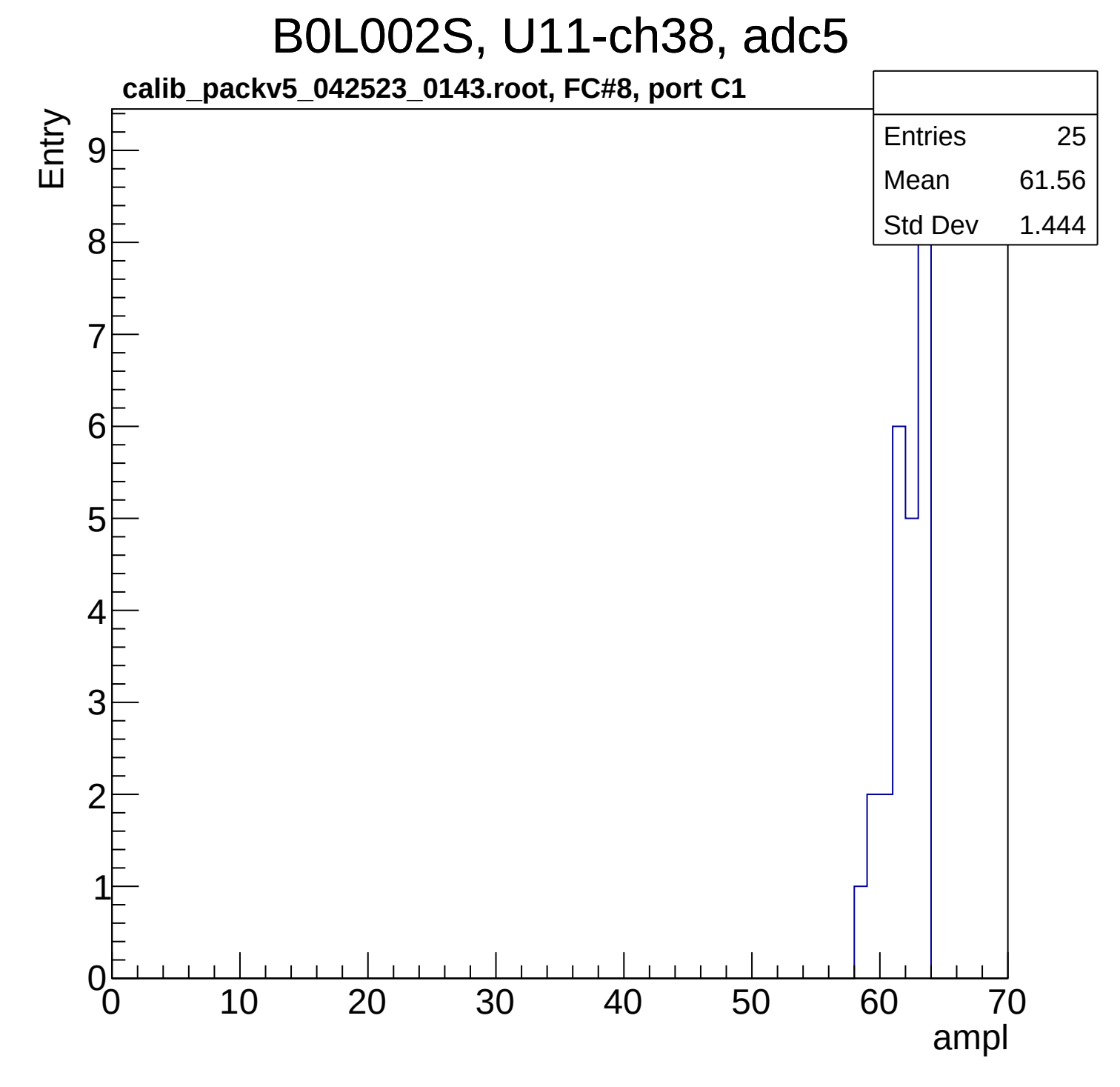
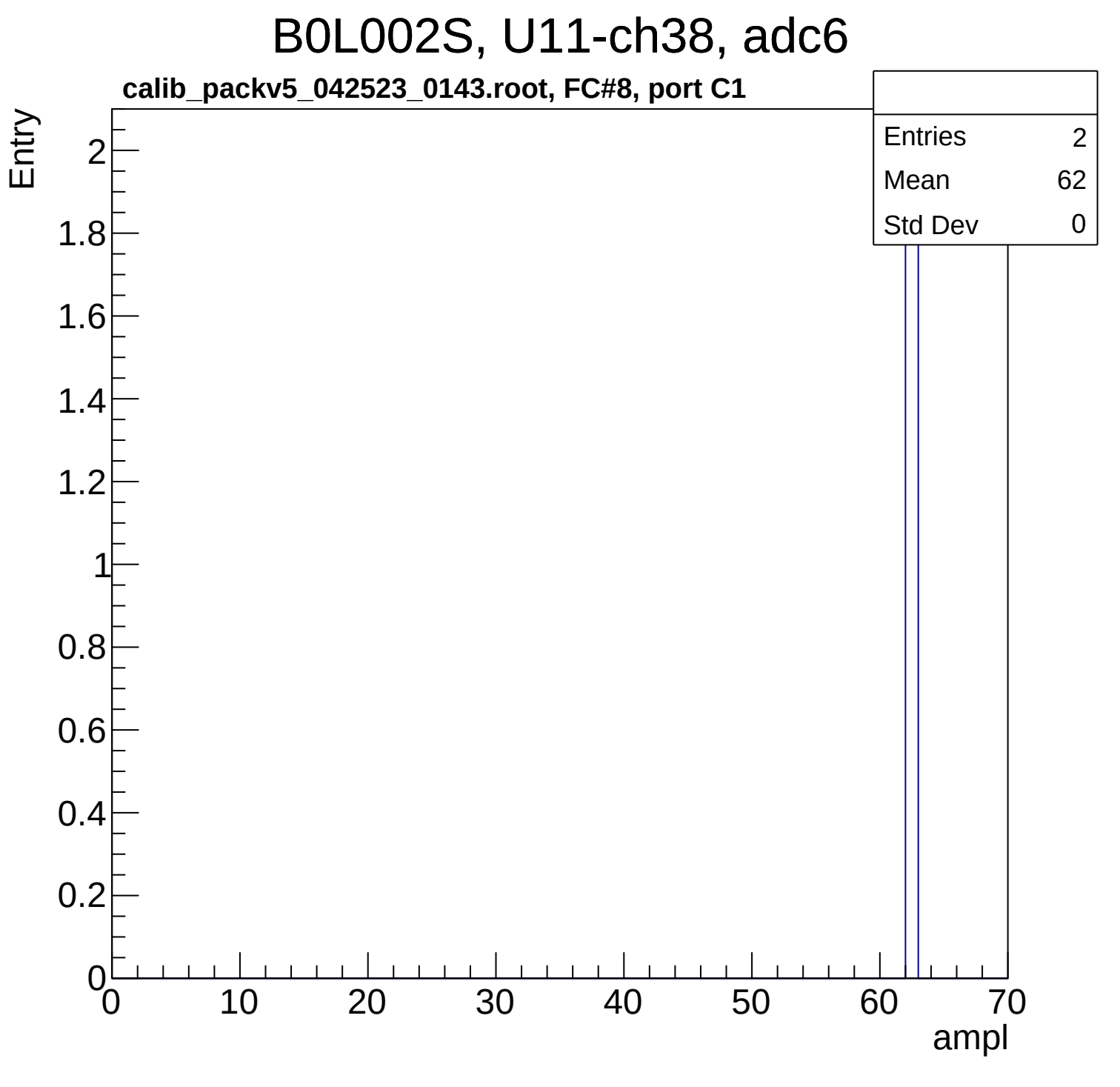
60
70


B0L002S, U11-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61.56
Std Dev 1.444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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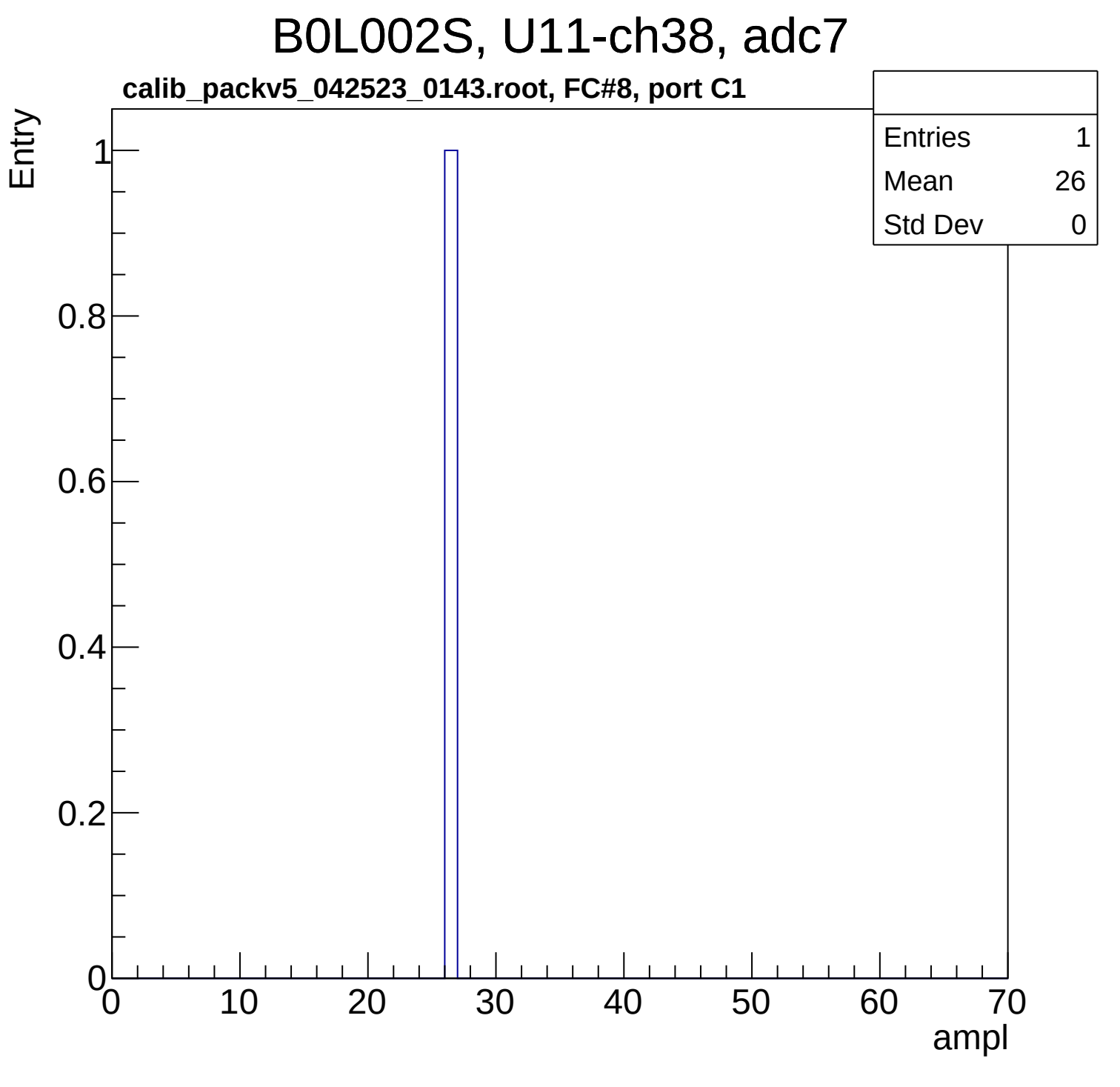
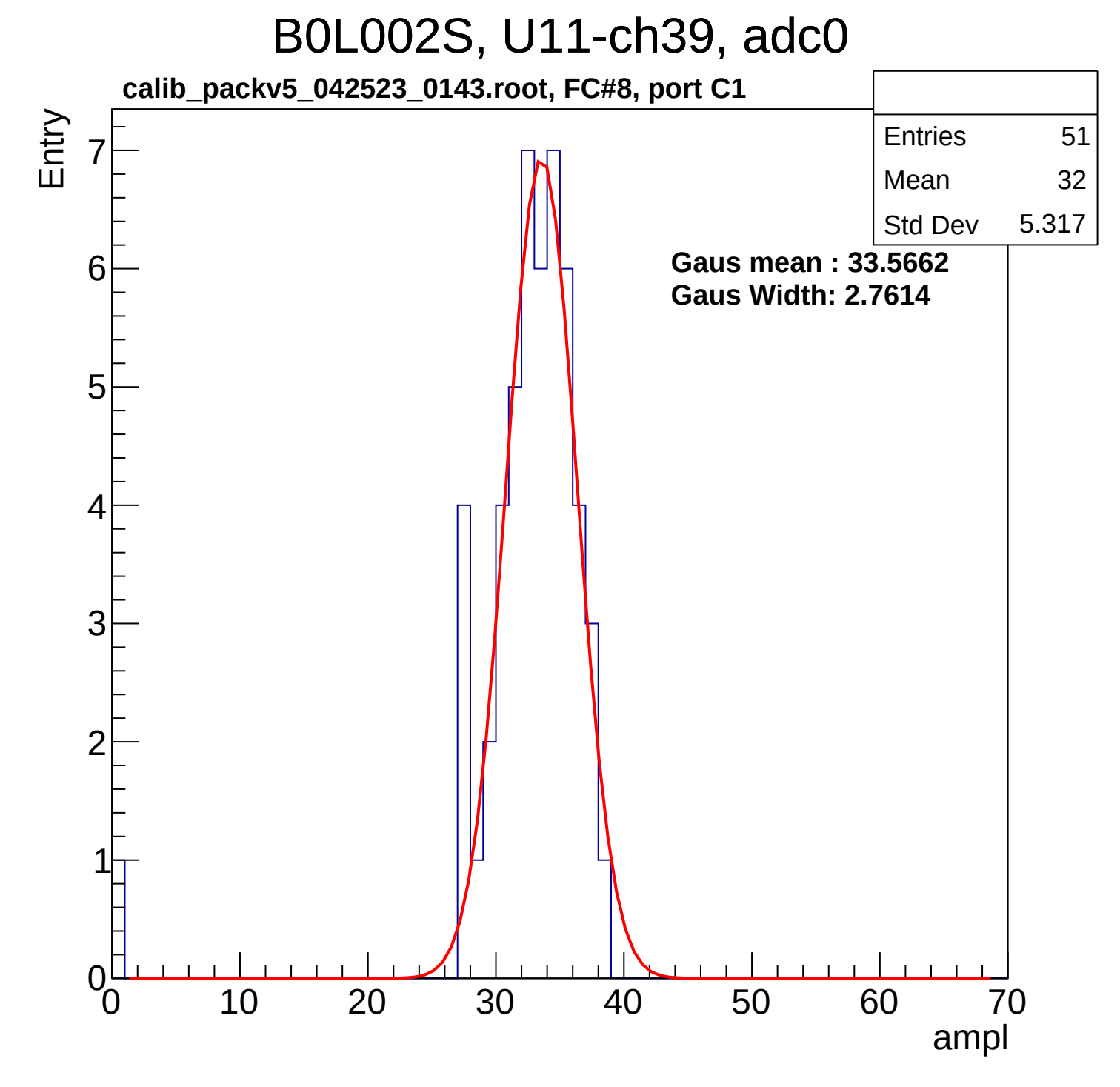

B0L002S, U11-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 32
Std Dev 5.317
Gaus mean : 33.5662
Gaus Width: 2.7614
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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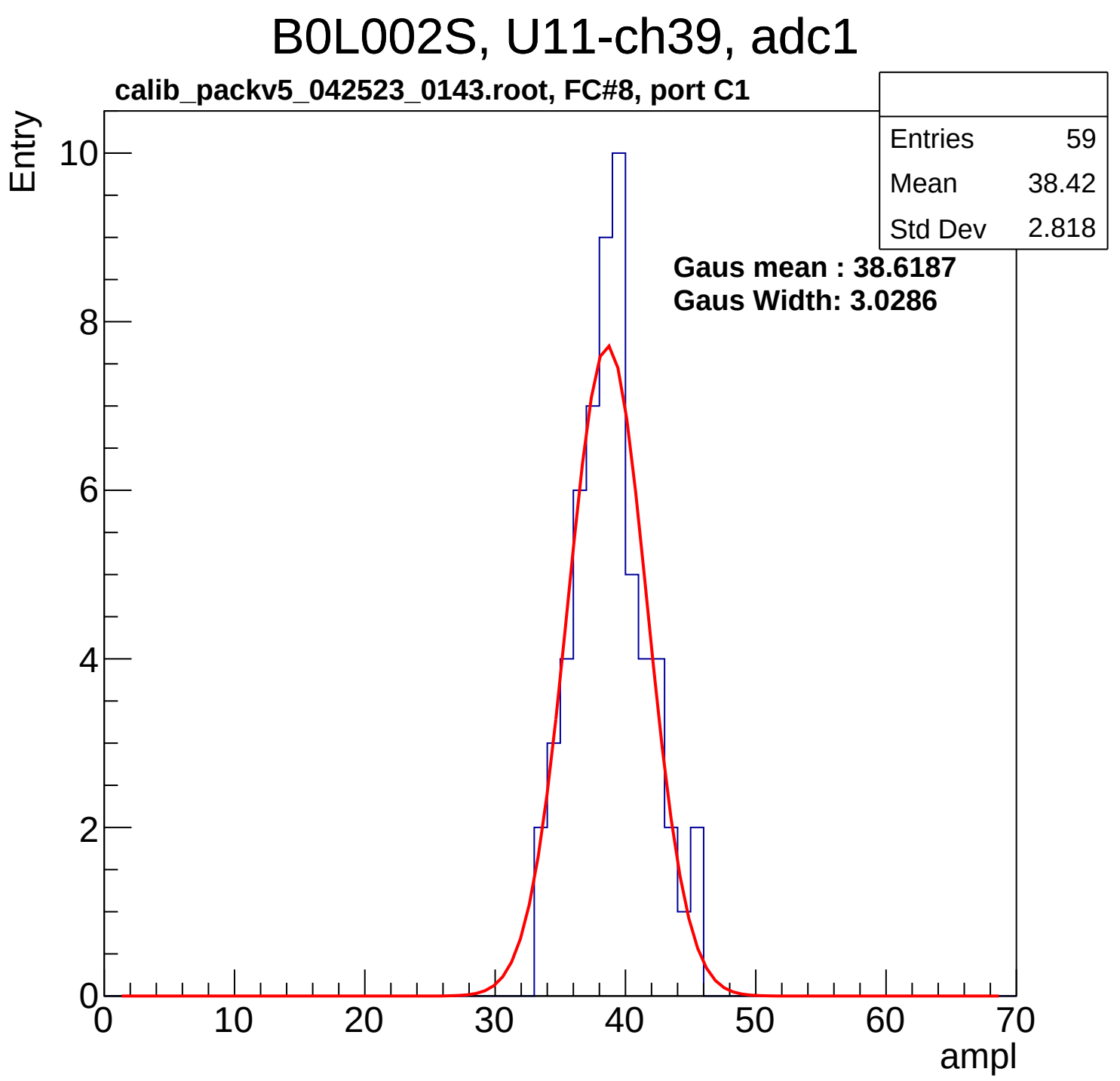
B0L002S, U11-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 38.42
Std Dev 2.818
Gaus mean : 38.6187
Gaus Width: 3.0286
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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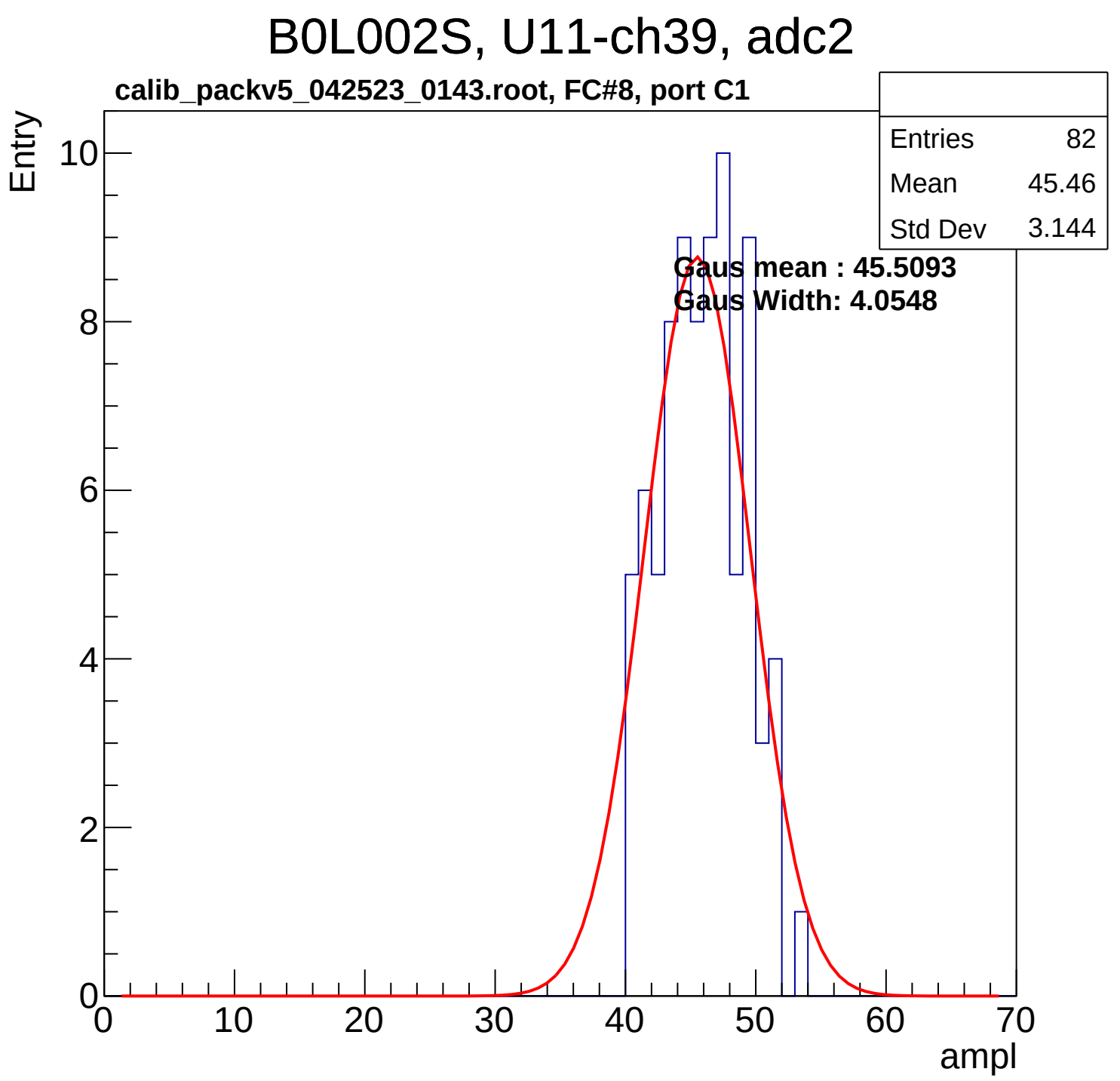
B0L002S, U11-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 45.46
Std Dev 3.144
Gaus mean : 45.5093
Gaus Width: 4.0548
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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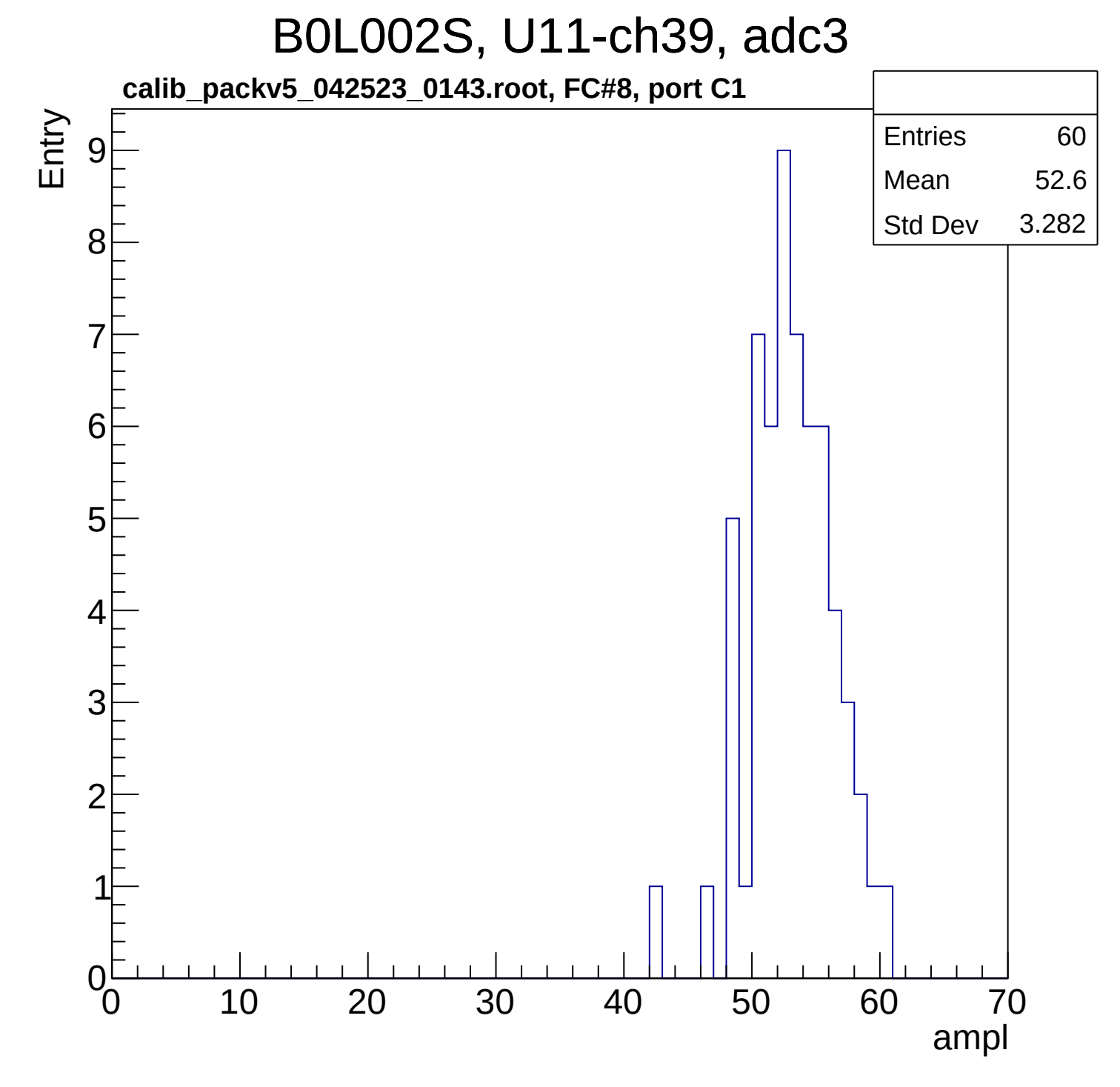
B0L002S, U11-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.6
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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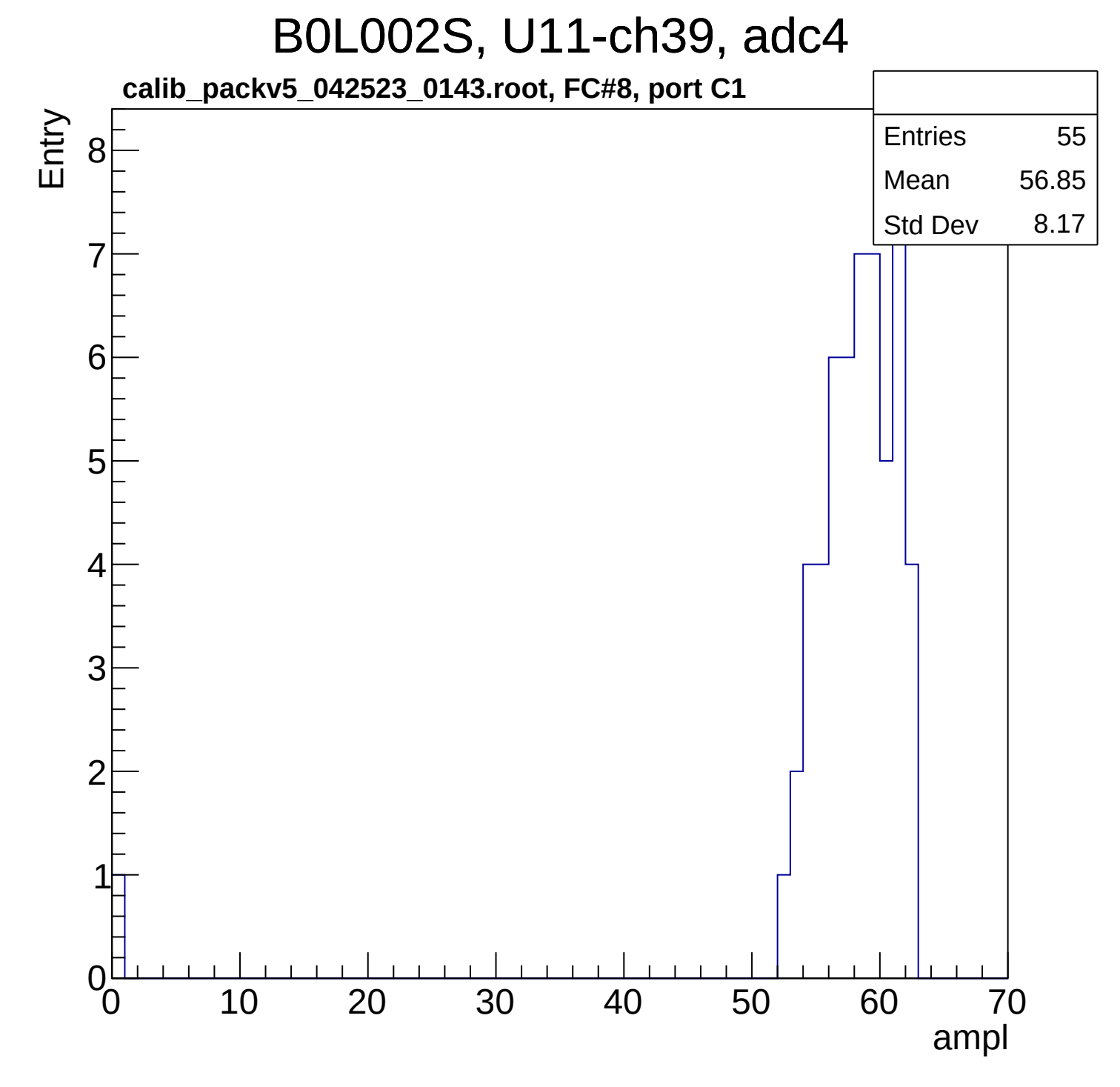
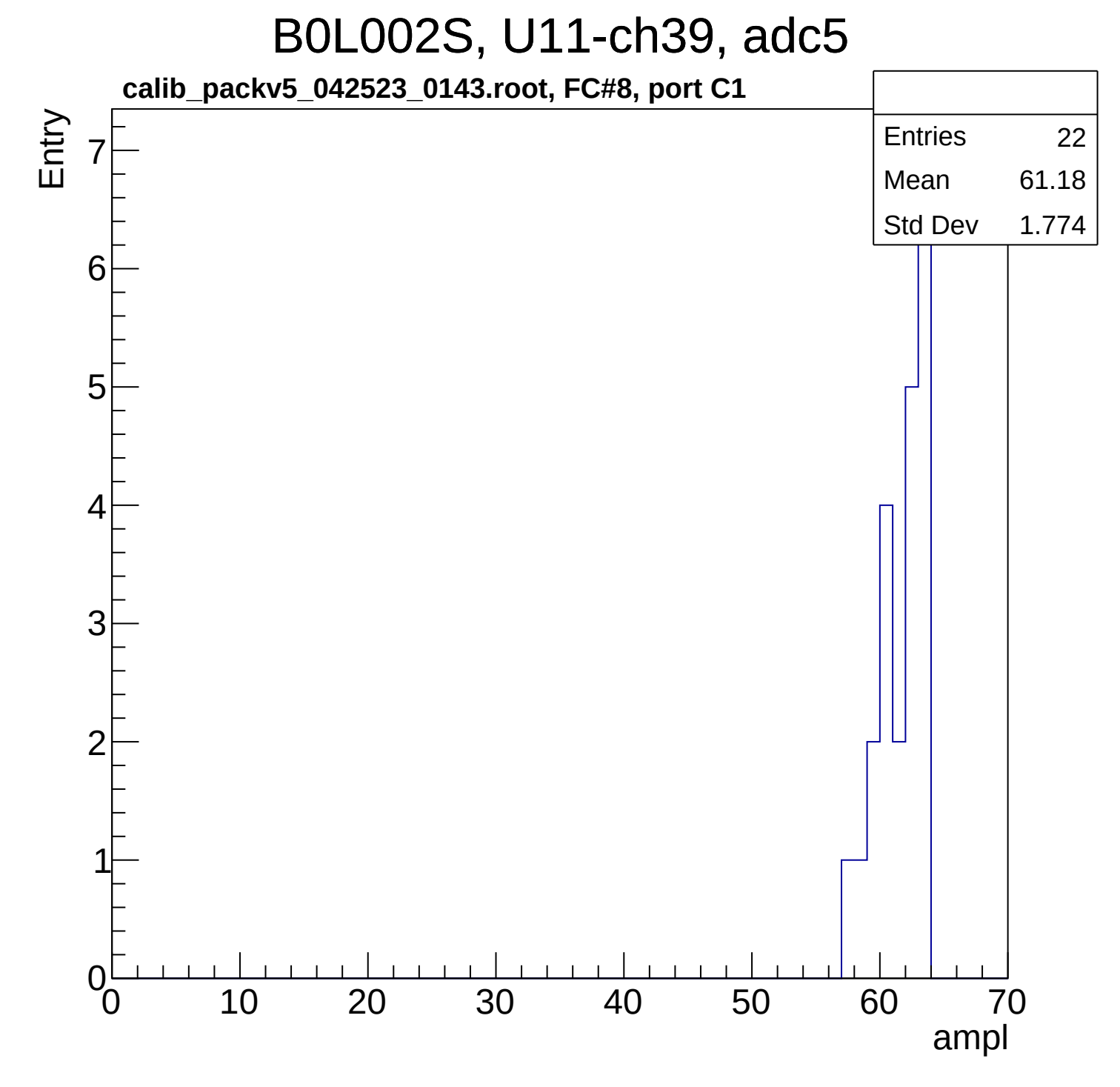

B0L002S, U11-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.85
Std Dev 8.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.18
Std Dev 1.774
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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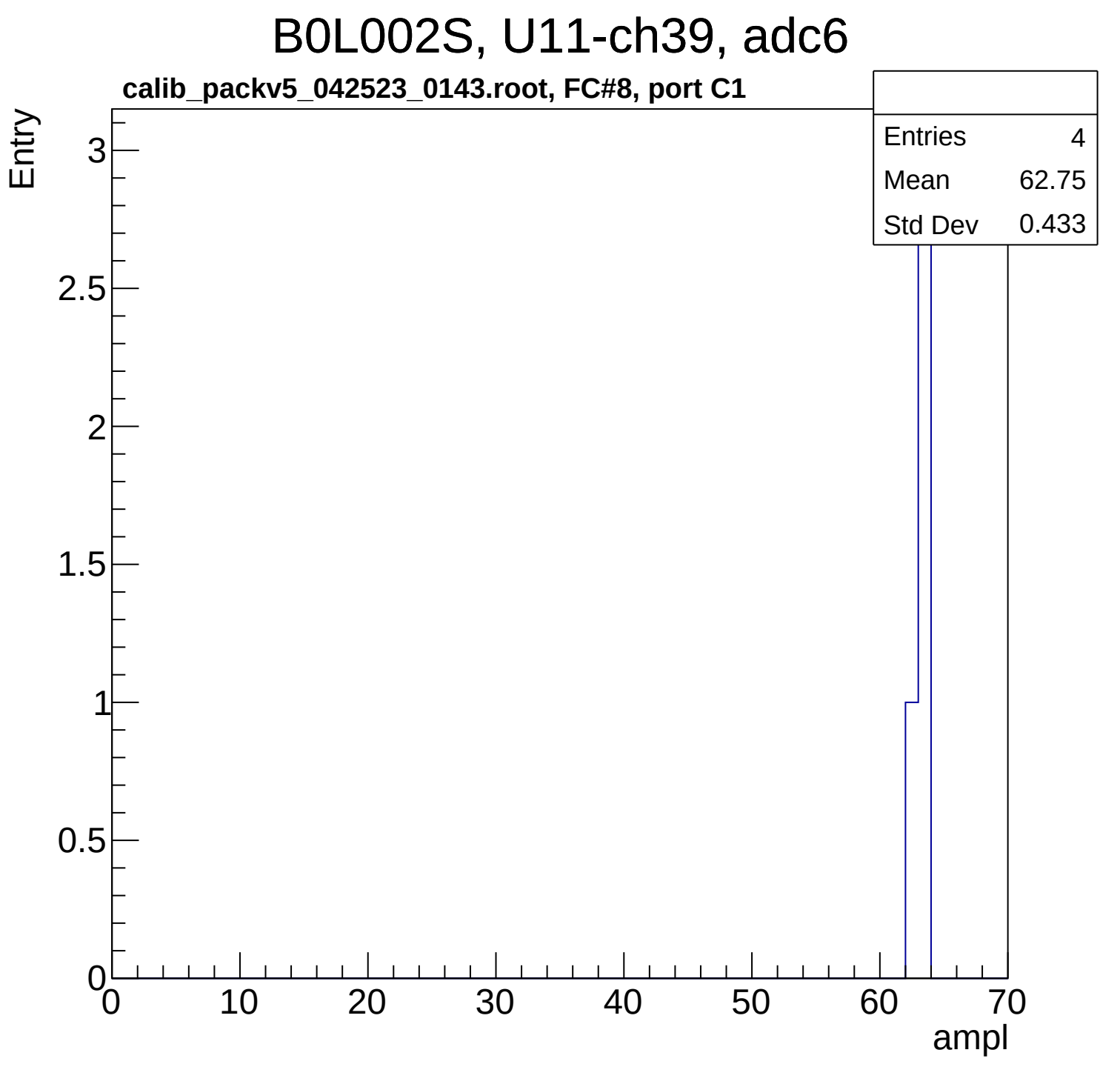
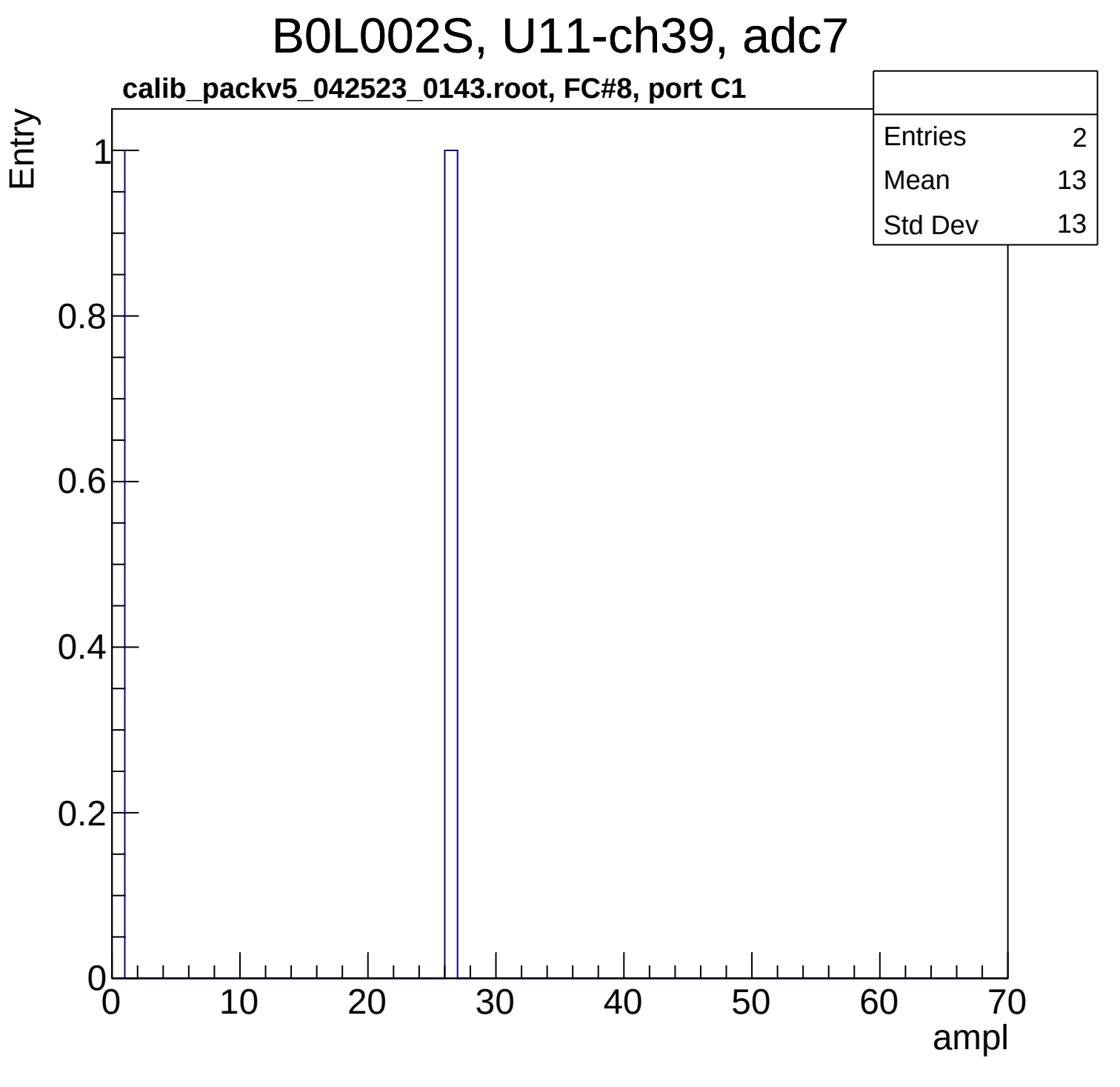
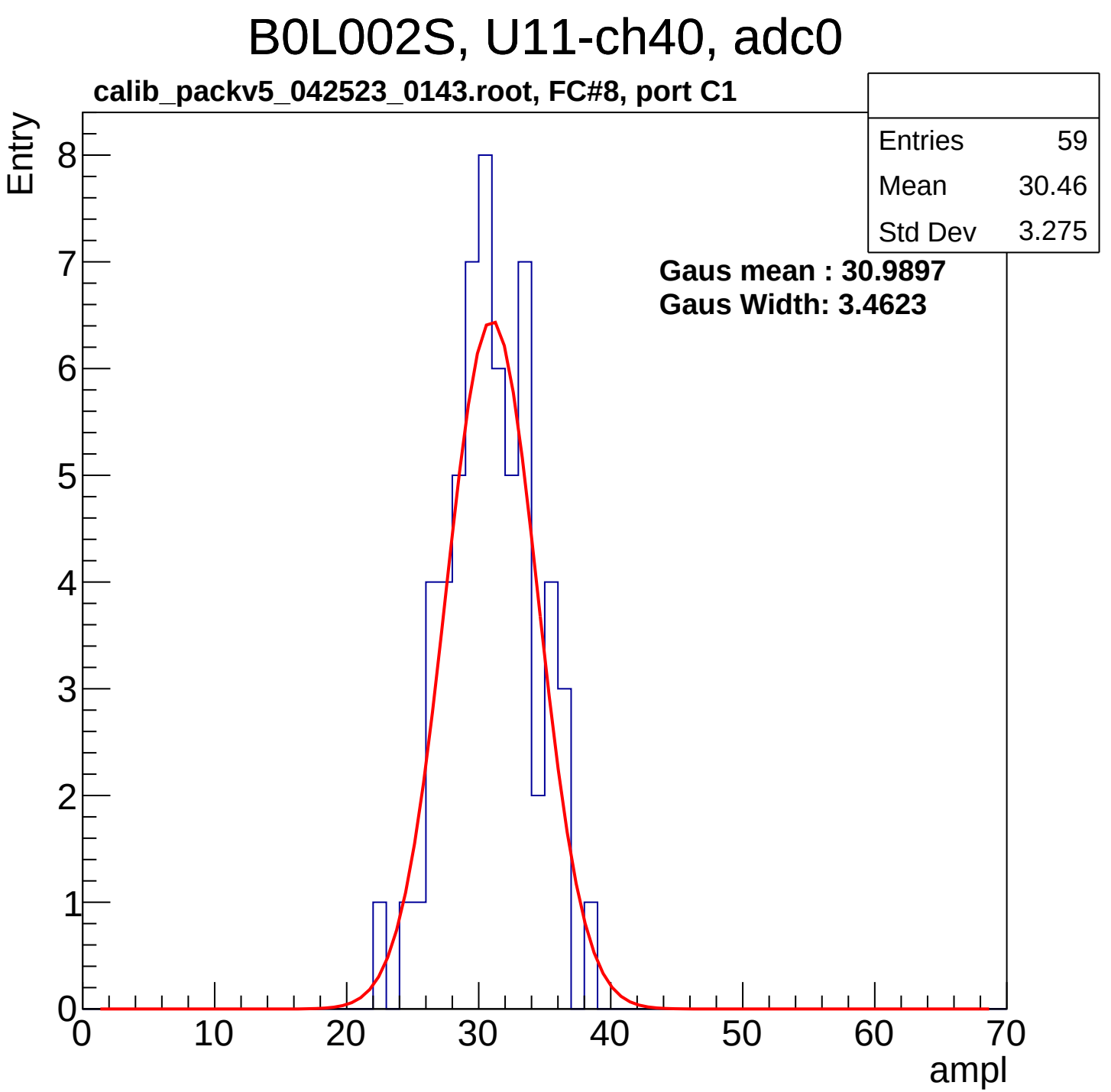
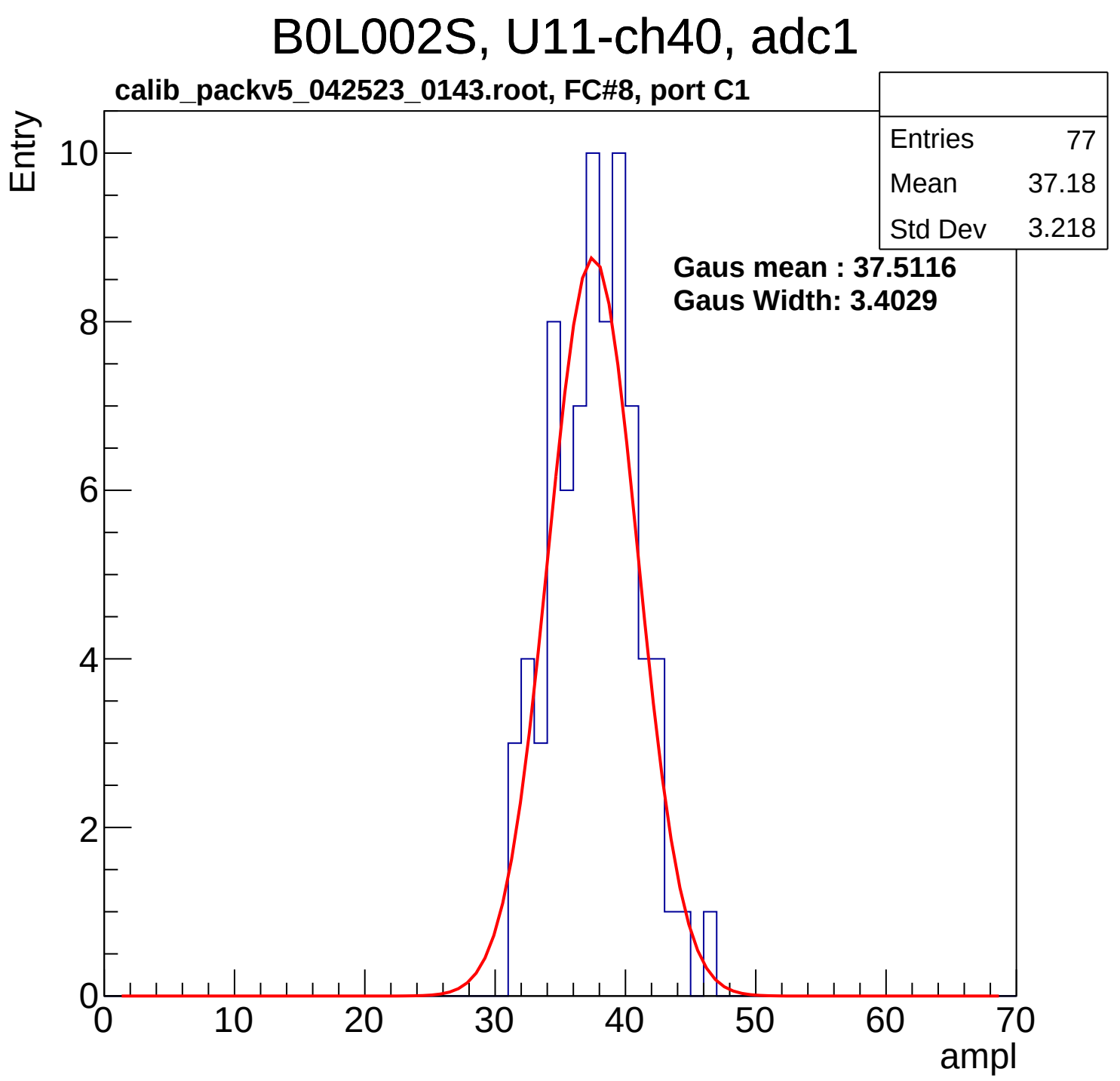

B0L002S, U11-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U11-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.46
Std Dev 3.275
Gaus mean : 30.9897
Gaus Width: 3.4623
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.18
Std Dev 3.218
Gaus mean : 37.5116
Gaus Width: 3.4029
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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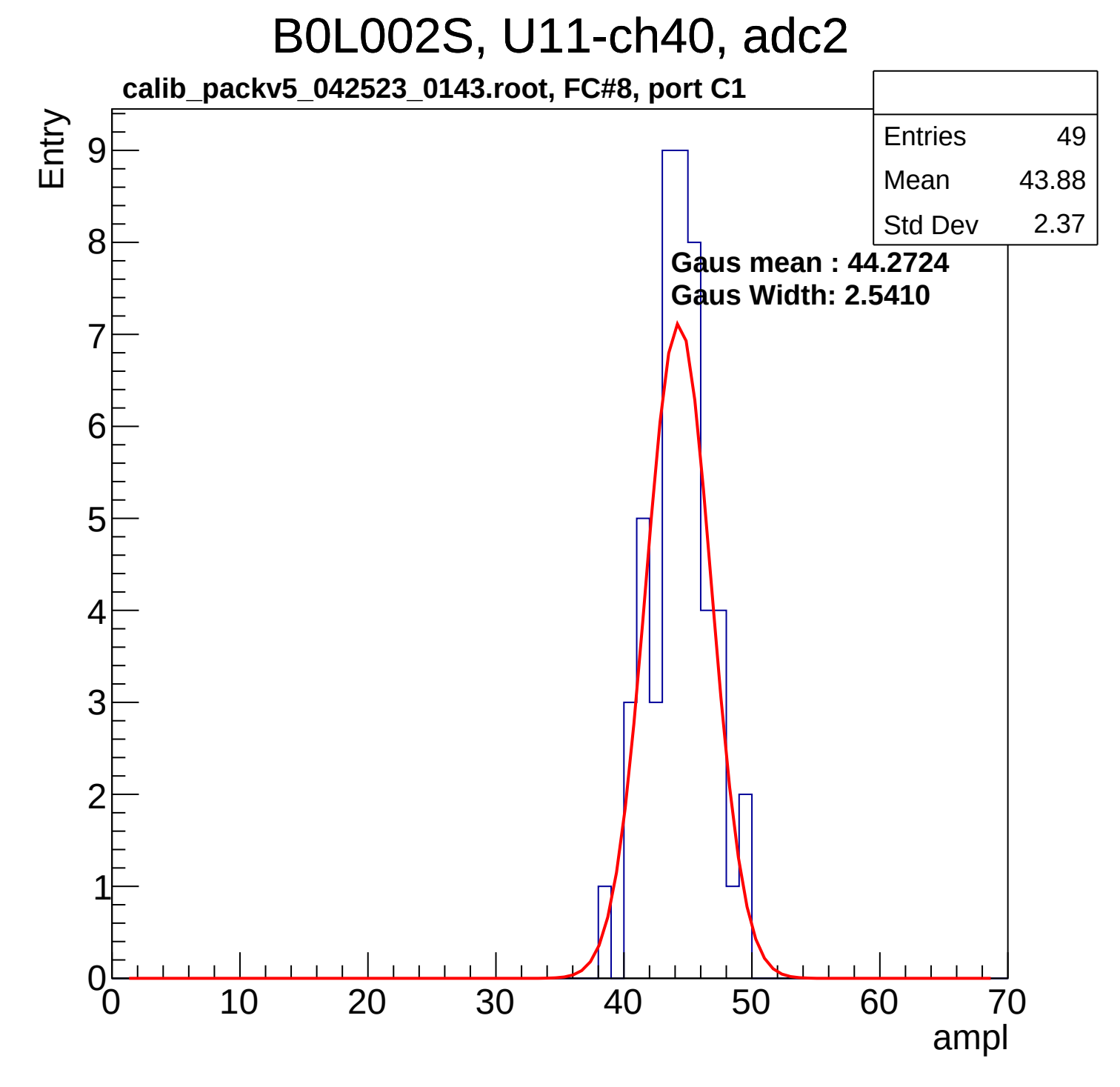
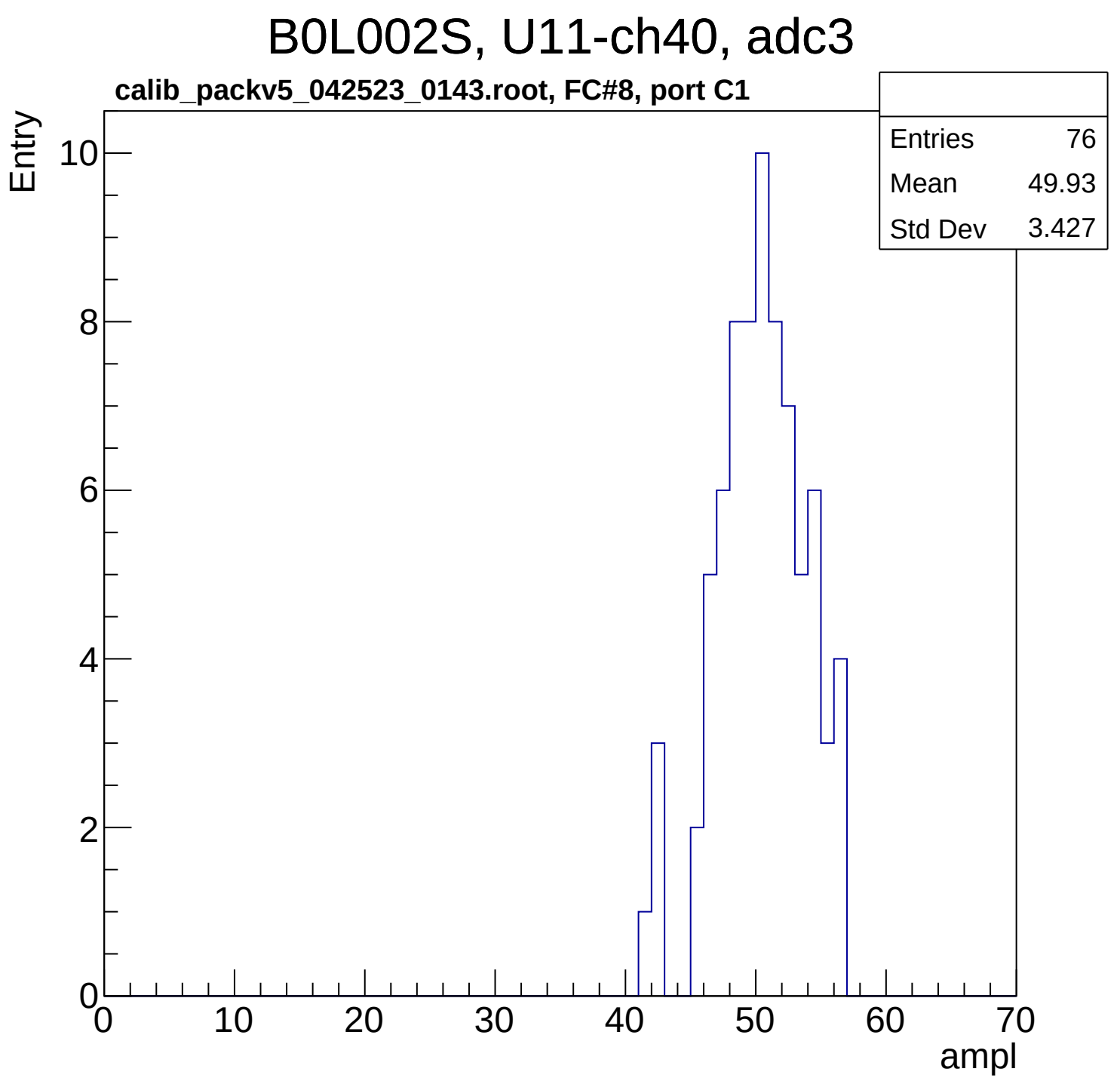
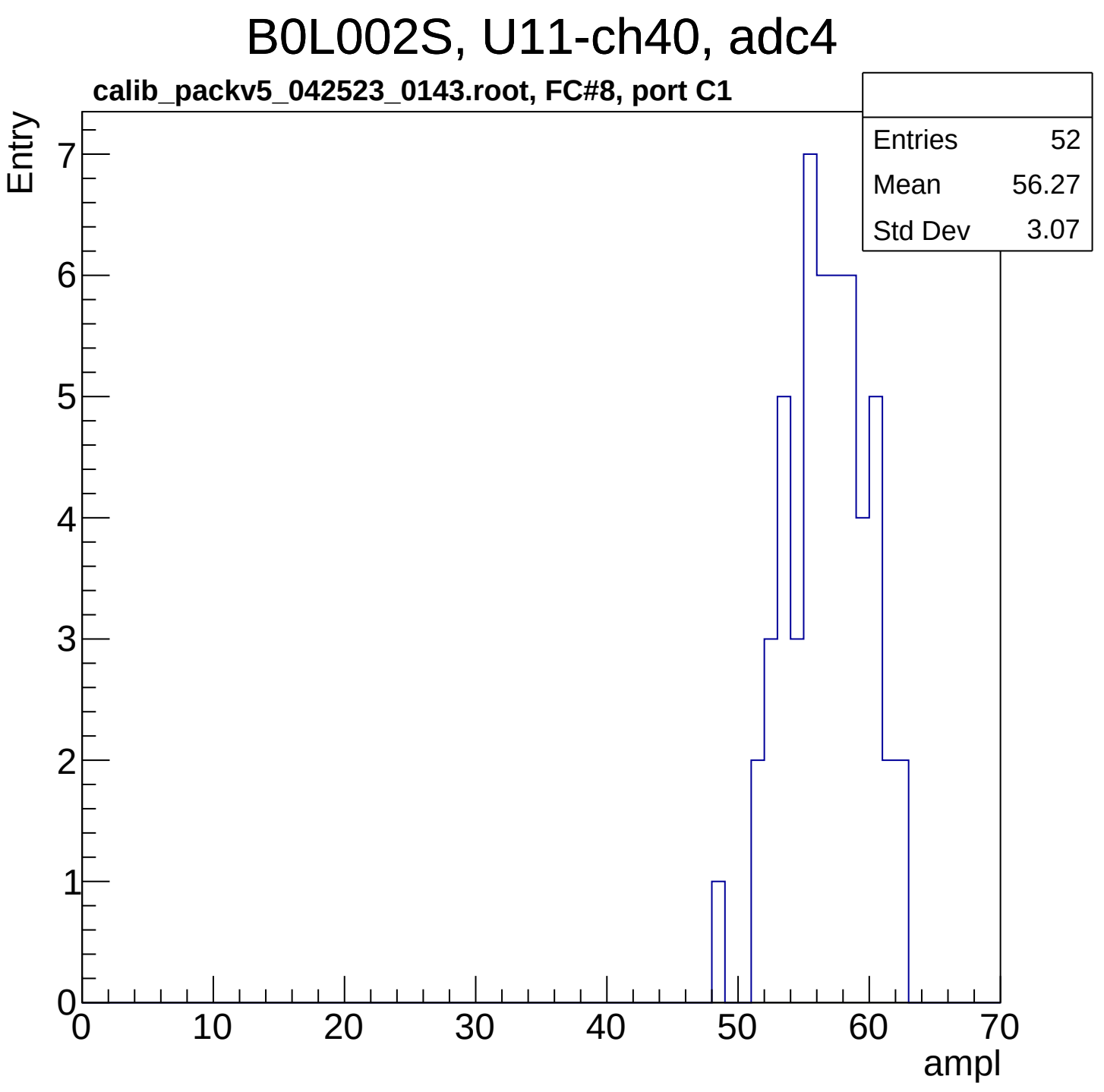

B0L002S, U11-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 43.88
Std Dev 2.37
Gaus mean : 44.2724
Gaus Width: 2.5410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 49.93
Std Dev 3.427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.27
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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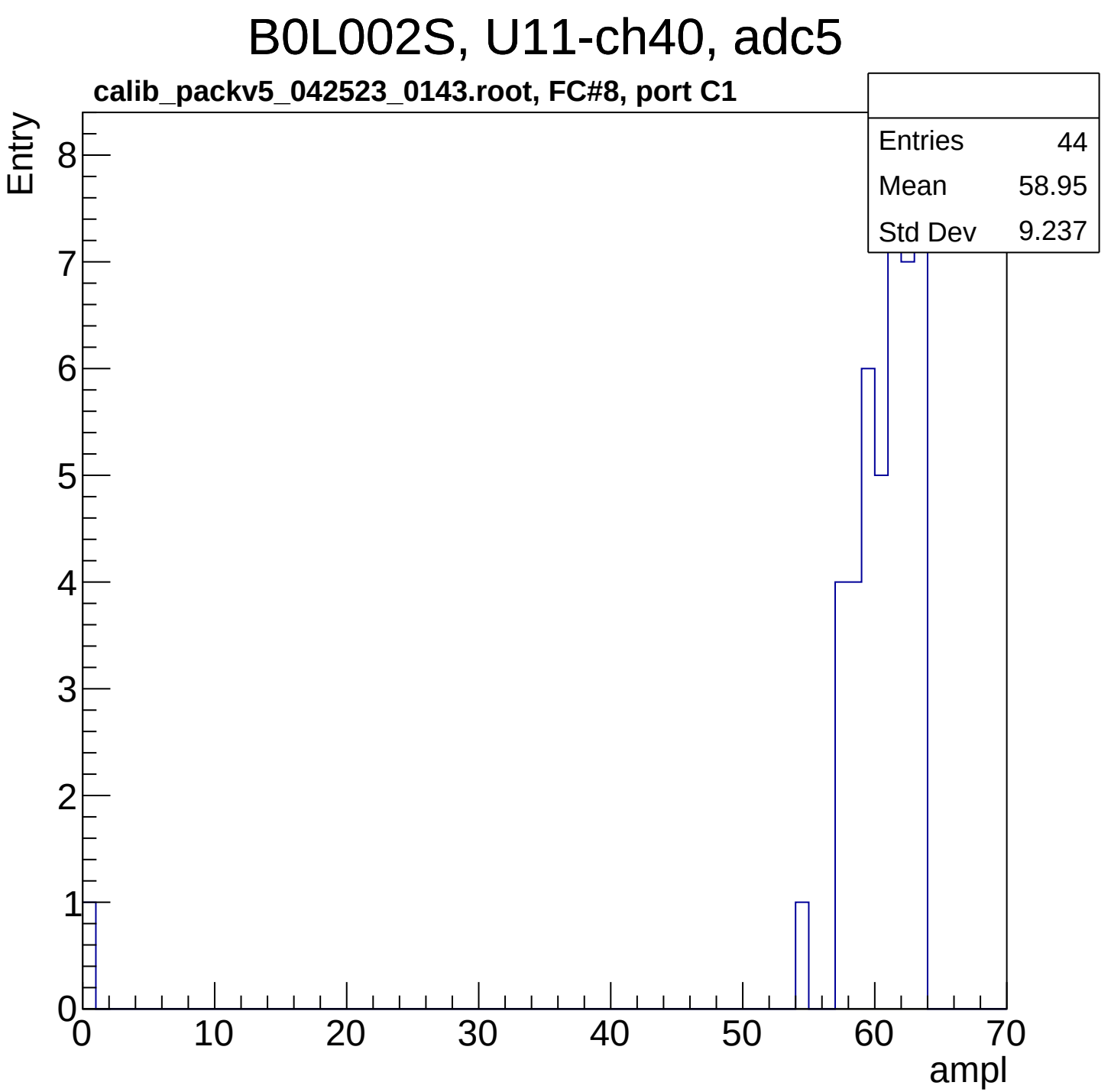
B0L002S, U11-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.95
Std Dev 9.237
Entry
ampl
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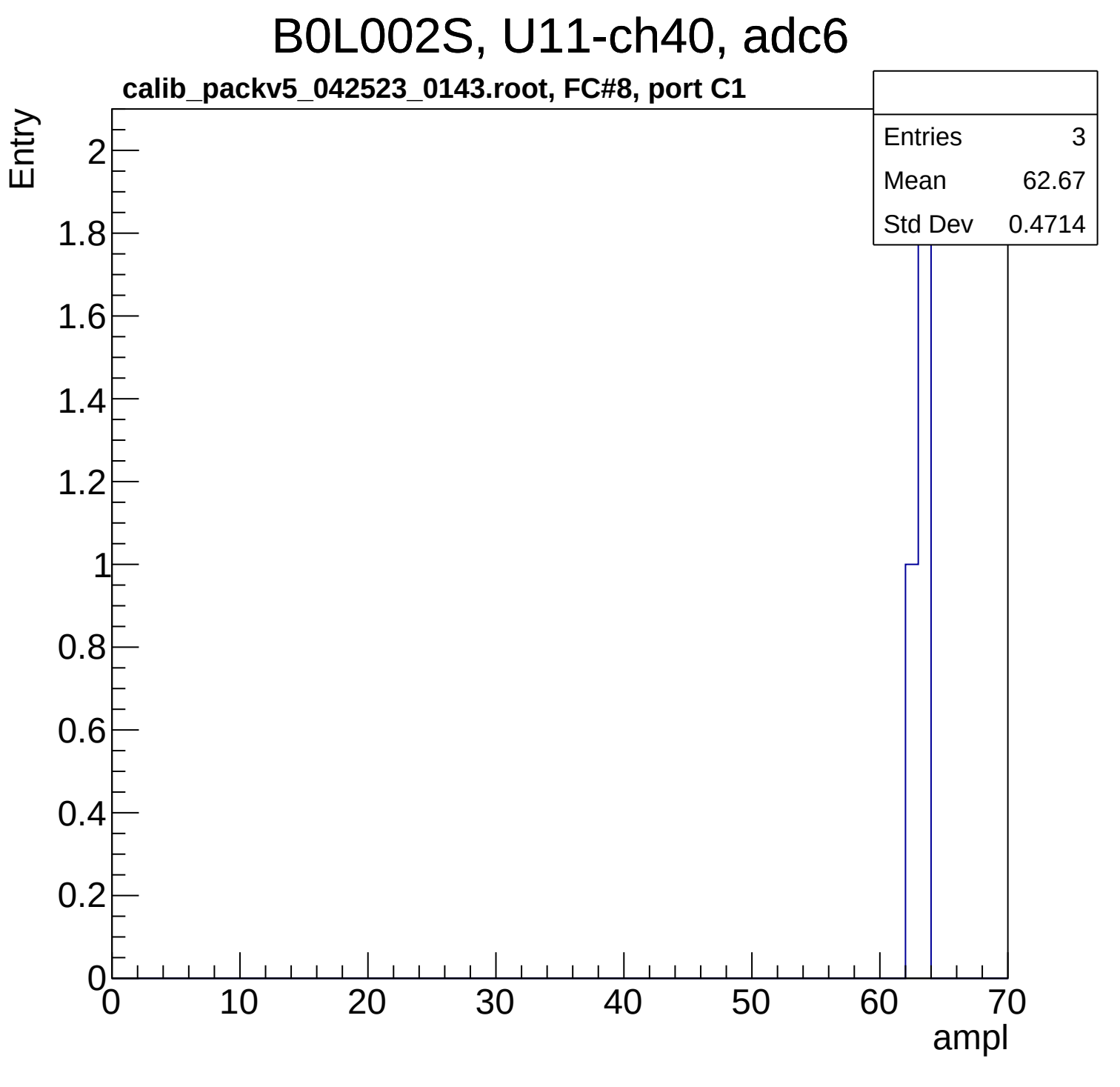
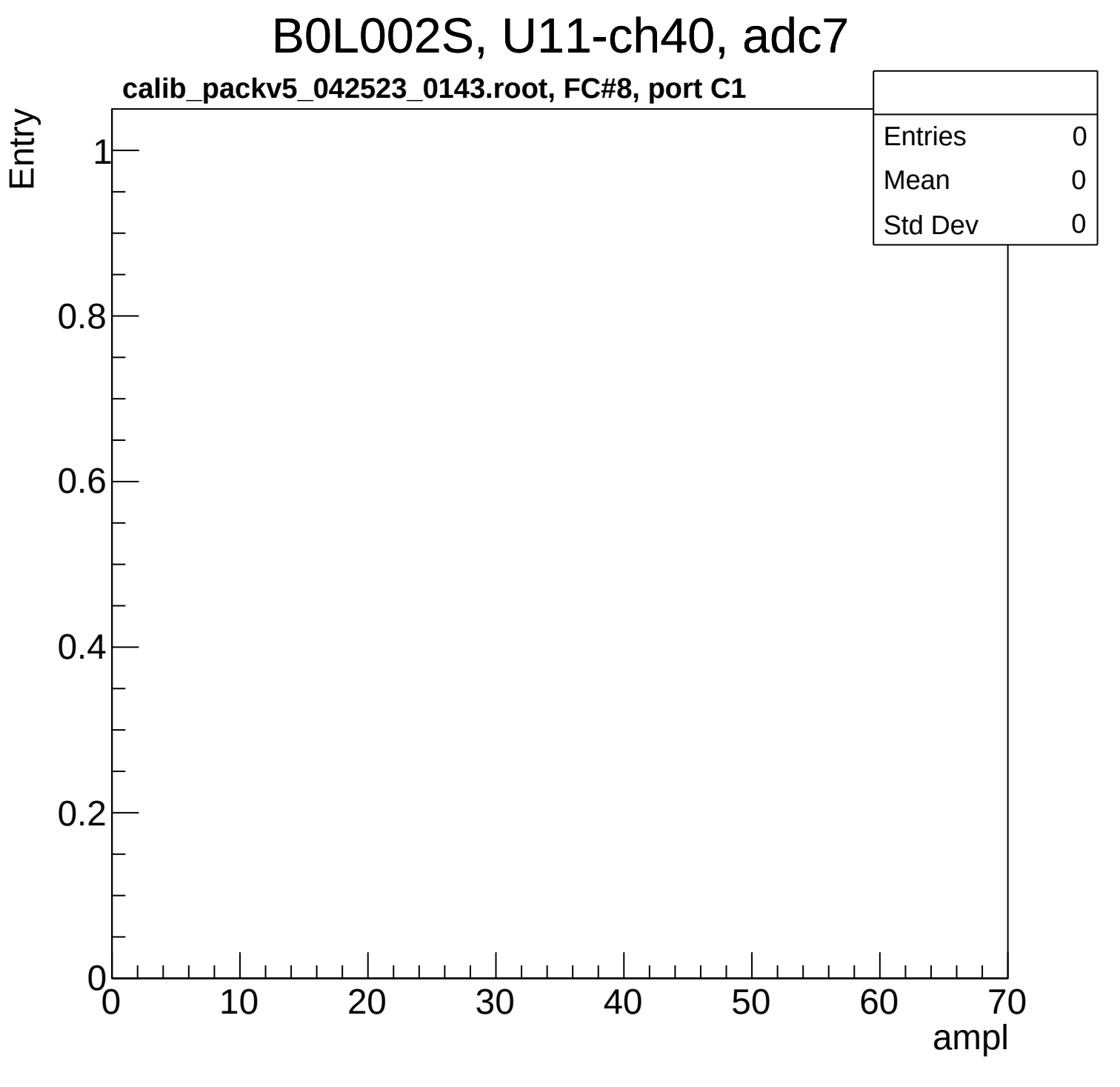
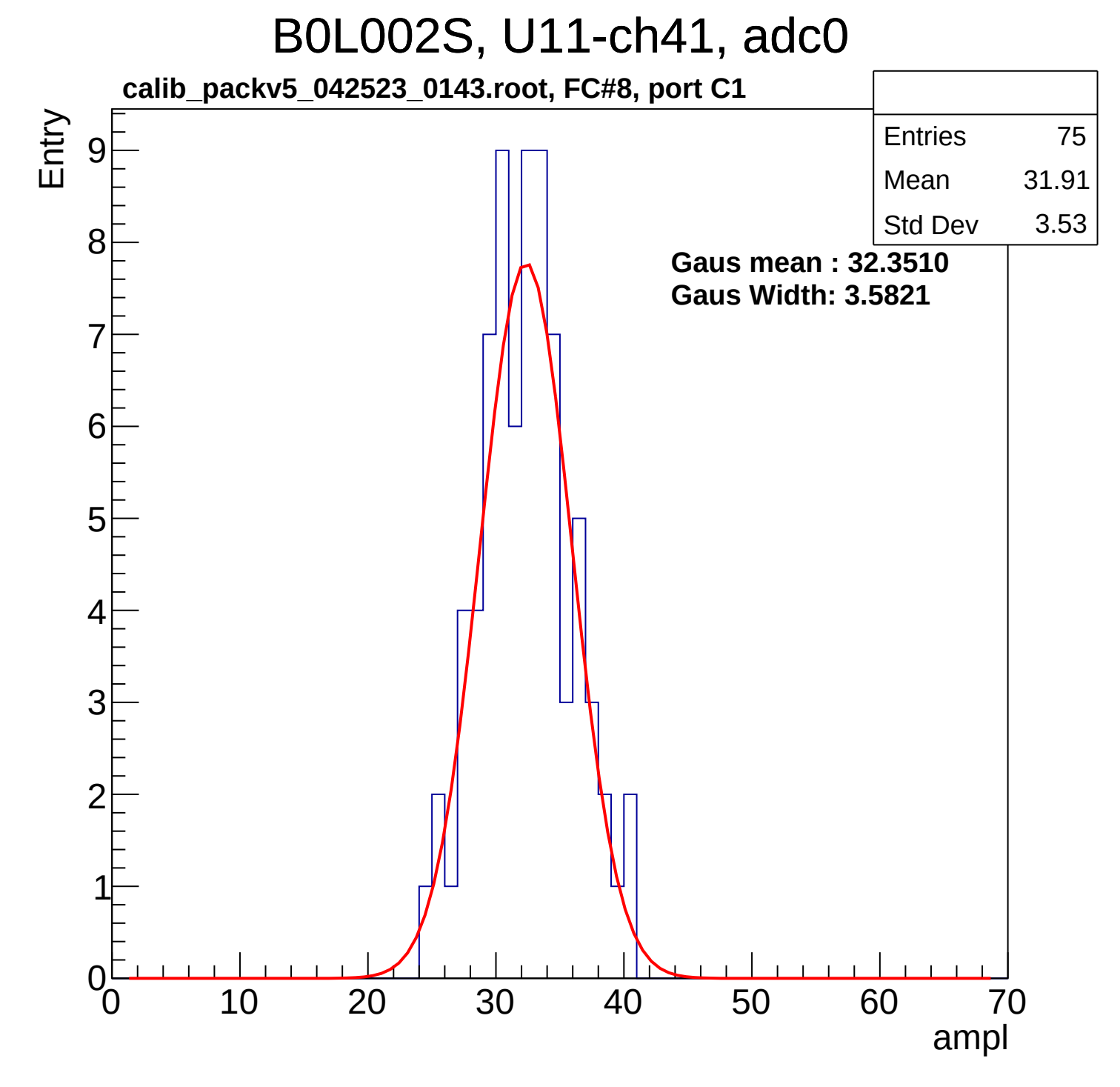
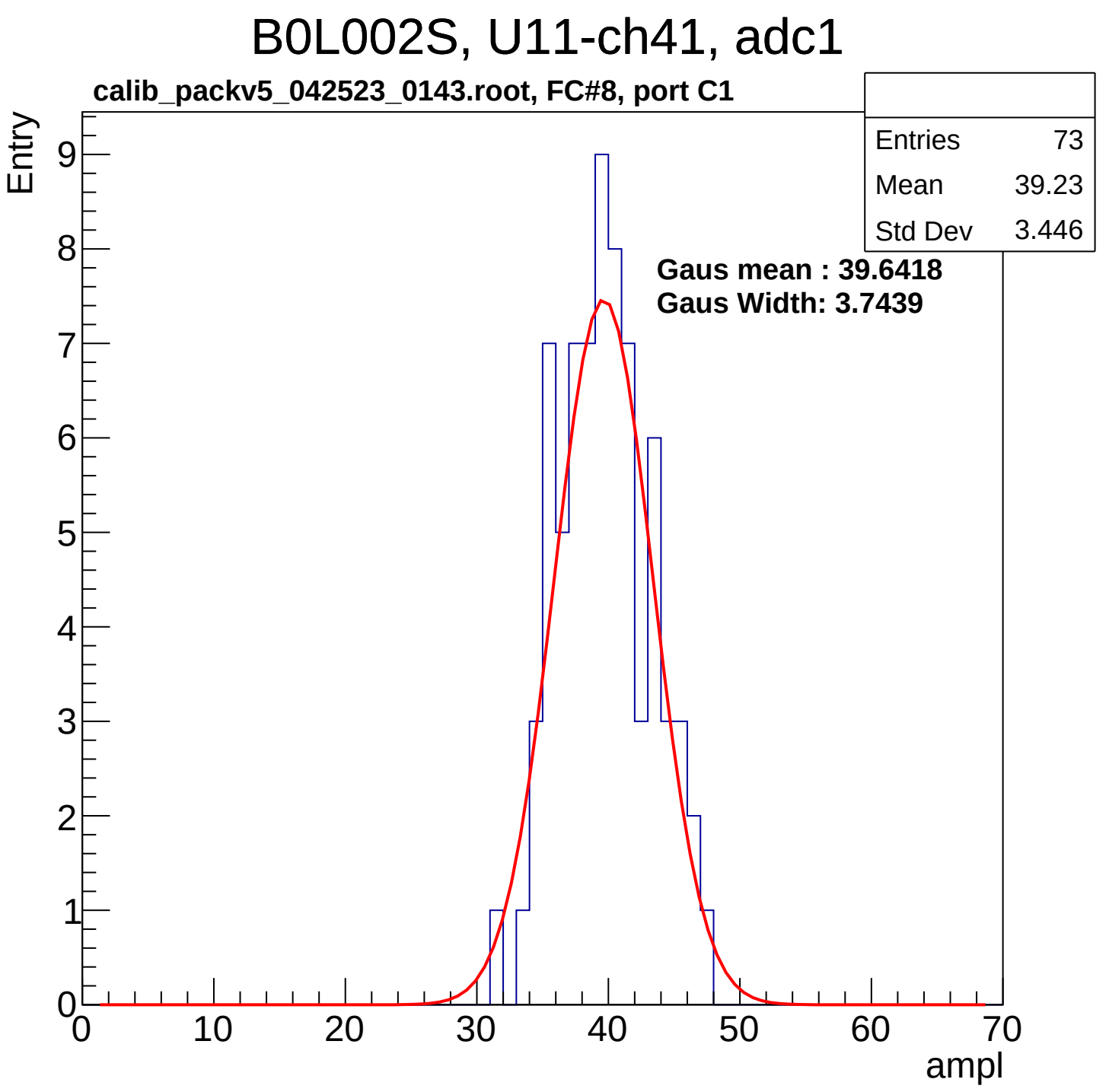
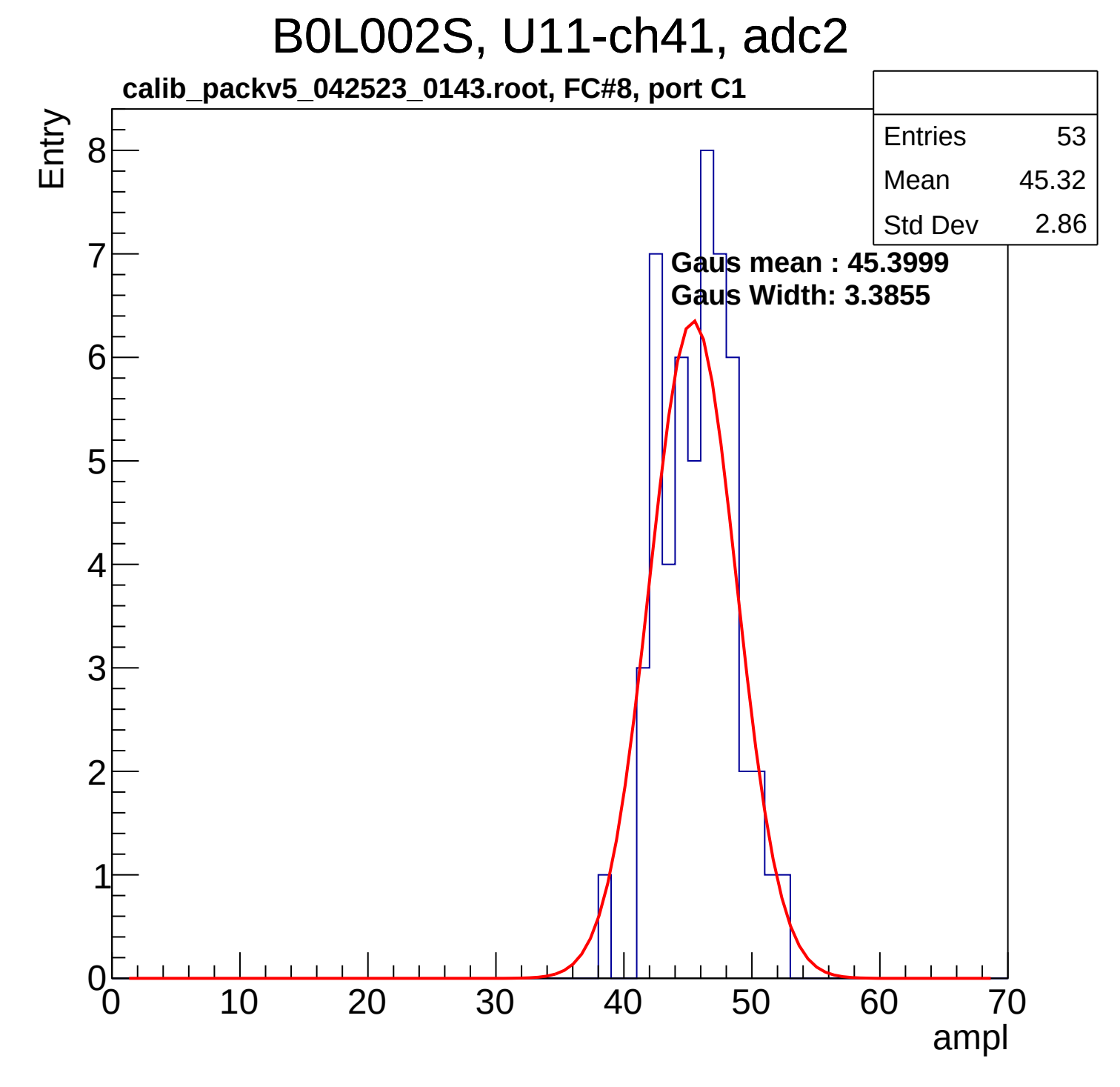
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.91
Std Dev 3.53
Gaus mean : 32.3510
Gaus Width: 3.5821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 39.23
Std Dev 3.446
Gaus mean : 39.6418
Gaus Width: 3.7439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 45.32
Std Dev 2.86
Gaus mean : 45.3999
Gaus Width: 3.3855
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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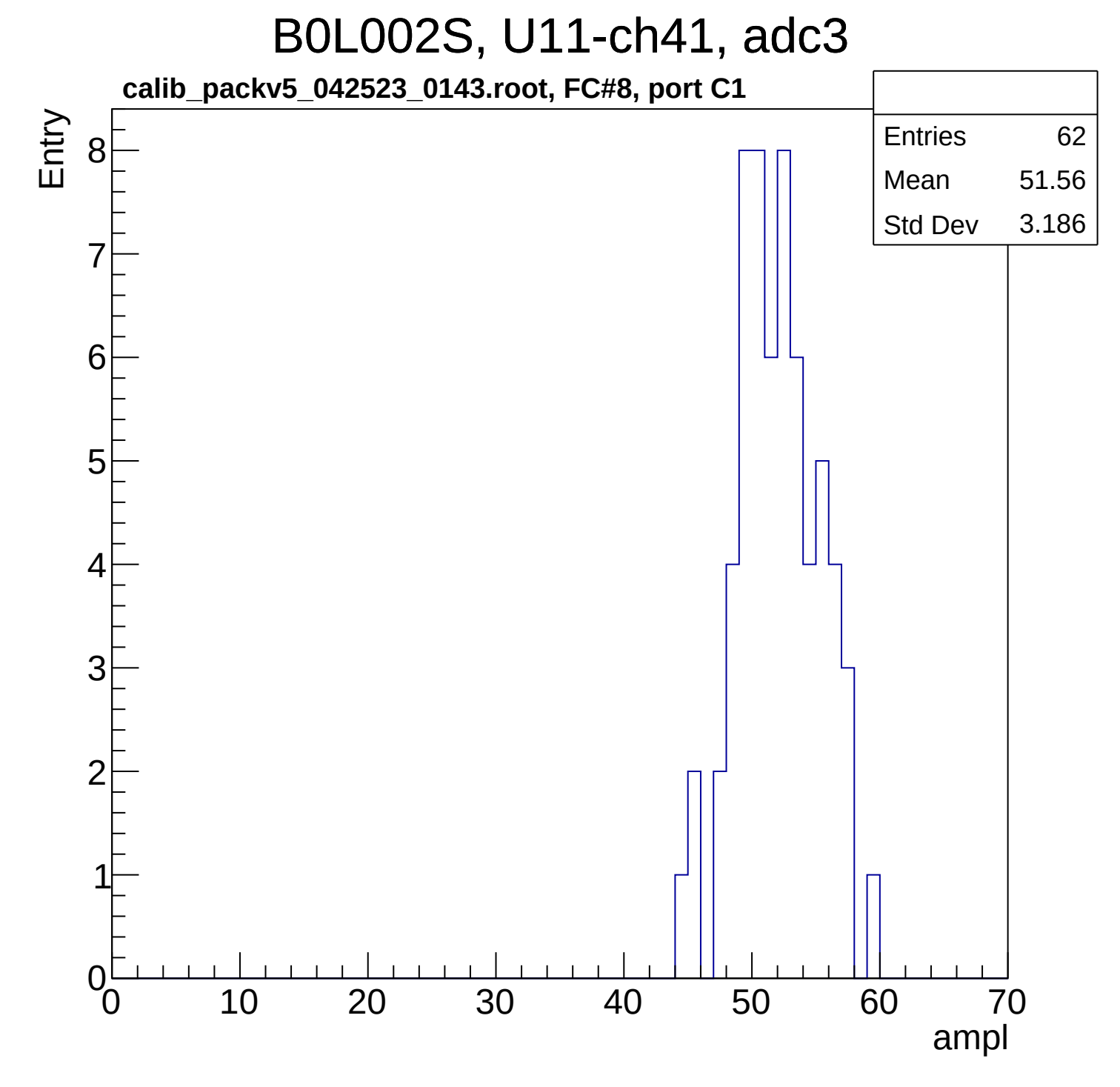
40
50
60
70


B0L002S, U11-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.56
Std Dev 3.186
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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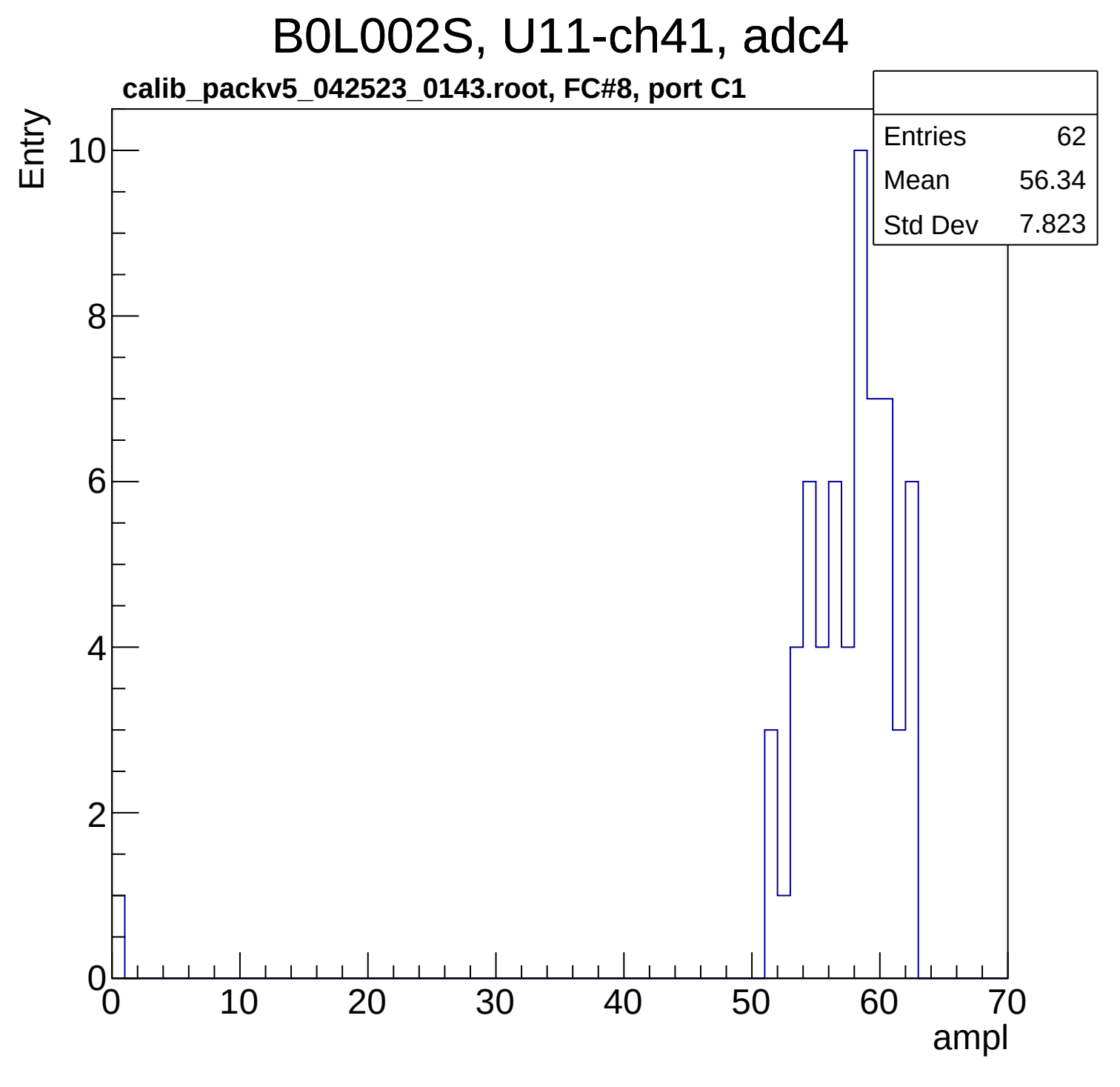
40
50
60
70


B0L002S, U11-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.34
Std Dev 7.823
Entry
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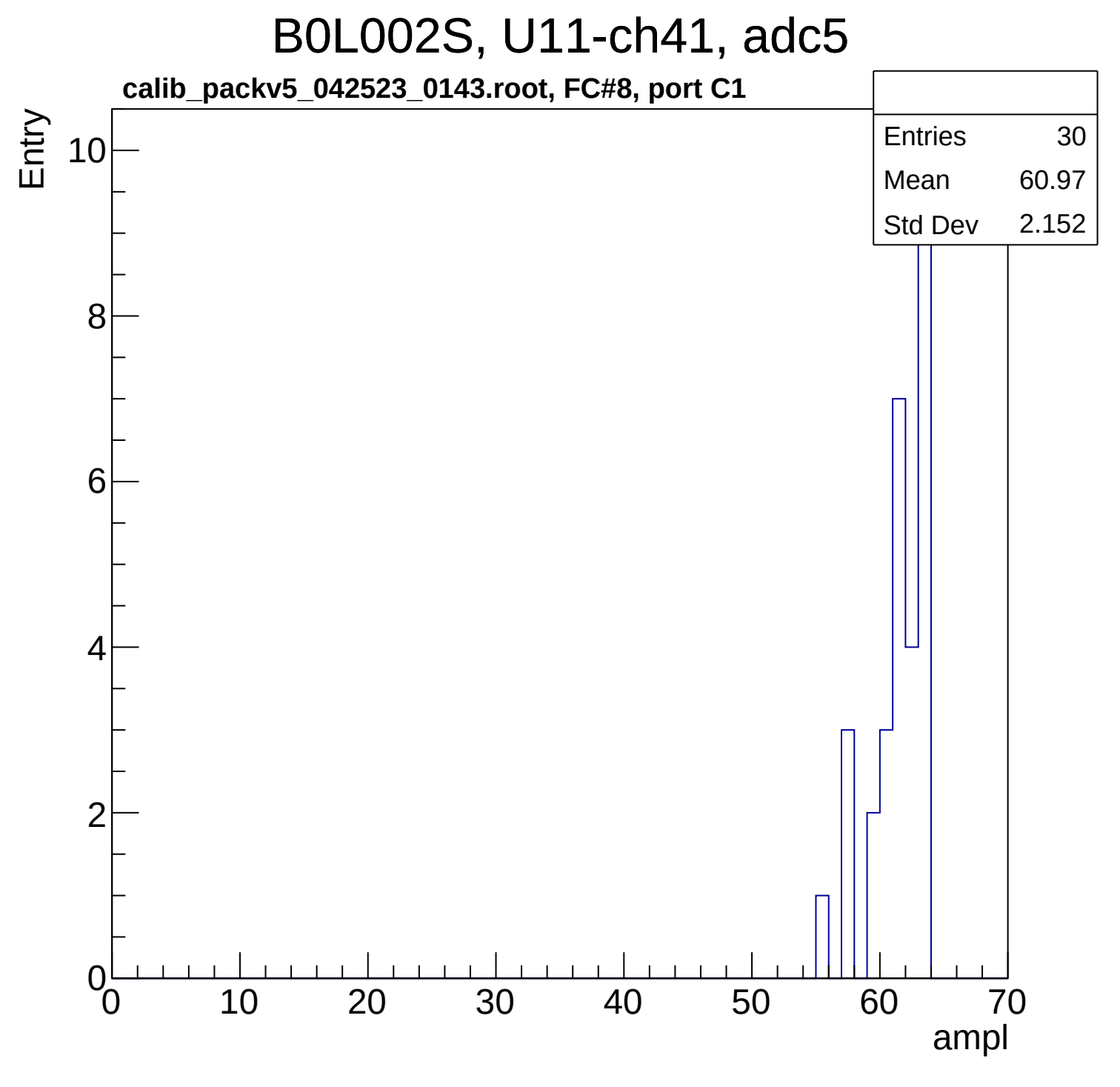
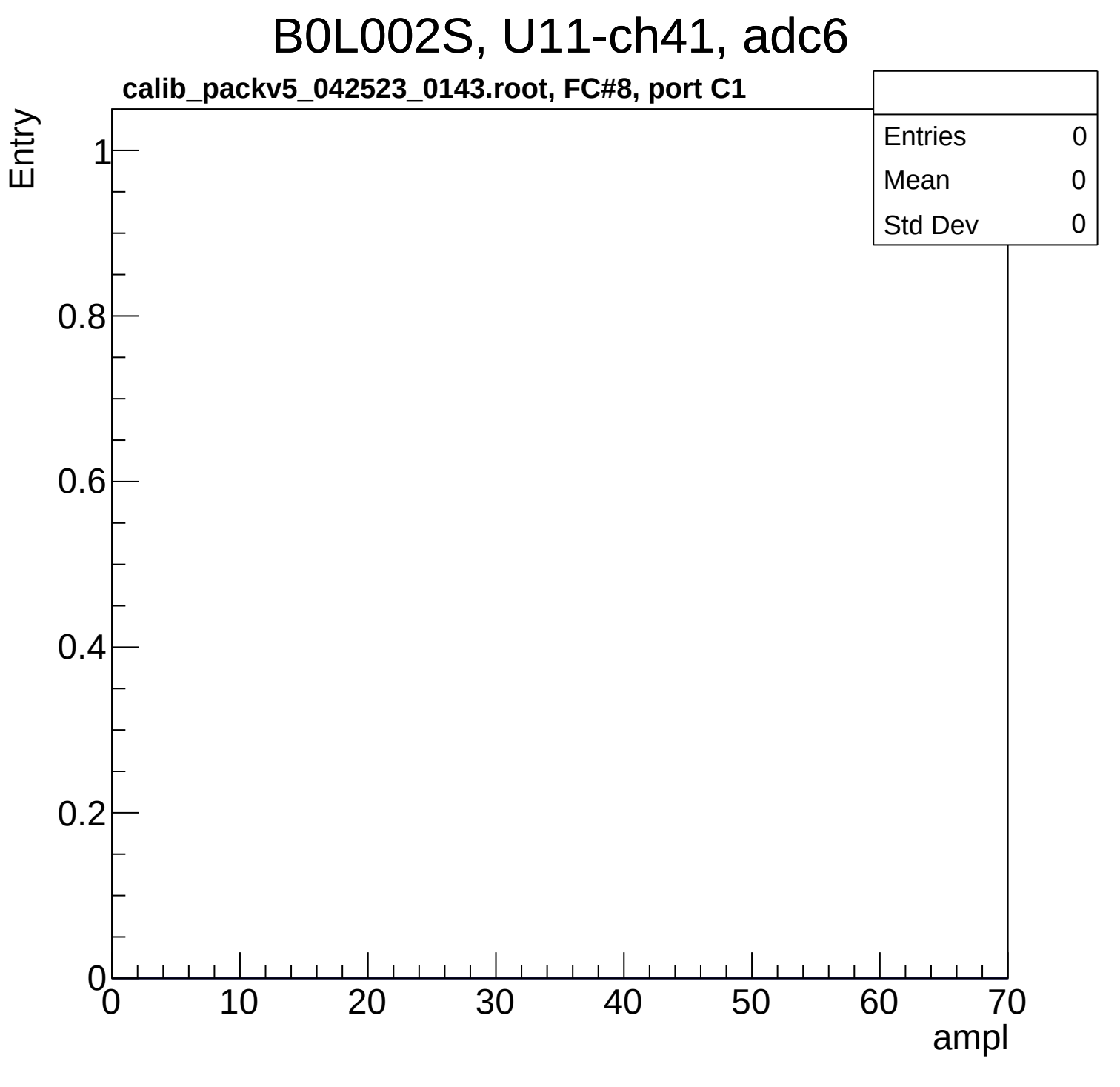
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 60.97
Std Dev 2.152
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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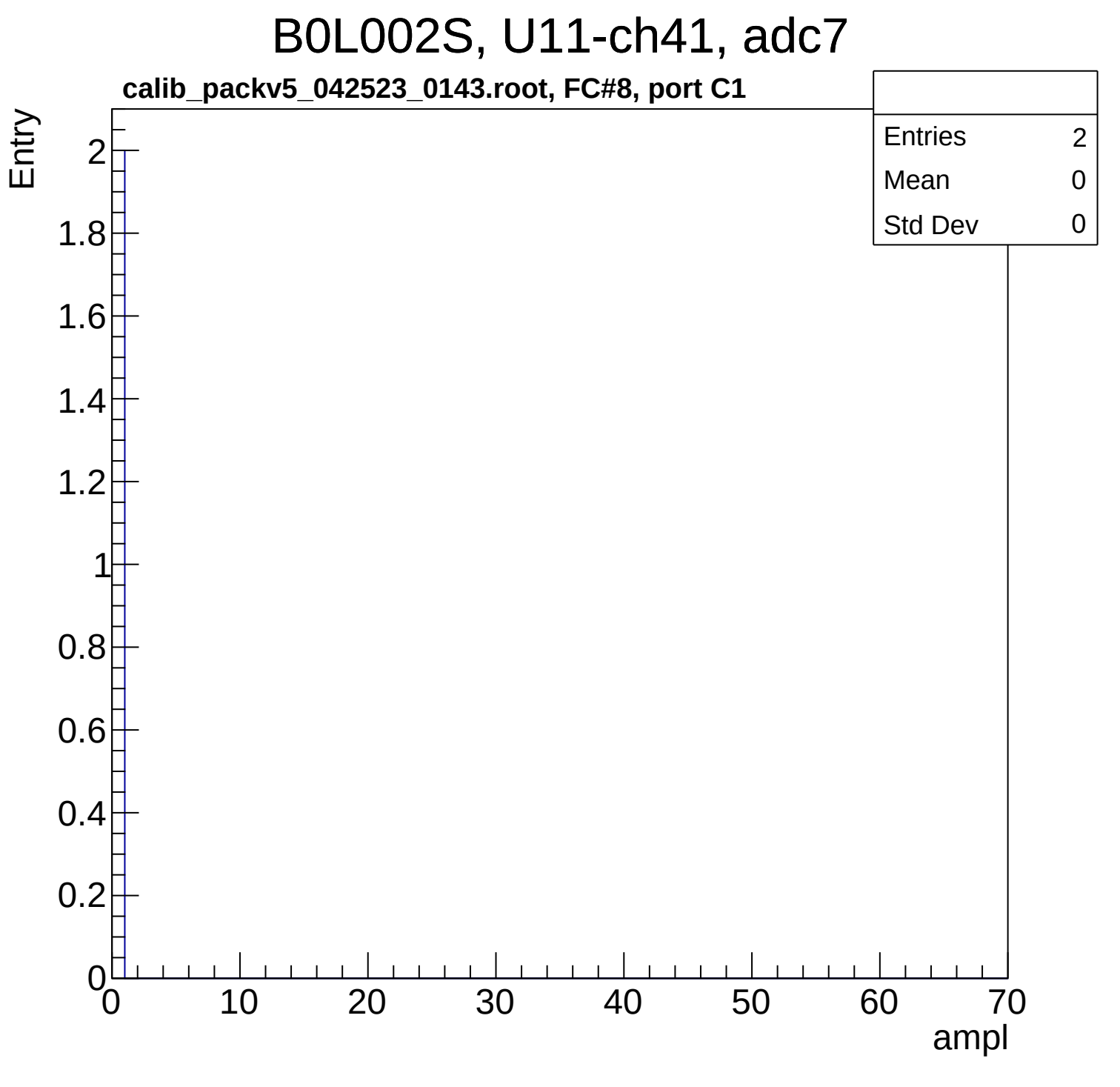
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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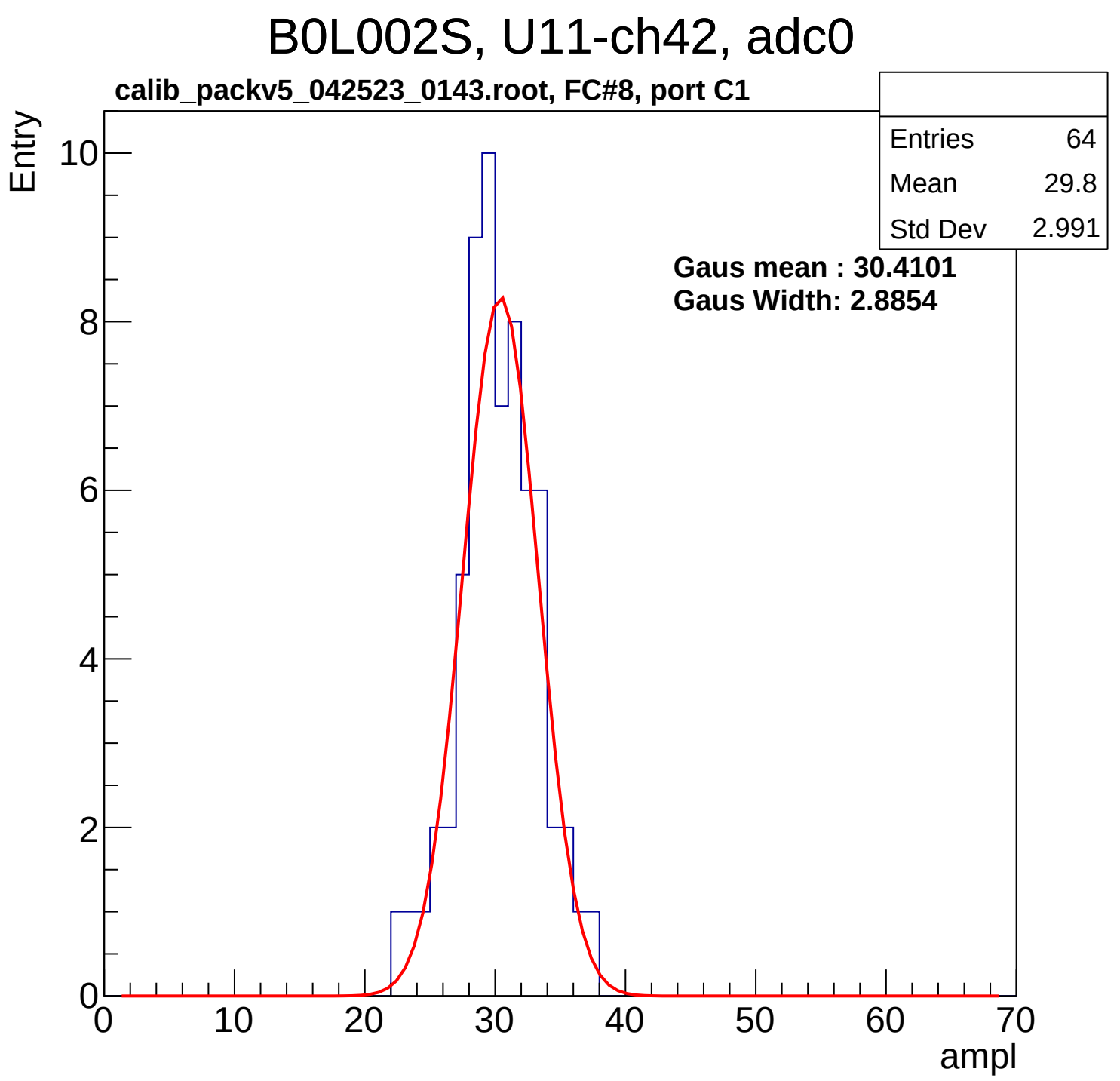
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.8
Std Dev 2.991
Gaus mean : 30.4101
Gaus Width: 2.8854
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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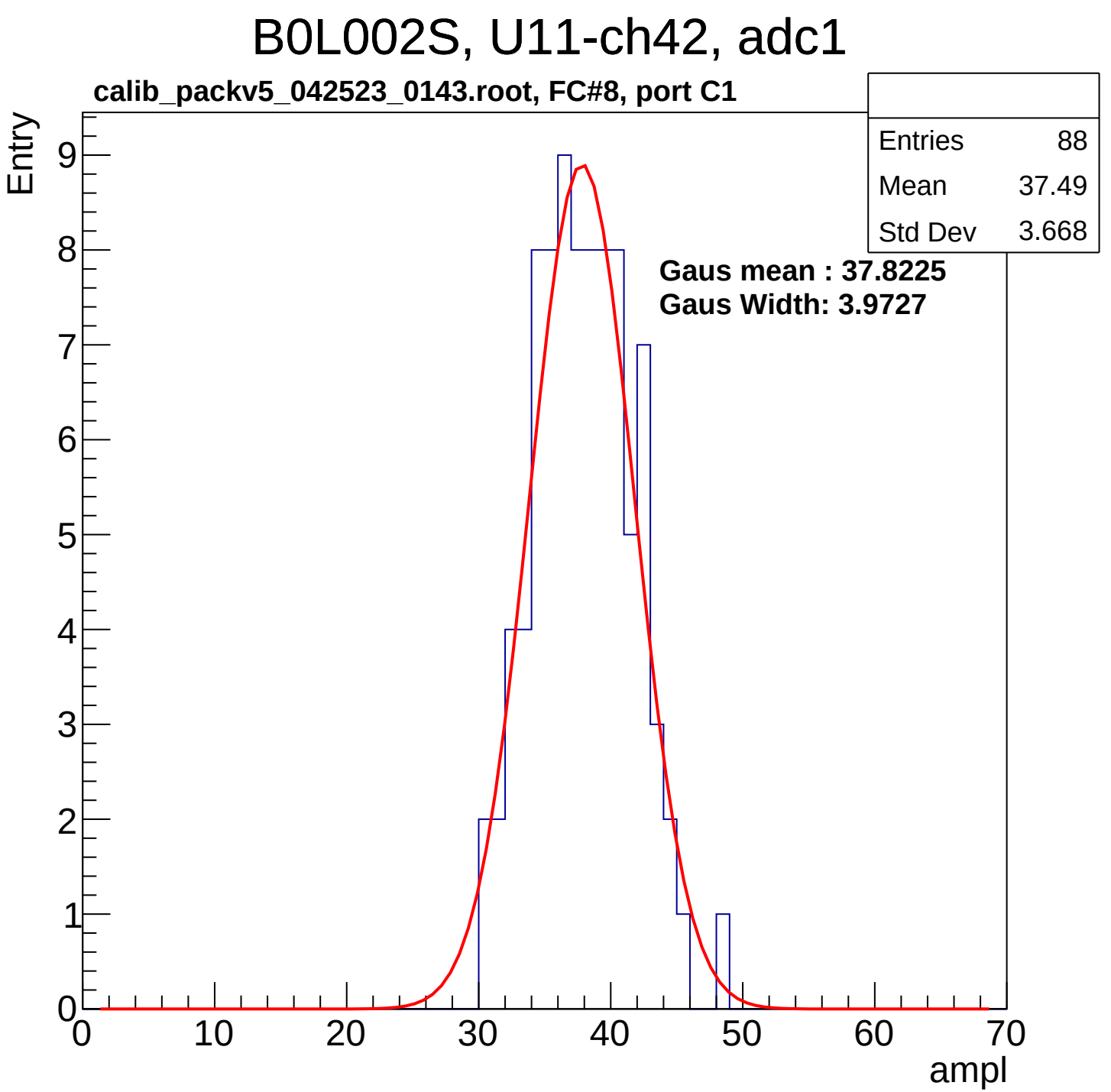
B0L002S, U11-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 37.49
Std Dev 3.668
Gaus mean : 37.8225
Gaus Width: 3.9727
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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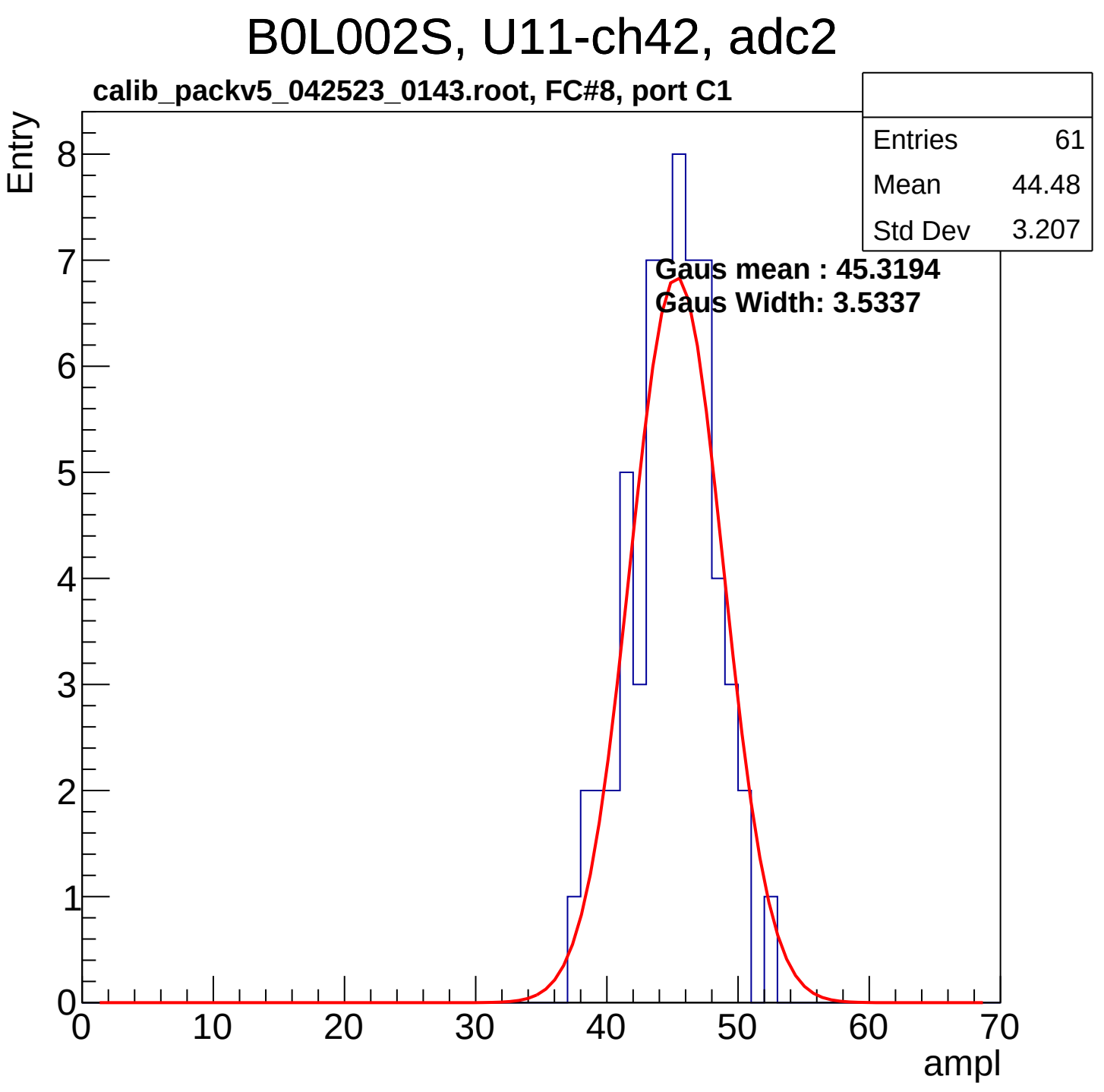
B0L002S, U11-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.48
Std Dev 3.207
Gaus mean : 45.3194
Gaus Width: 3.5337
Entry
ampl
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1
2
3
4
5
6
7
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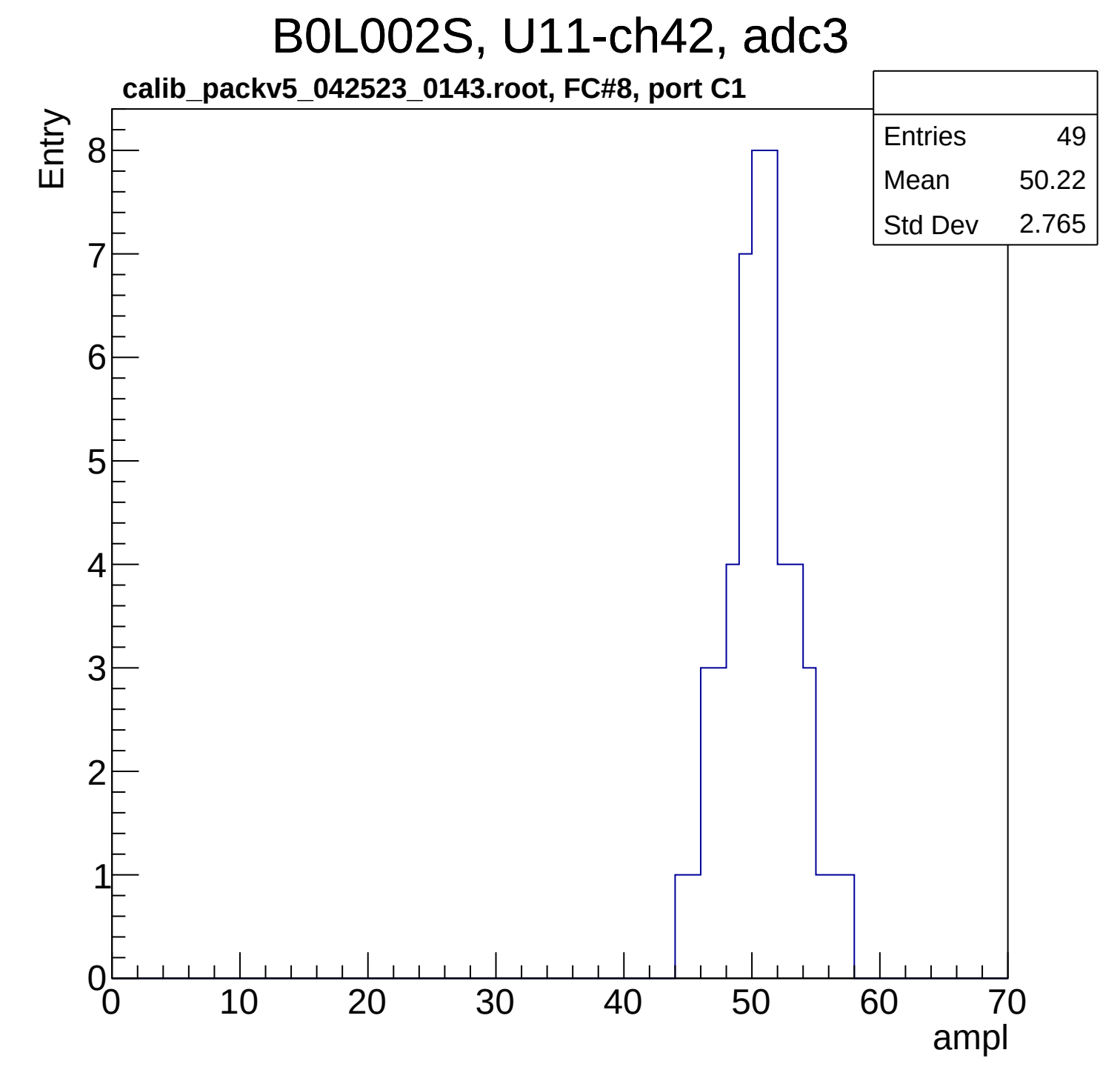
B0L002S, U11-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 50.22
Std Dev 2.765
Entry
ampl
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1
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5
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50
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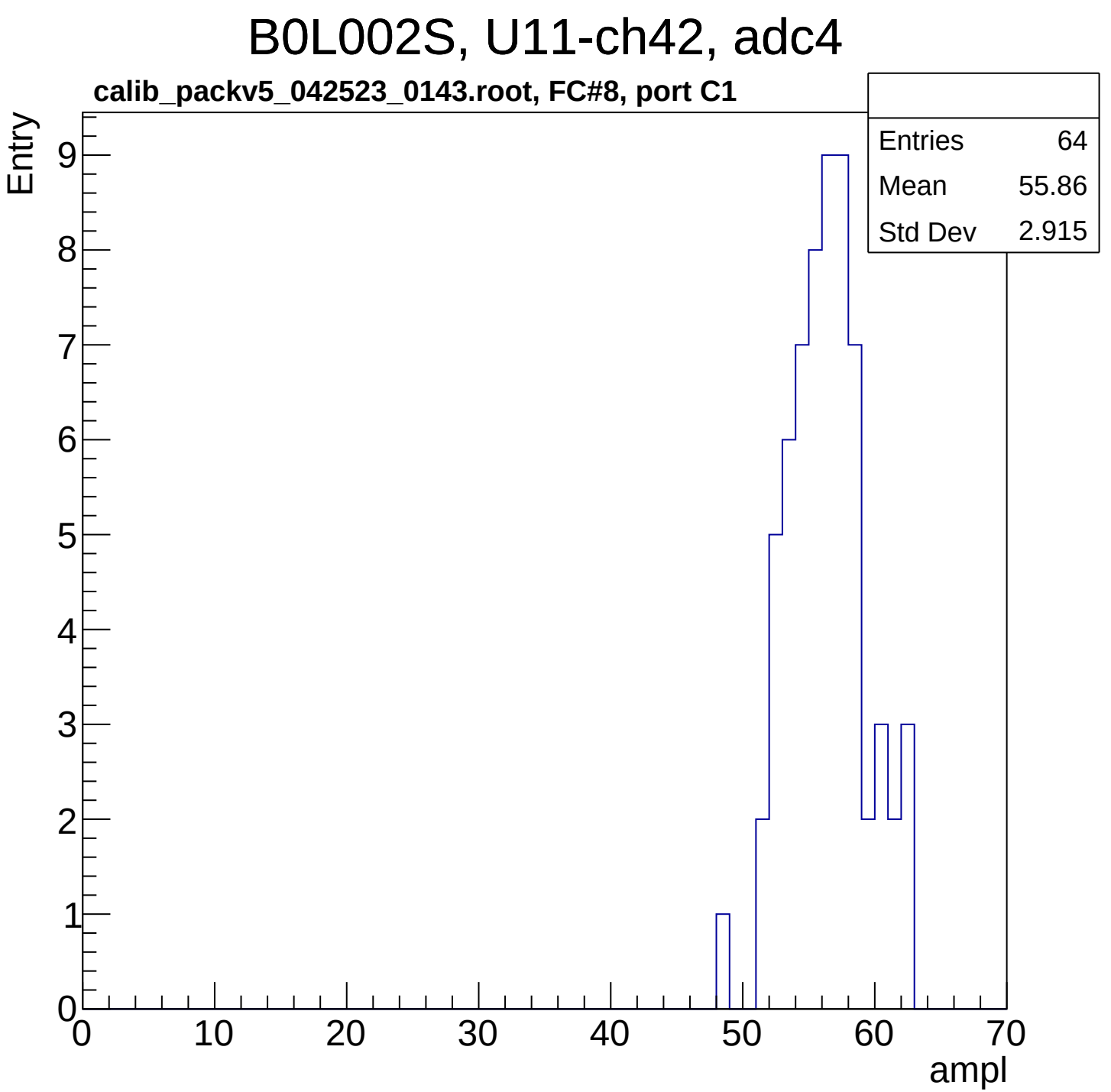
B0L002S, U11-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.86
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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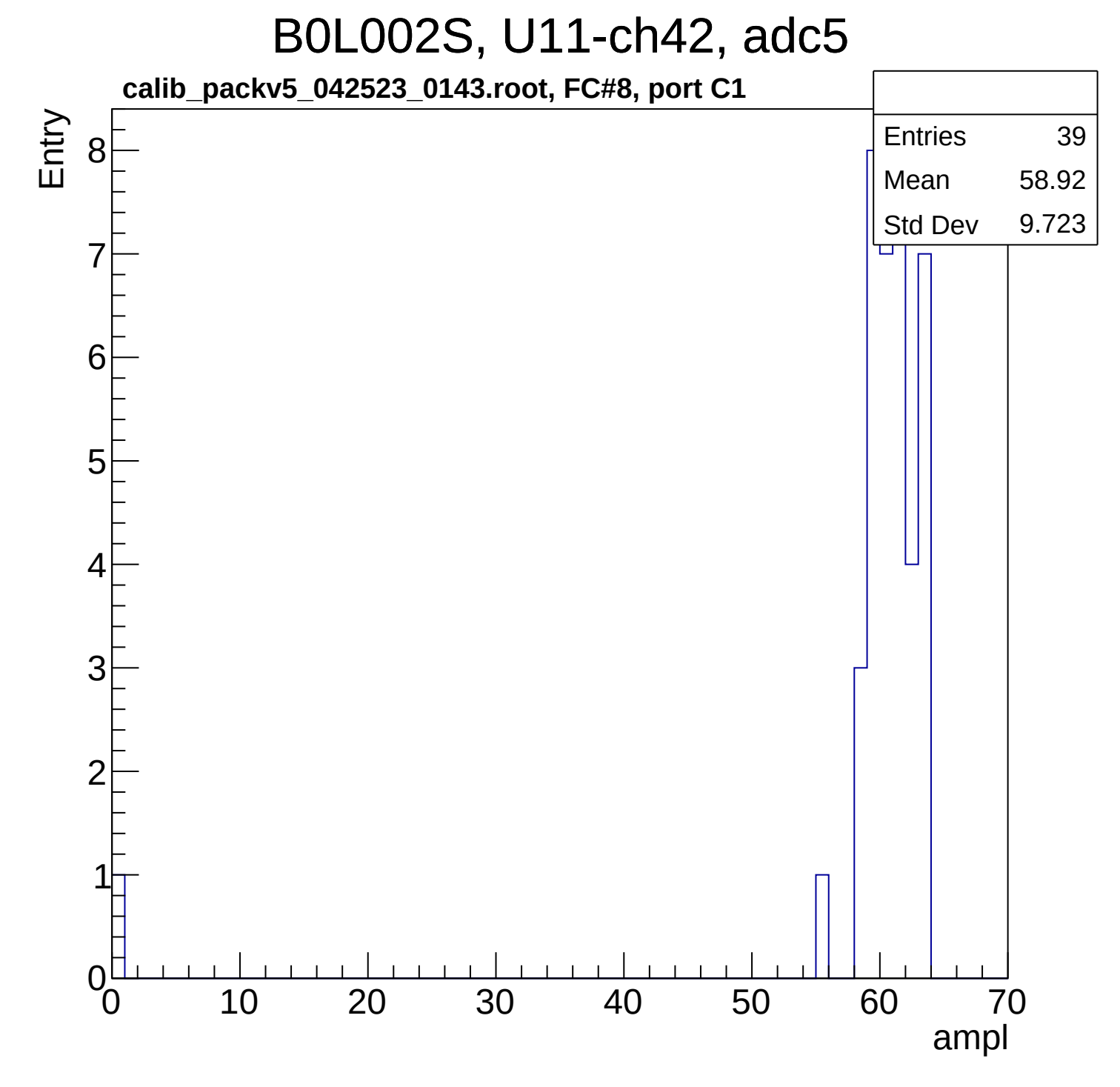
B0L002S, U11-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.92
Std Dev 9.723
Entry
ampl
0
1
2
3
4
5
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0
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60
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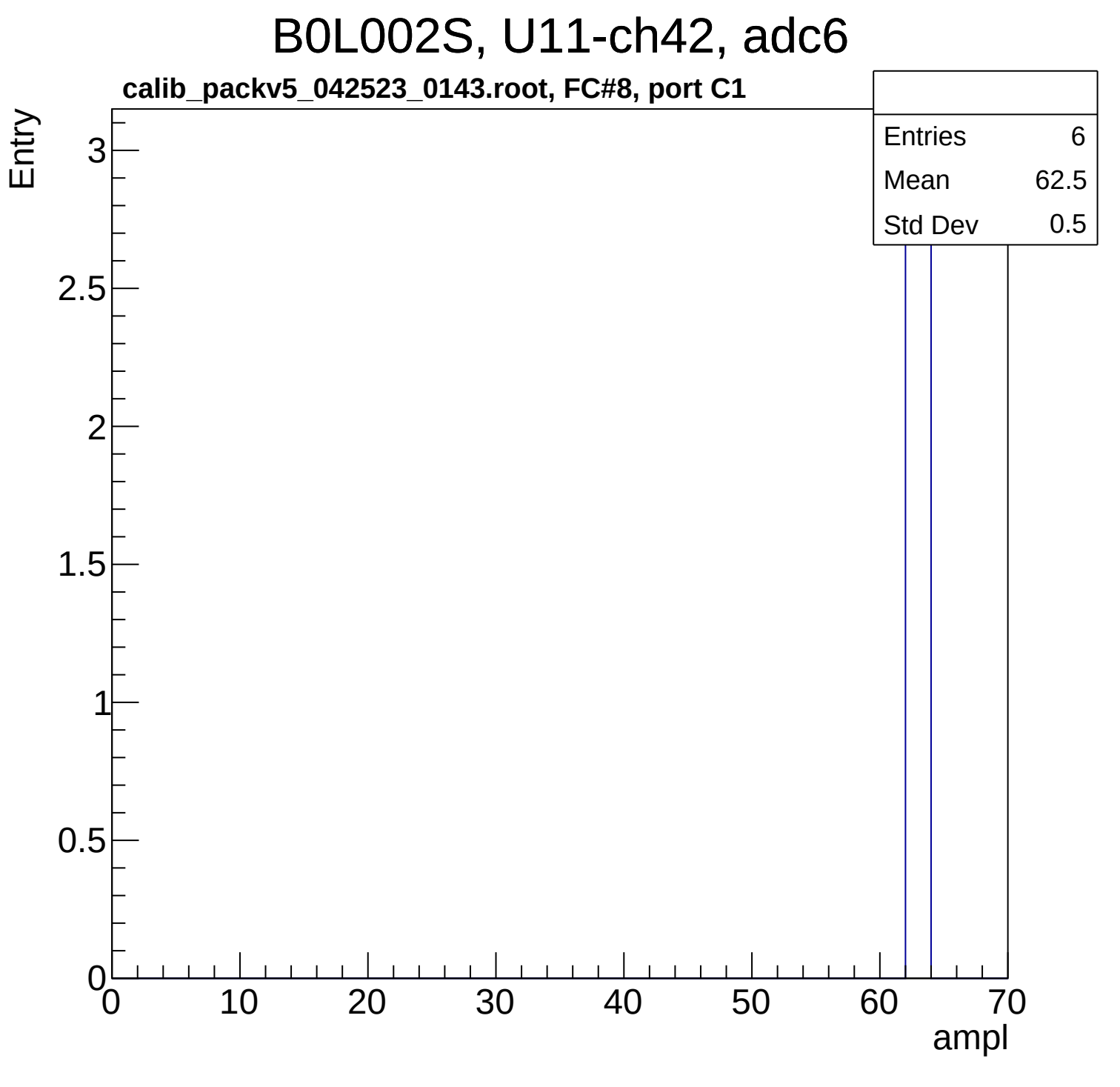
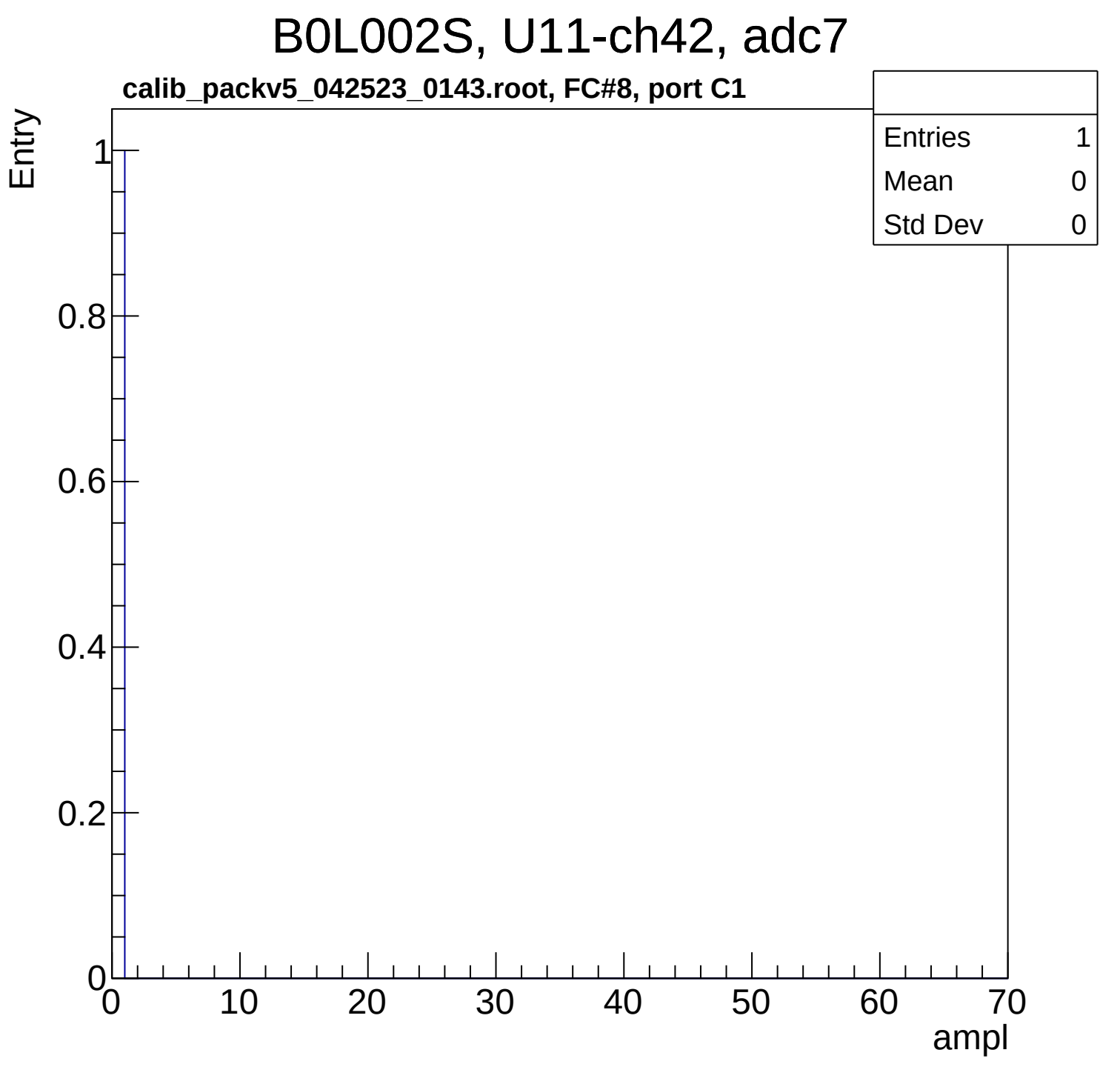
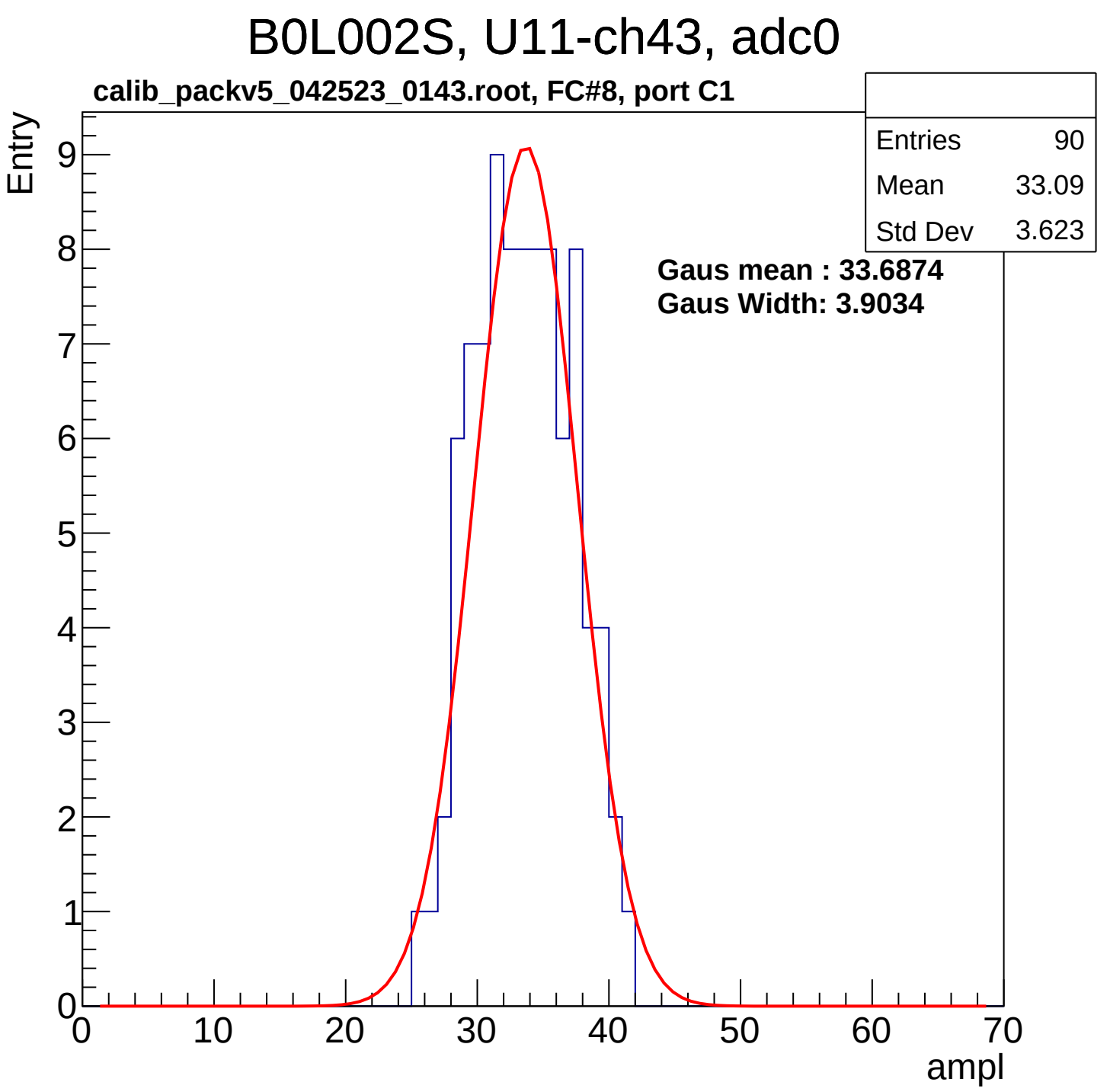

B0L002S, U11-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 33.09
Std Dev 3.623
Gaus mean : 33.6874
Gaus Width: 3.9034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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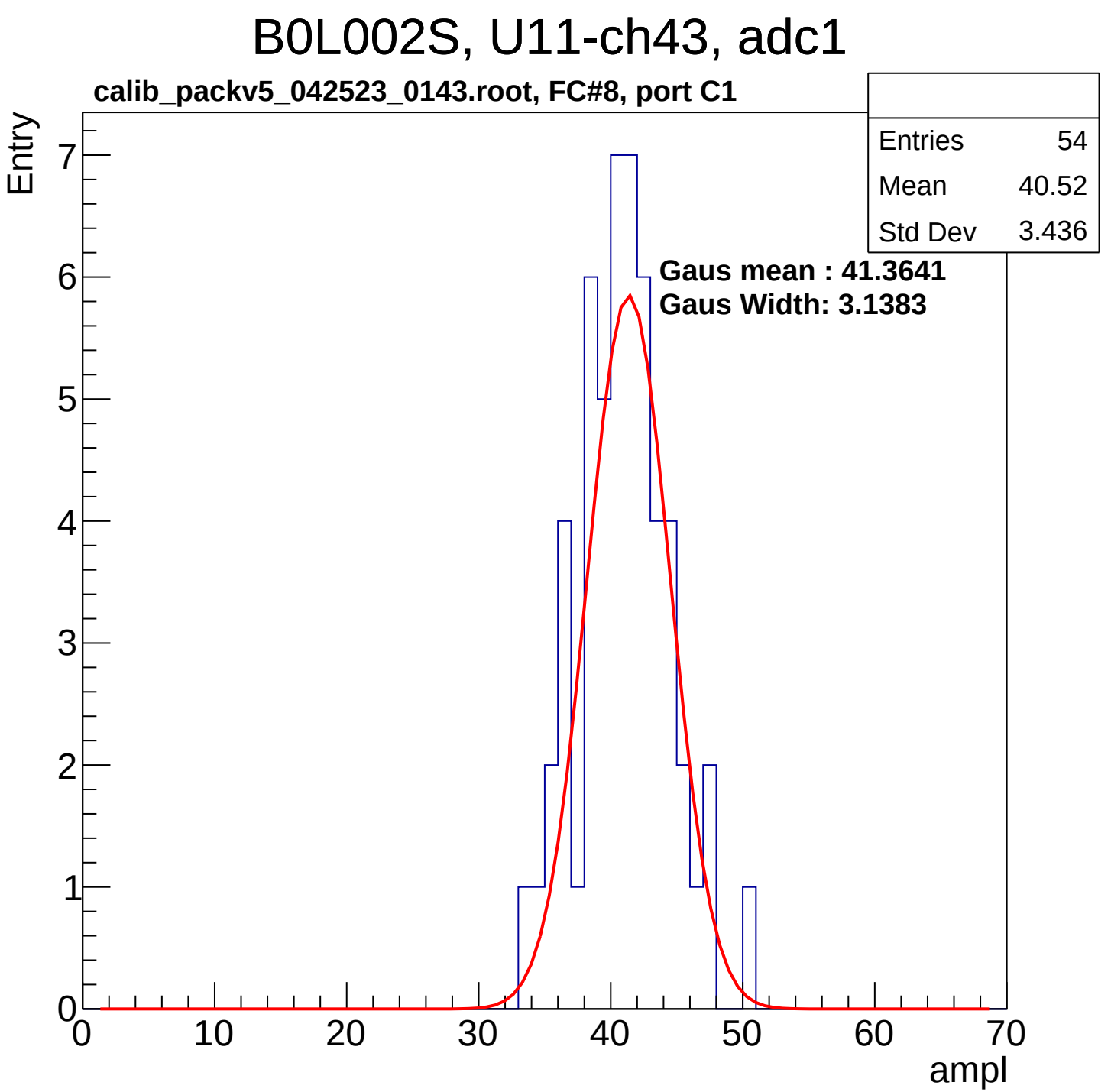
B0L002S, U11-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 40.52
Std Dev 3.436
Gaus mean : 41.3641
Gaus Width: 3.1383
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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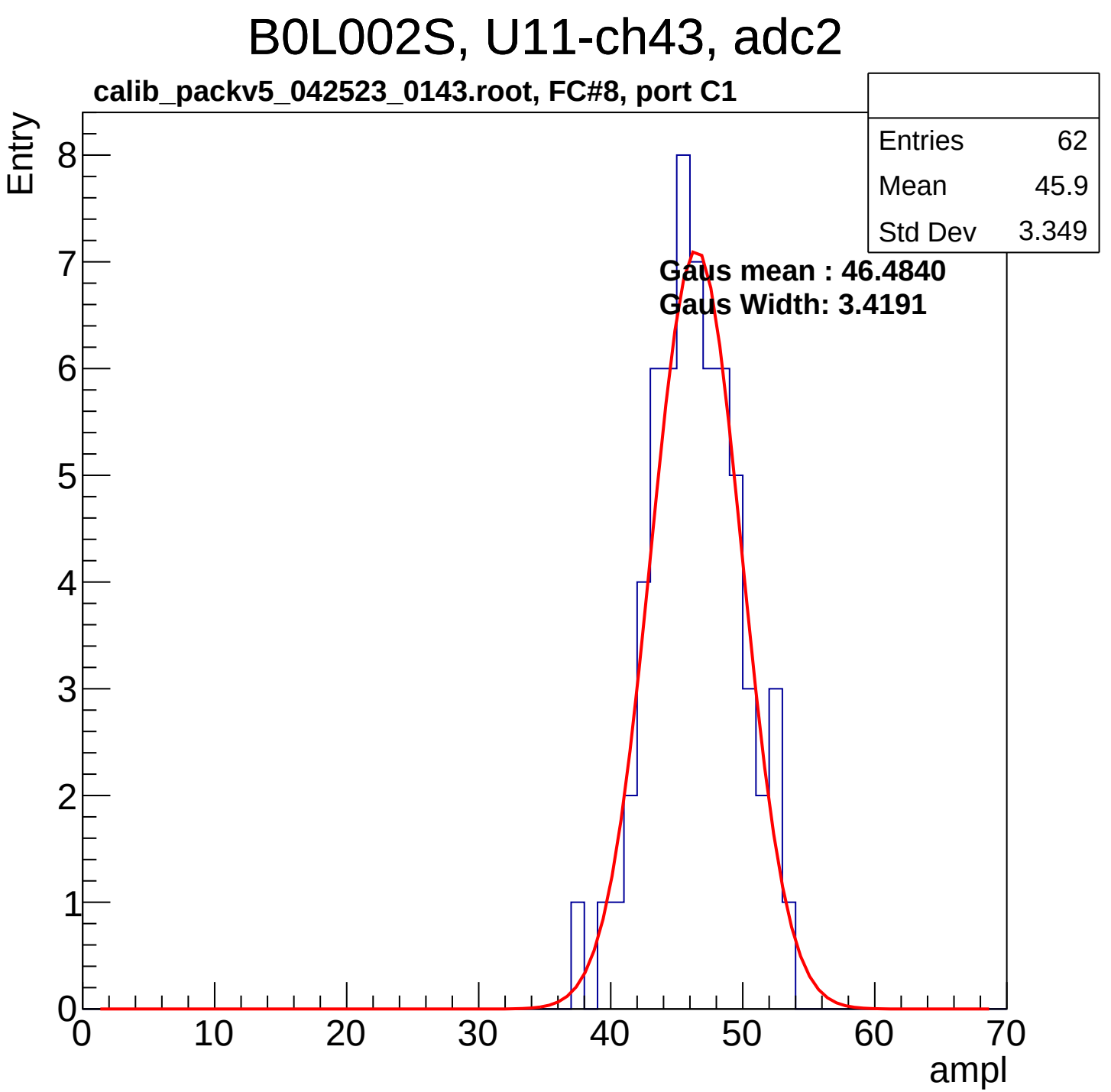
B0L002S, U11-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.9
Std Dev 3.349
Gaus mean : 46.4840
Gaus Width: 3.4191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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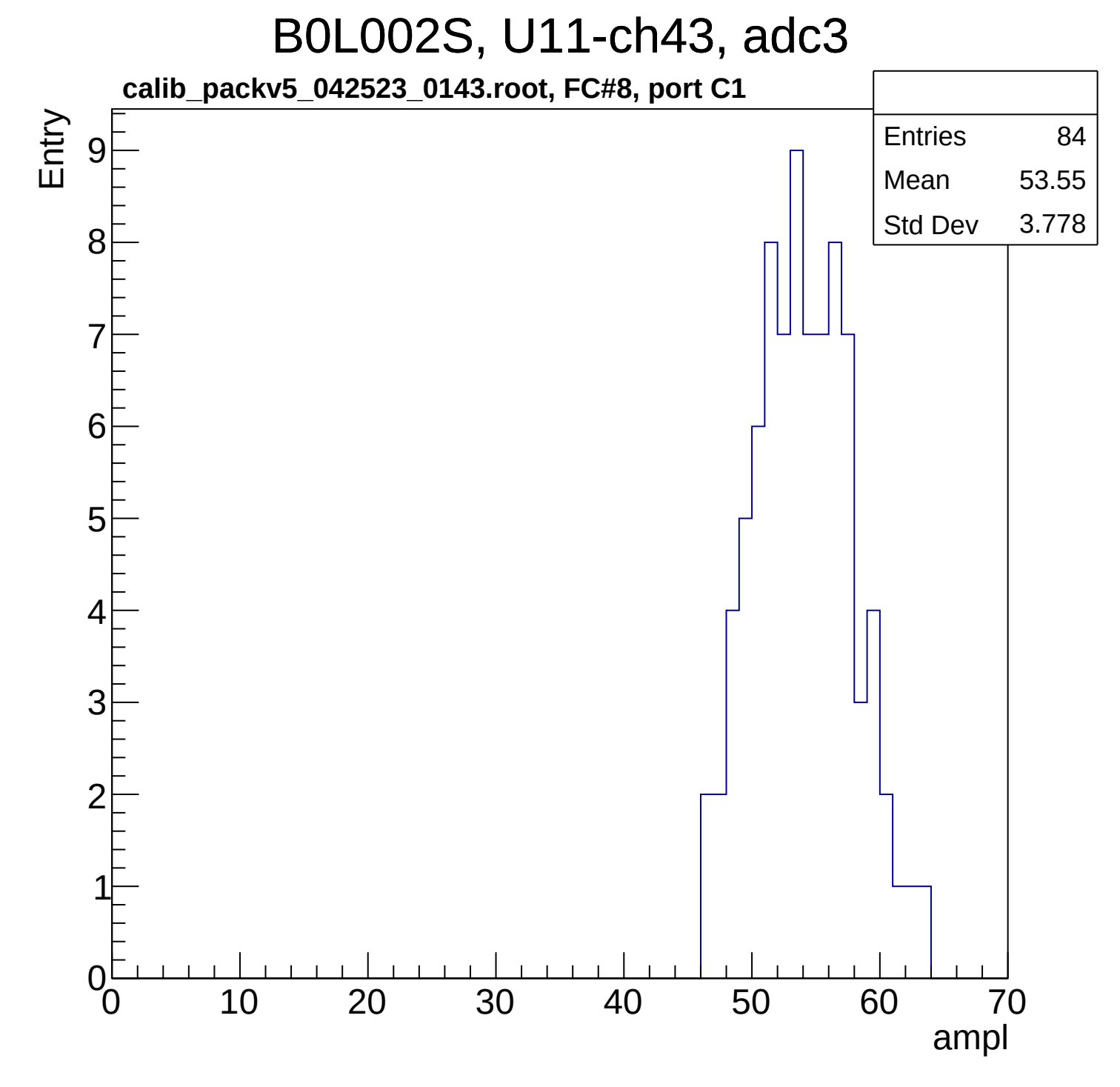
B0L002S, U11-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 53.55
Std Dev 3.778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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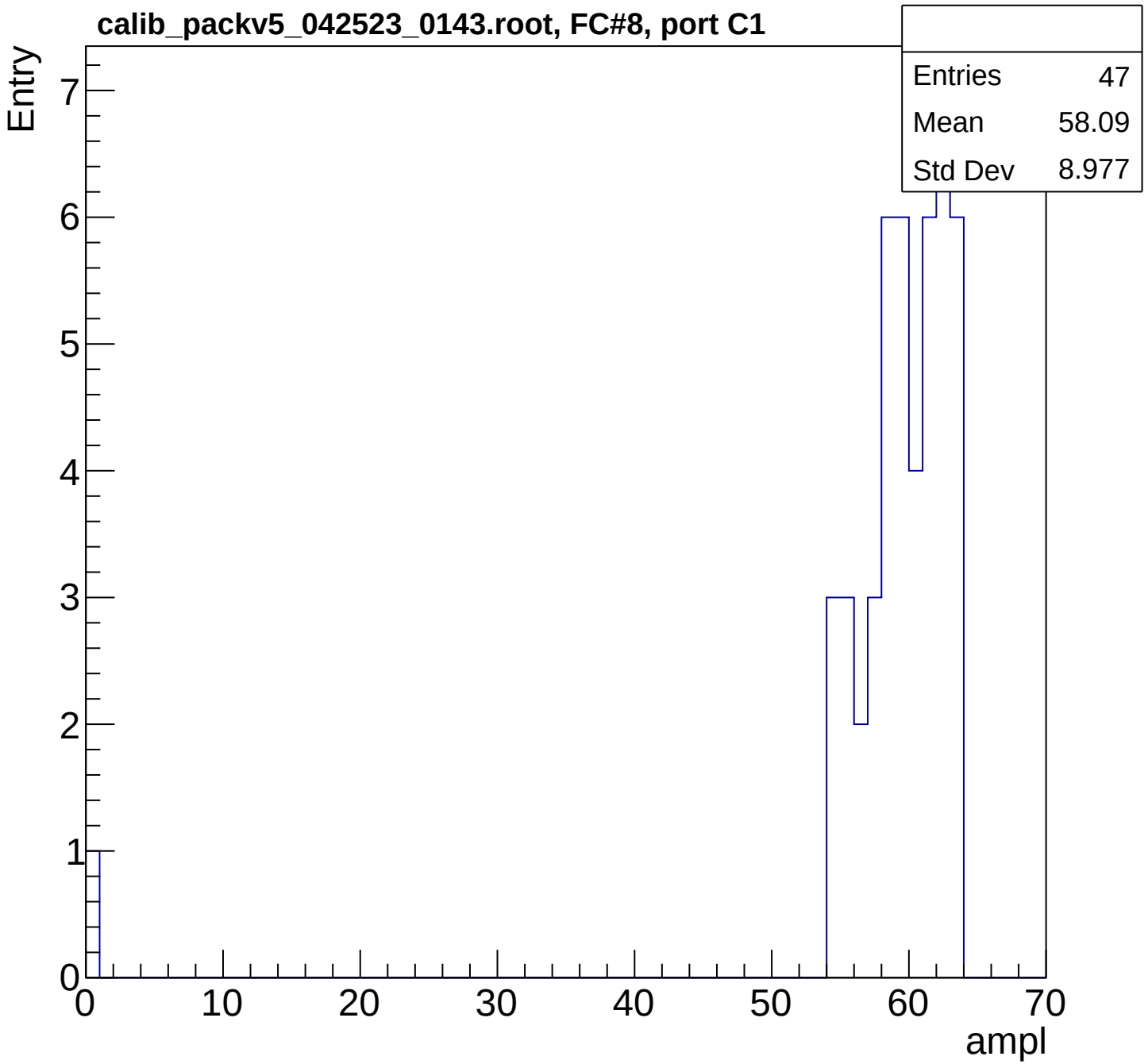
B0L002S, U11-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.09
Std Dev 8.977
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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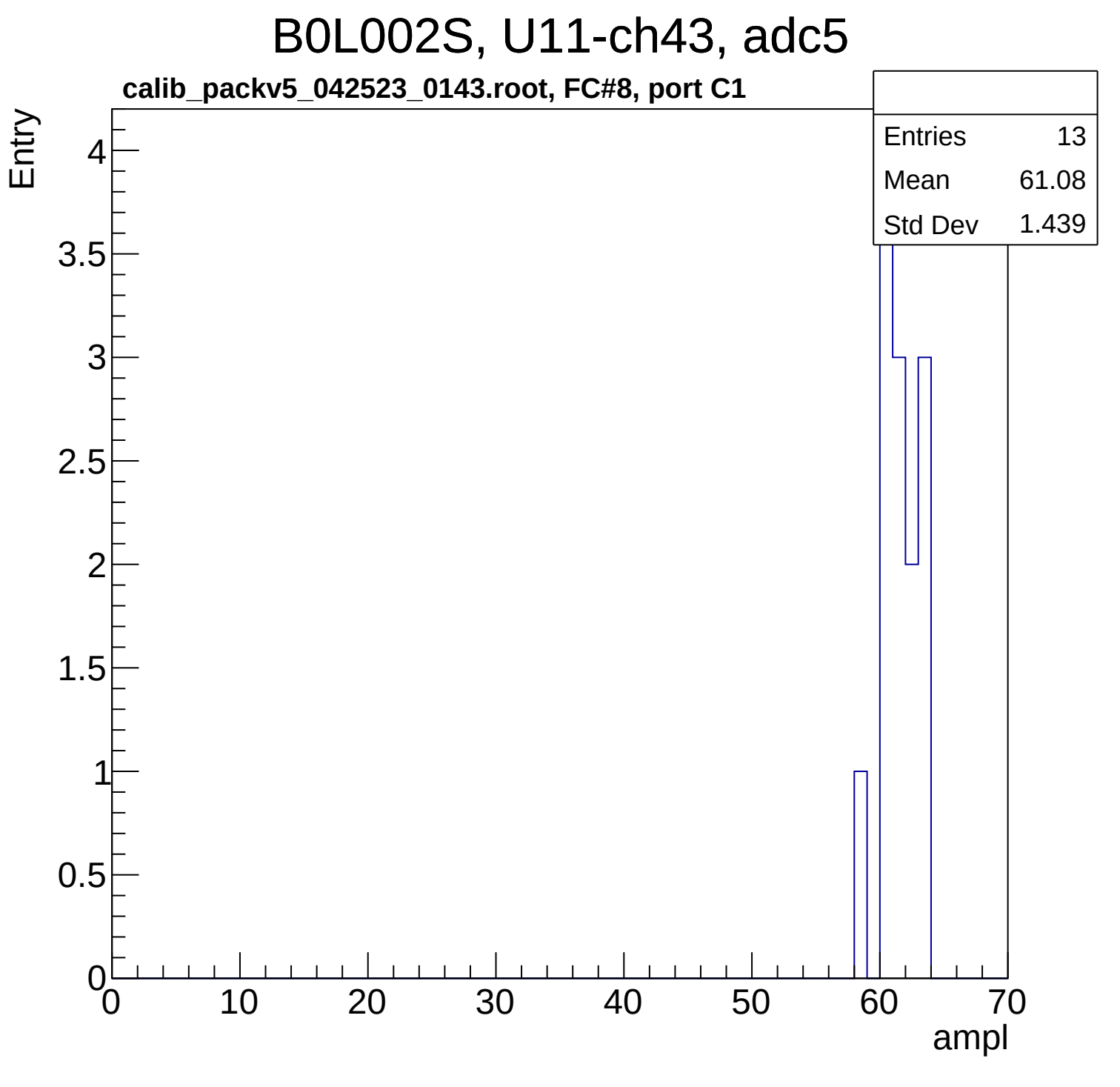
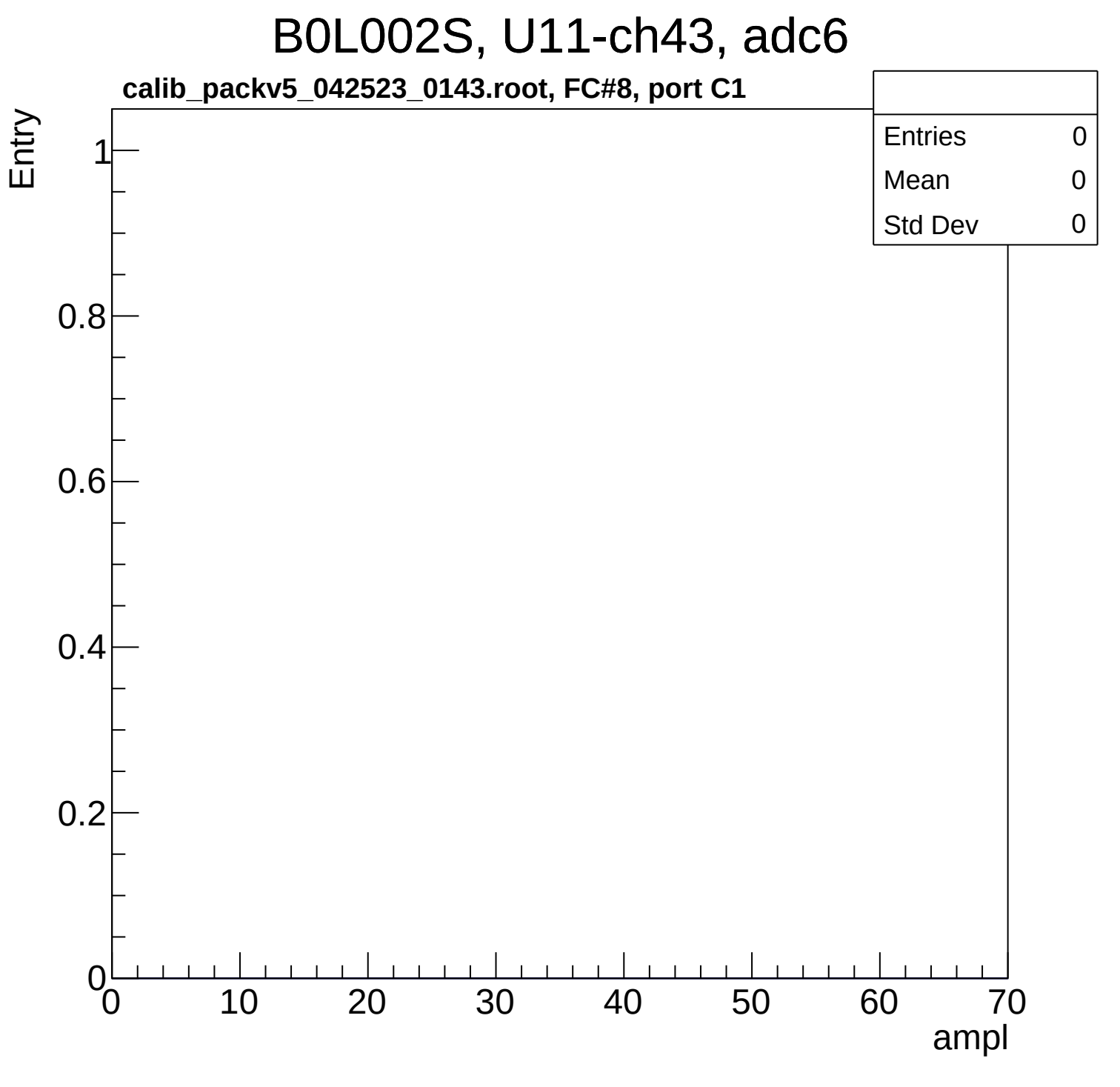
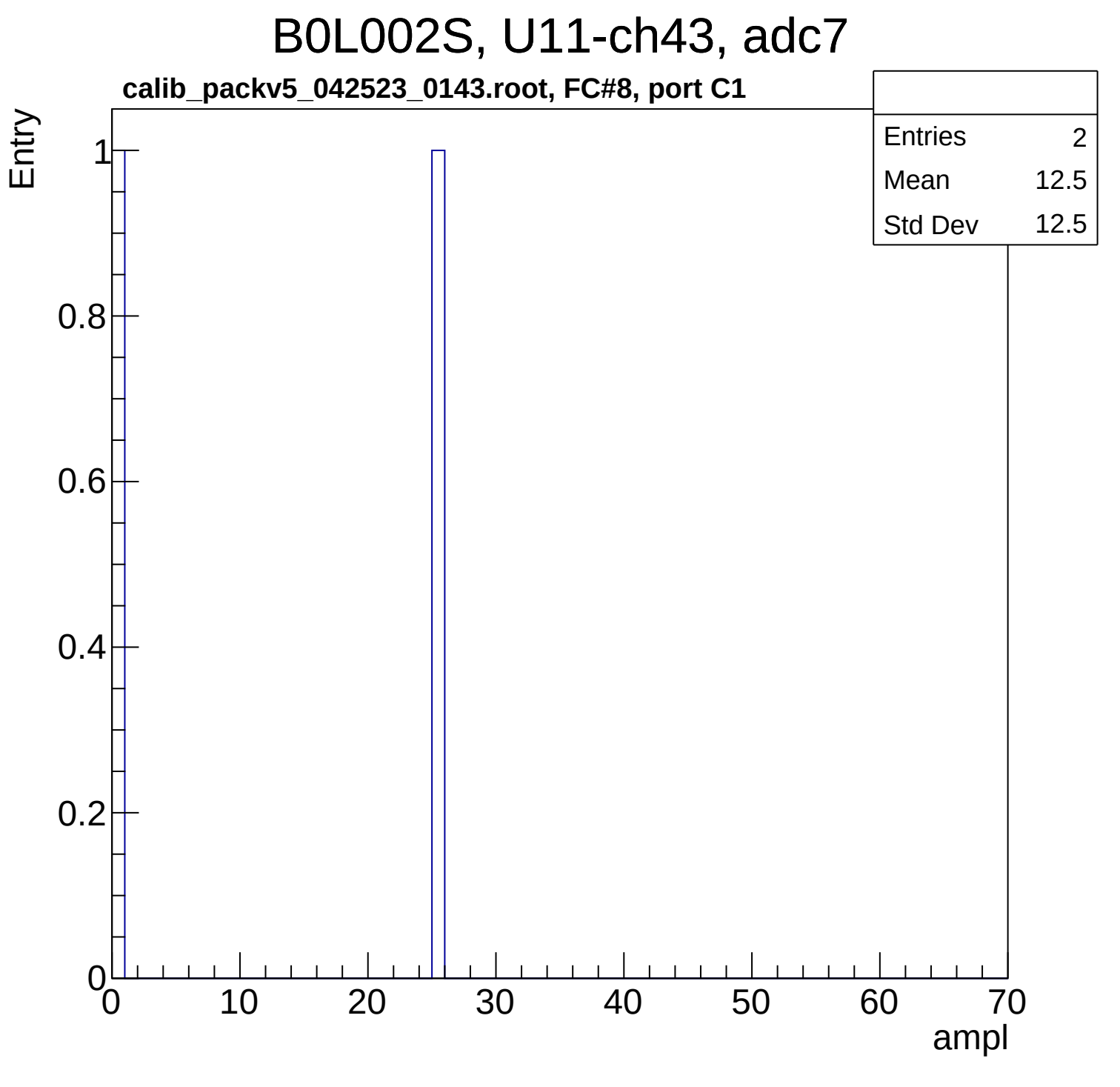
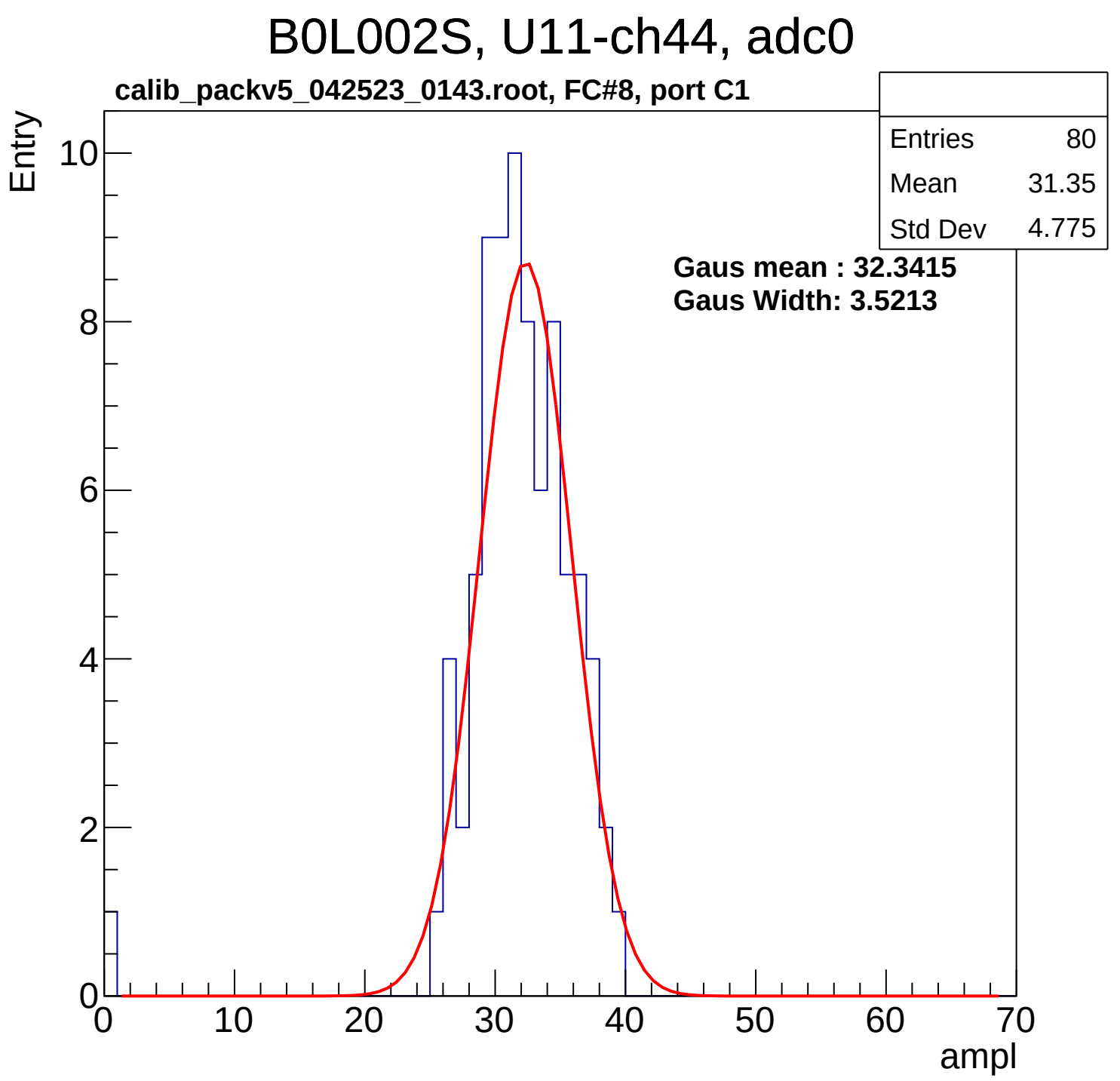
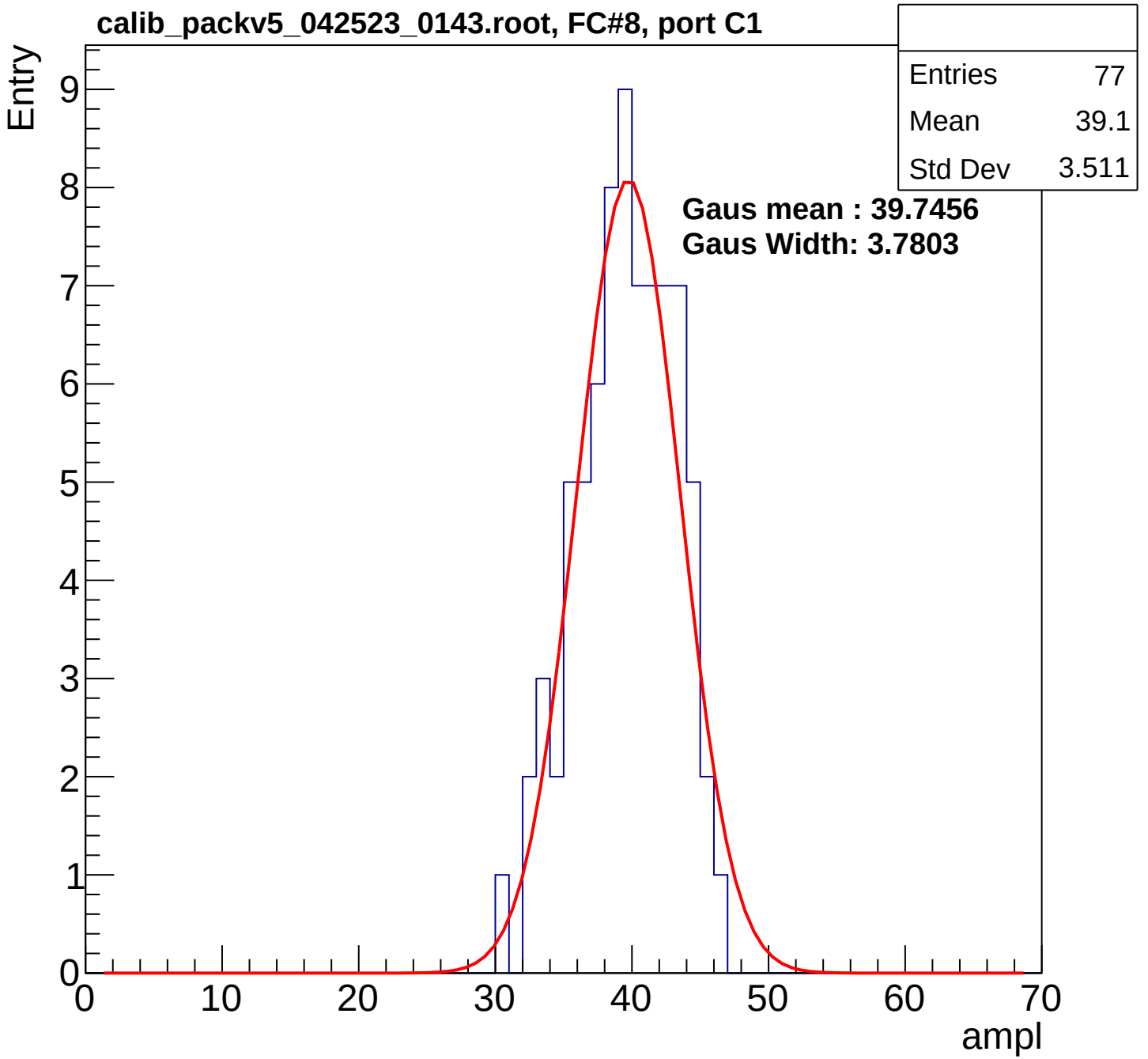

B0L002S, U11-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 61.08
Std Dev 1.439
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U11-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 31.35
Std Dev 4.775
Gaus mean : 32.3415
Gaus Width: 3.5213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.1
Std Dev 3.511
Gaus mean : 39.7456
Gaus Width: 3.7803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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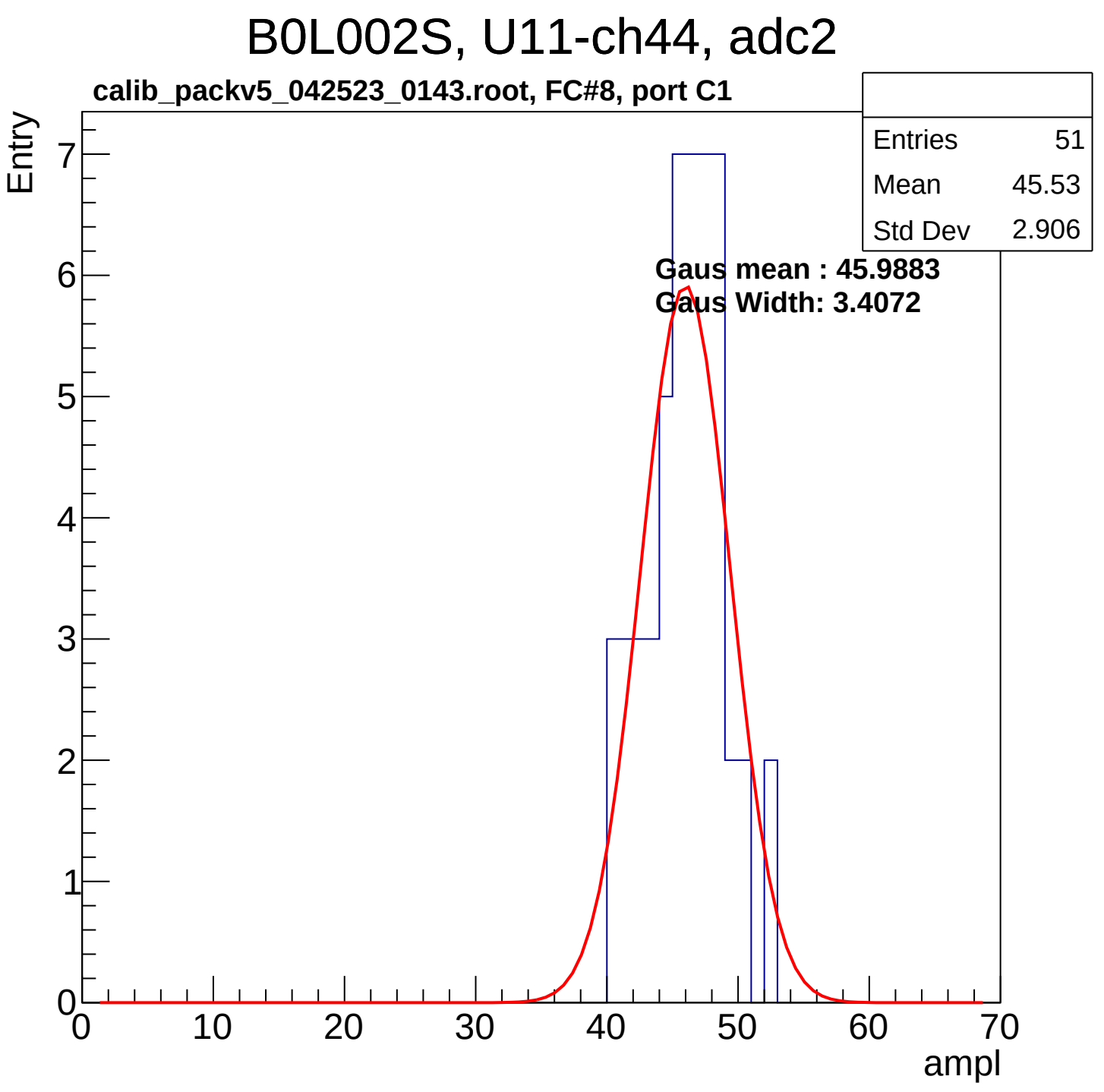
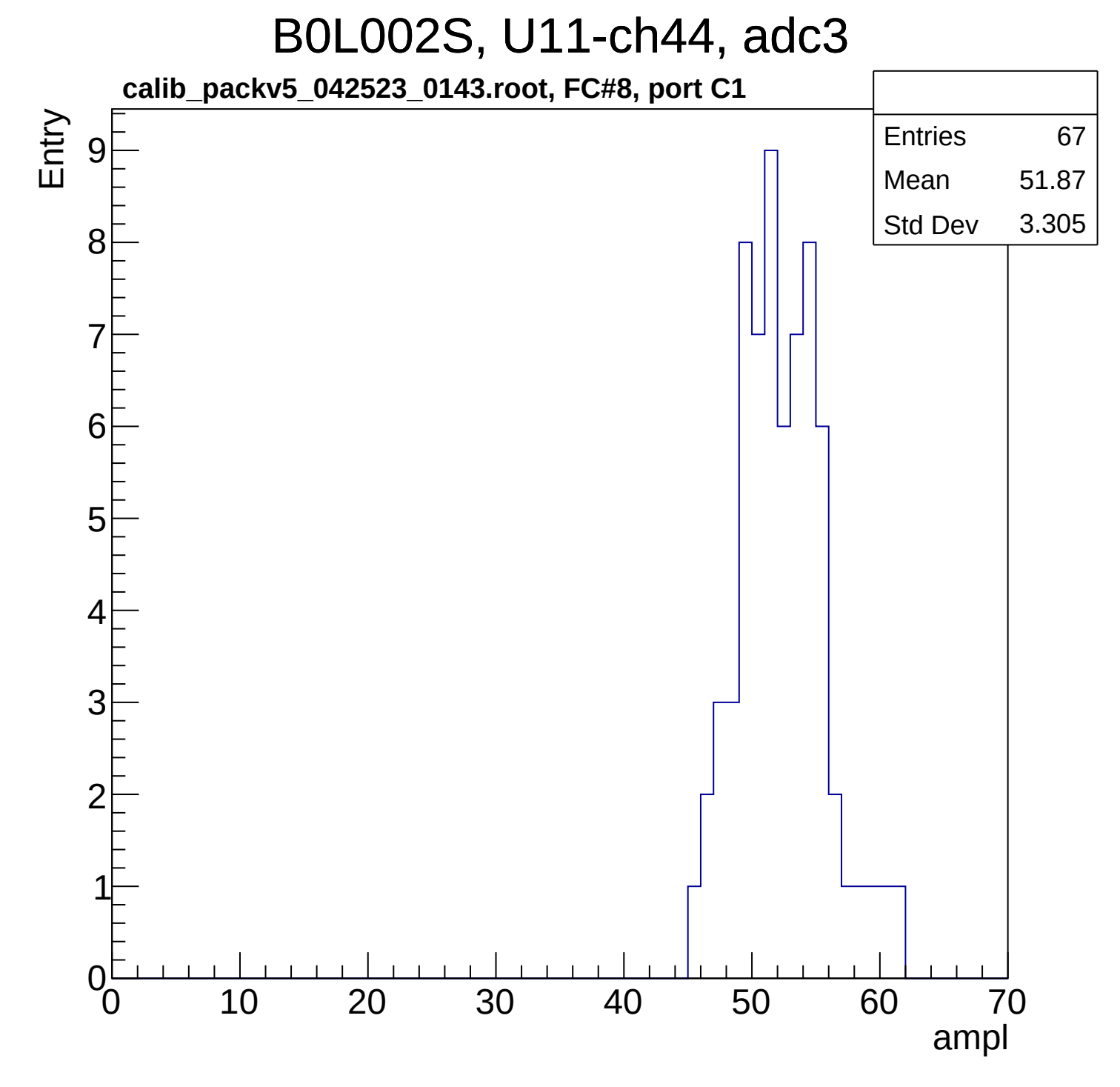
60
70


B0L002S, U11-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 45.53
Std Dev 2.906
Gaus mean : 45.9883
Gaus Width: 3.4072
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.87
Std Dev 3.305
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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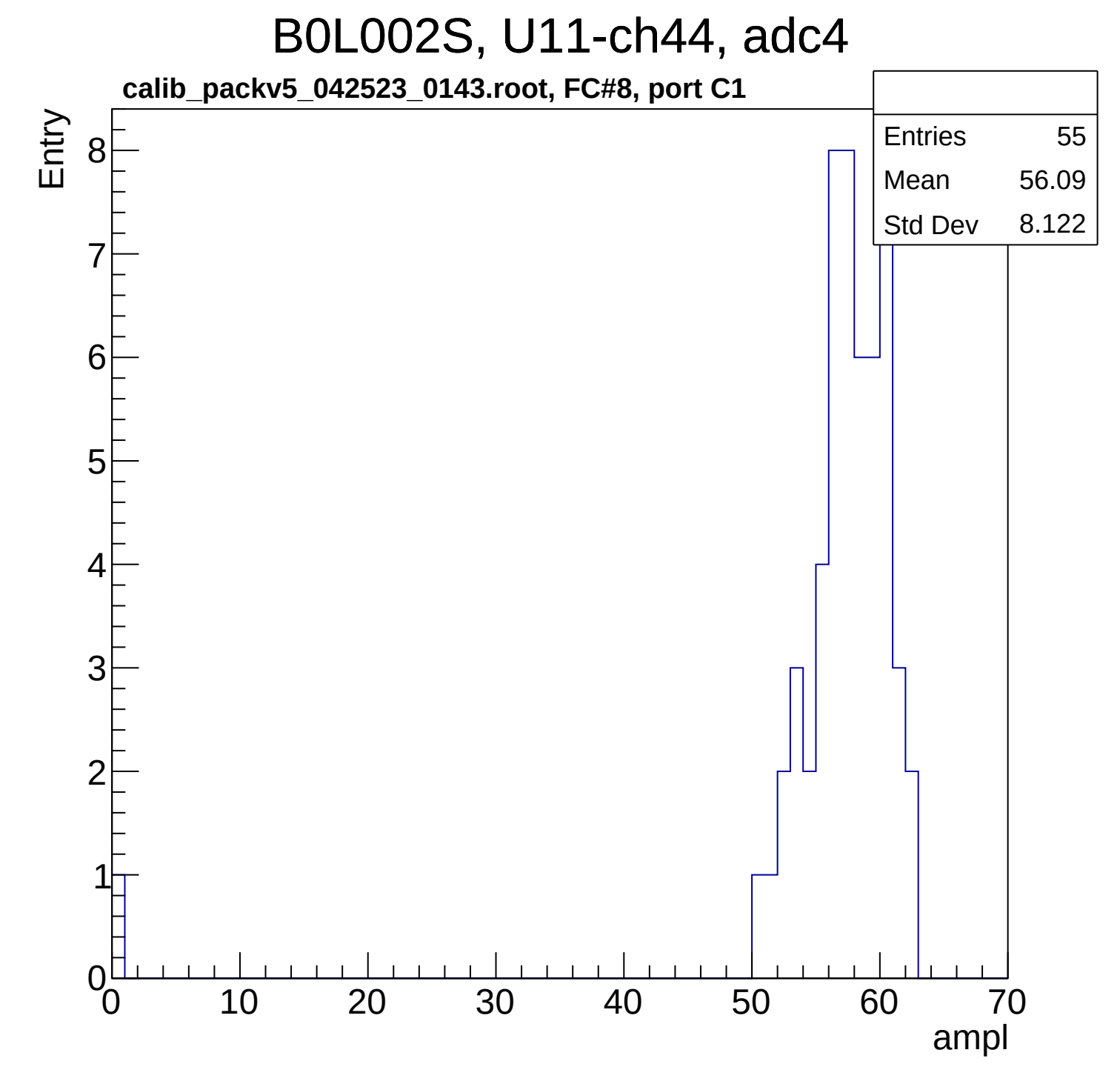
B0L002S, U11-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.09
Std Dev 8.122
Entry
ampl
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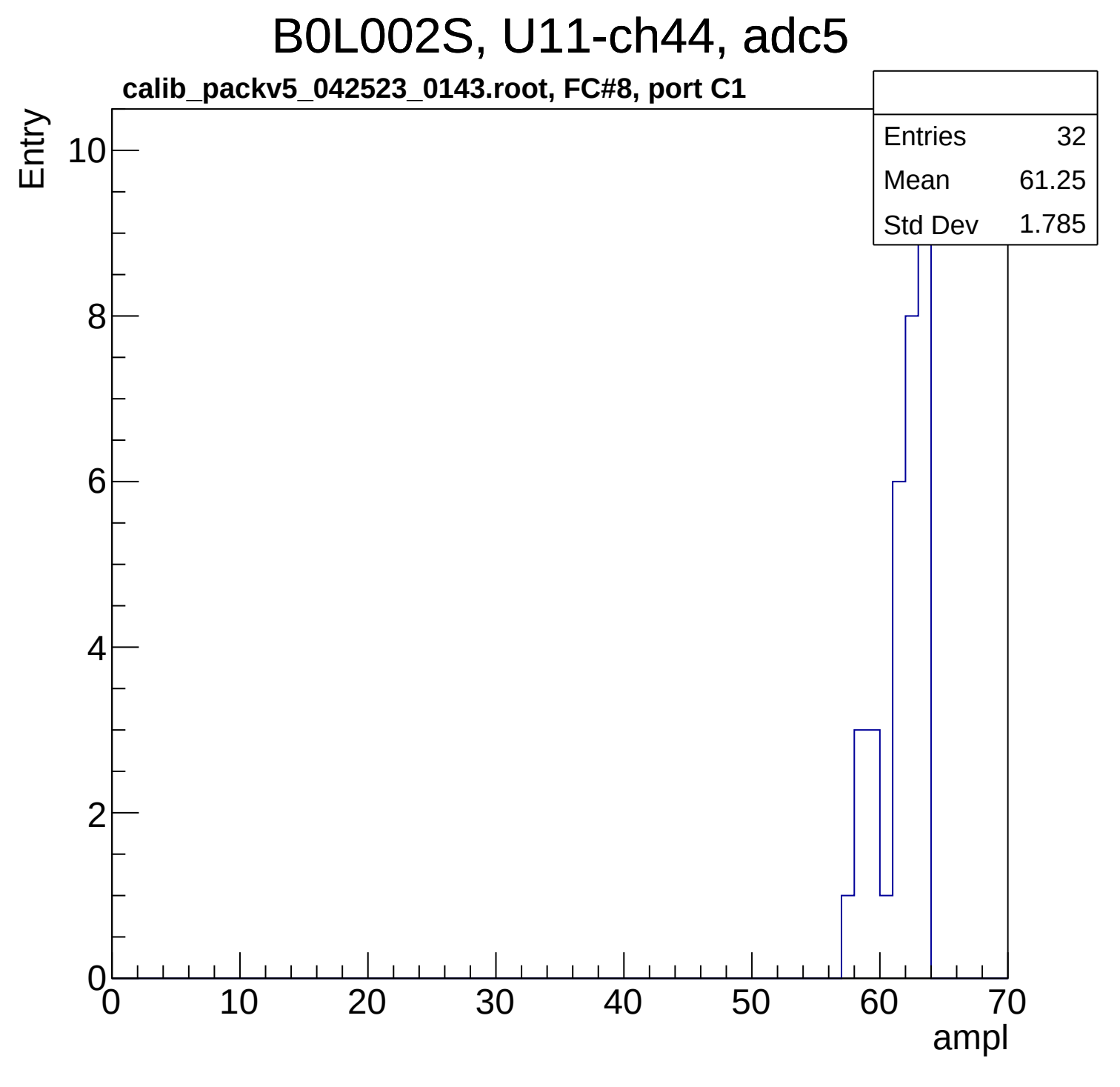
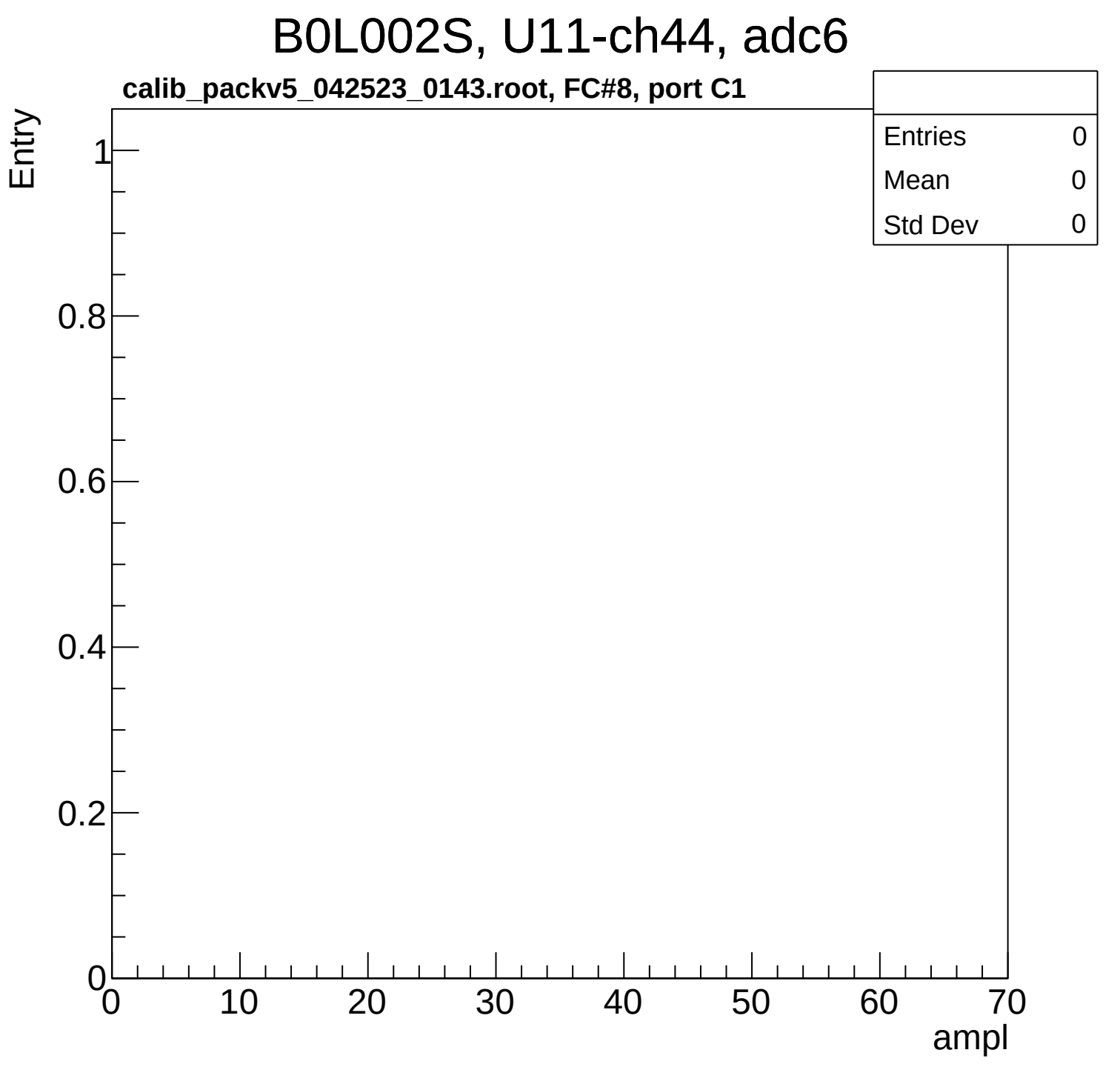
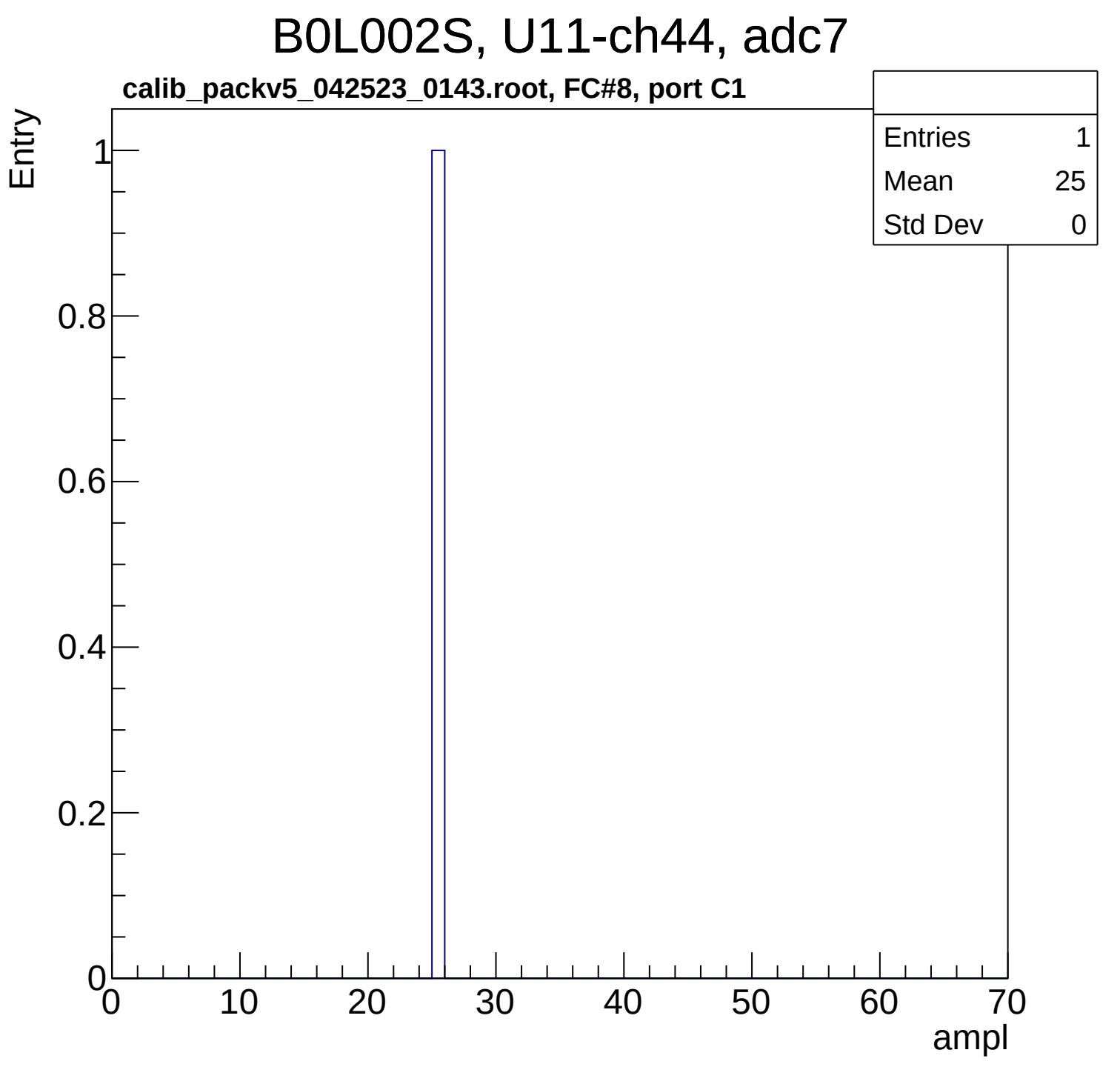
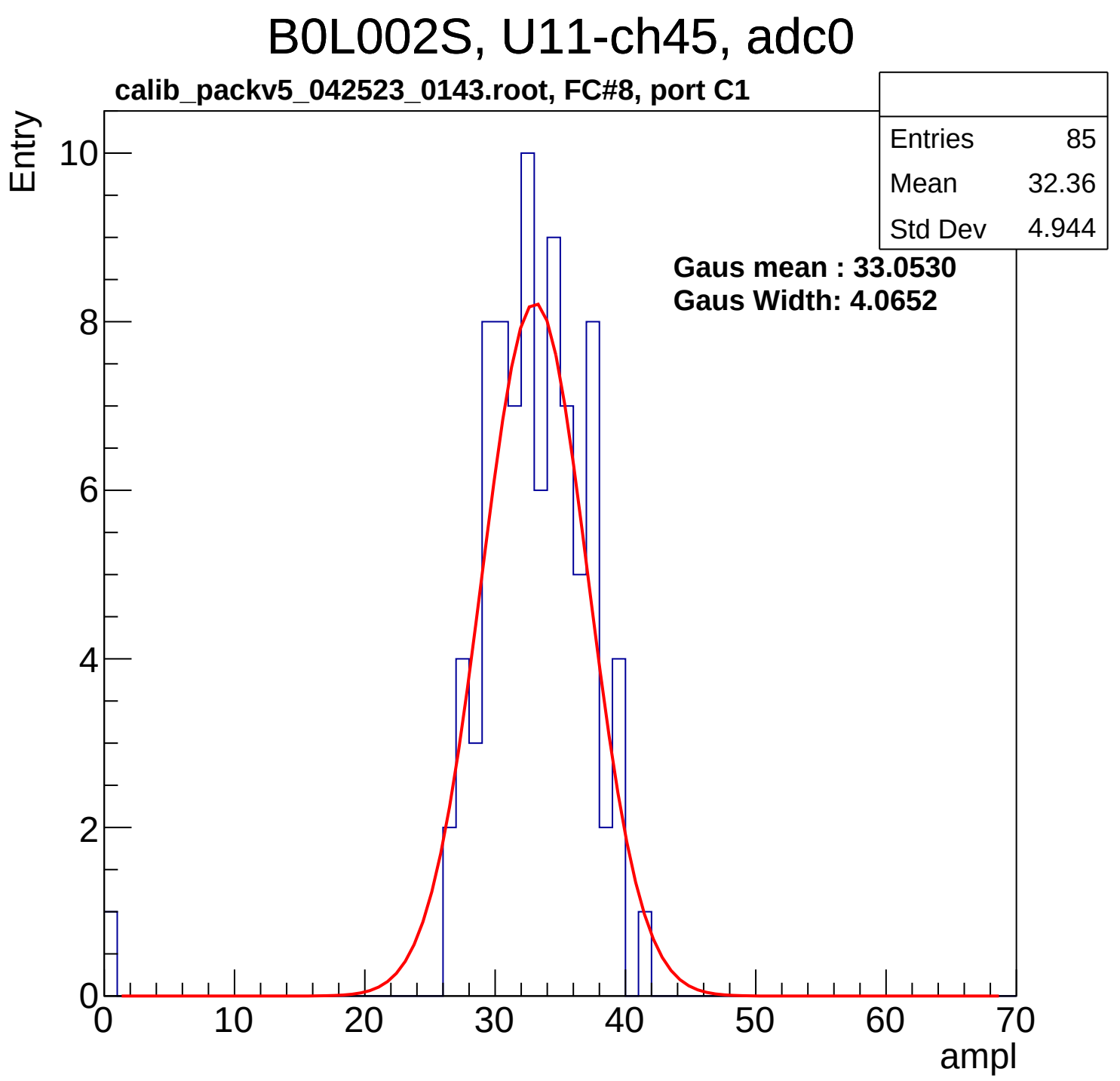
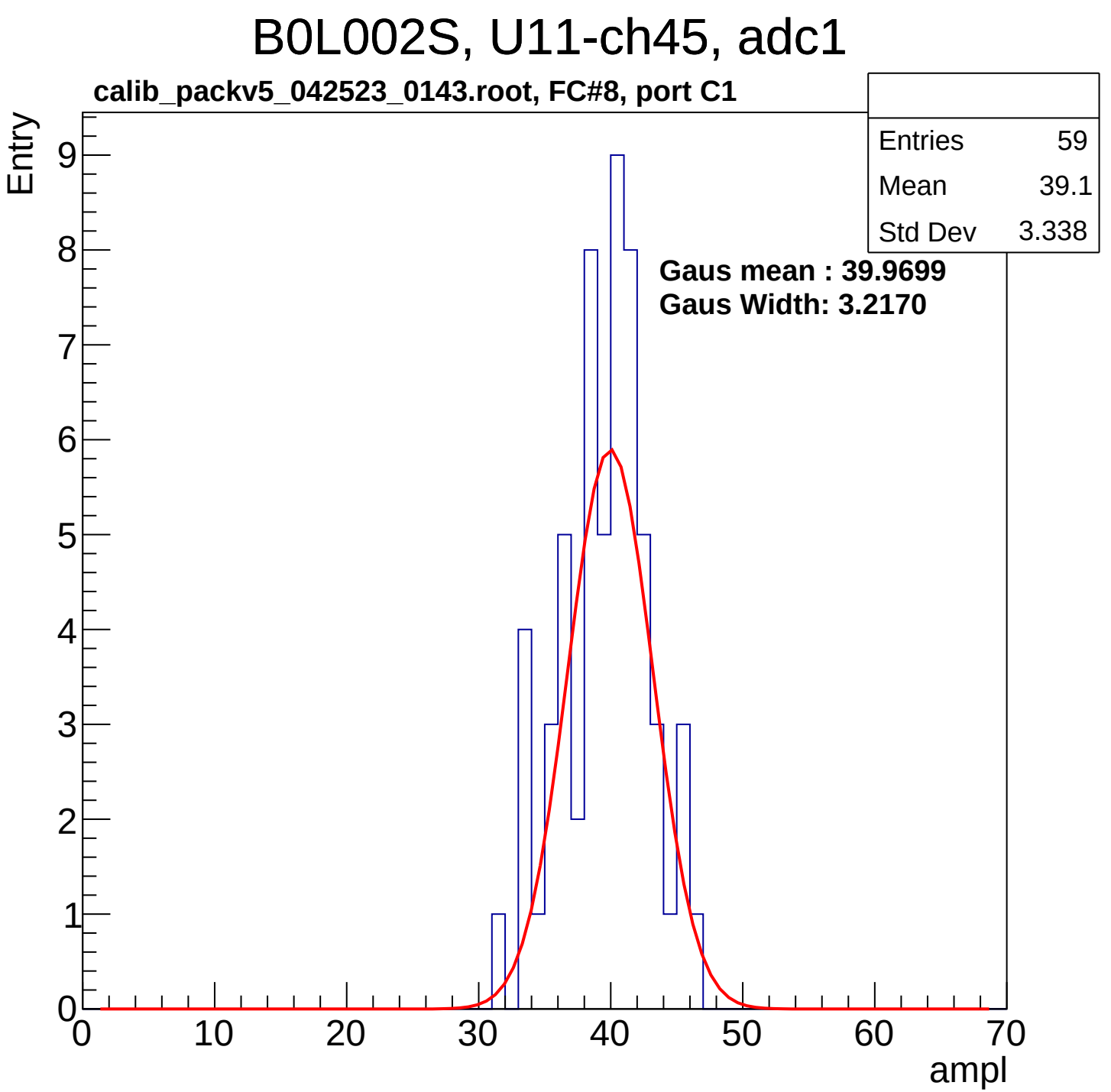
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 61.25
Std Dev 1.785
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 32.36
Std Dev 4.944
Gaus mean : 33.0530
Gaus Width: 4.0652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 39.1
Std Dev 3.338
Gaus mean : 39.9699
Gaus Width: 3.2170
Entry
ampl
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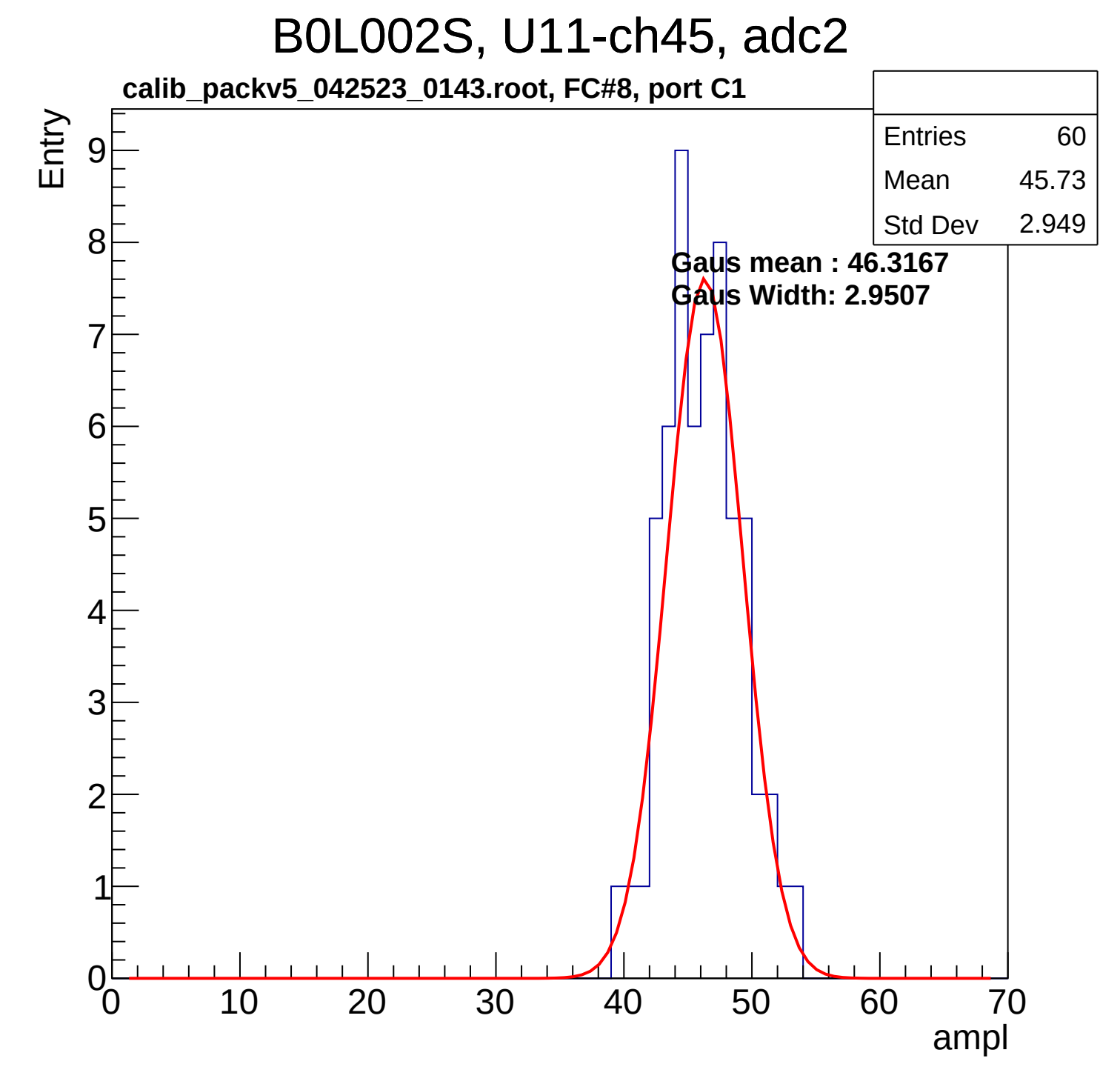
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.73
Std Dev 2.949
Gaus mean : 46.3167
Gaus Width: 2.9507
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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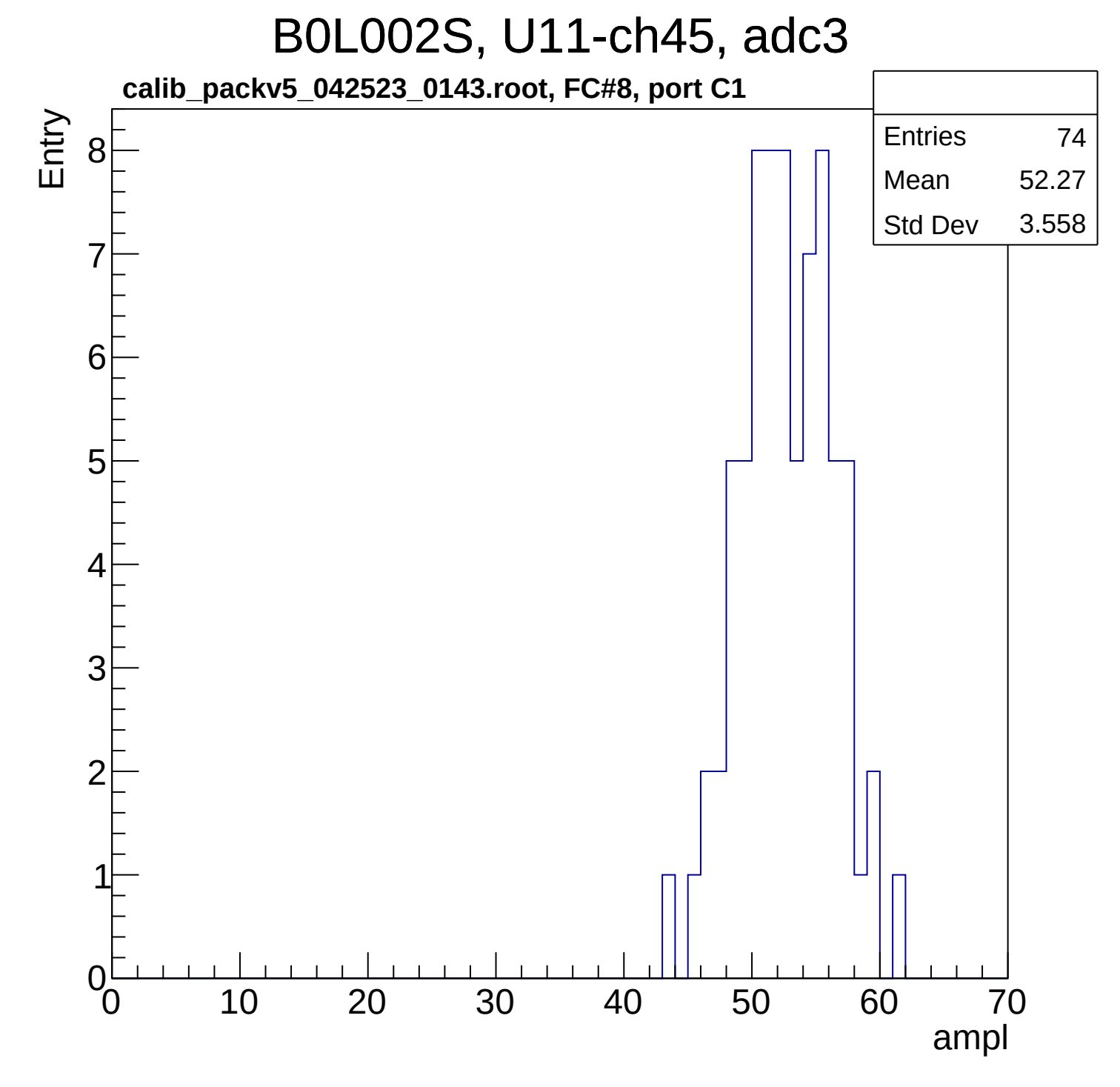
B0L002S, U11-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 52.27
Std Dev 3.558
Entry
ampl
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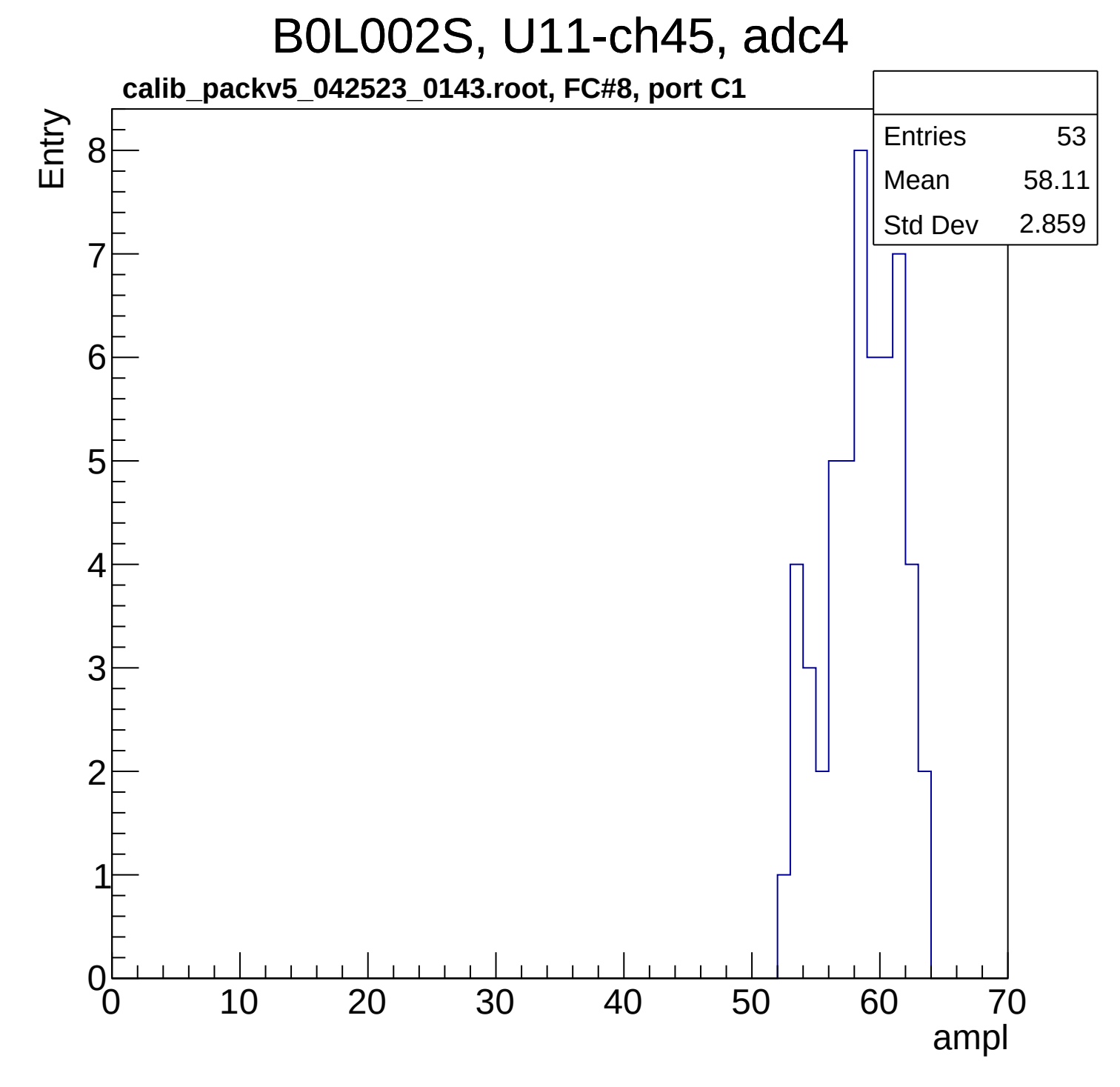
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 58.11
Std Dev 2.859
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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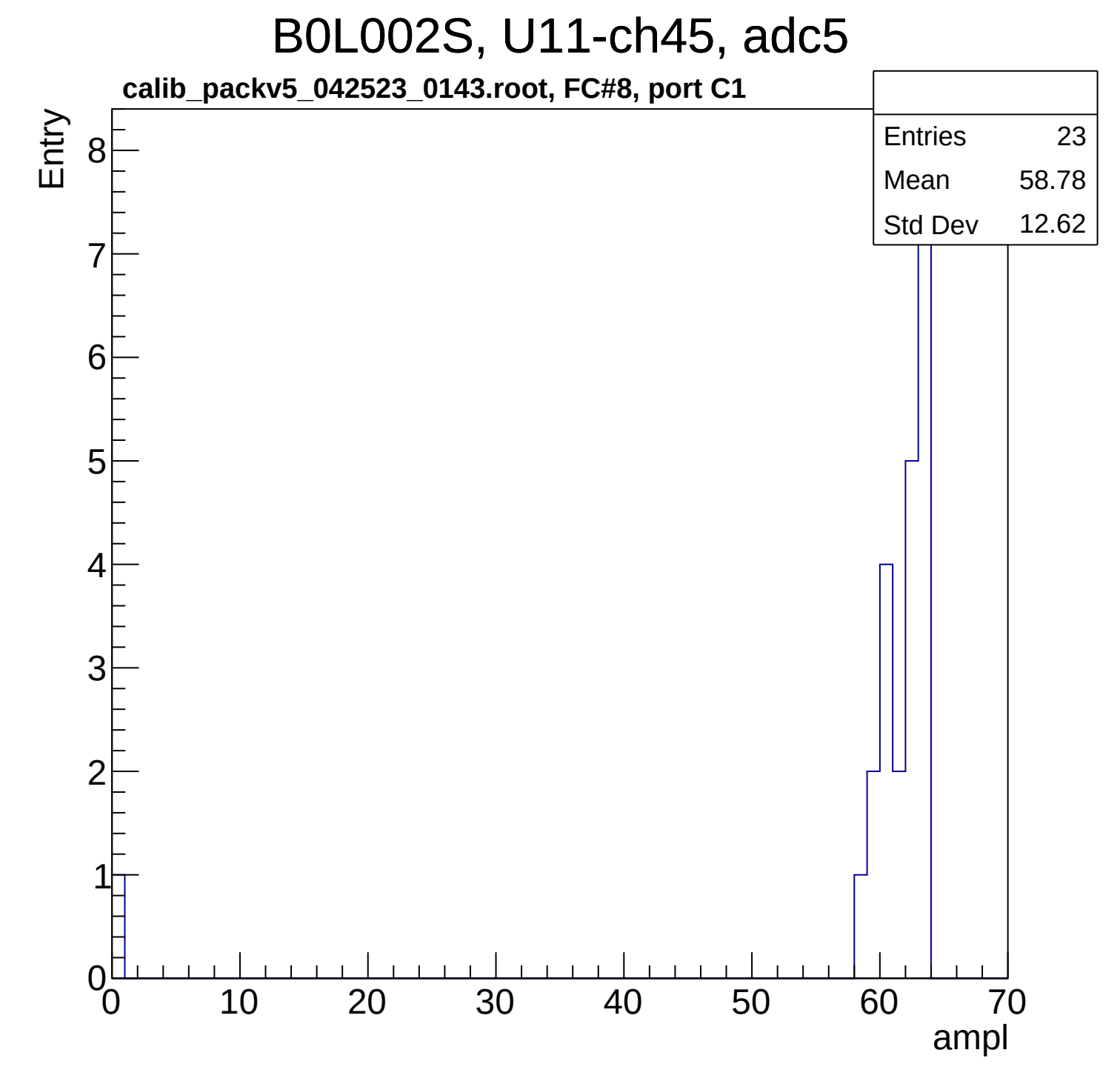
B0L002S, U11-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.78
Std Dev 12.62
Entry
ampl
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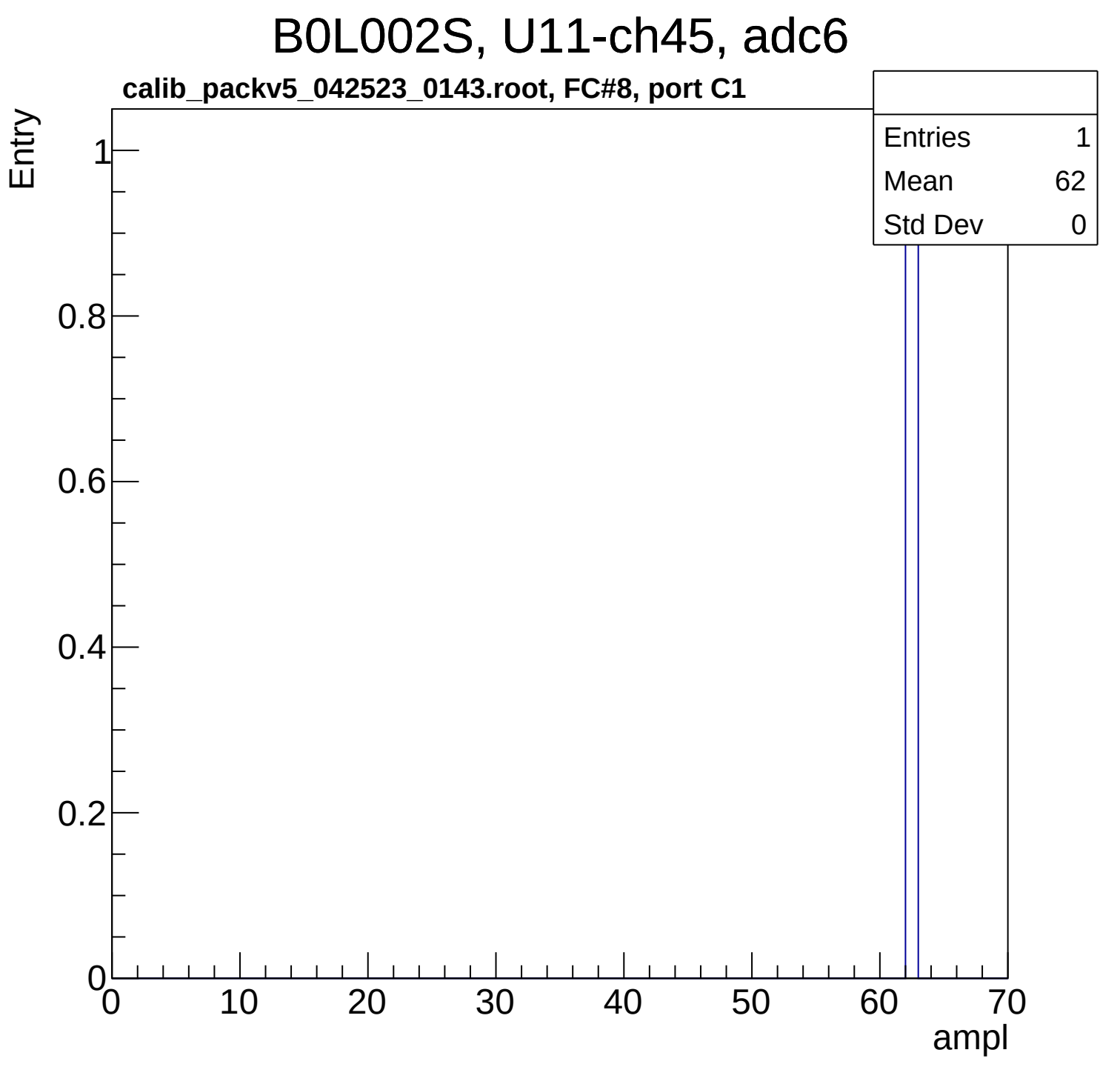
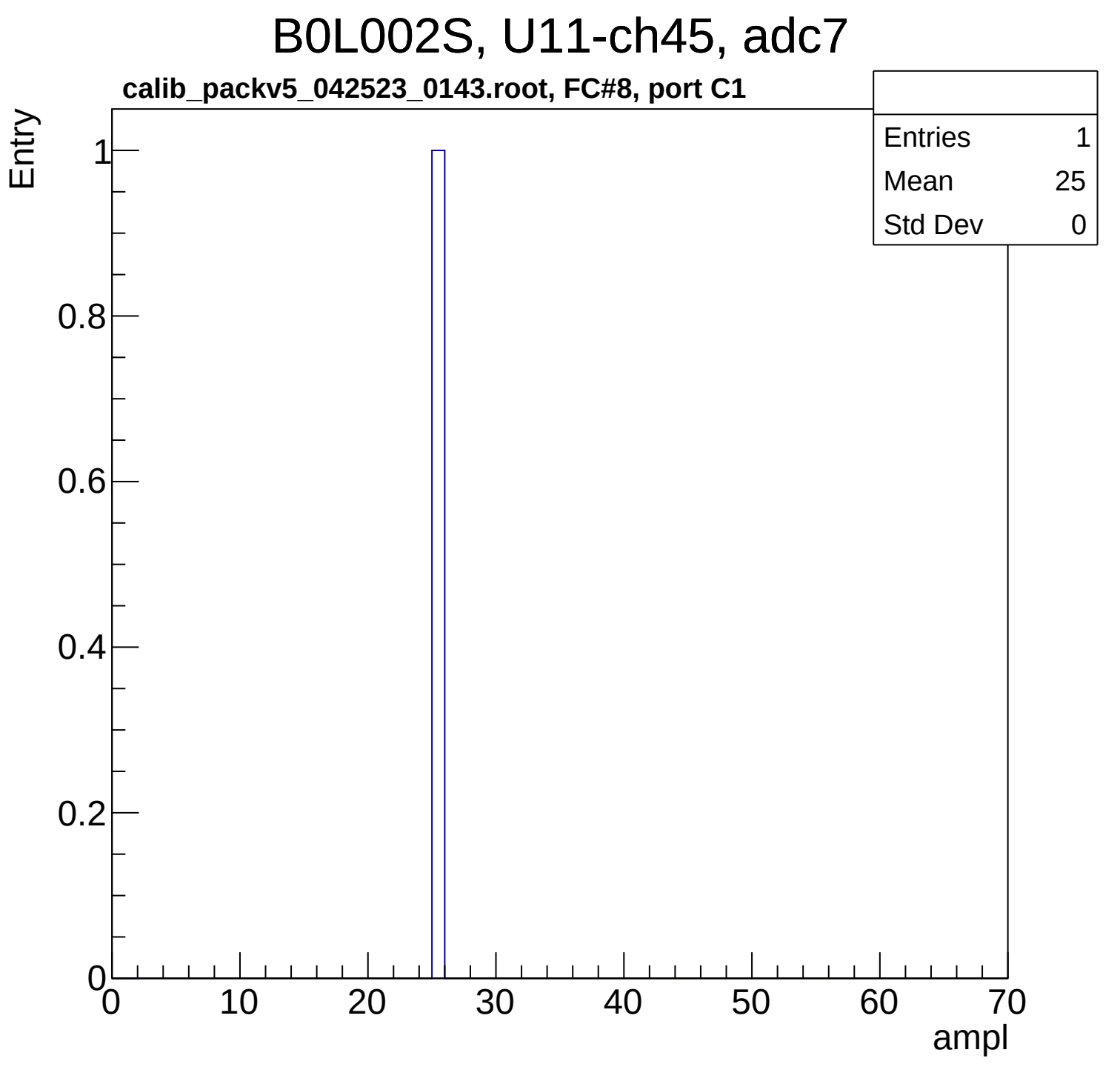
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
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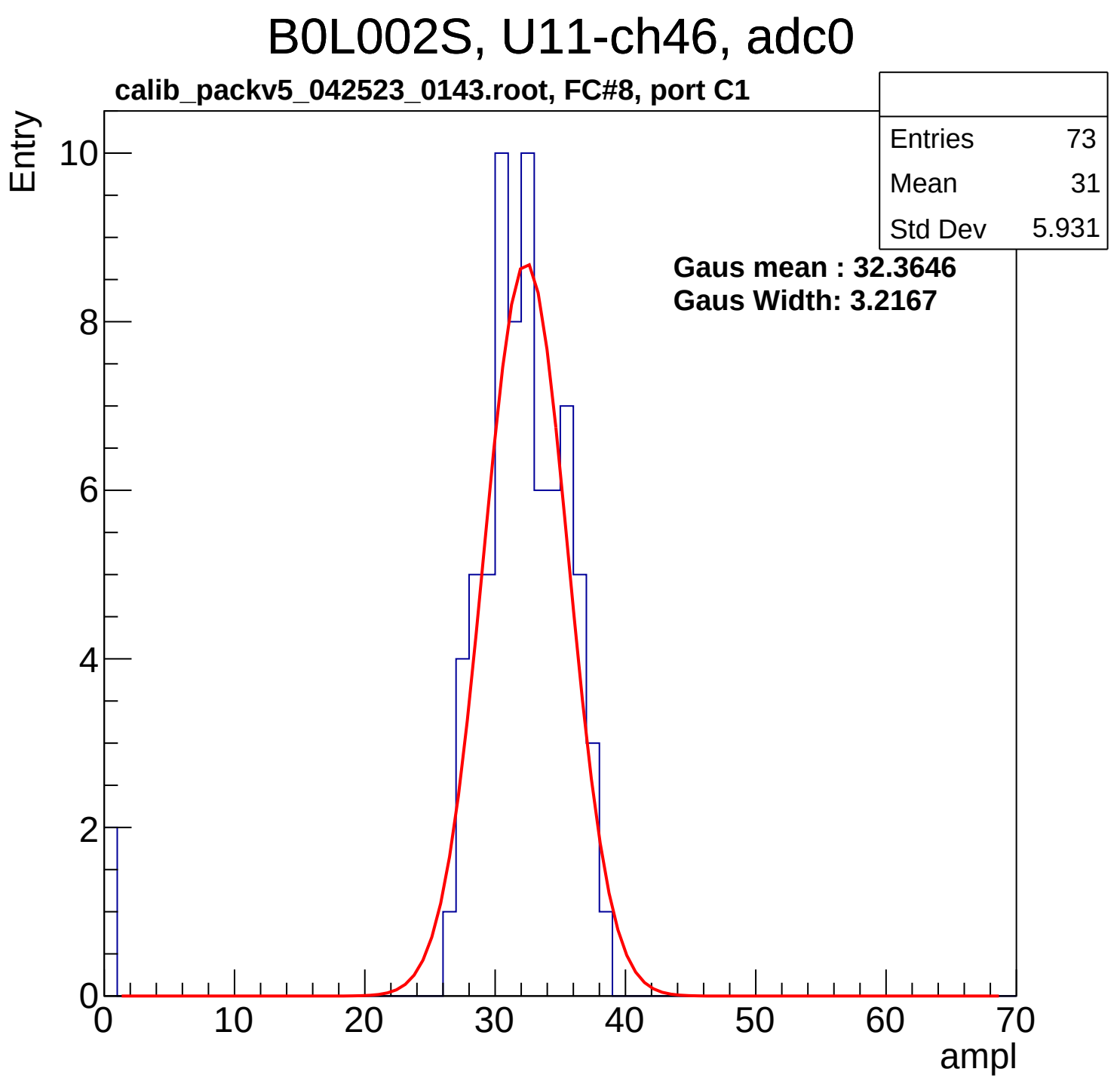
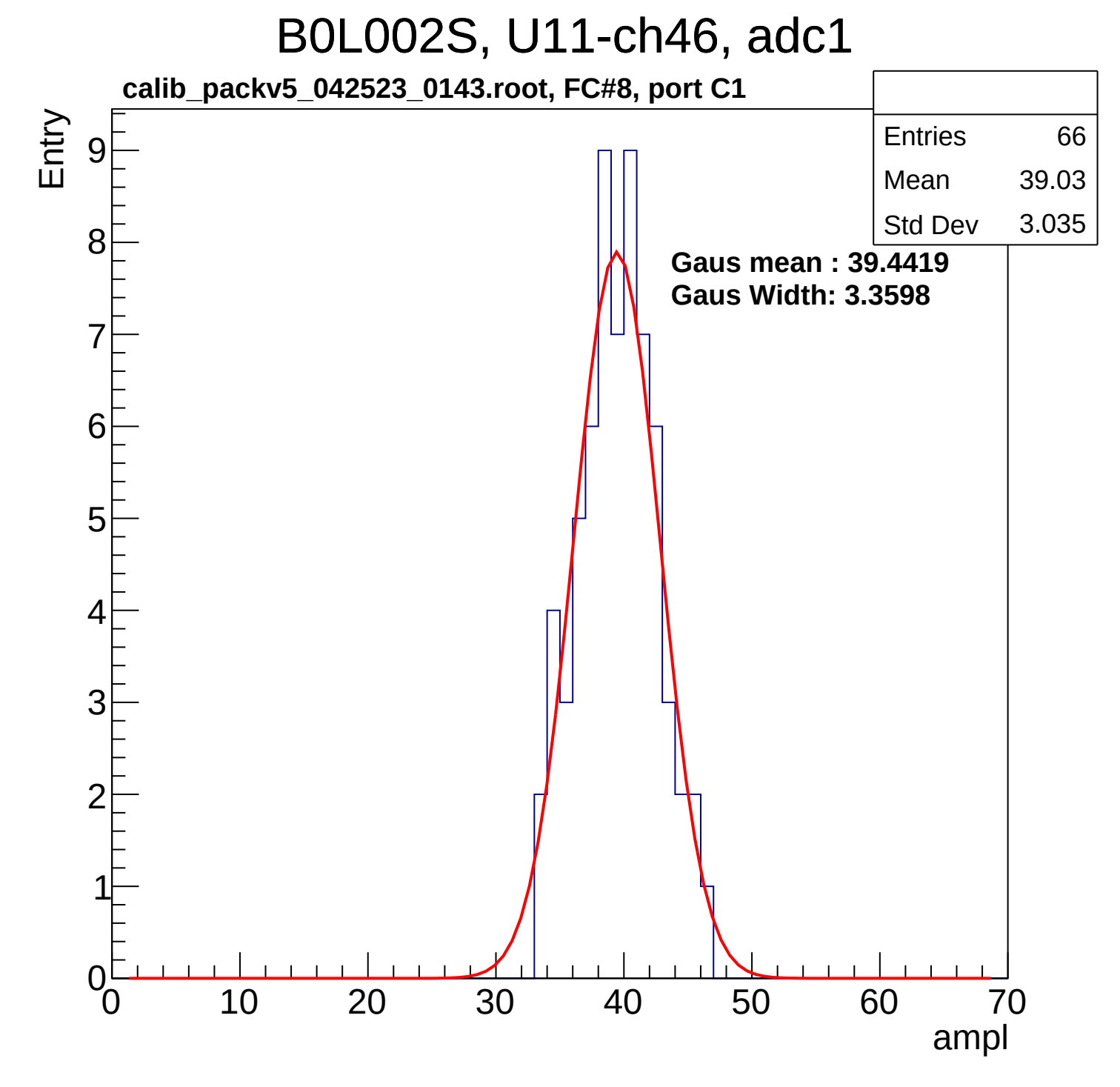
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 31
Std Dev 5.931
Gaus mean : 32.3646
Gaus Width: 3.2167
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 39.03
Std Dev 3.035
Gaus mean : 39.4419
Gaus Width: 3.3598
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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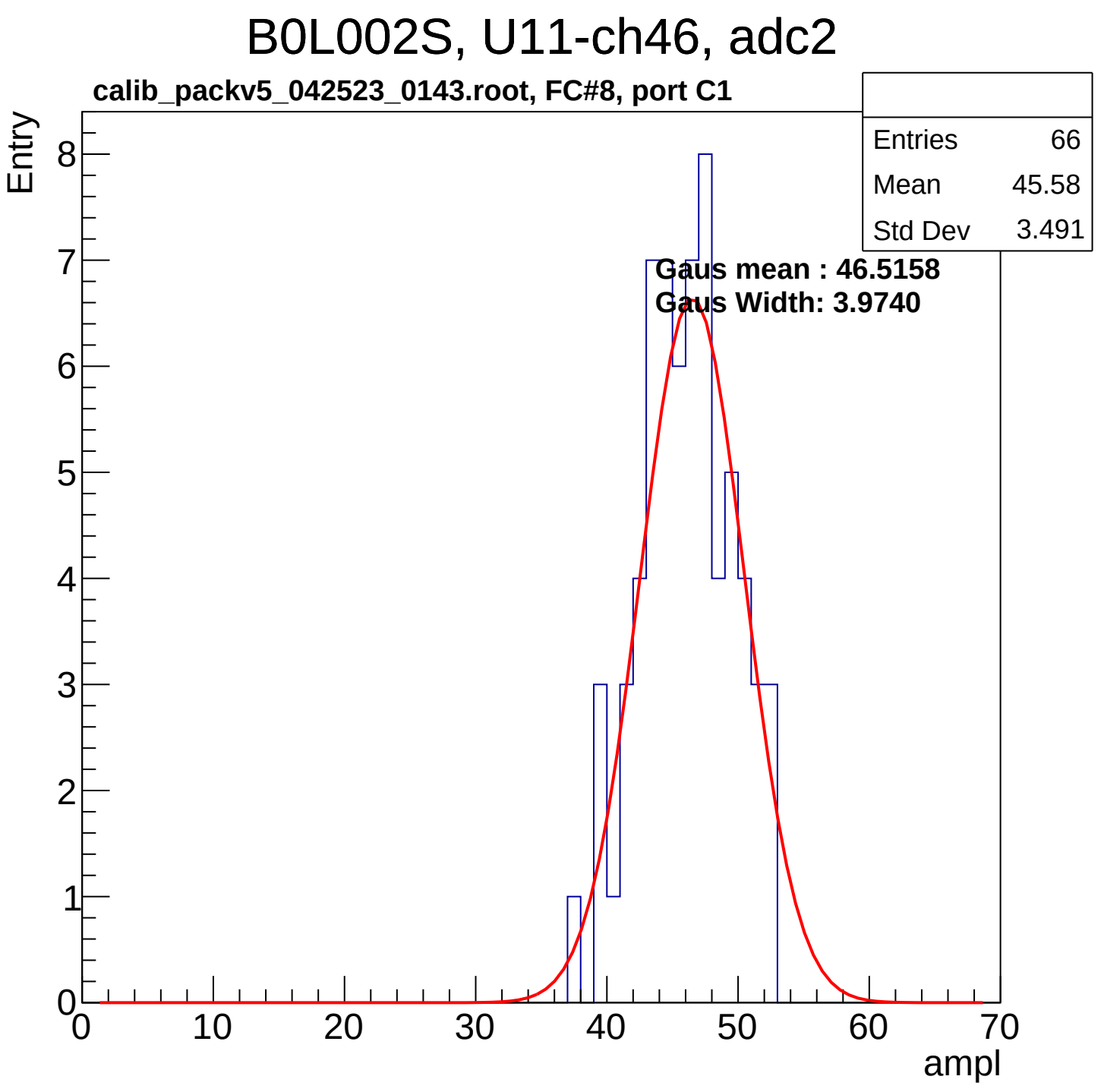
B0L002S, U11-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.58
Std Dev 3.491
Gaus mean : 46.5158
Gaus Width: 3.9740
Entry
ampl
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2
3
4
5
6
7
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0
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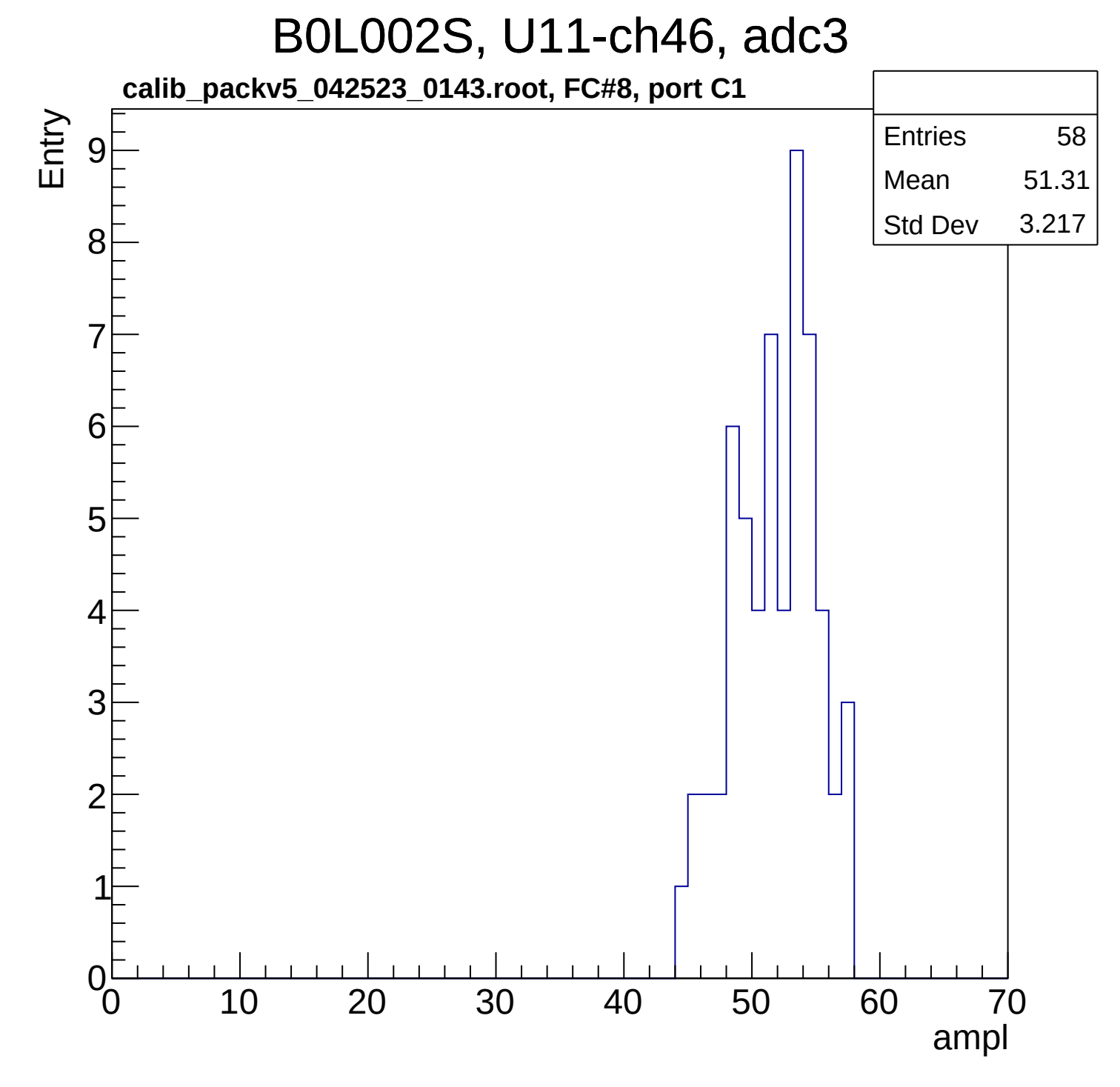
B0L002S, U11-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.31
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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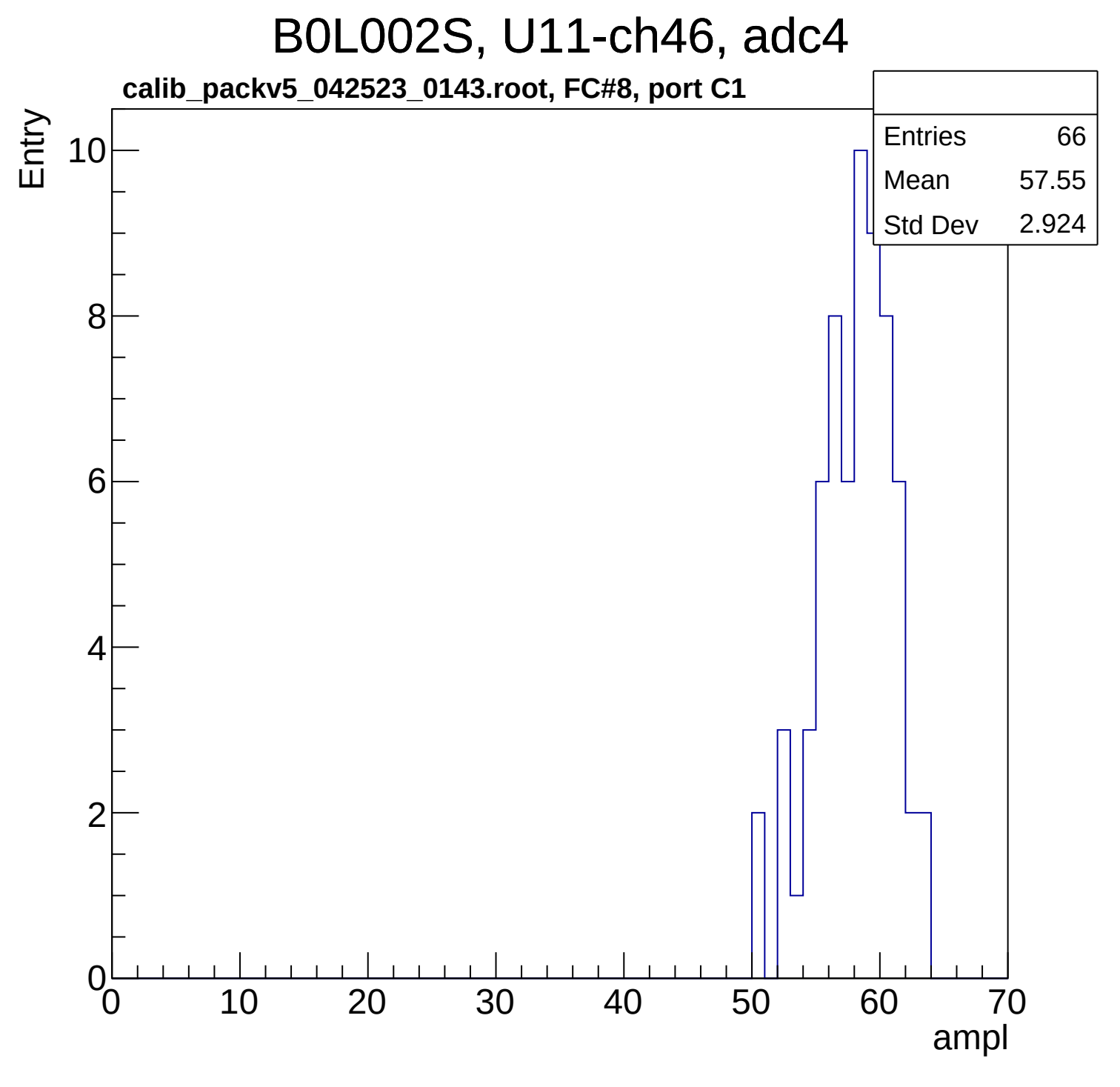
B0L002S, U11-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.55
Std Dev 2.924
Entry
0
2
4
6
8
10
0
10
20
30
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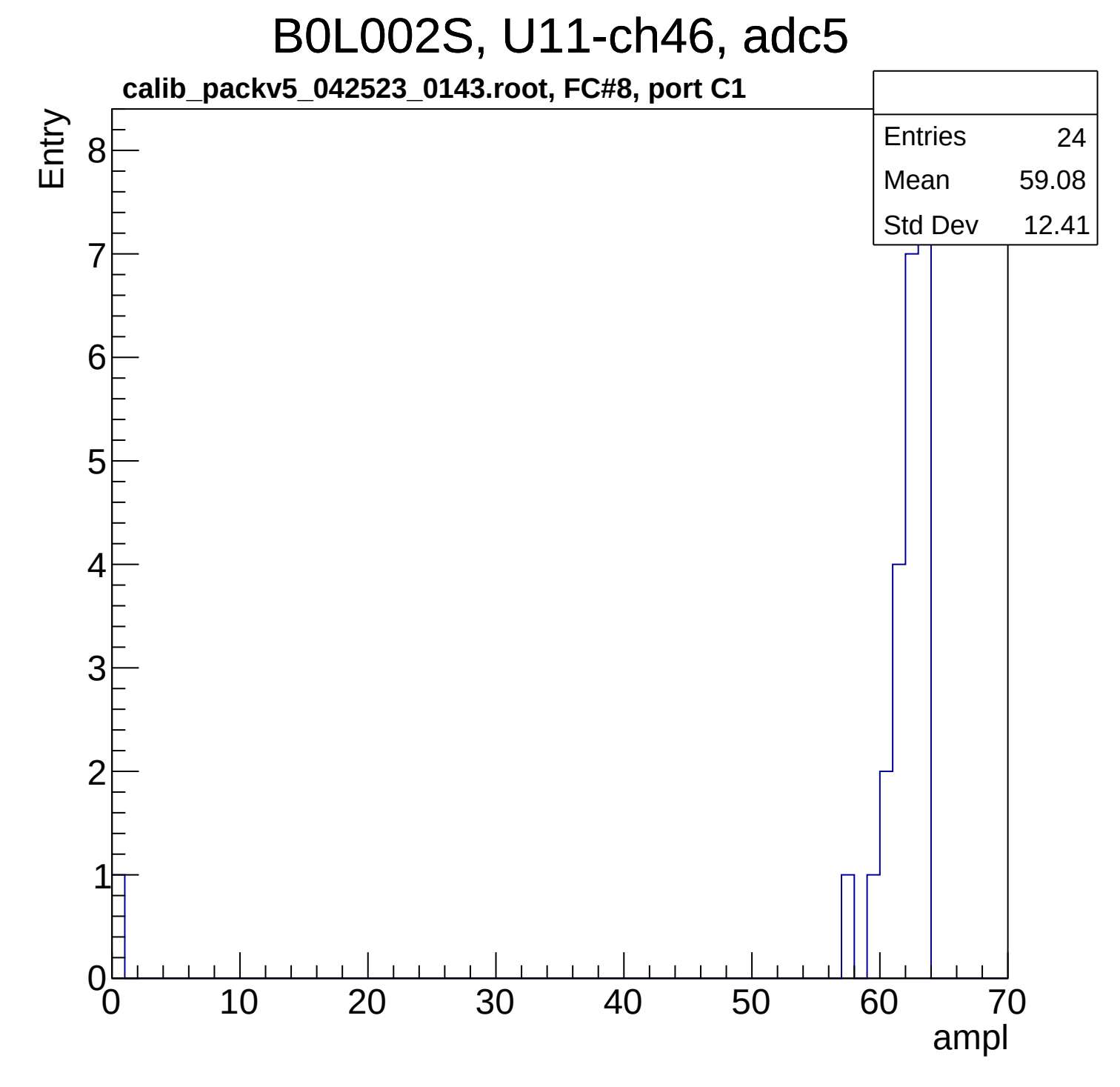
70
ampl


B0L002S, U11-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 59.08
Std Dev 12.41
Entry
ampl
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1
2
3
4
5
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7
8
0
10
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40
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60
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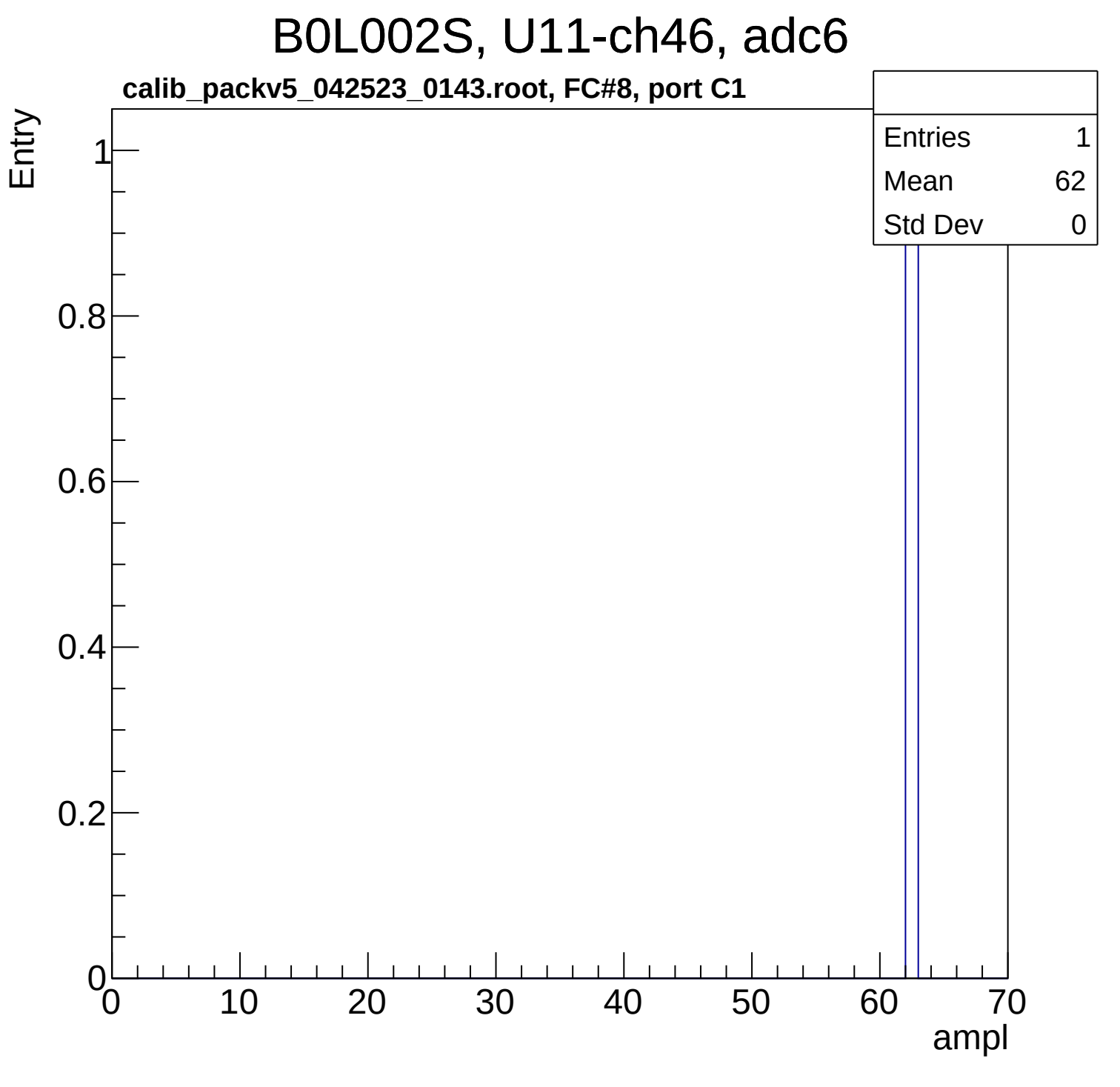
B0L002S, U11-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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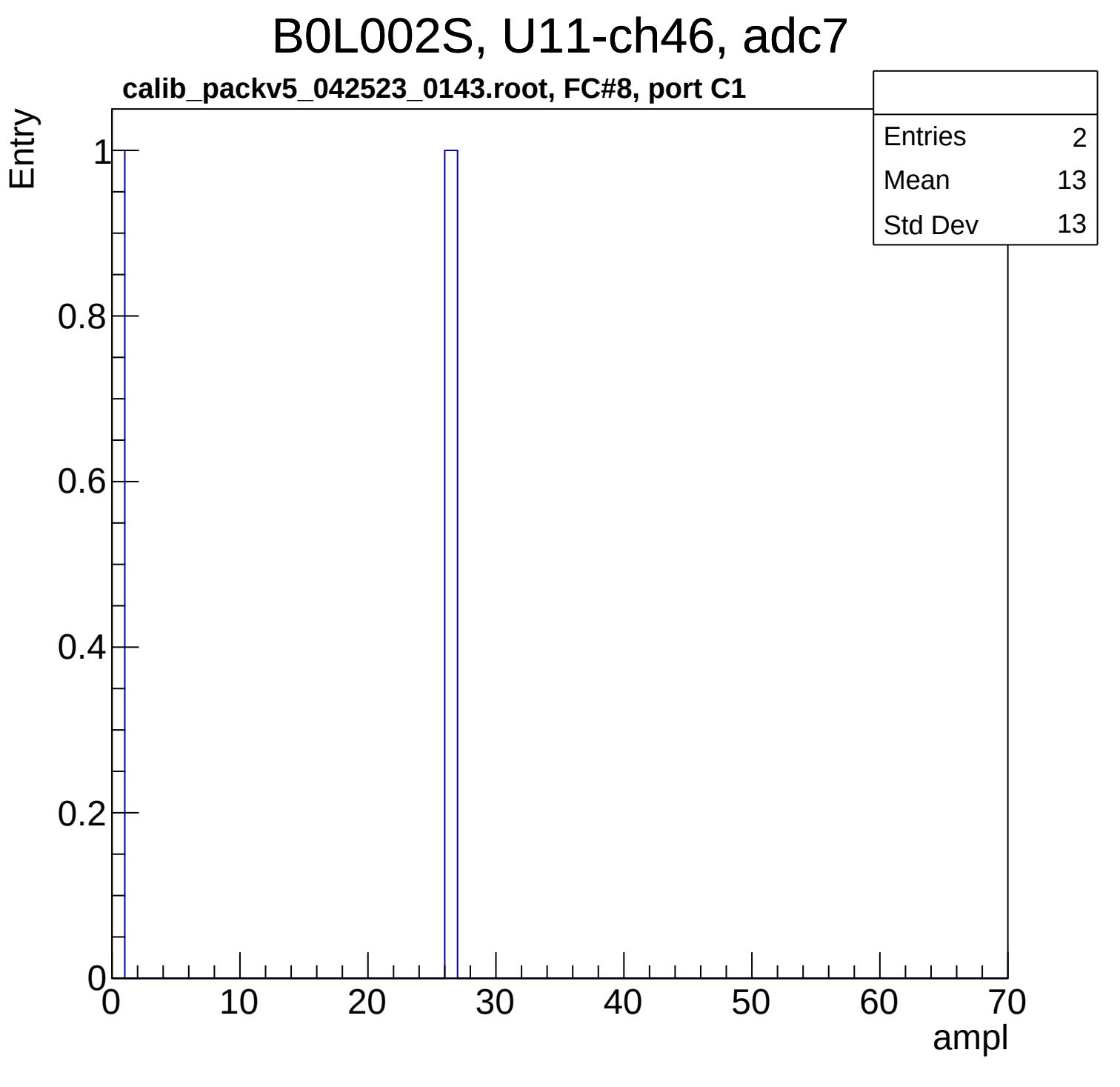
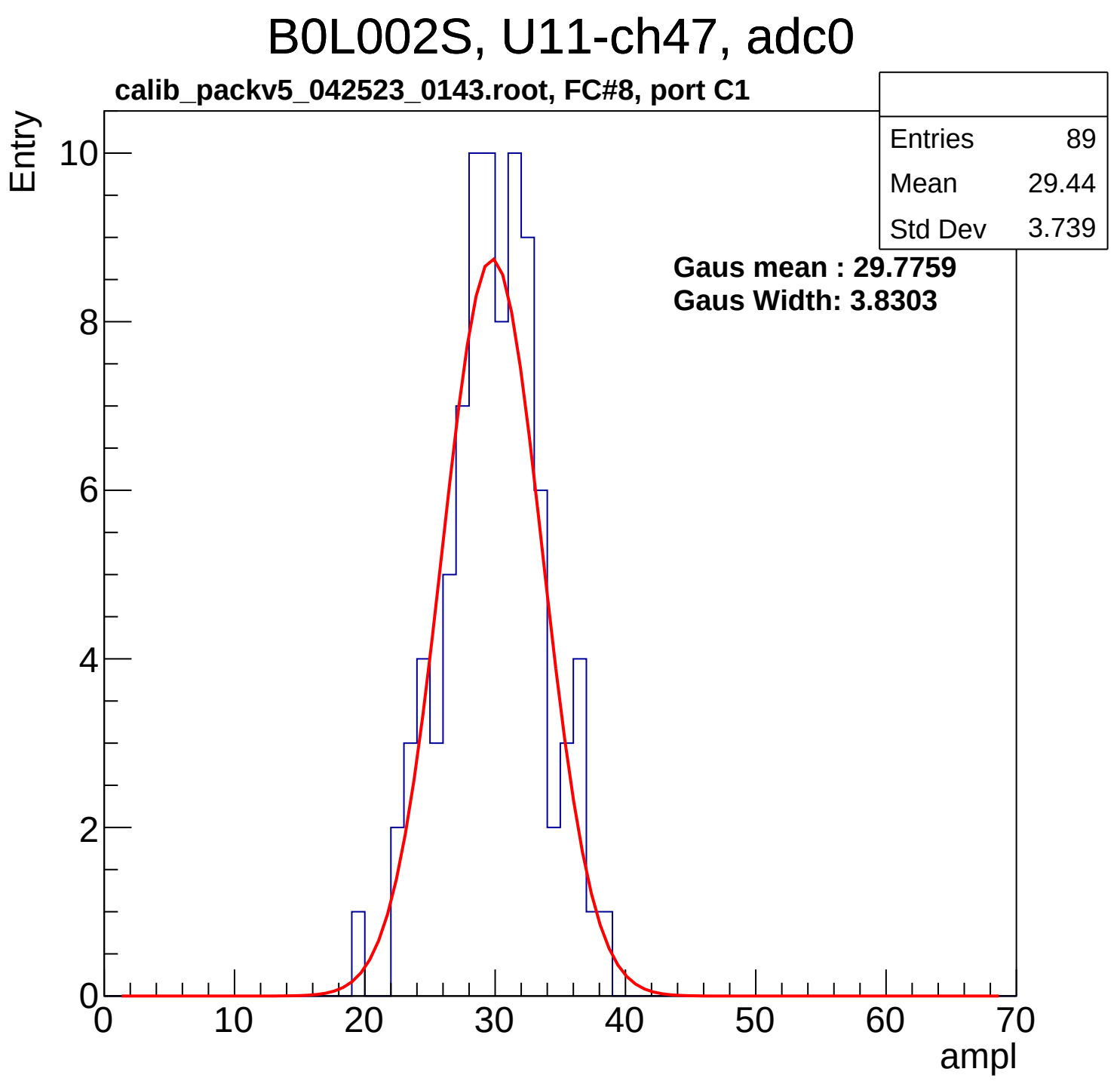
40
50
60
70
ampl


B0L002S, U11-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 29.44
Std Dev 3.739
Gaus mean : 29.7759
Gaus Width: 3.8303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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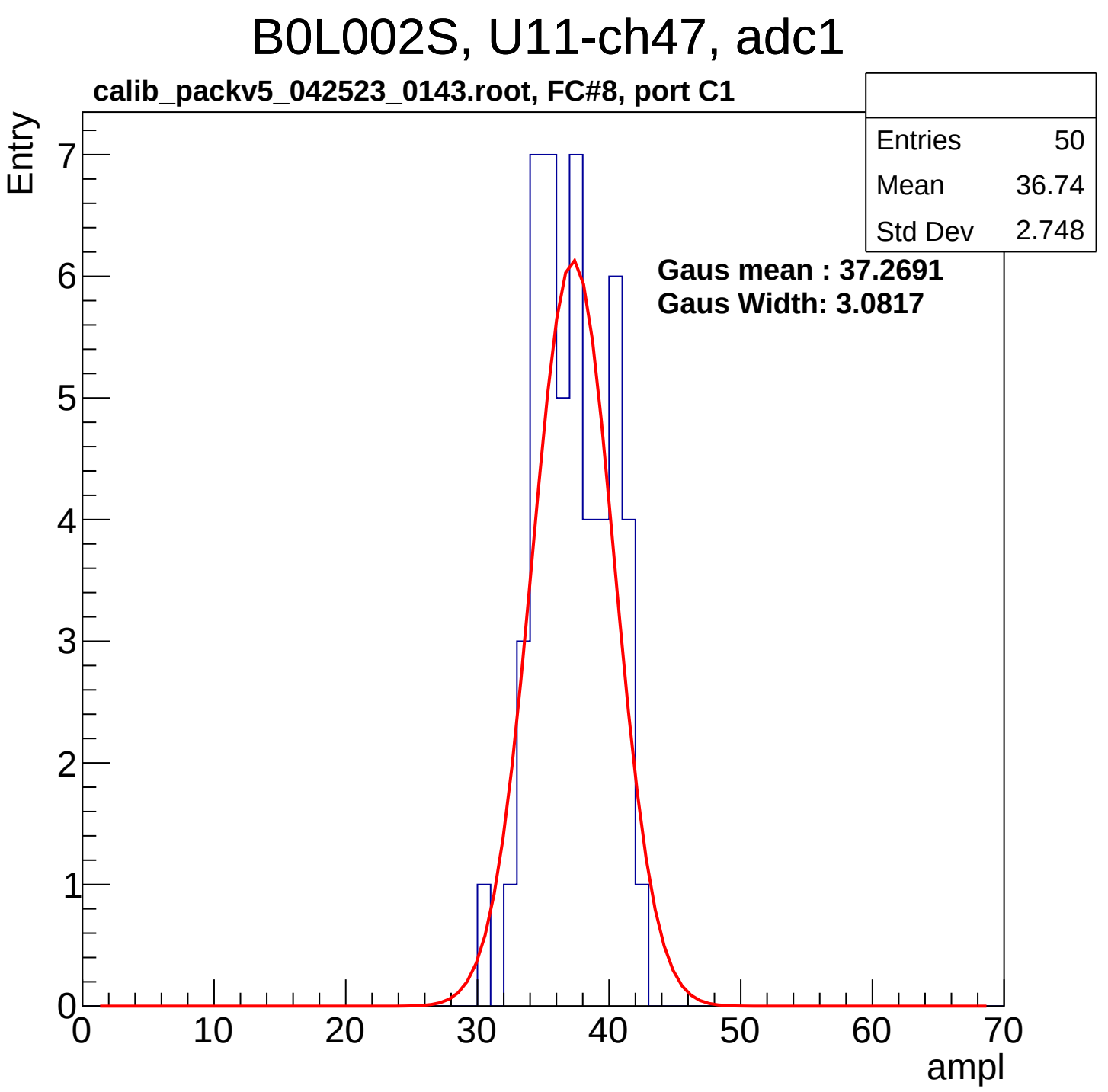
B0L002S, U11-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 36.74
Std Dev 2.748
Gaus mean : 37.2691
Gaus Width: 3.0817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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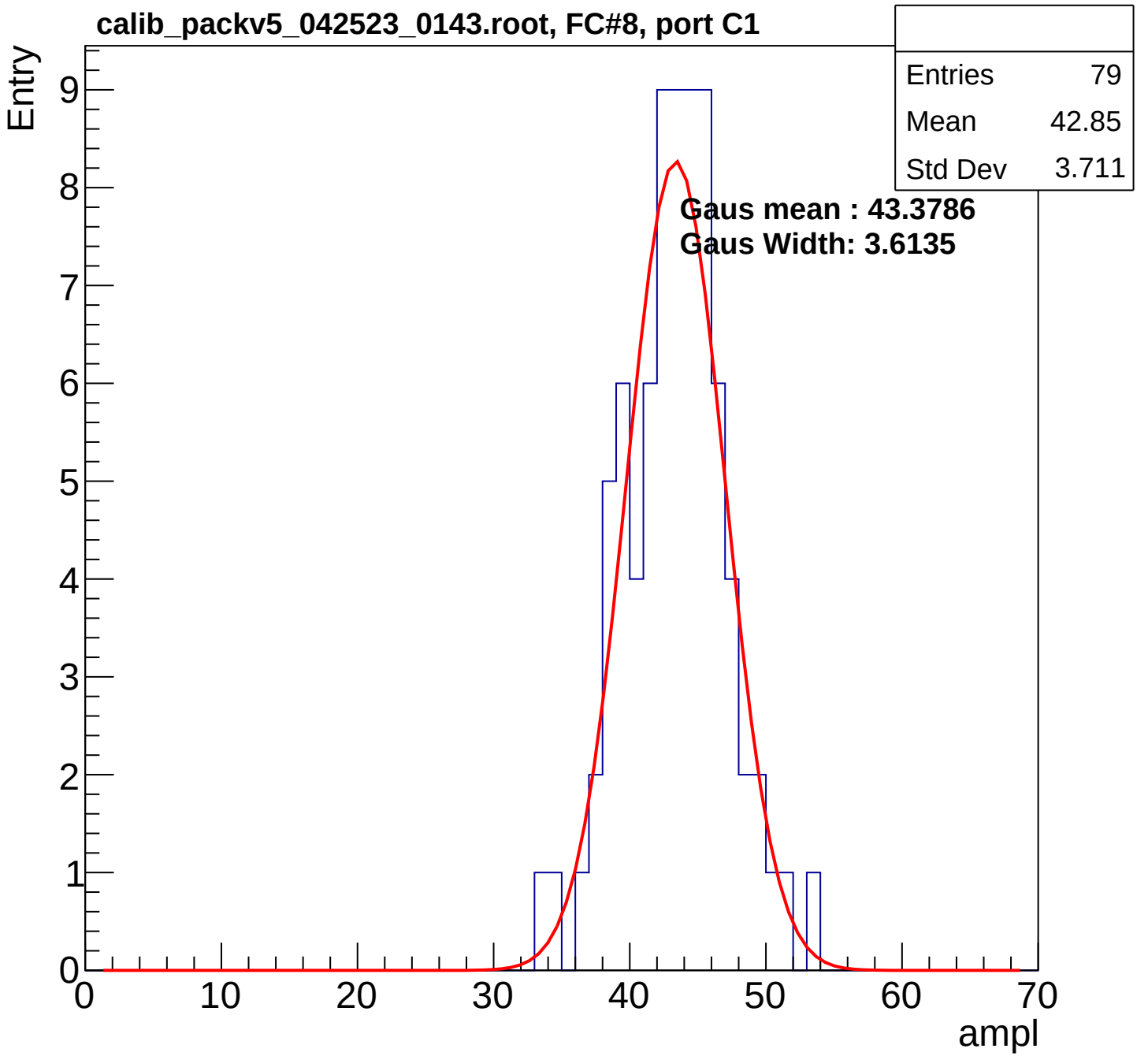
B0L002S, U11-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 42.85
Std Dev 3.711
Gaus mean : 43.3786
Gaus Width: 3.6135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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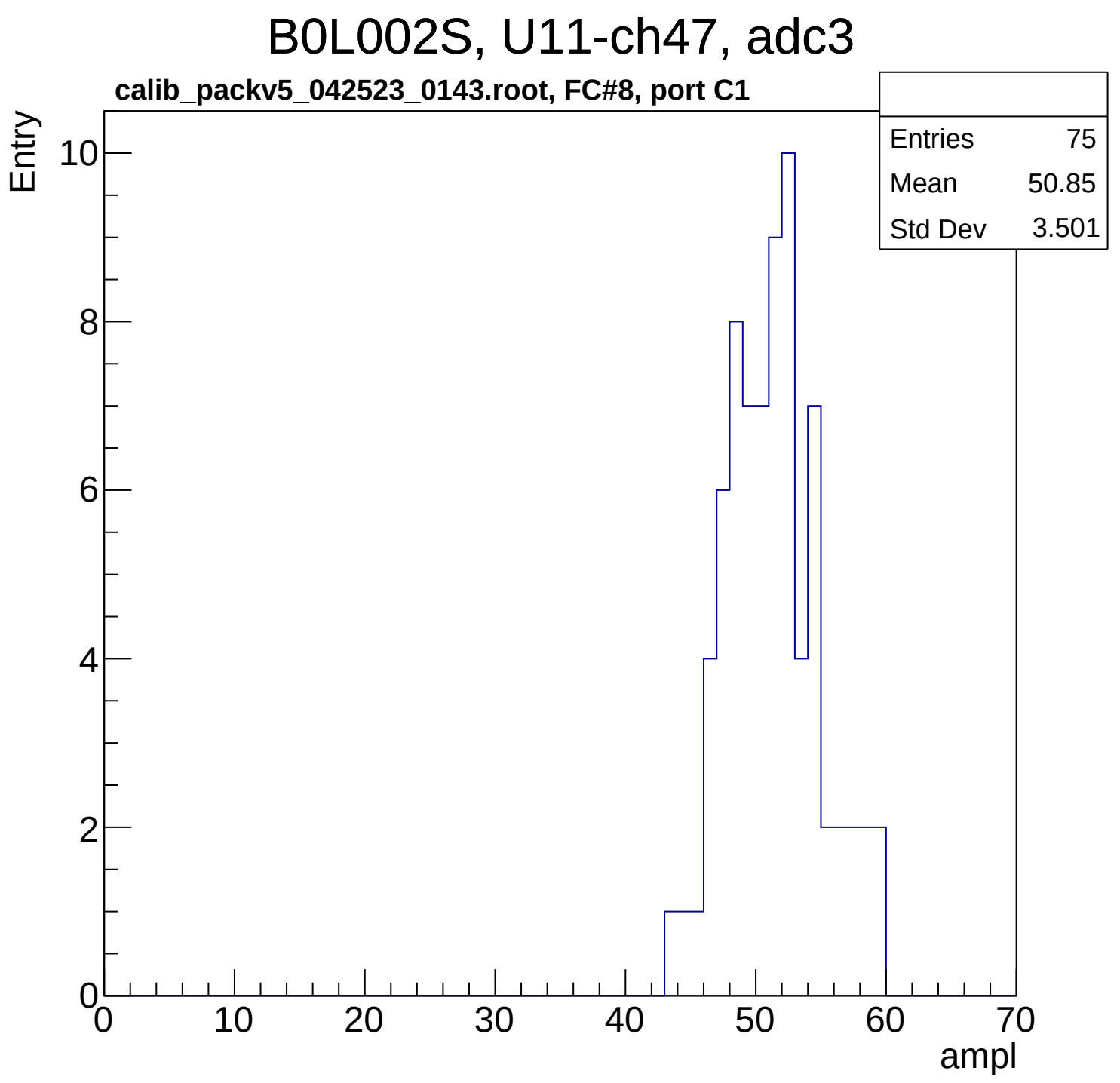
B0L002S, U11-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.85
Std Dev 3.501
Entry
0
2
4
6
8
10
0
10
20
30
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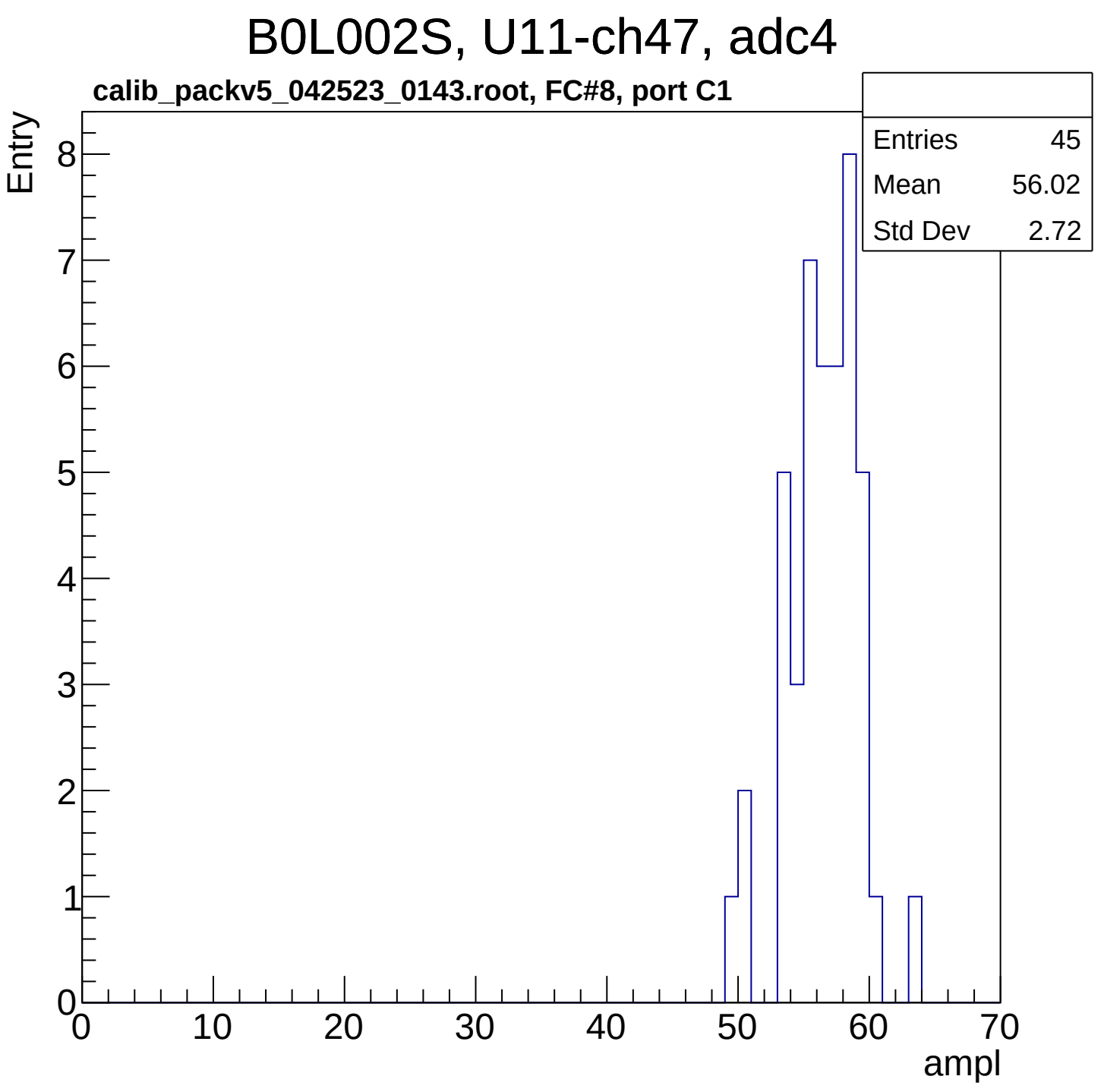
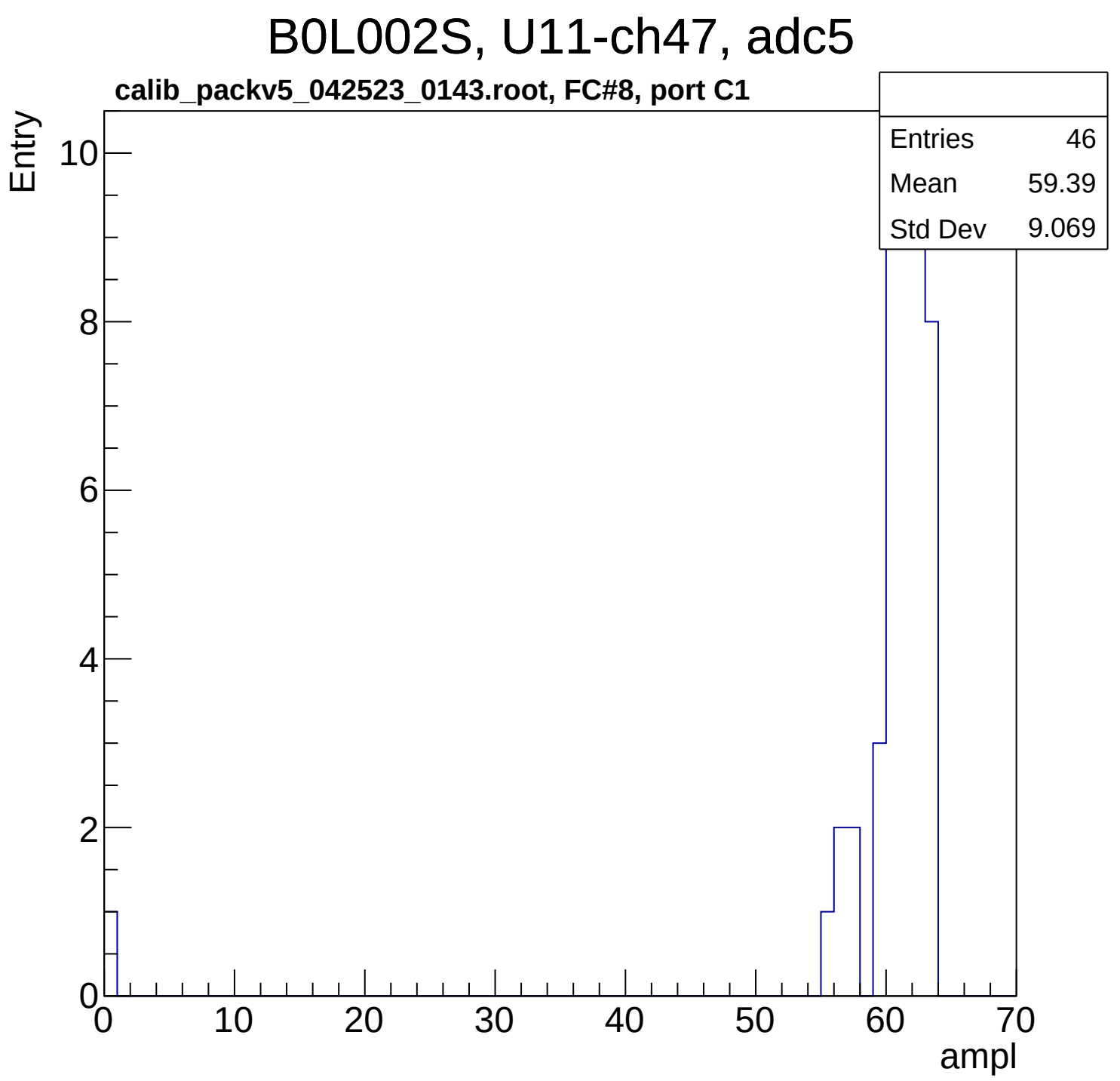
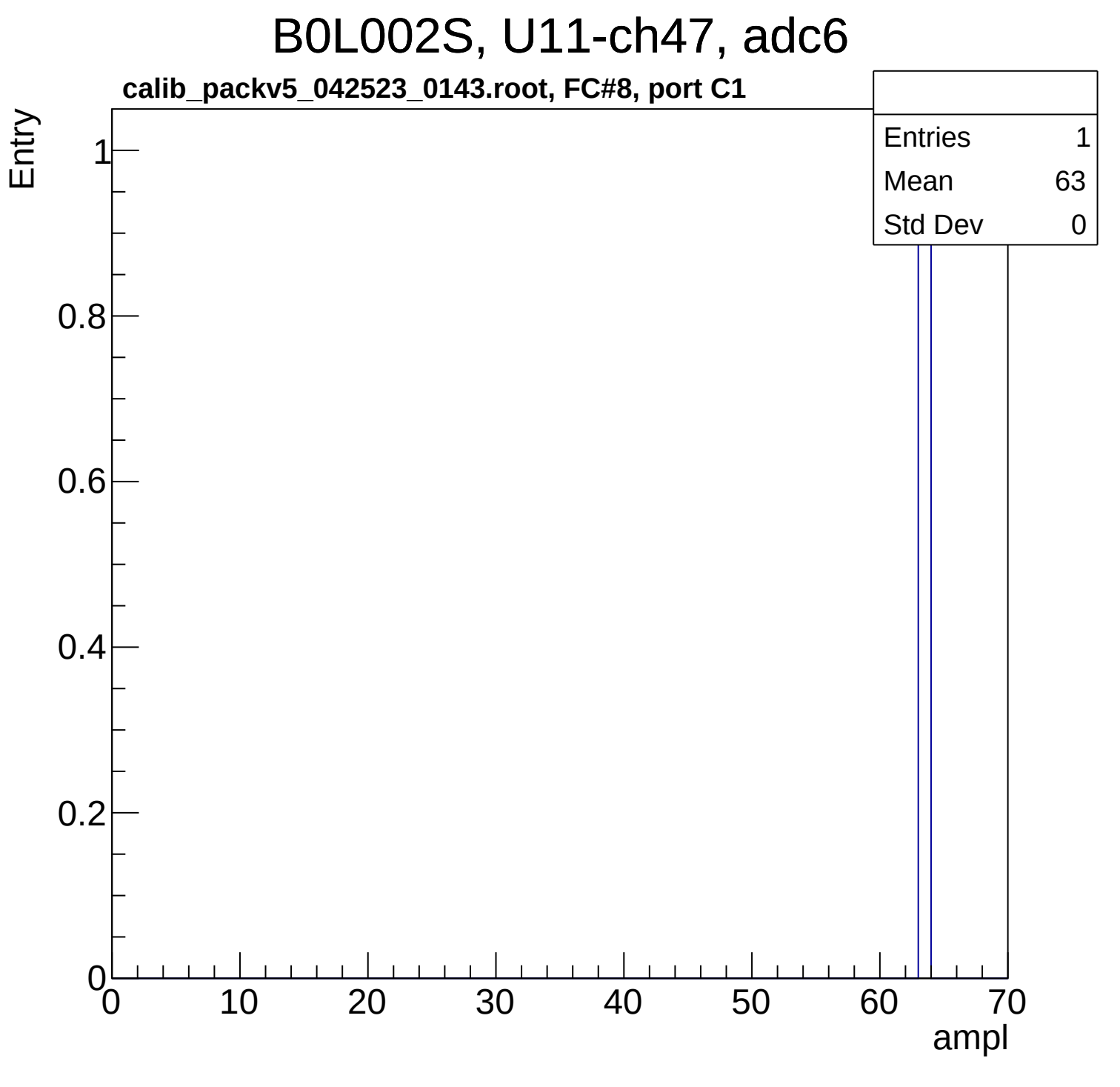
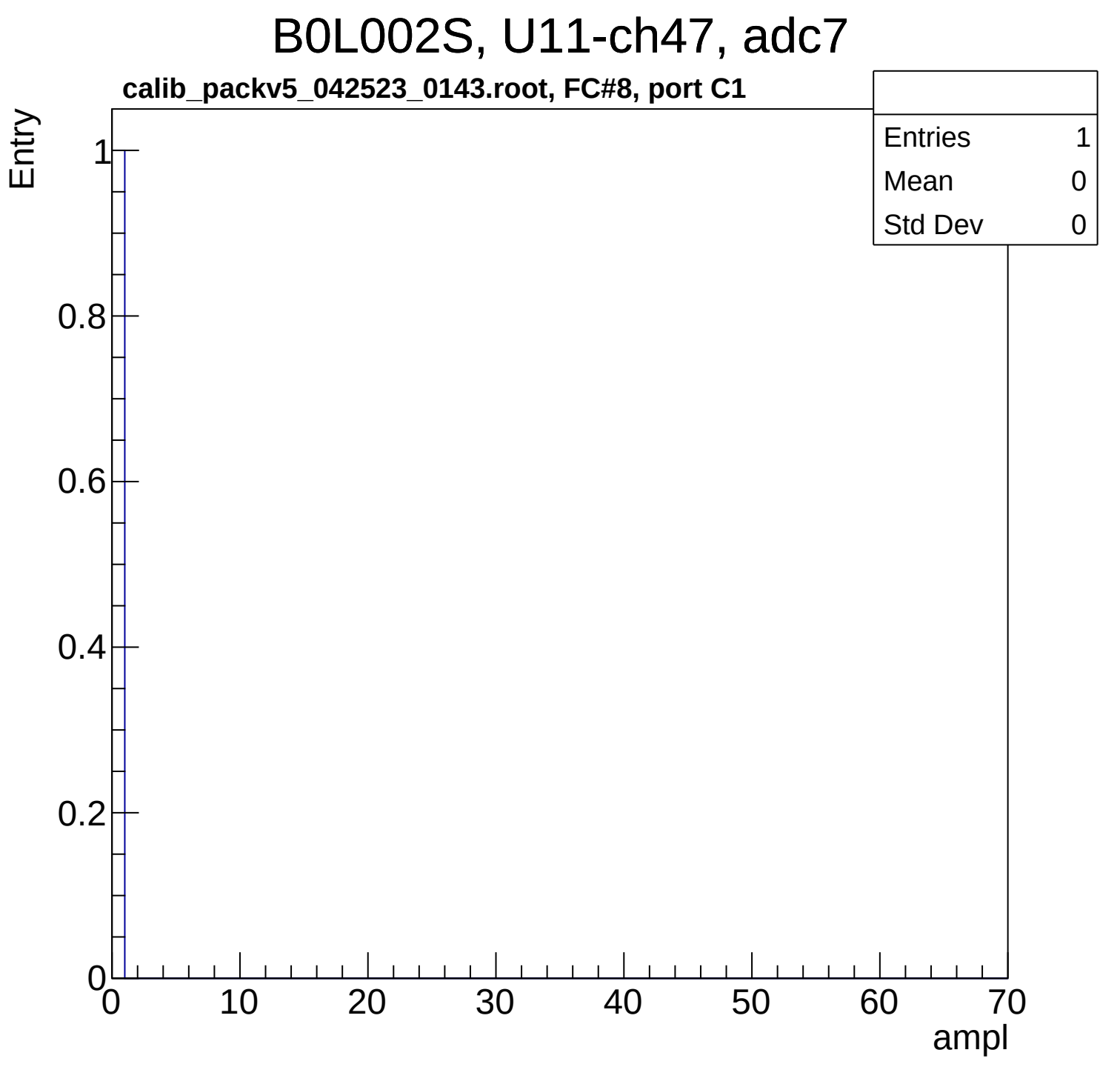
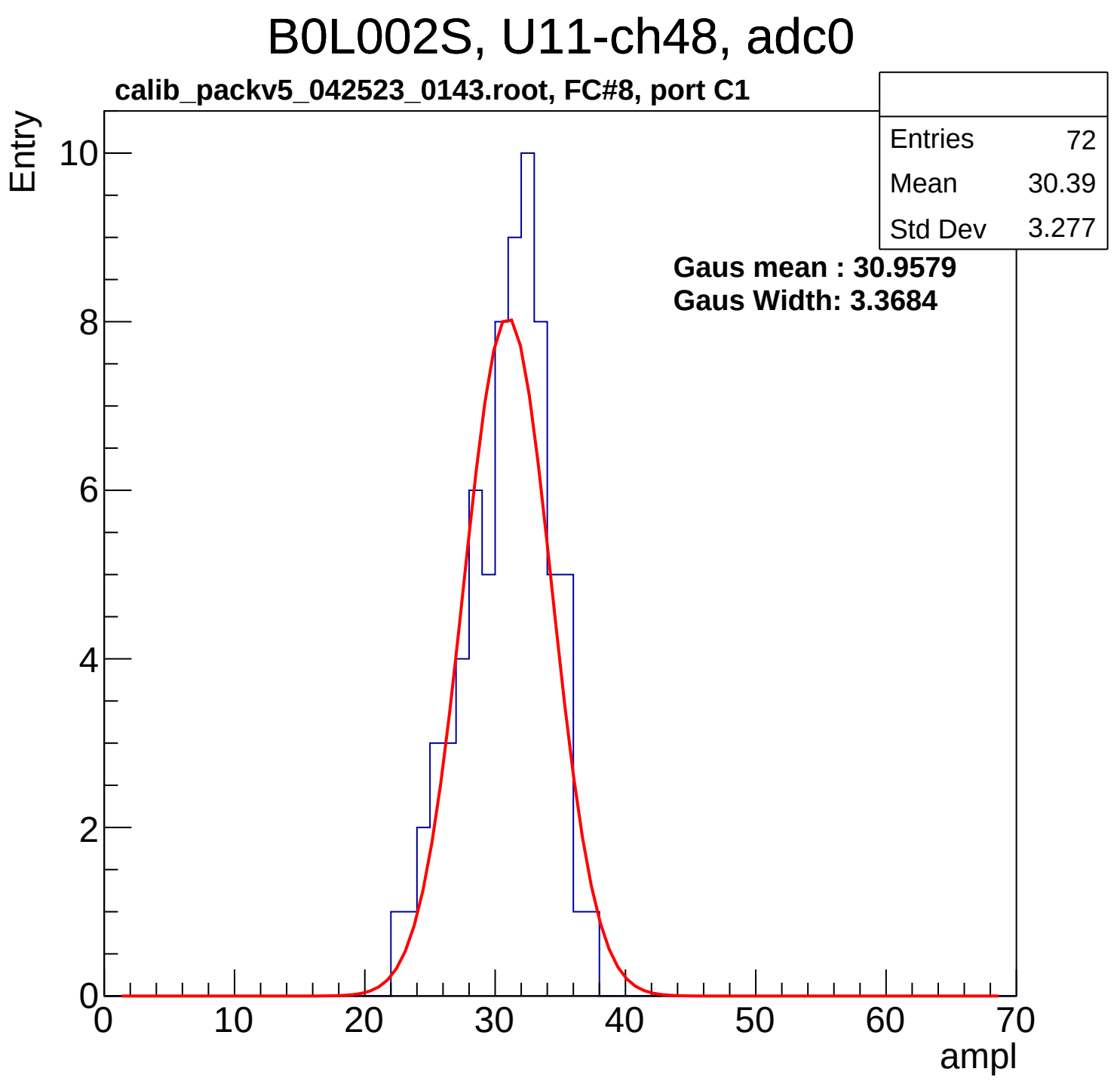
50
60
70
ampl

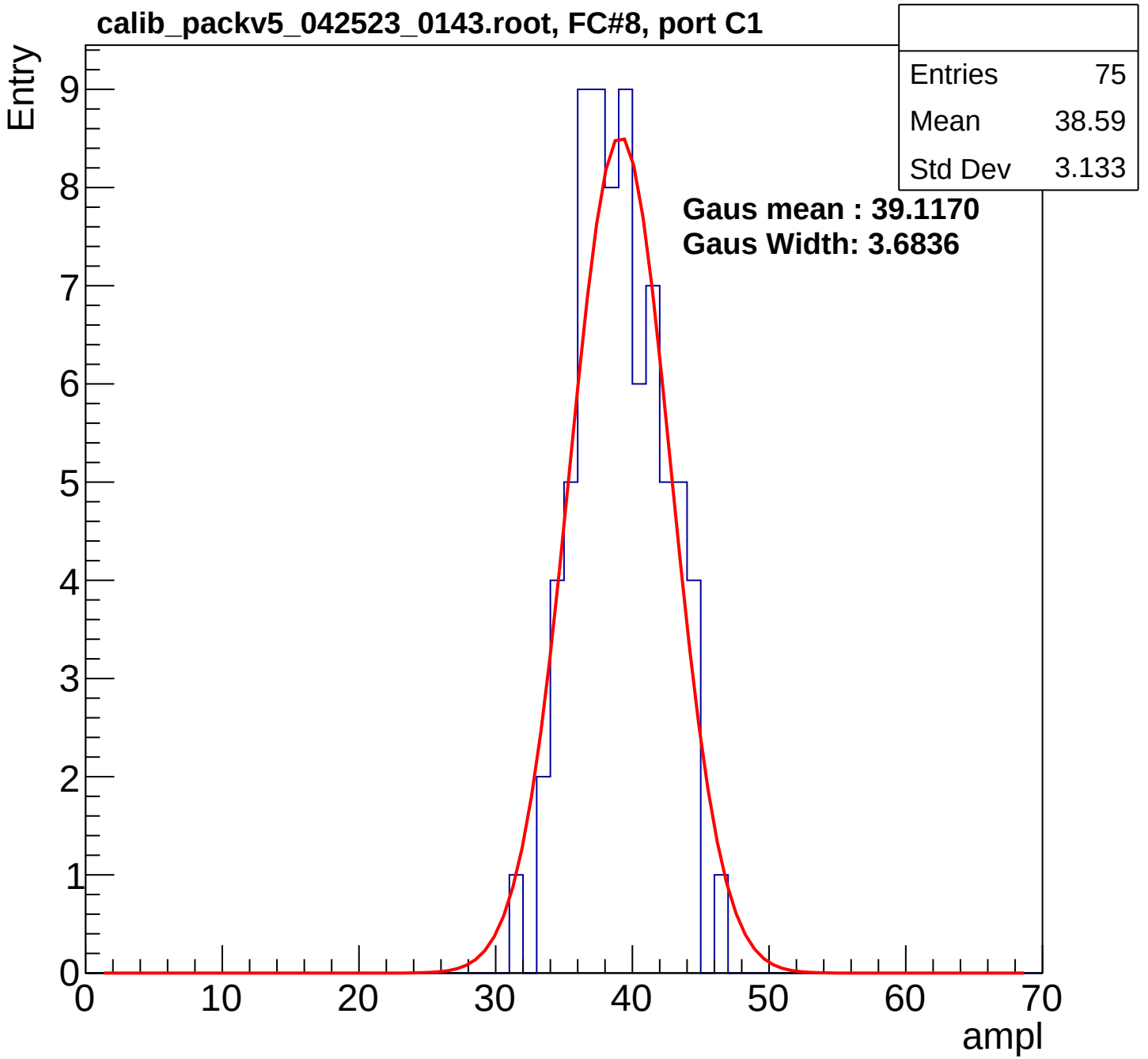

B0L002S, U11-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.02
Std Dev 2.72
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

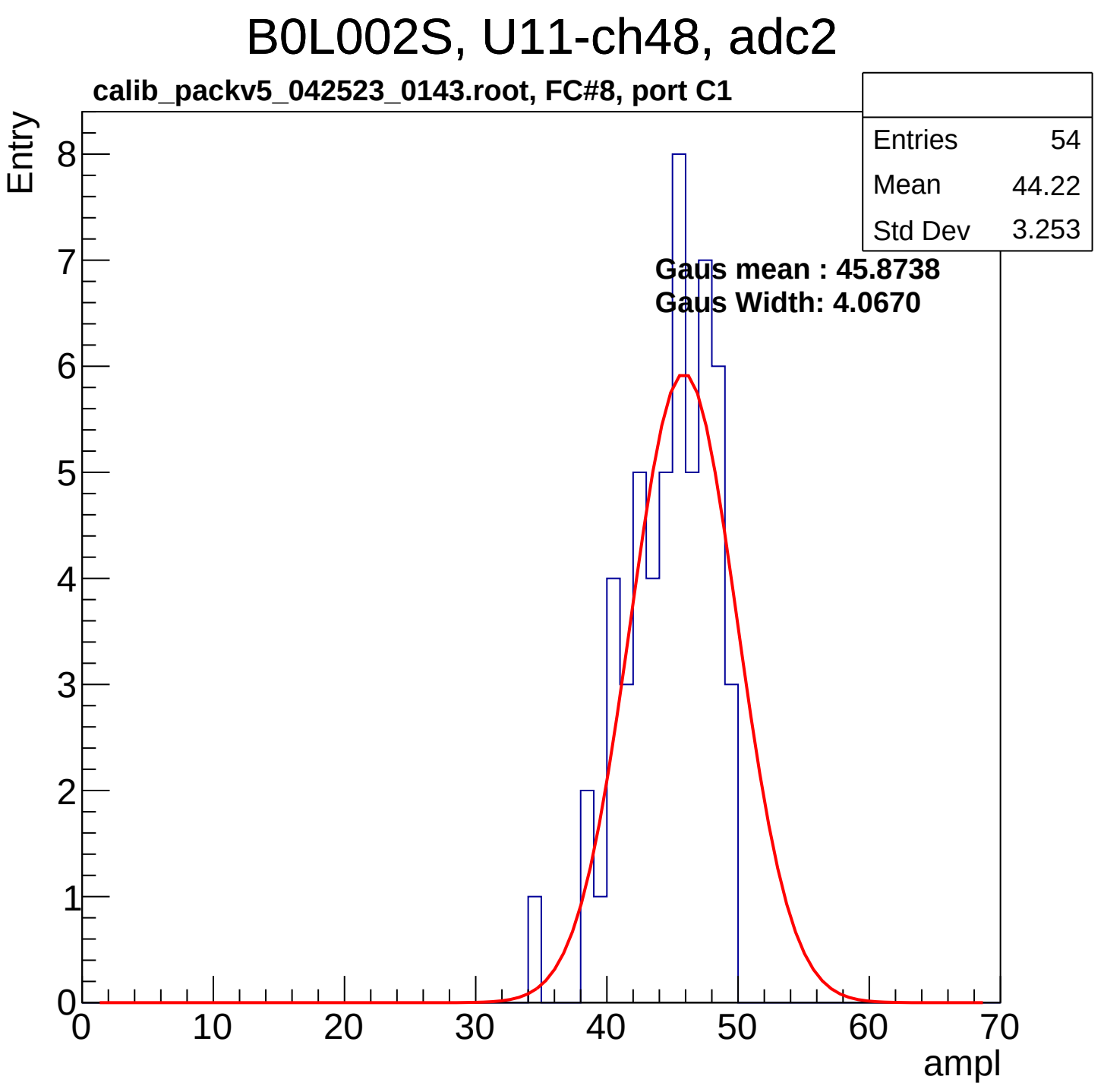

B0L002S, U11-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.39
Std Dev 9.069
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

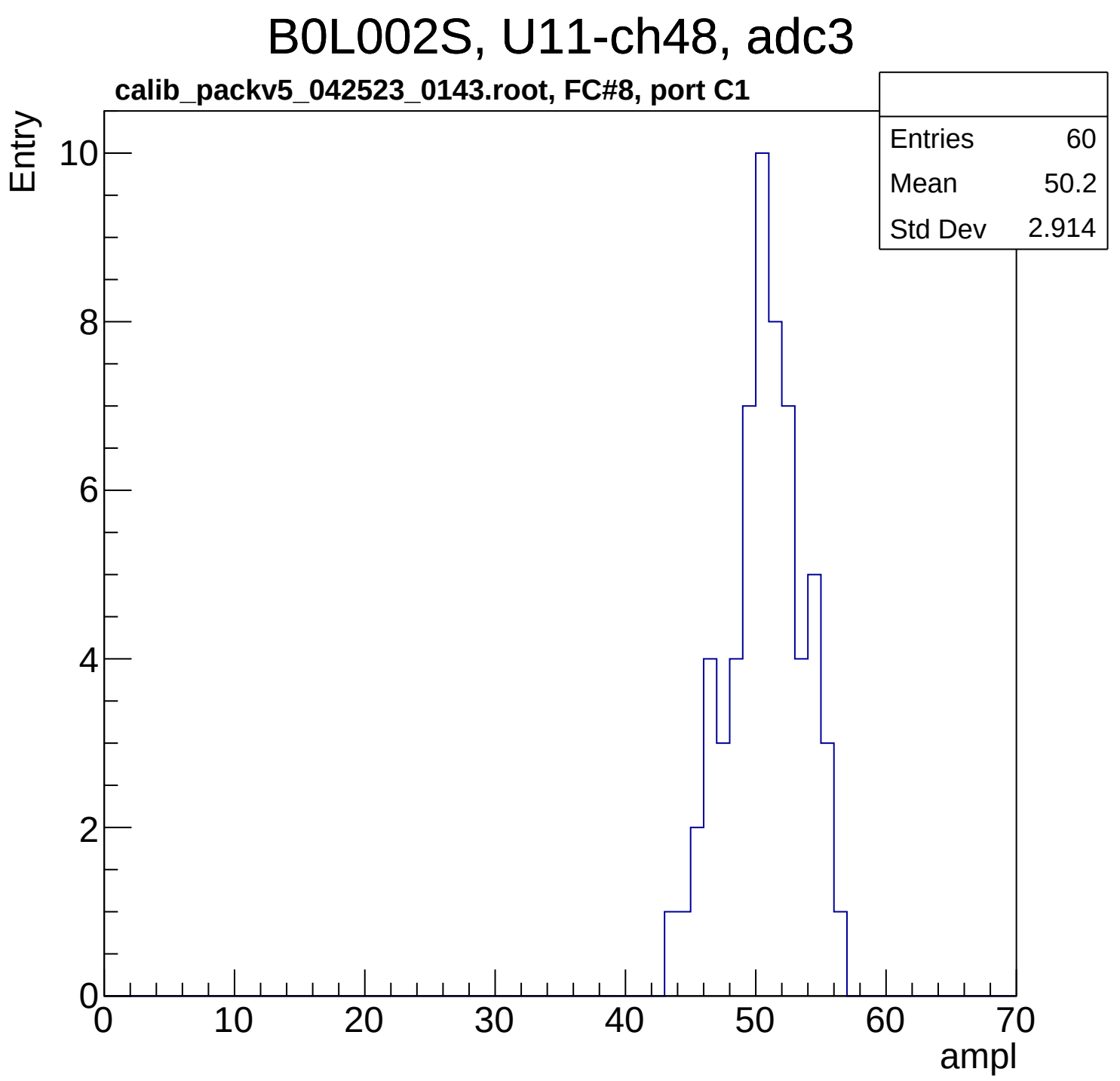

B0L002S, U11-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

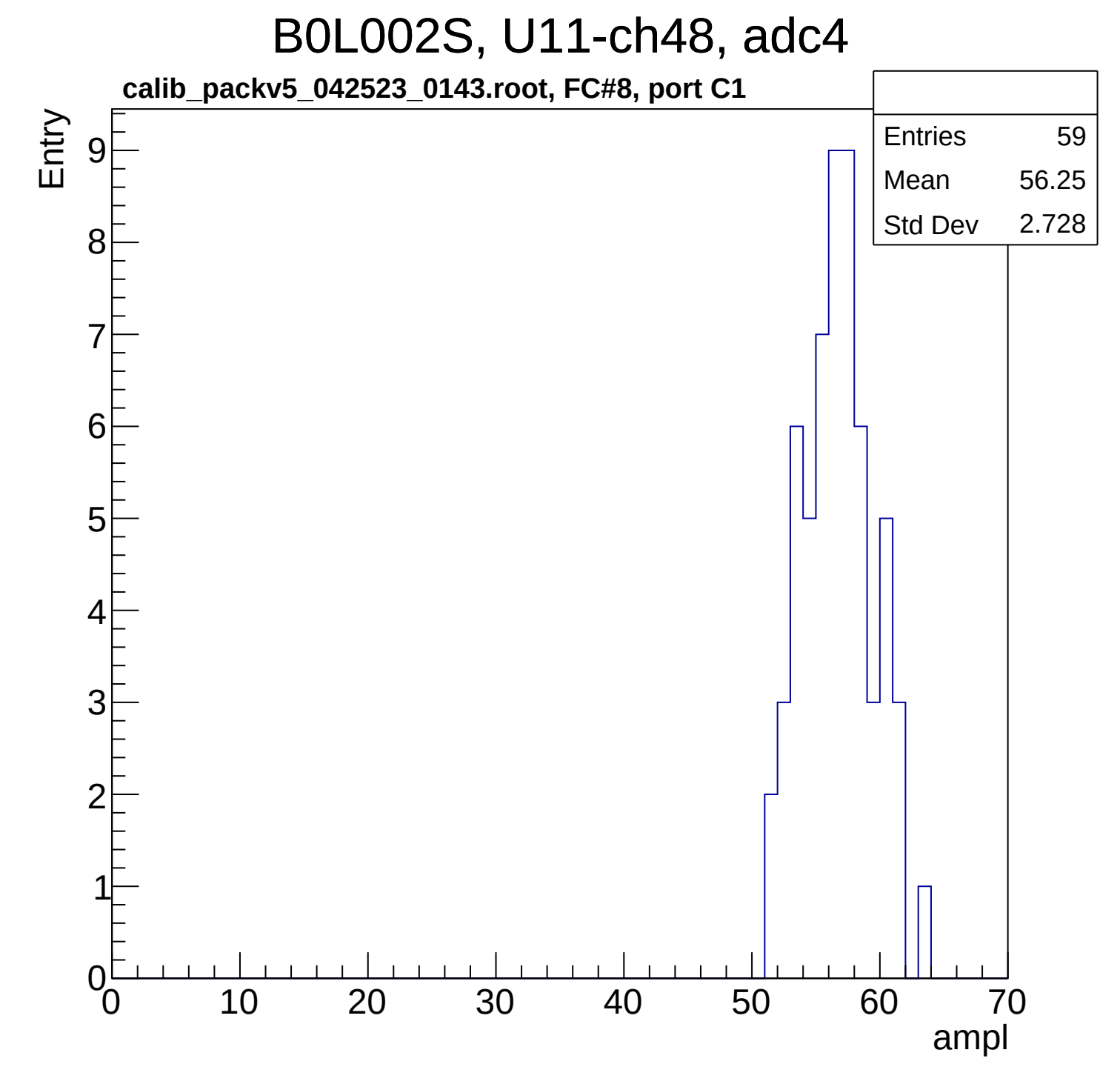

B0L002S, U11-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

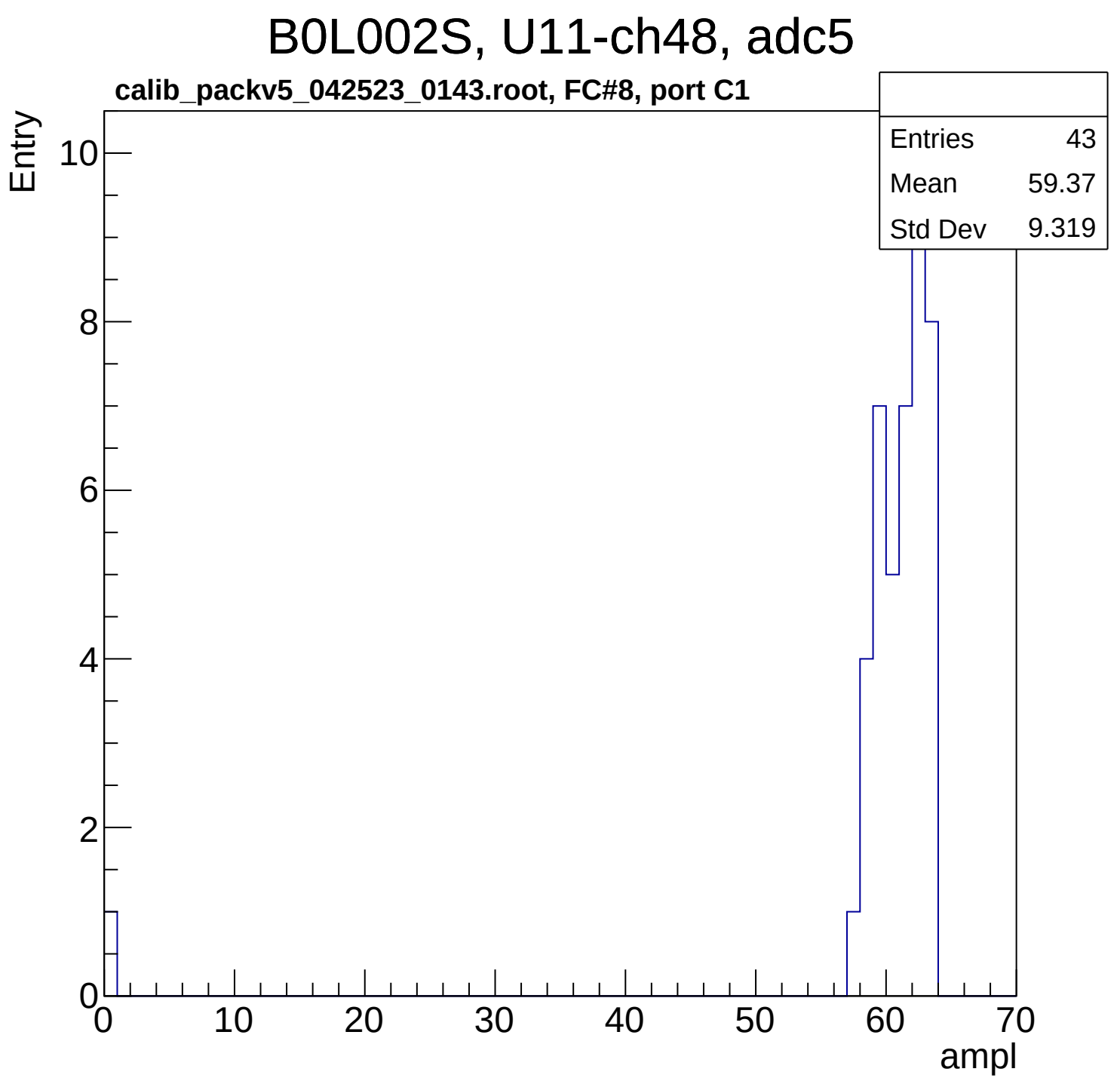

B0L002S, U11-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.39
Std Dev 3.277
Gaus mean : 30.9579
Gaus Width: 3.3684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

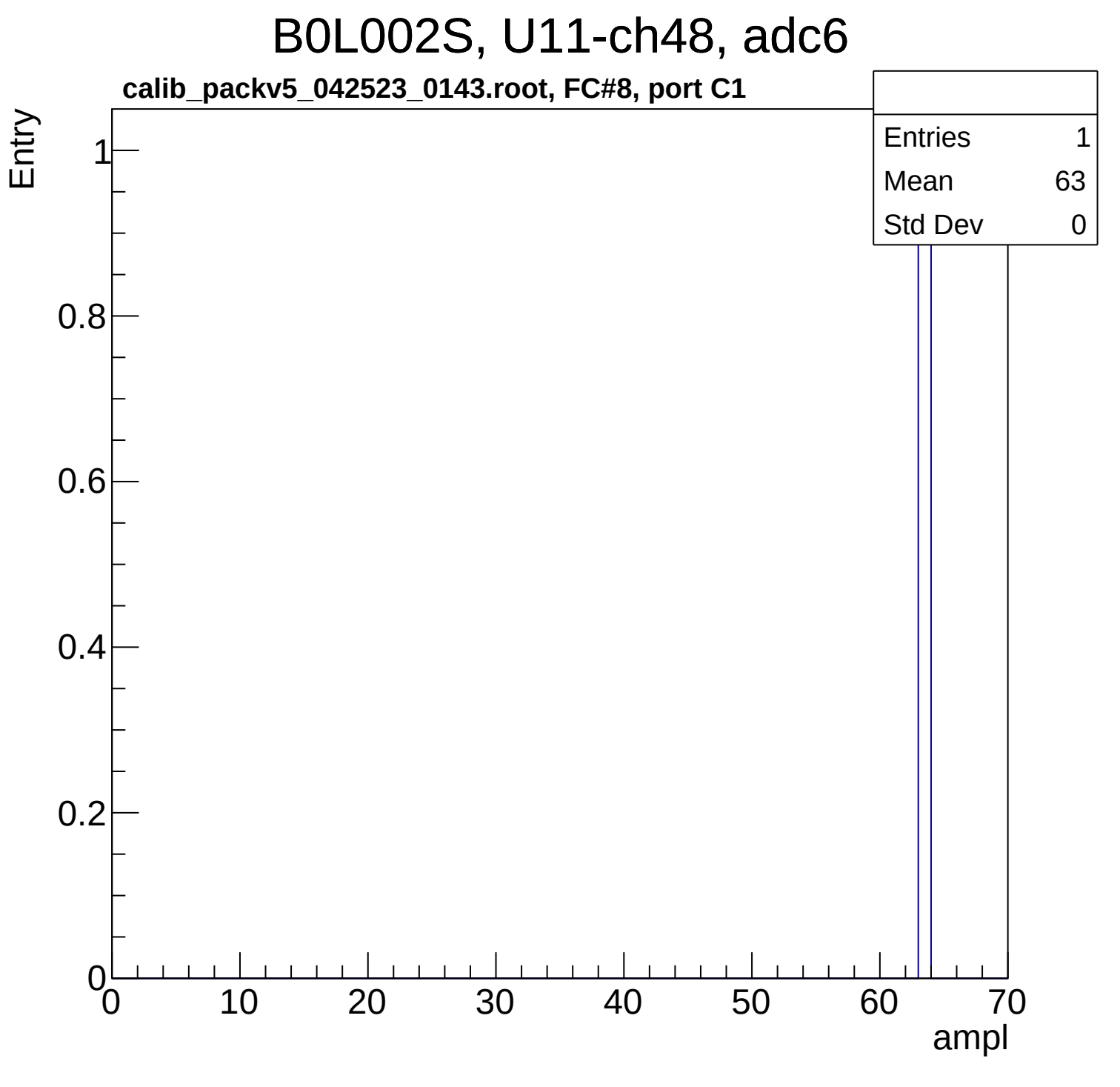

B0L002S, U11-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 38.59
Std Dev 3.133
Gaus mean : 39.1170
Gaus Width: 3.6836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

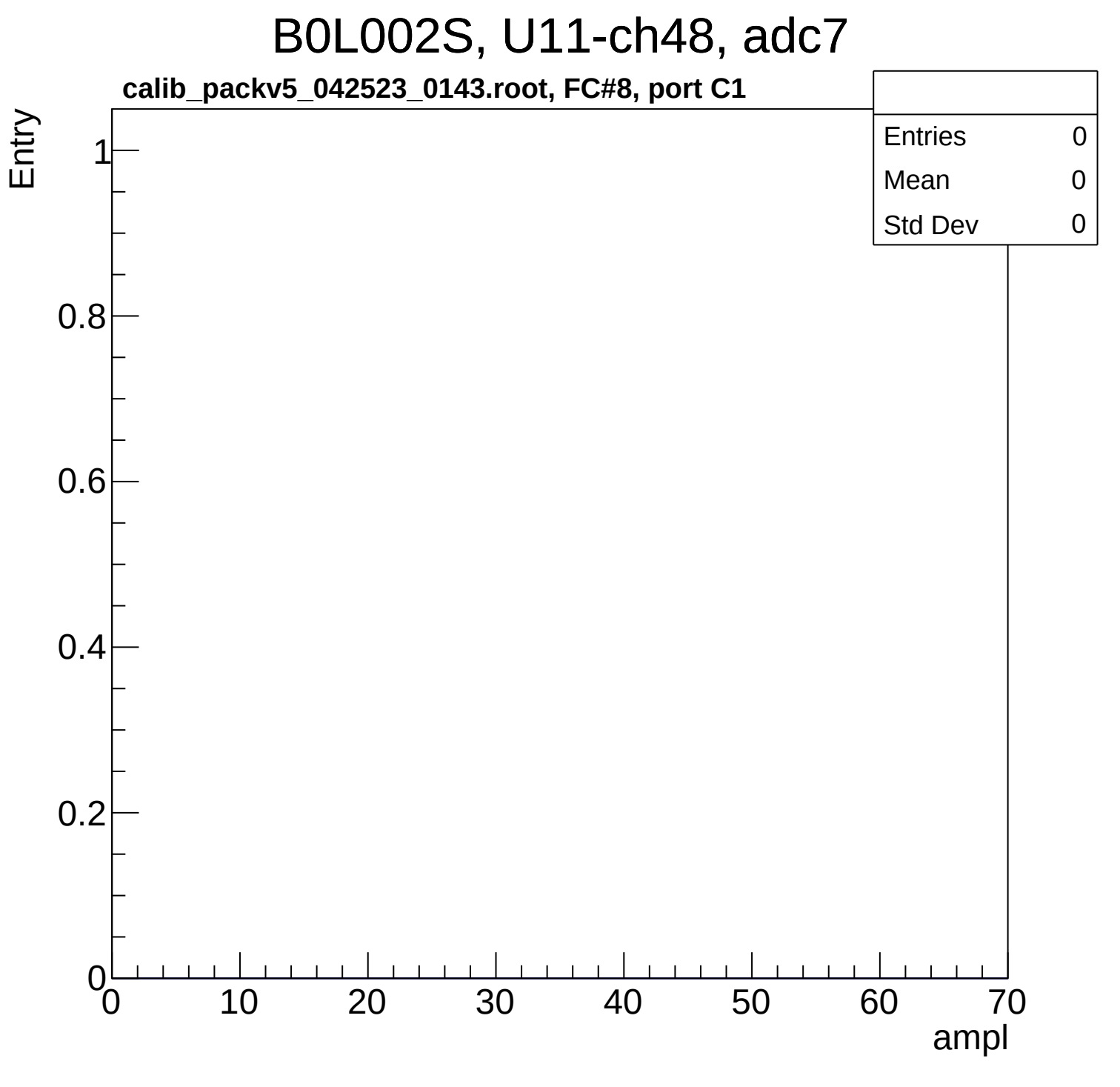

B0L002S, U11-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.22
Std Dev 3.253
Gaus mean : 45.8738
Gaus Width: 4.0670
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

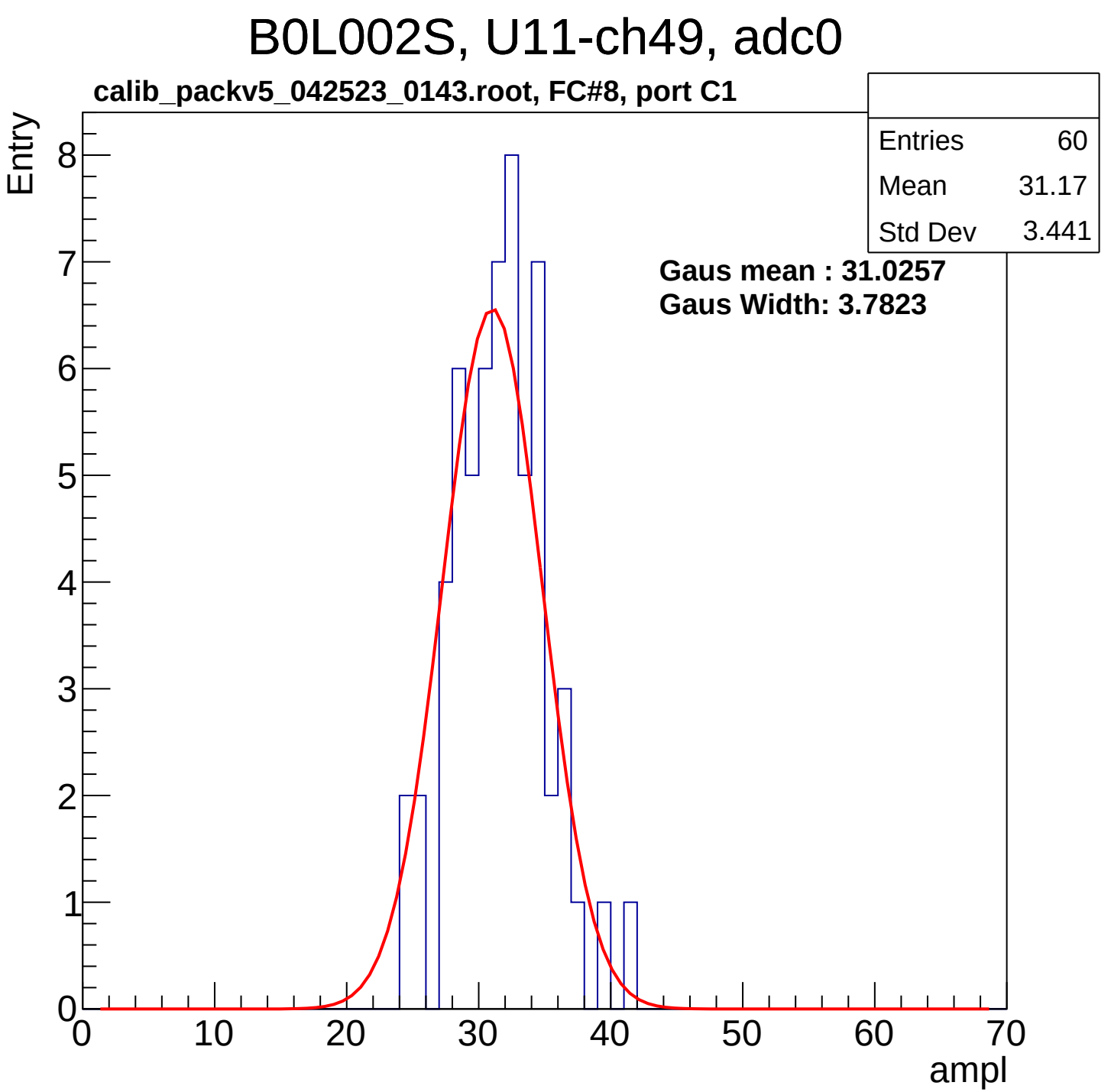

B0L002S, U11-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.2
Std Dev 2.914
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.25
Std Dev 2.728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.37
Std Dev 9.319
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.17
Std Dev 3.441
Gaus mean : 31.0257
Gaus Width: 3.7823
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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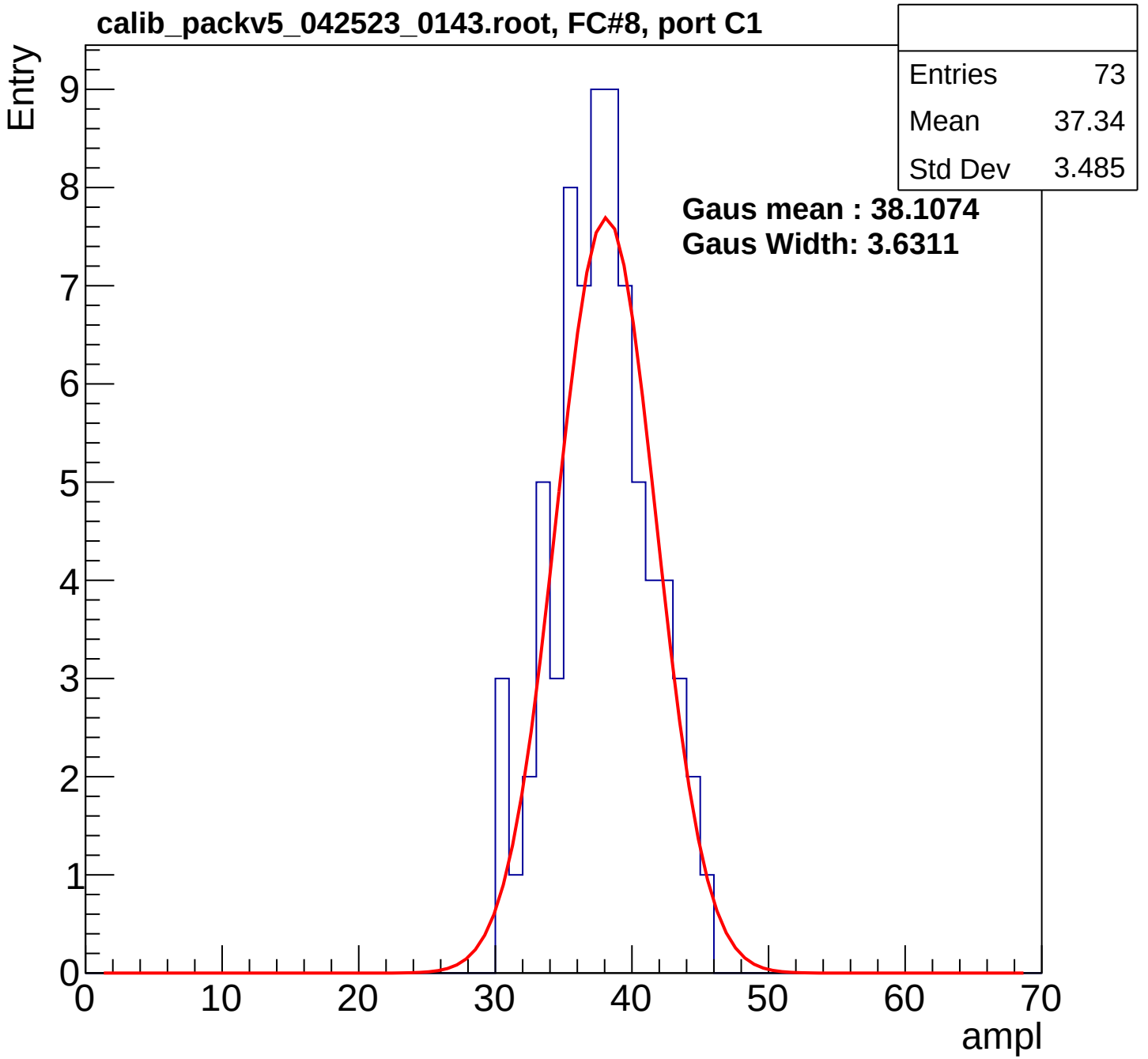
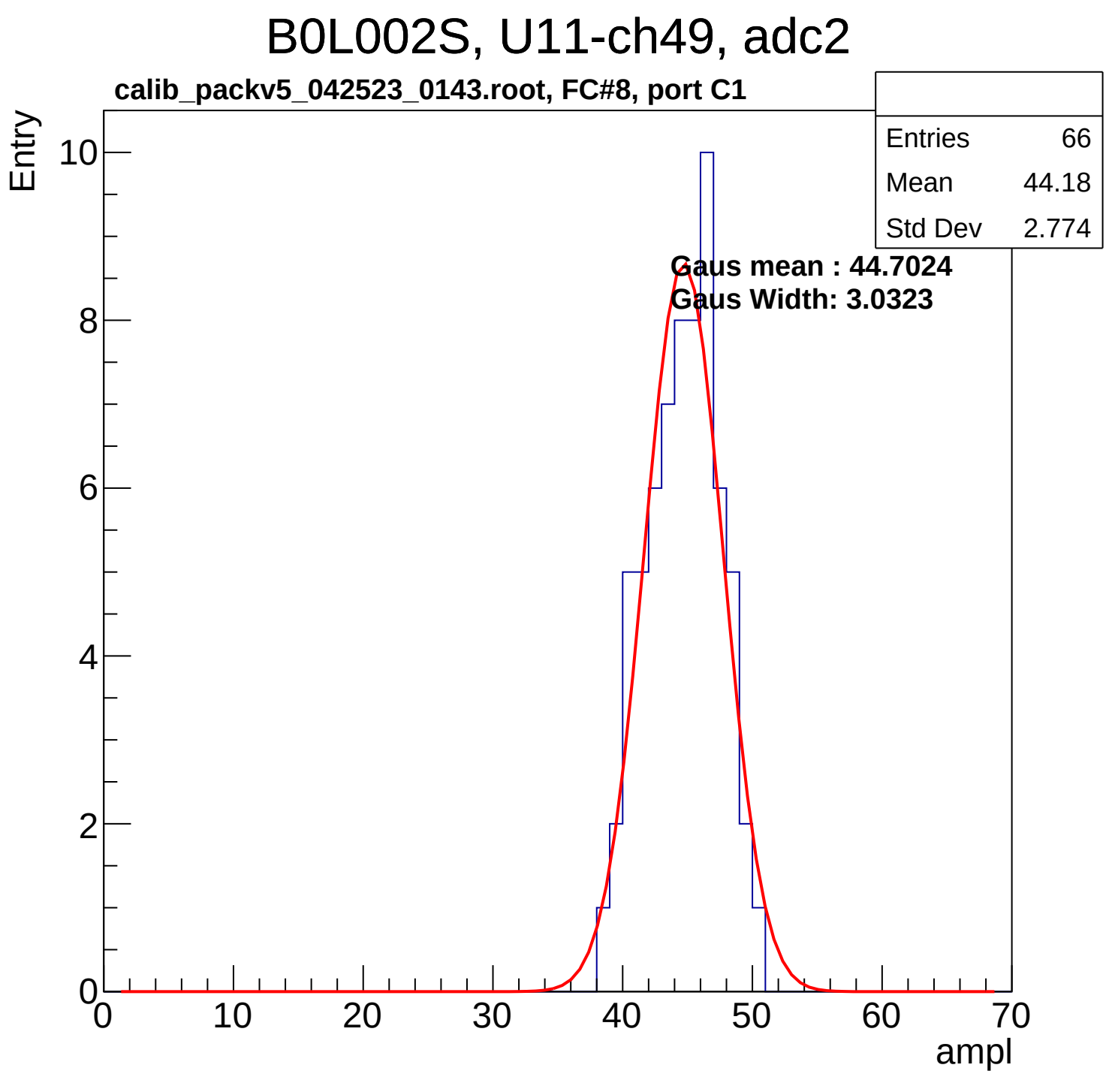
40
50
60
70


B0L002S, U11-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.34
Std Dev 3.485
Gaus mean : 38.1074
Gaus Width: 3.6311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.18
Std Dev 2.774
Gaus mean : 44.7024
Gaus Width: 3.0323
Entry
ampl
0
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6
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10
0
10
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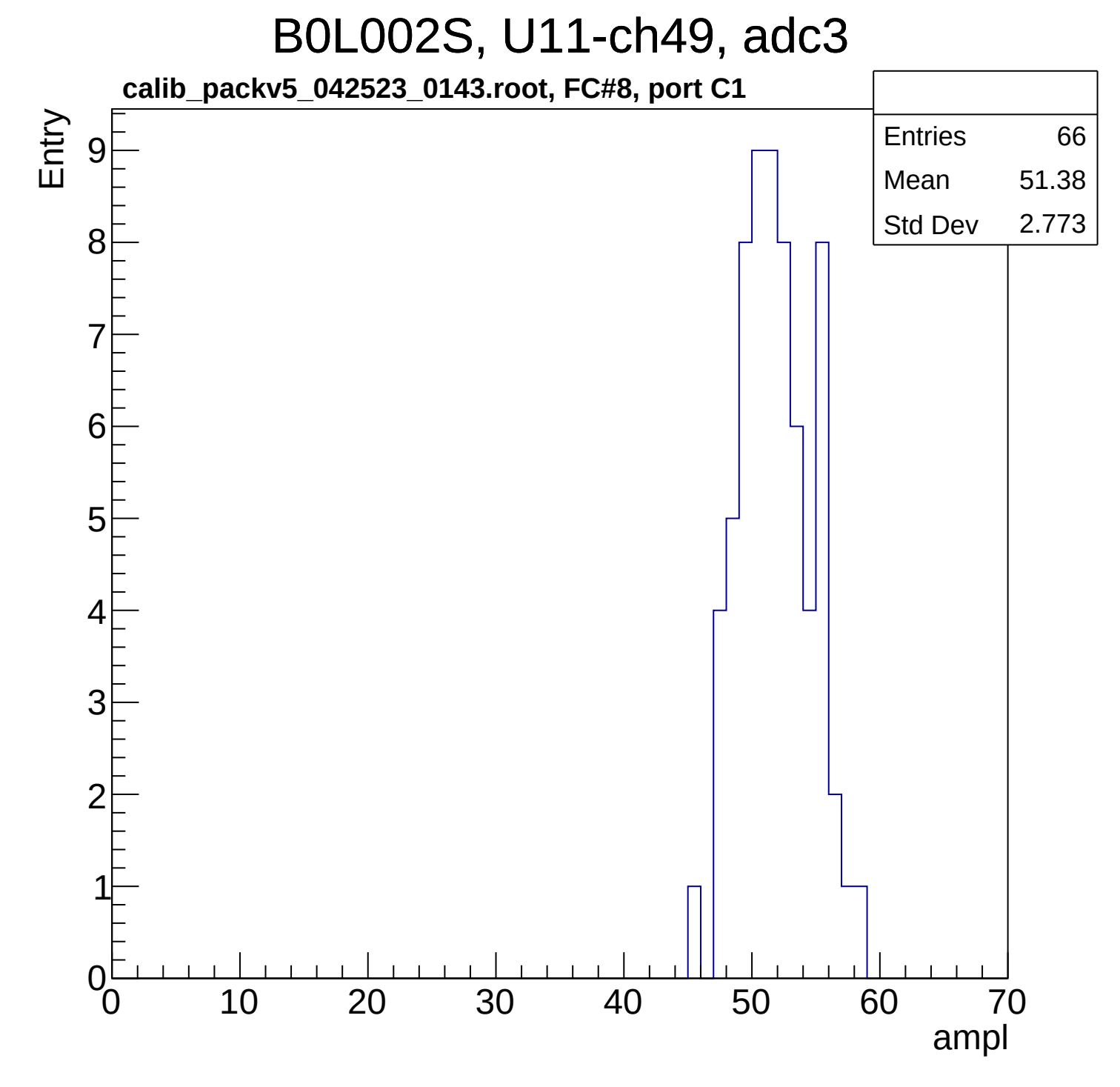
30
40
50
60
70


B0L002S, U11-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.38
Std Dev 2.773
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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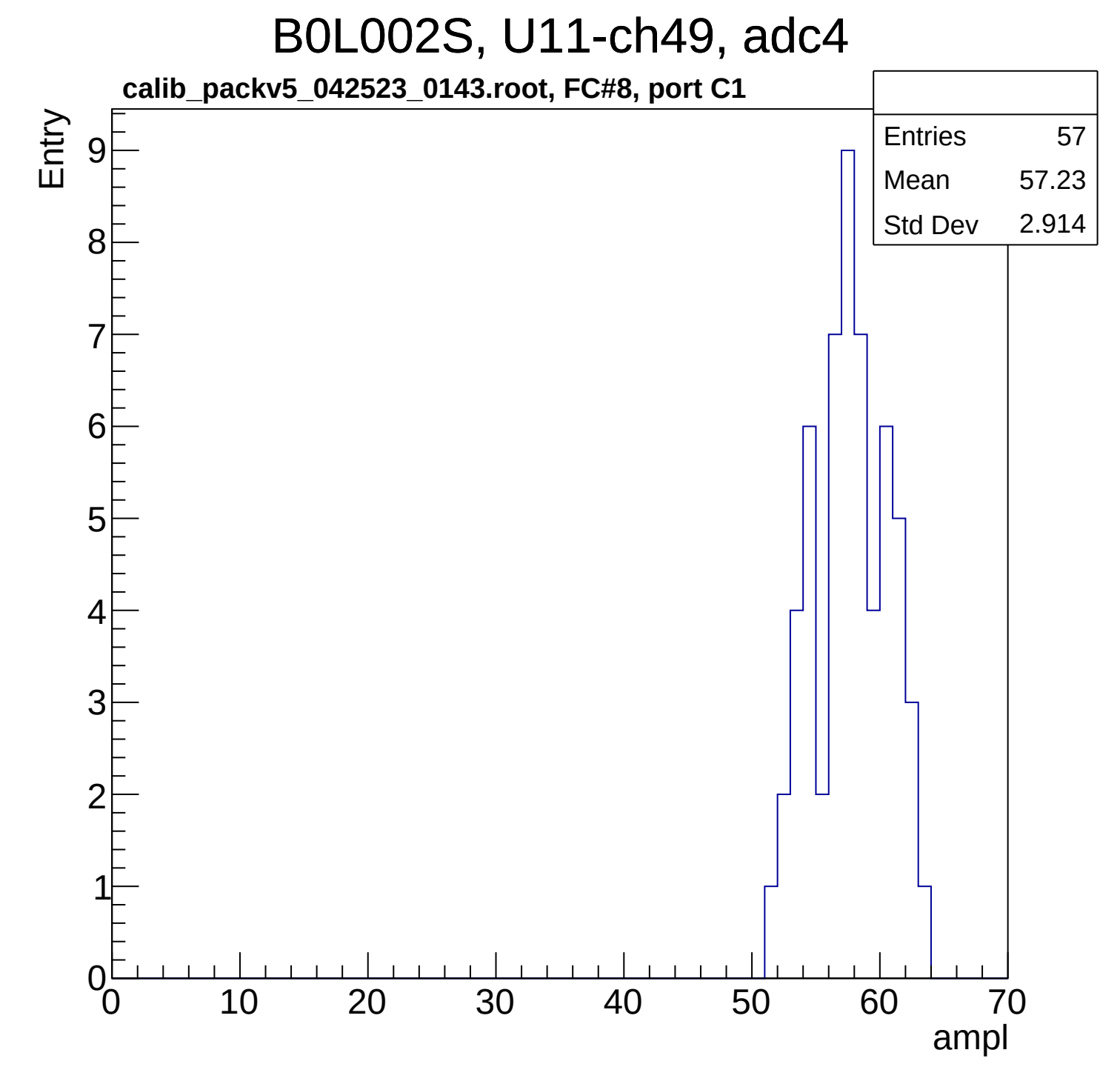
60
70


B0L002S, U11-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.23
Std Dev 2.914
Entry
ampl
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7
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0
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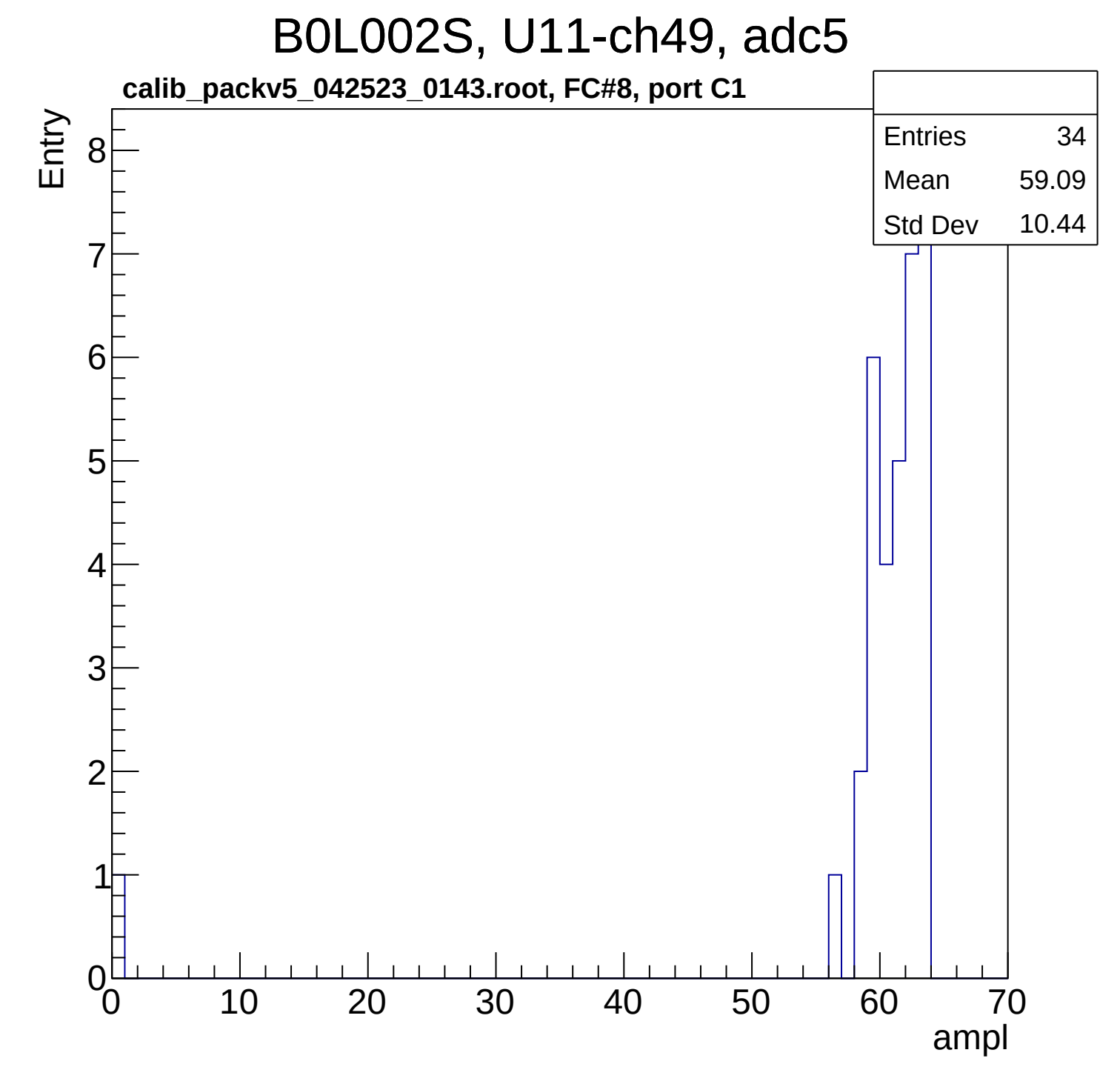
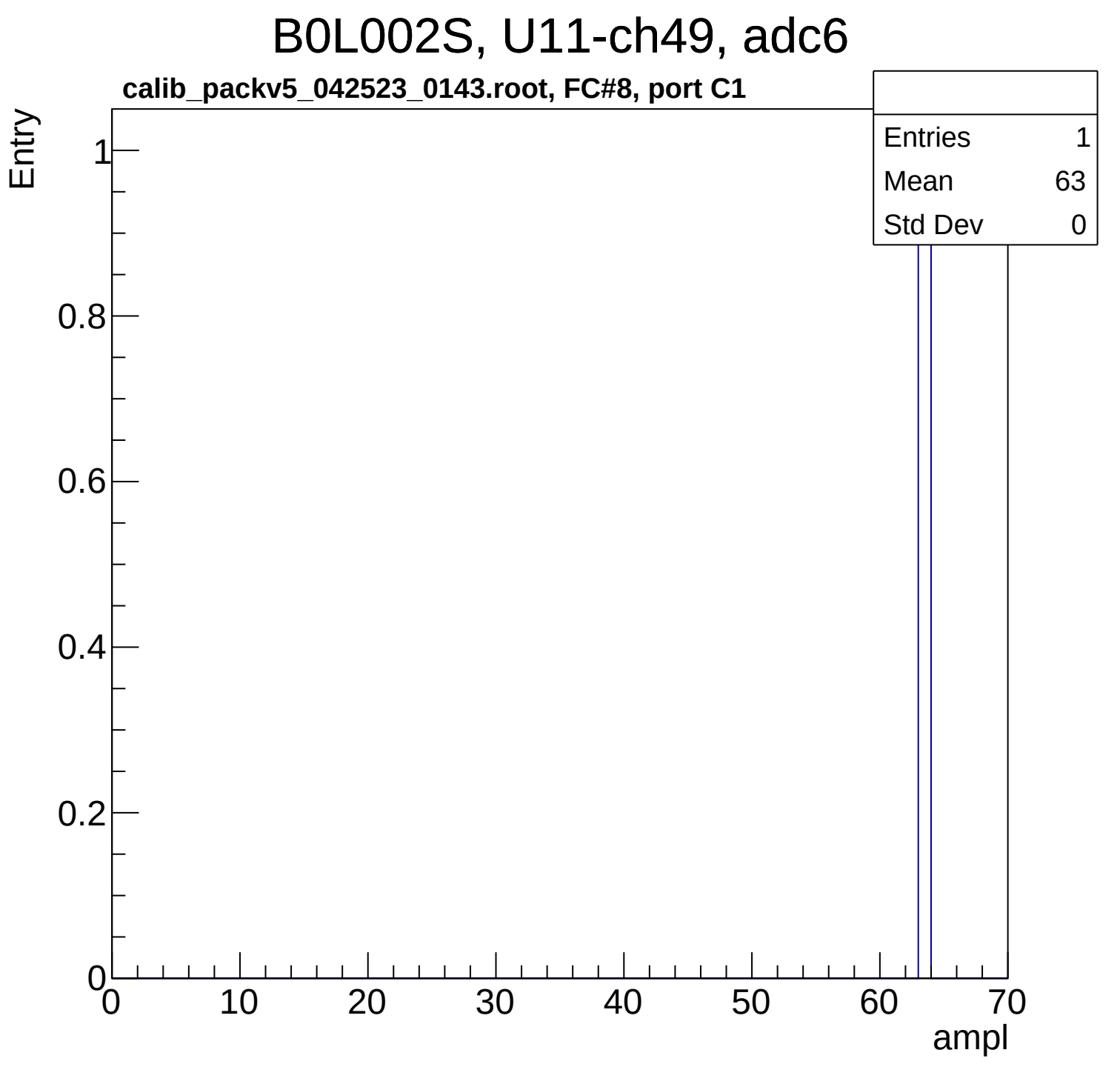
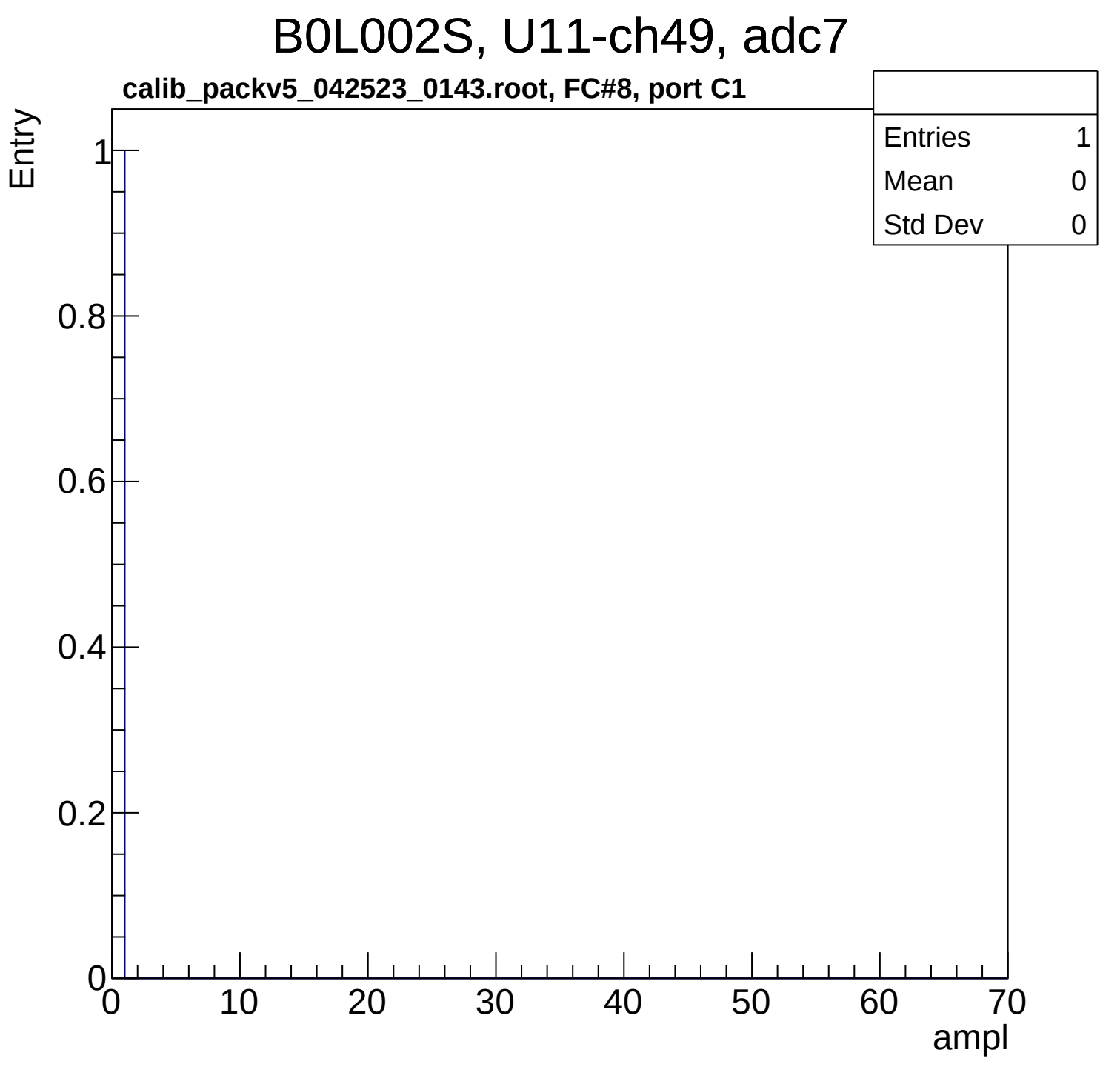
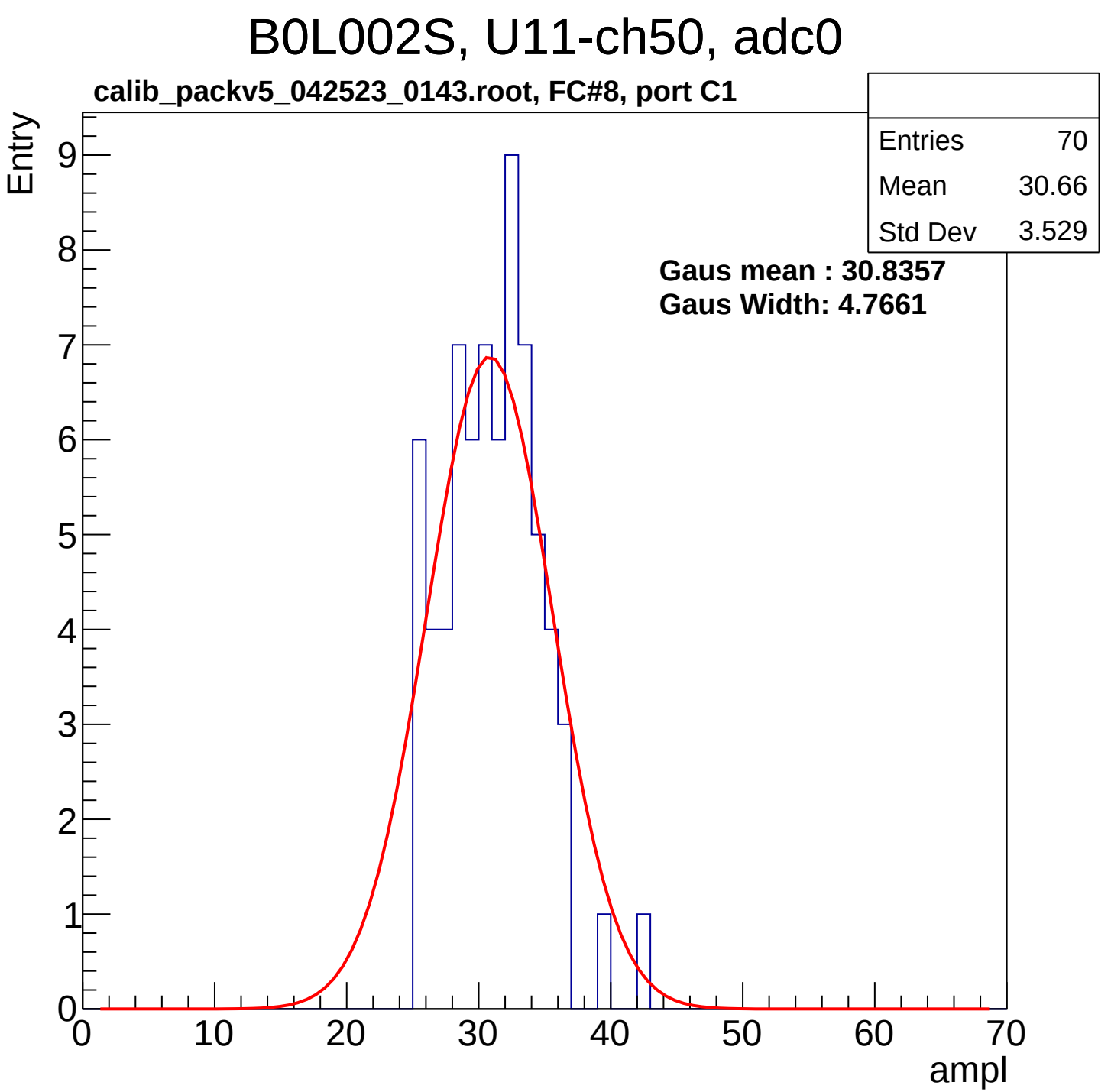

B0L002S, U11-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.09
Std Dev 10.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.66
Std Dev 3.529
Gaus mean : 30.8357
Gaus Width: 4.7661
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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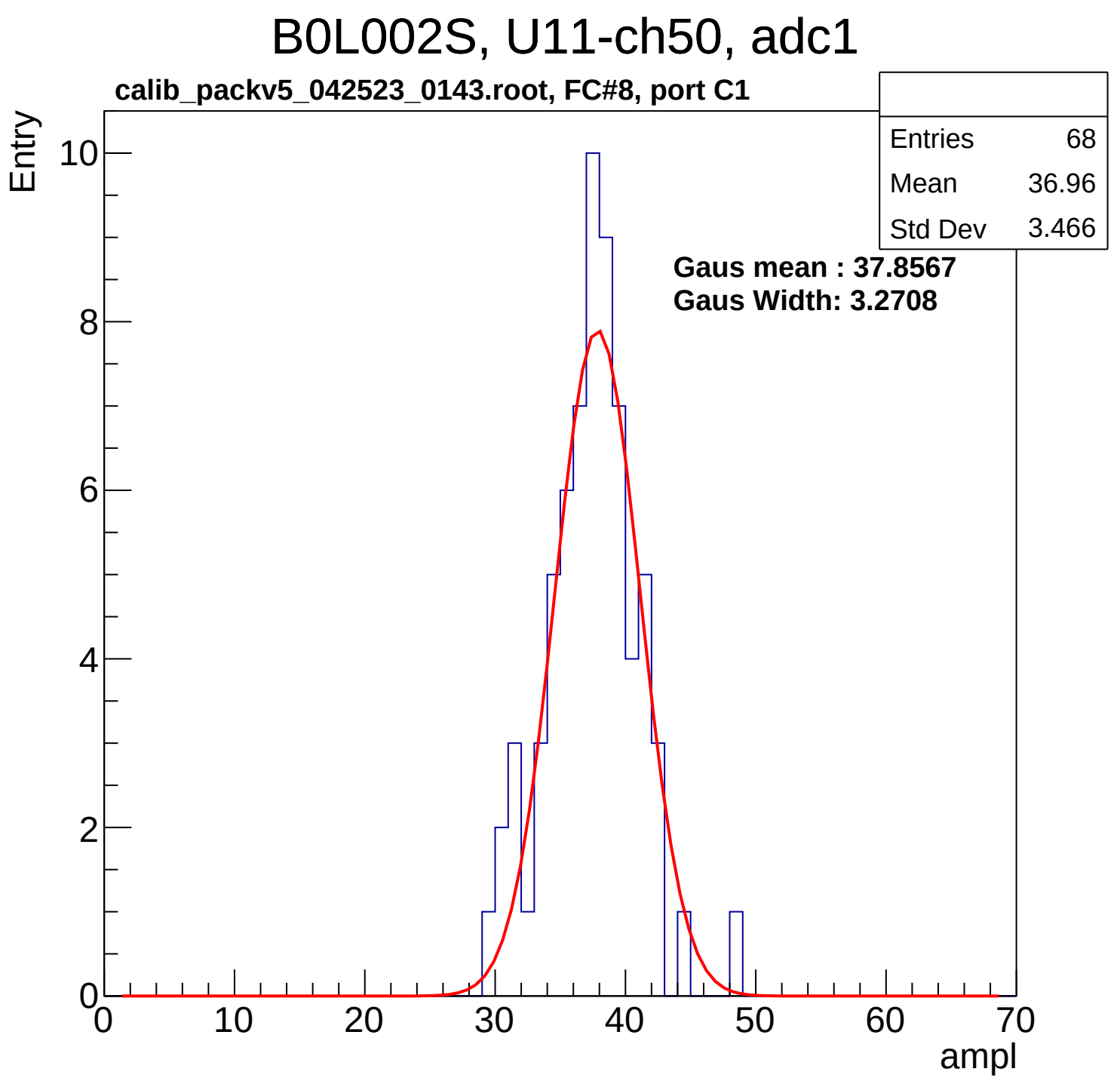
B0L002S, U11-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.96
Std Dev 3.466
Gaus mean : 37.8567
Gaus Width: 3.2708
Entry
ampl
0
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4
6
8
10
0
10
20
30
40
50
60
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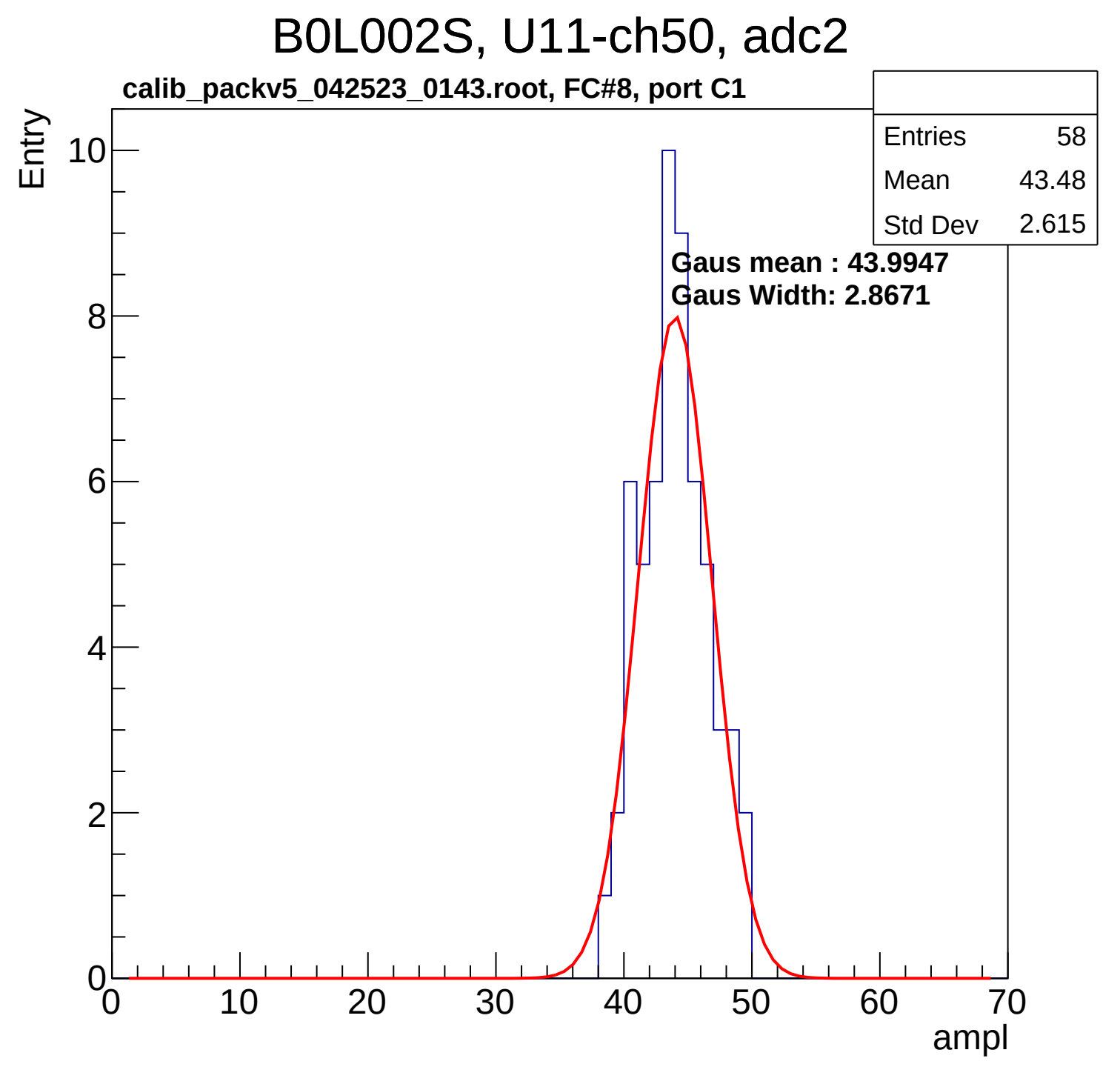
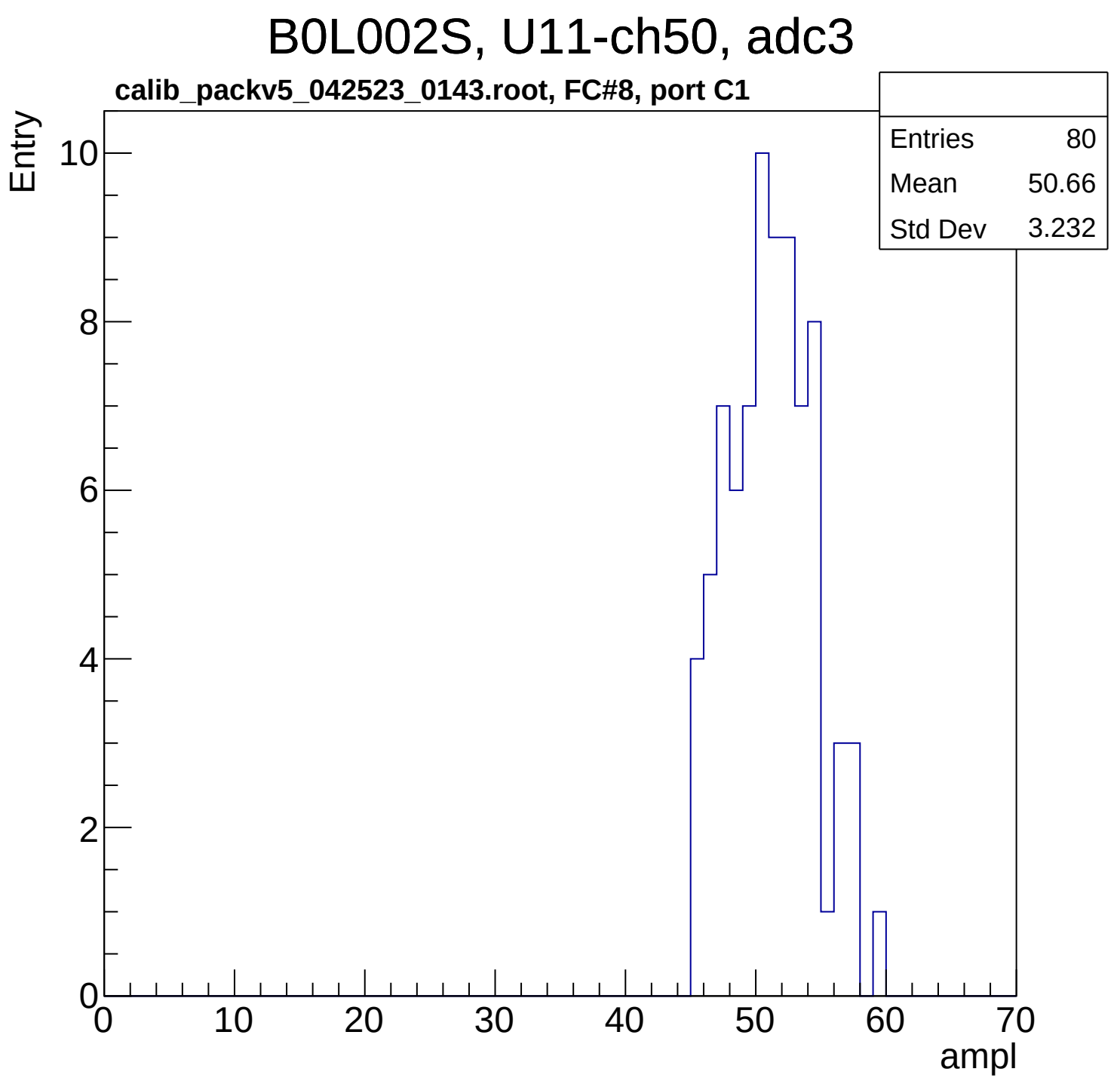

B0L002S, U11-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.48
Std Dev 2.615
Gaus mean : 43.9947
Gaus Width: 2.8671
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 50.66
Std Dev 3.232
Entry
0
2
4
6
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10
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50
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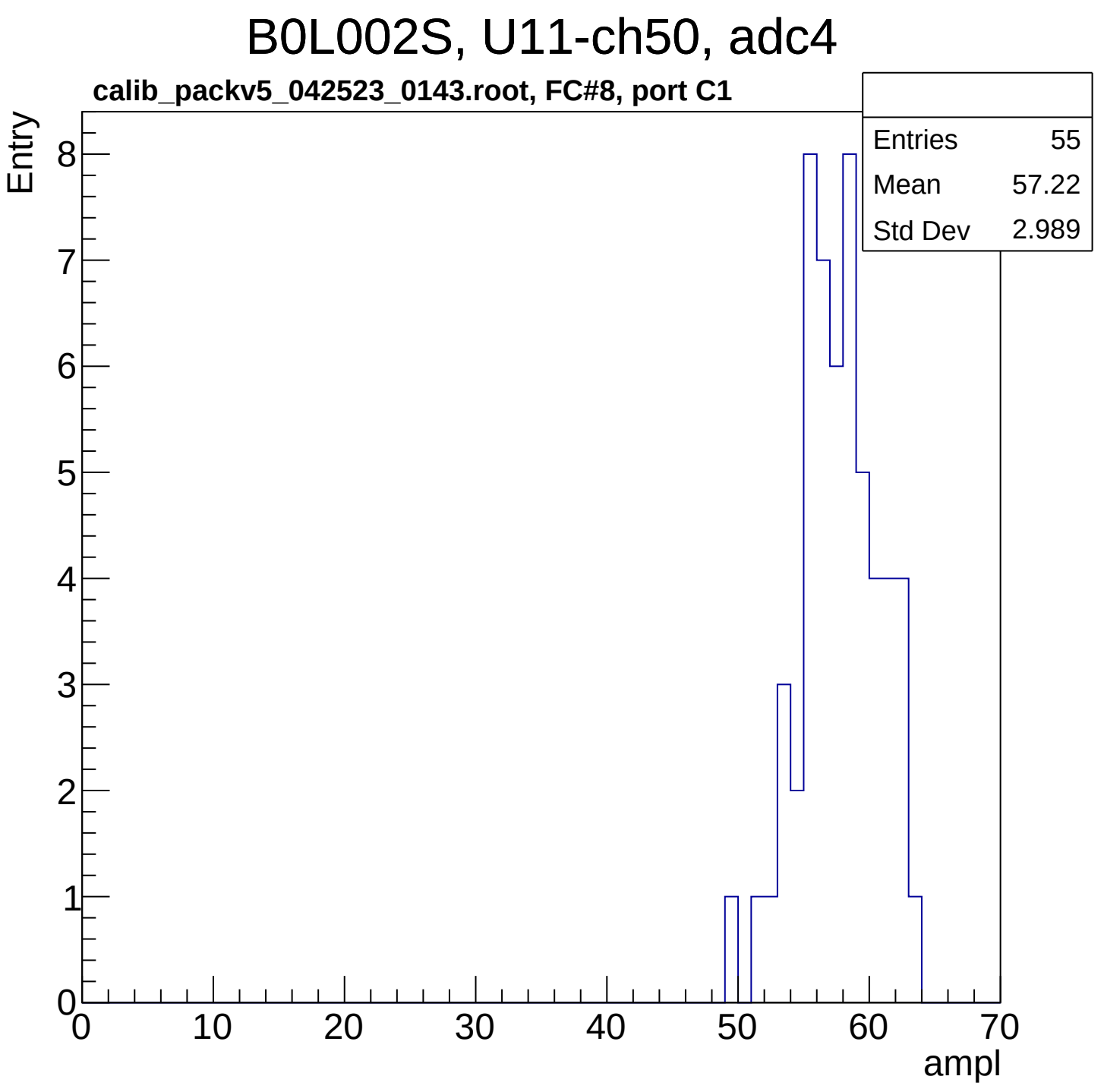
70
ampl


B0L002S, U11-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.22
Std Dev 2.989
Entry
ampl
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1
2
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4
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60
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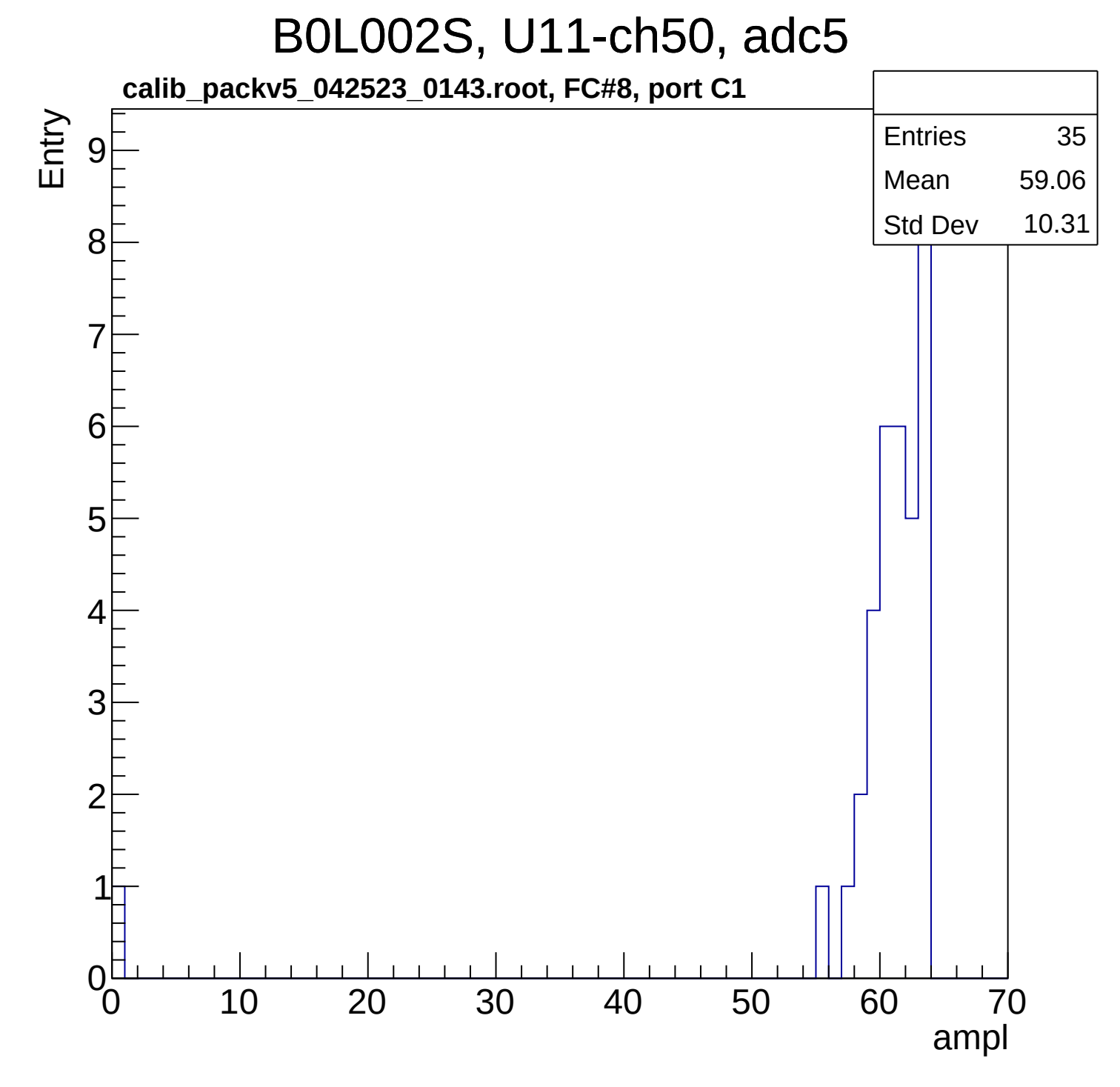
B0L002S, U11-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.06
Std Dev 10.31
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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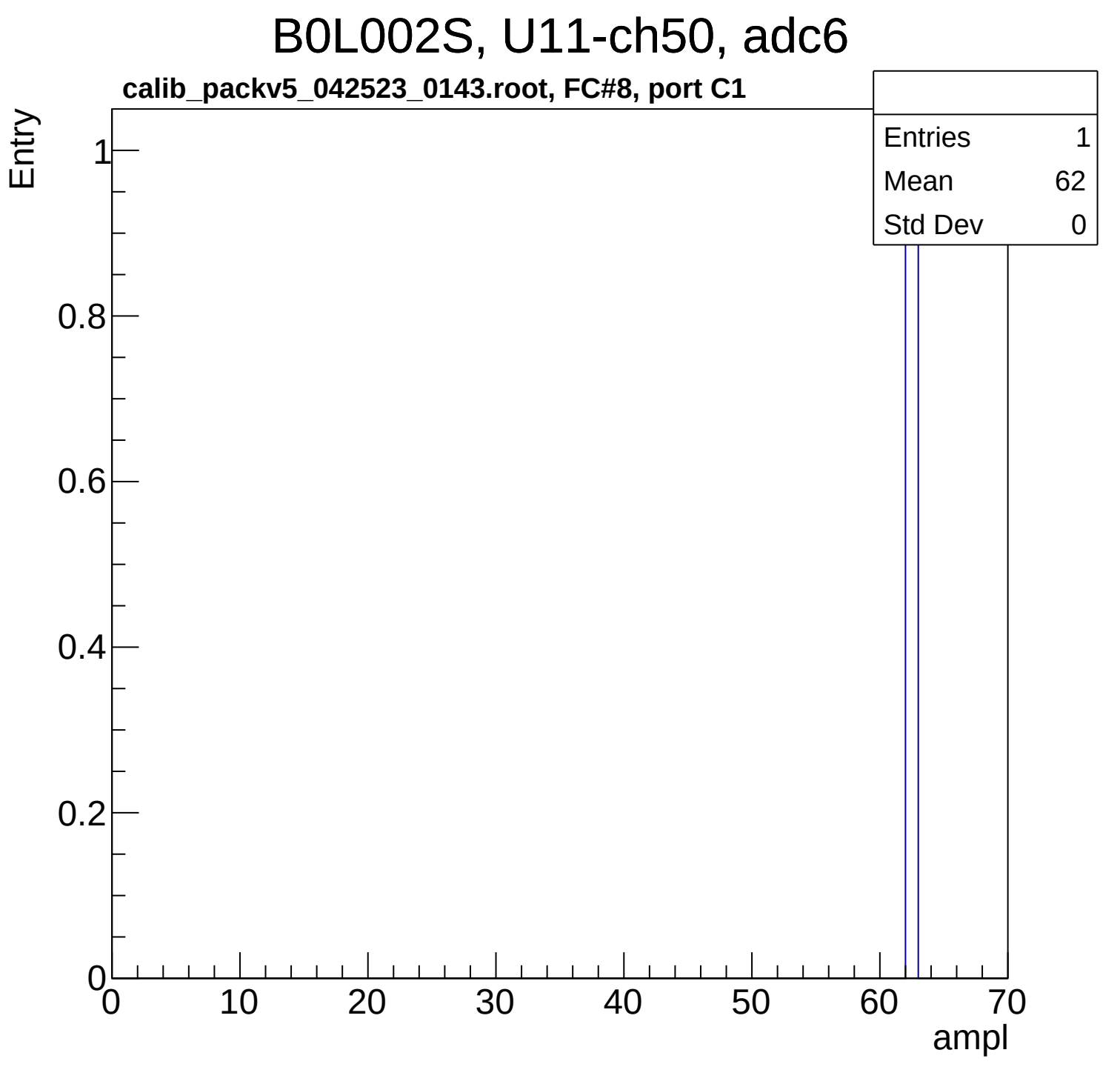
B0L002S, U11-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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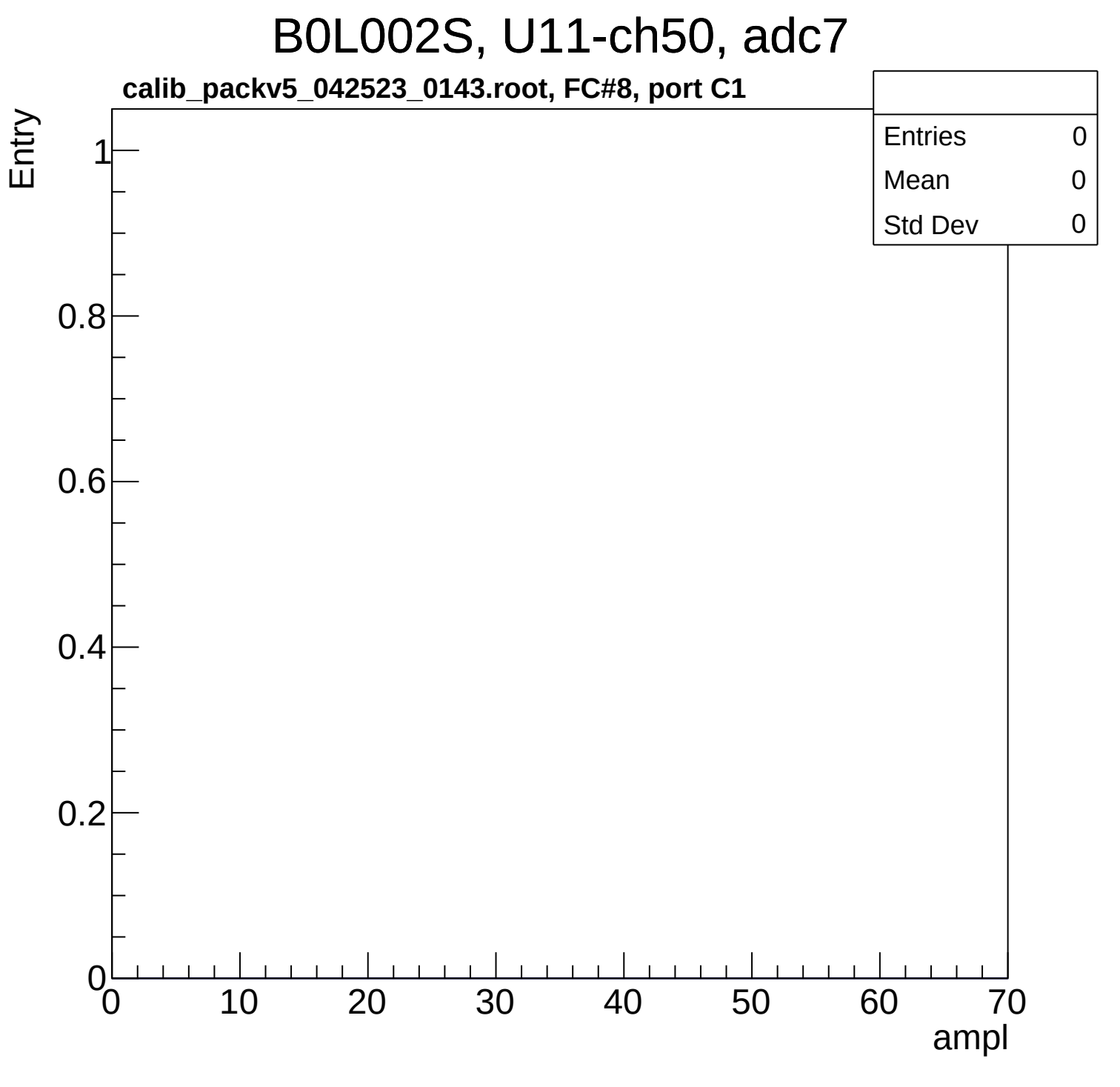
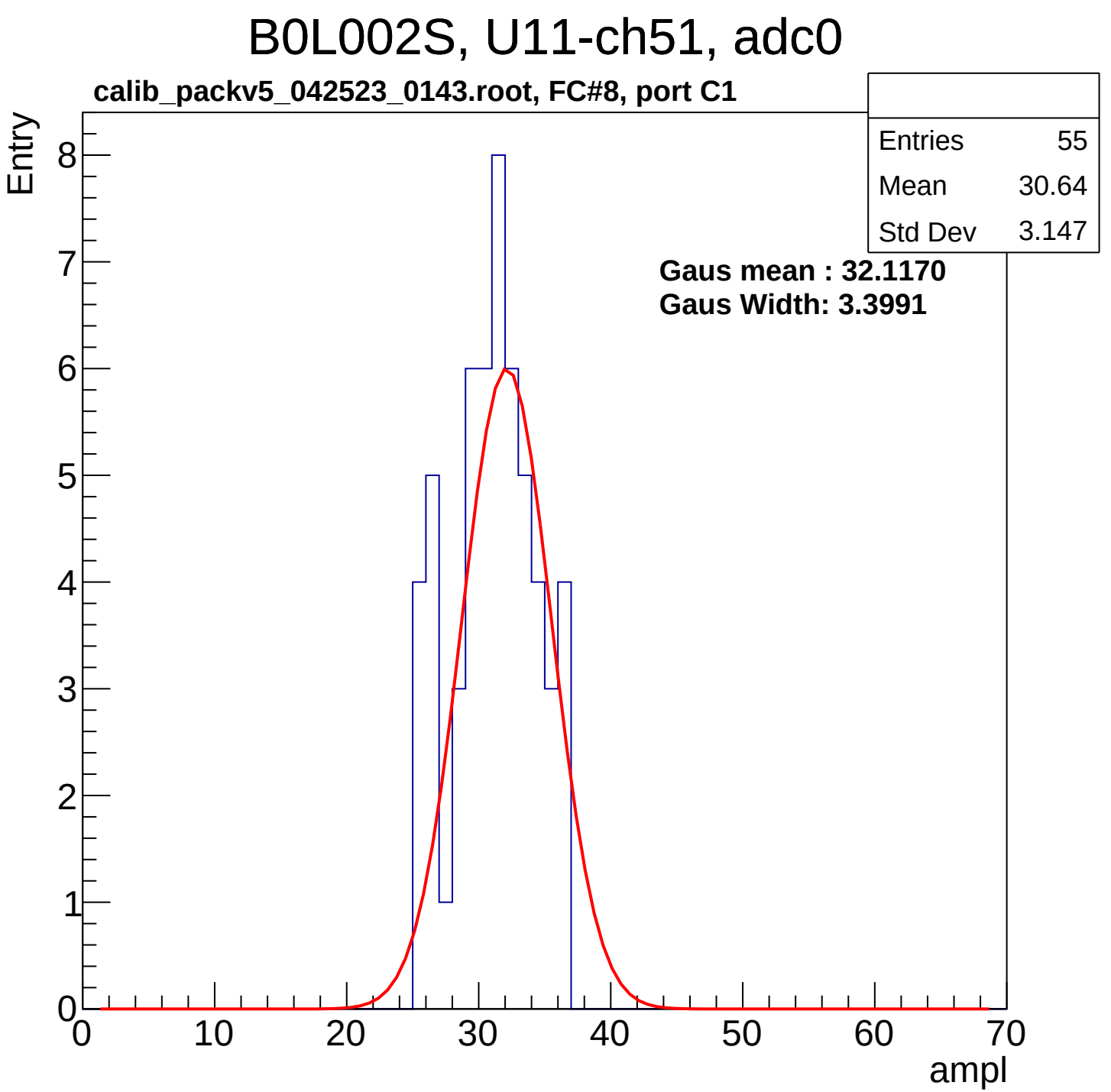
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 30.64
Std Dev 3.147
Gaus mean : 32.1170
Gaus Width: 3.3991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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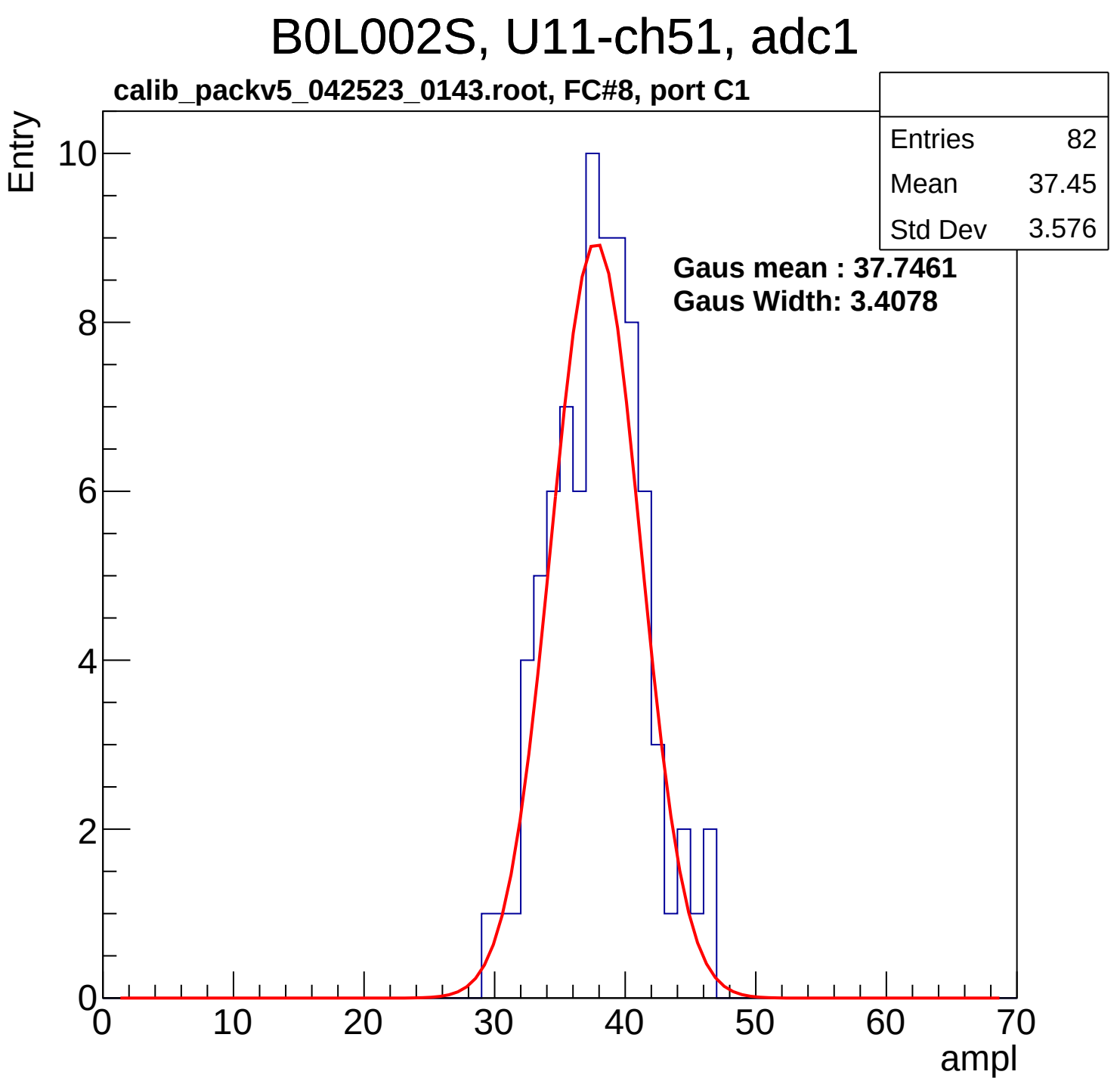
B0L002S, U11-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 37.45
Std Dev 3.576
Gaus mean : 37.7461
Gaus Width: 3.4078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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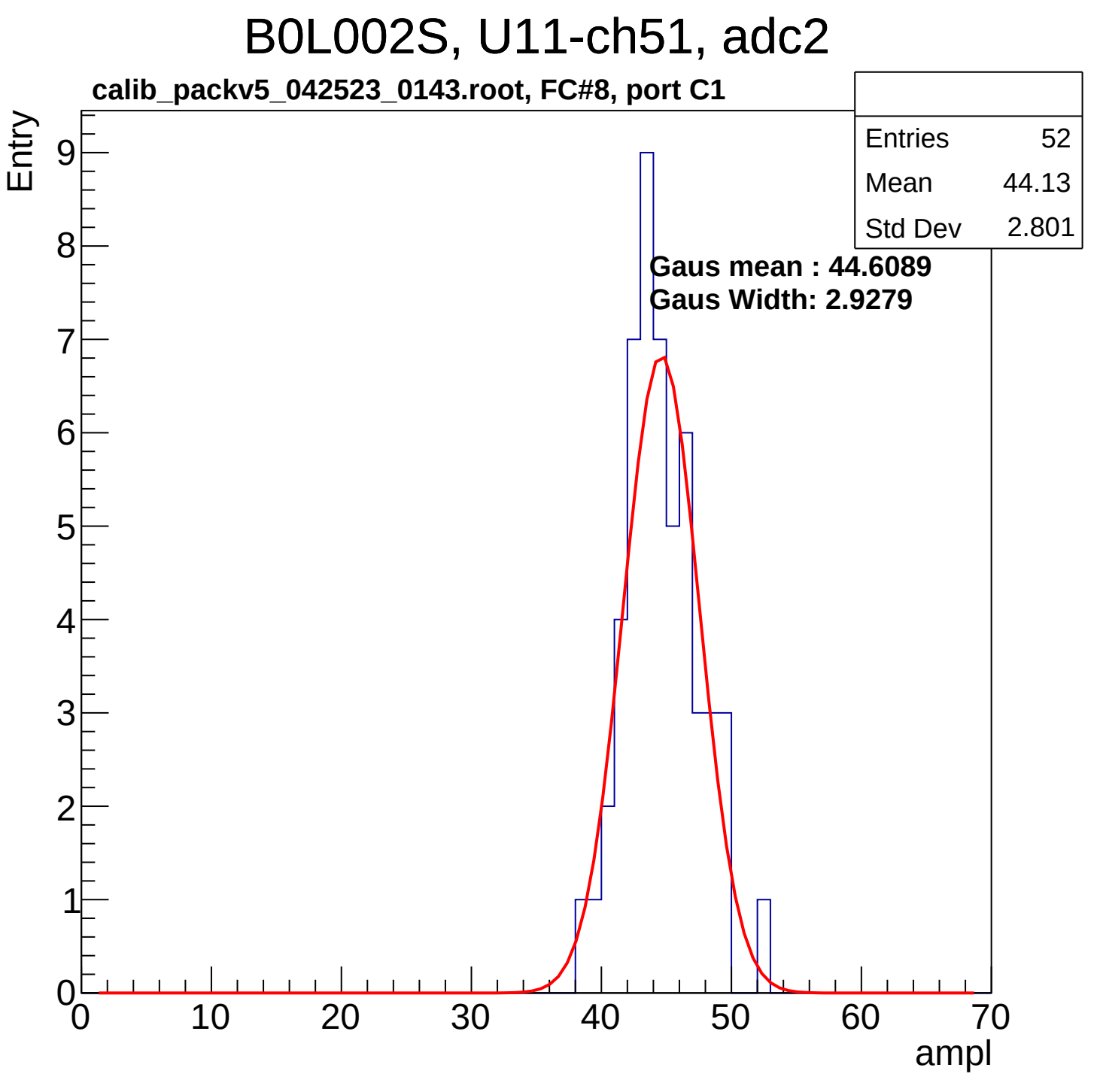
B0L002S, U11-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 44.13
Std Dev 2.801
Gaus mean : 44.6089
Gaus Width: 2.9279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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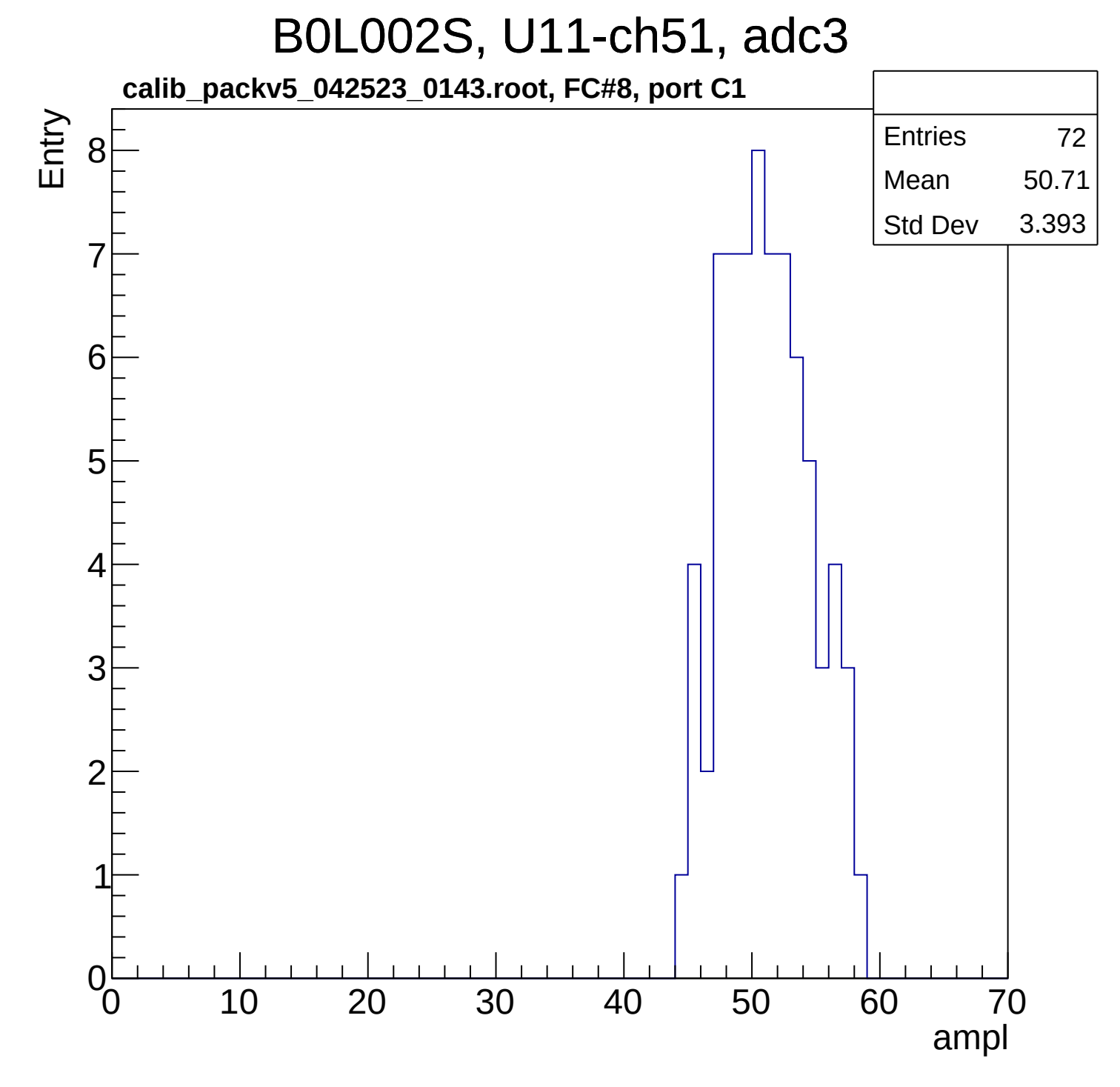
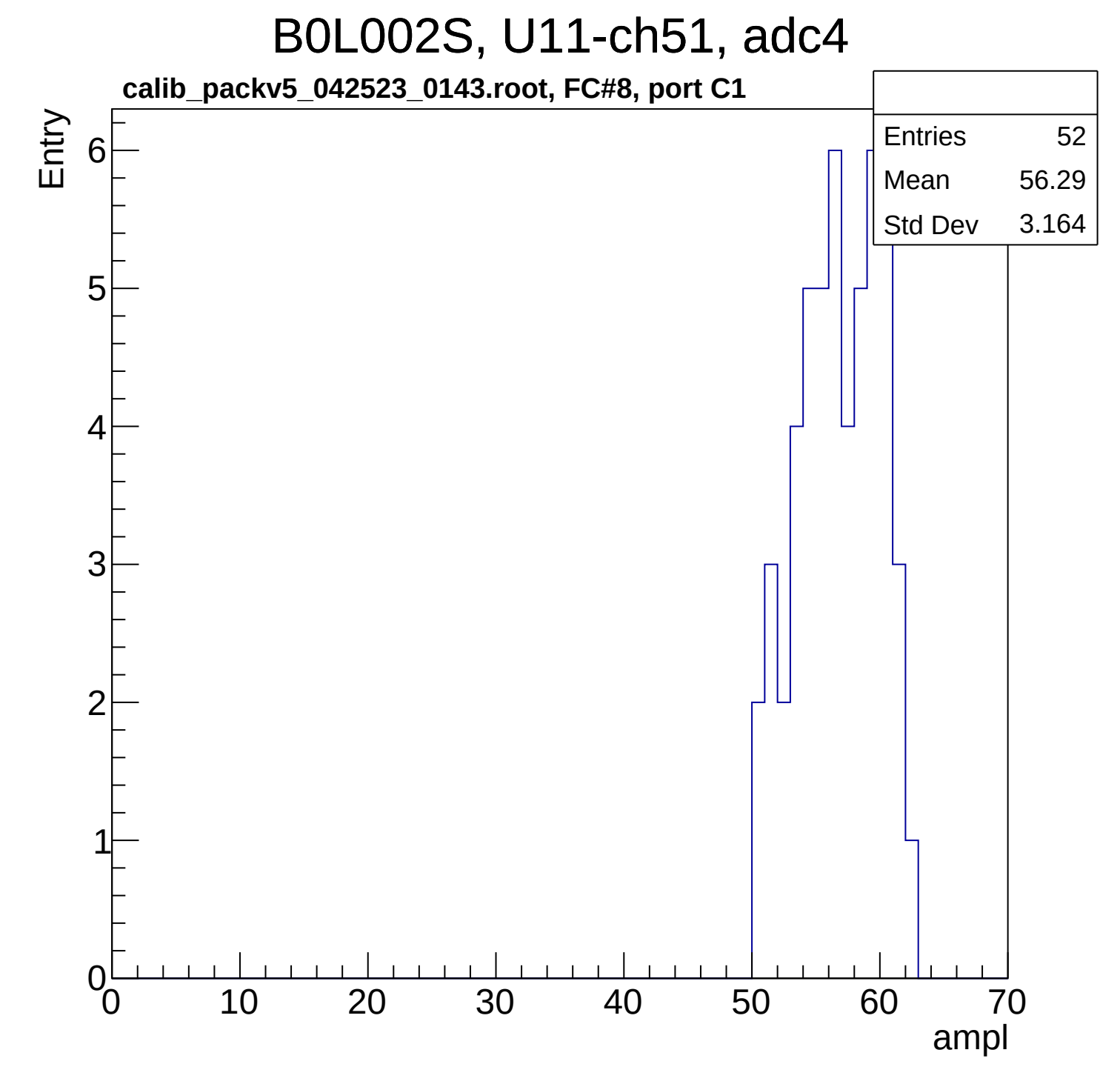
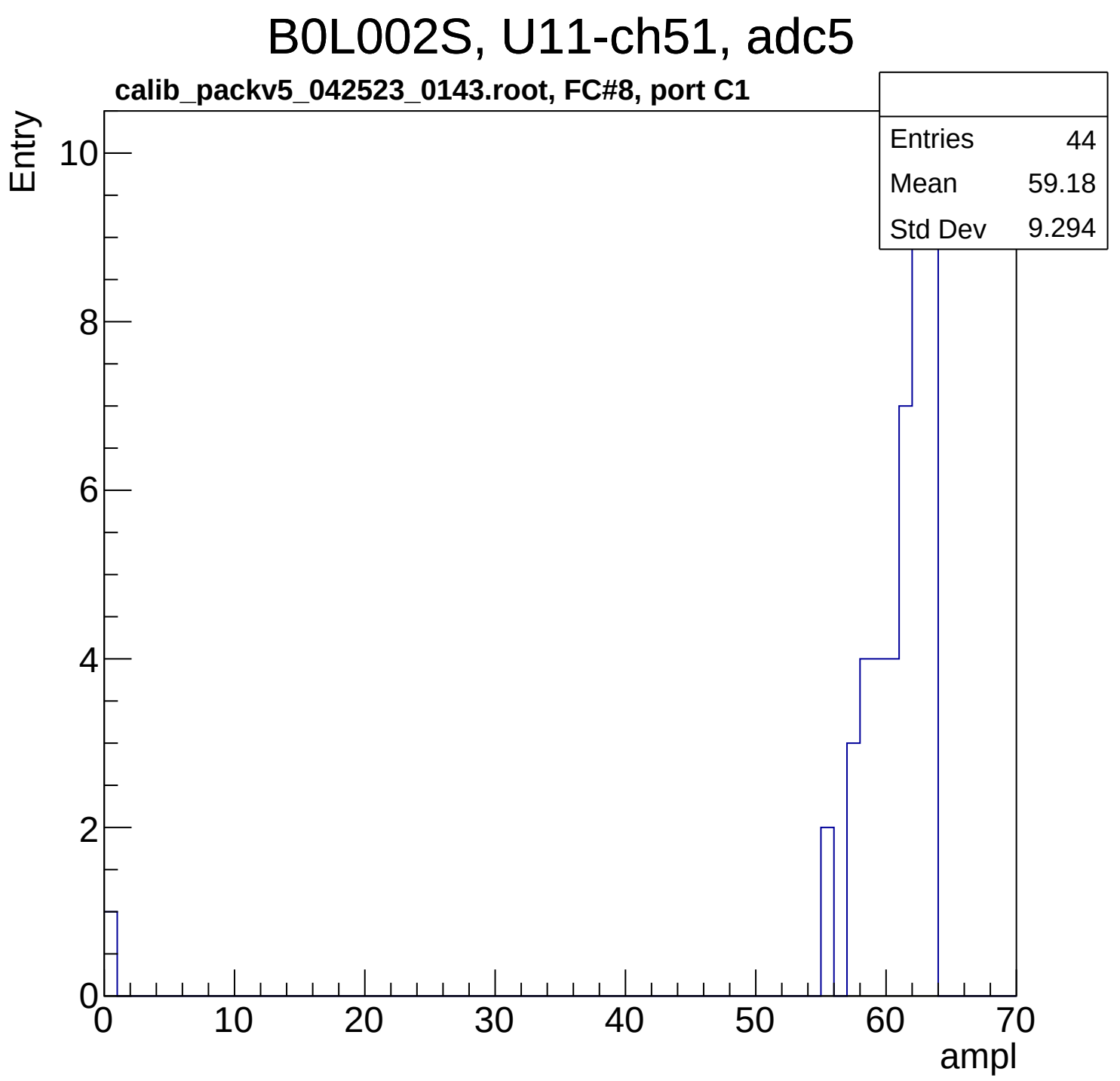
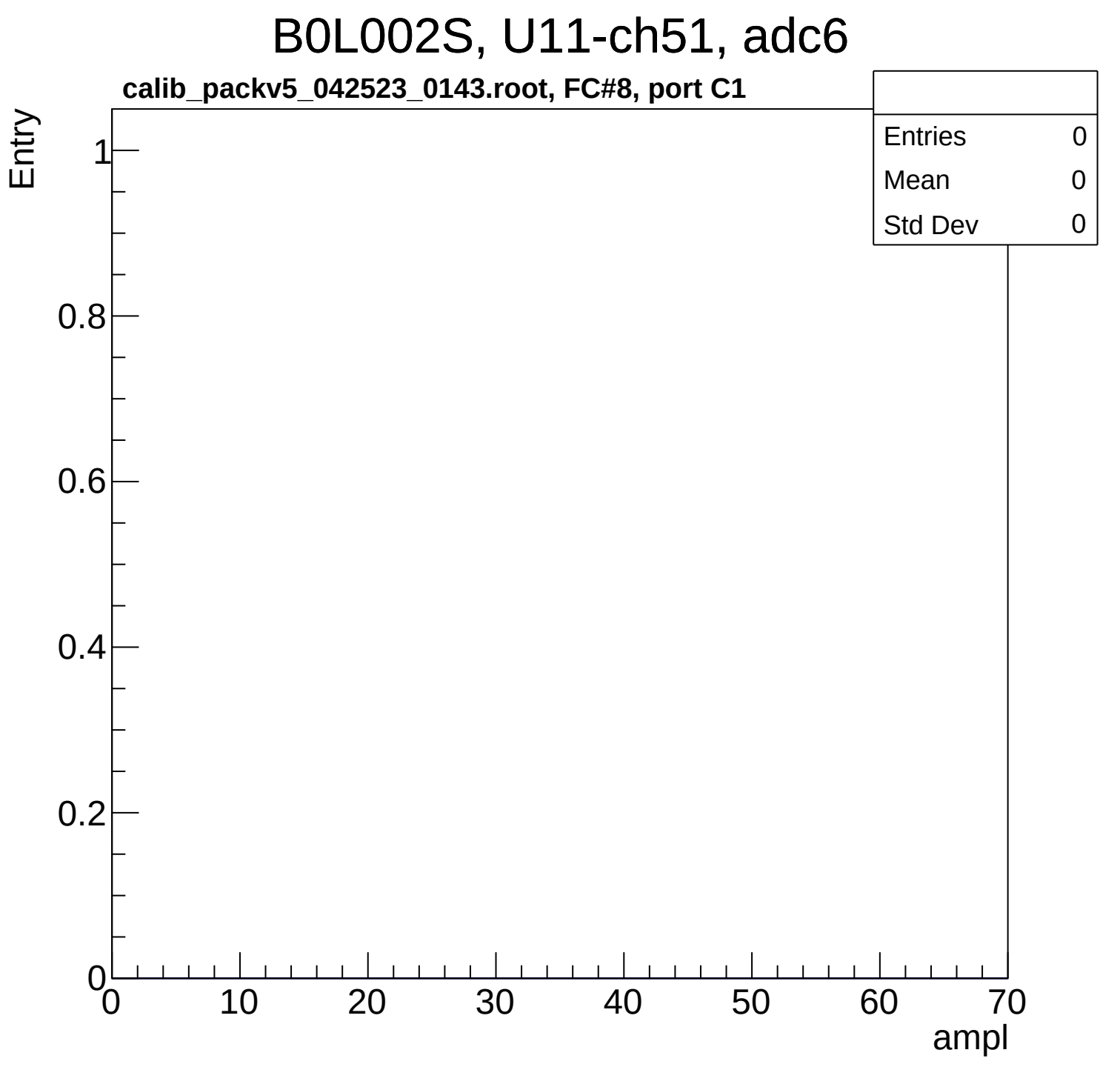

B0L002S, U11-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.71
Std Dev 3.393
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.29
Std Dev 3.164
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U11-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.18
Std Dev 9.294
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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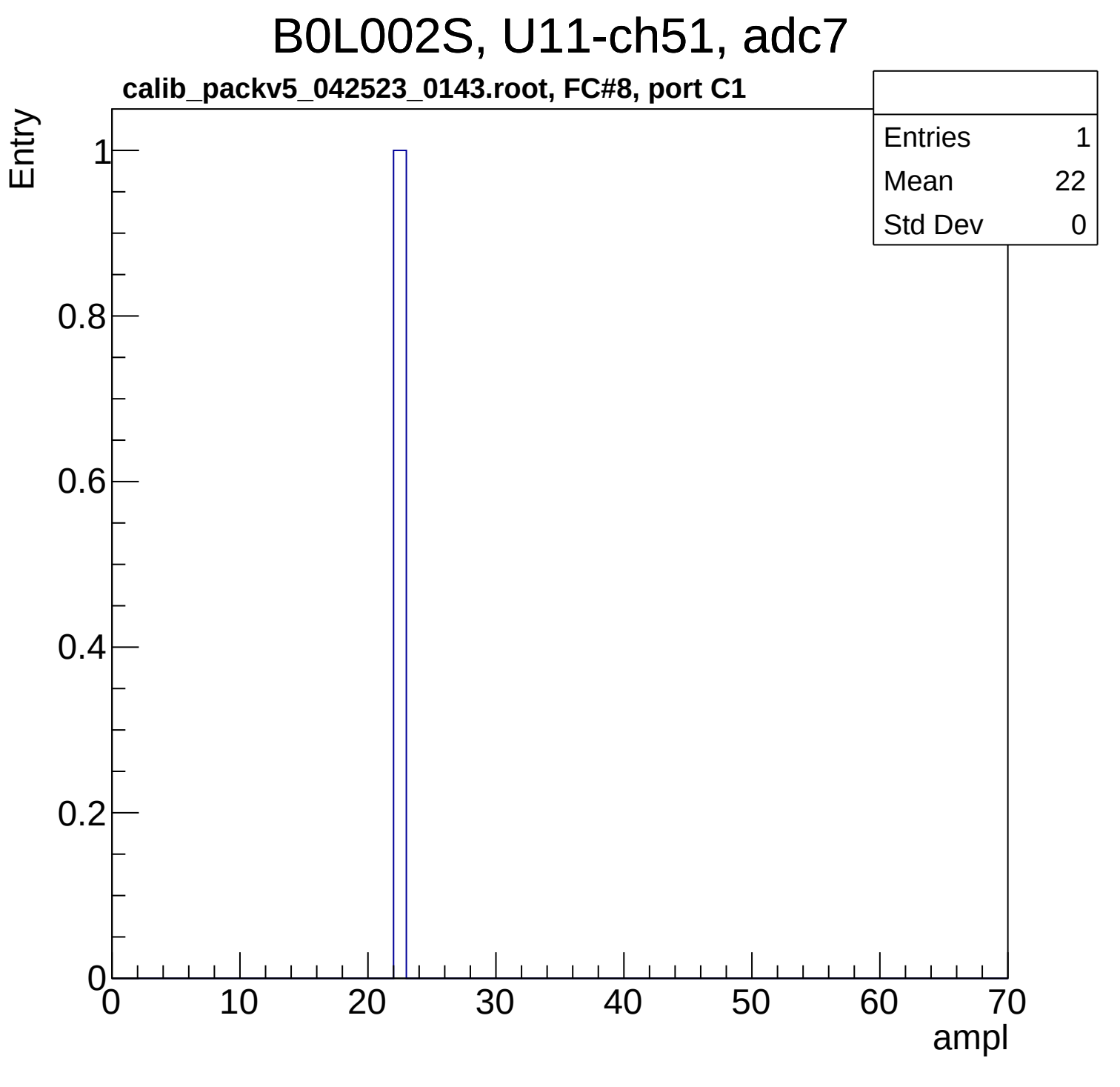
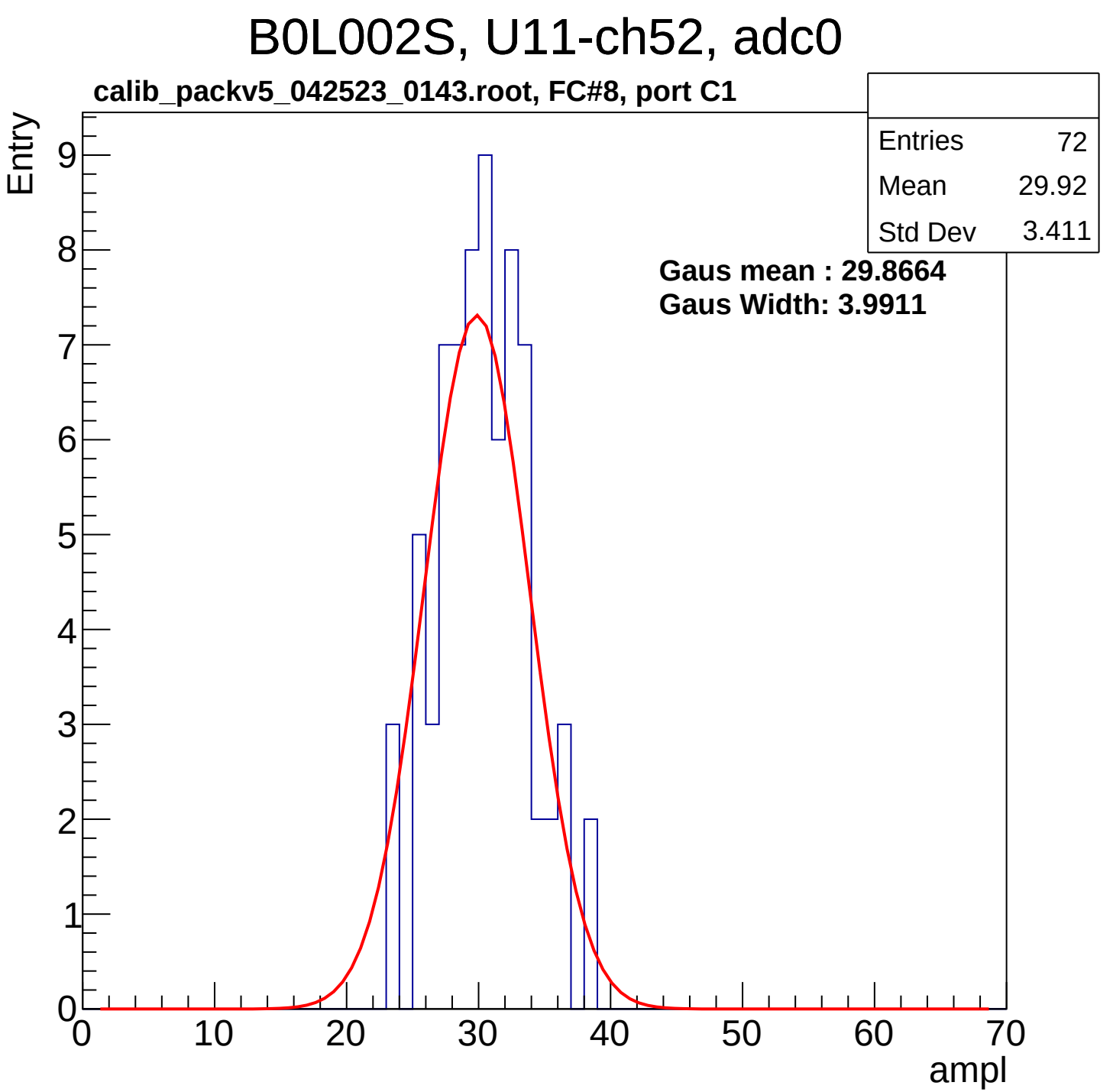
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.92
Std Dev 3.411
Gaus mean : 29.8664
Gaus Width: 3.9911
Entry
ampl
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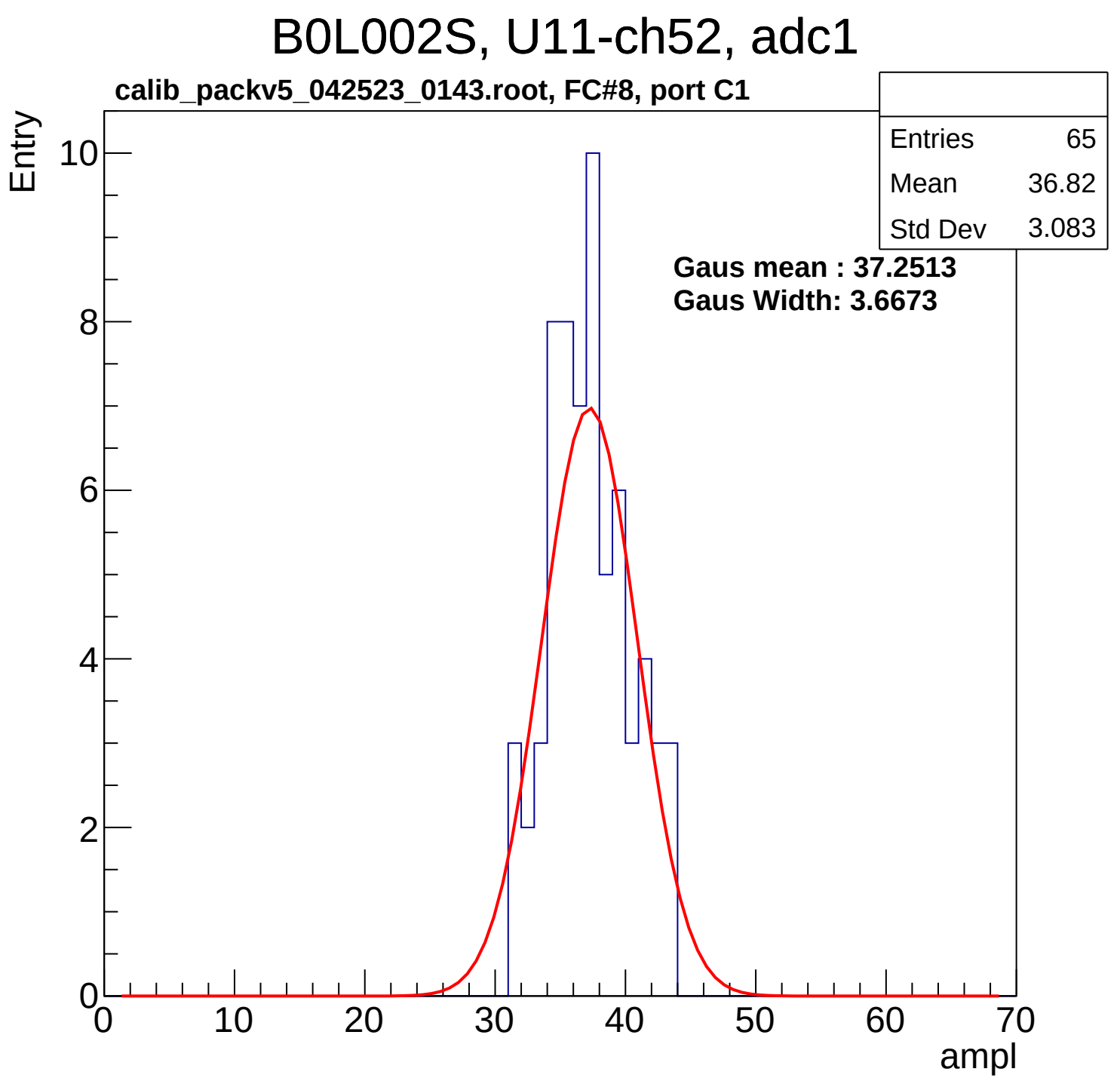
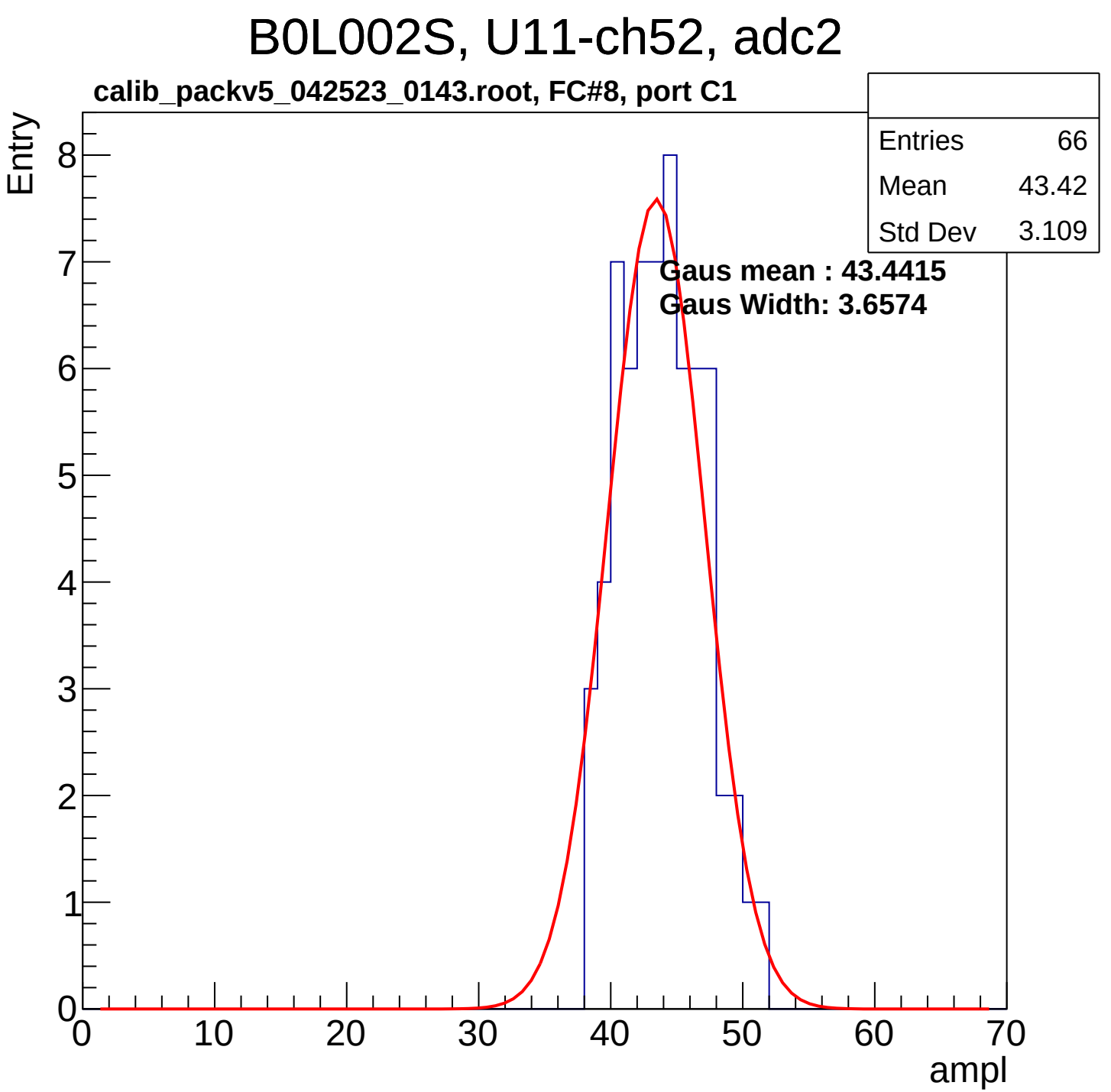
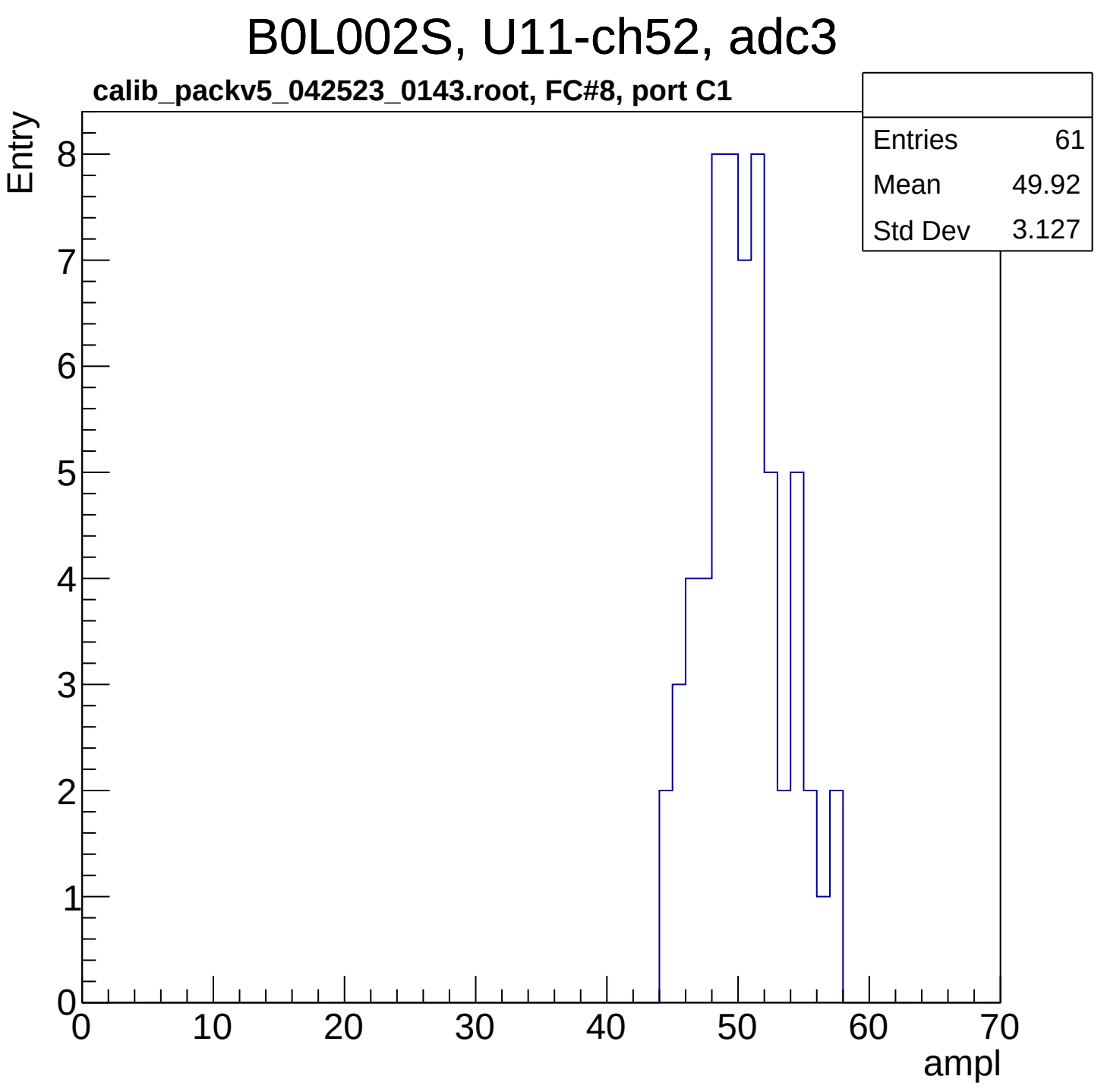
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.82
Std Dev 3.083
Gaus mean : 37.2513
Gaus Width: 3.6673
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.42
Std Dev 3.109
Gaus mean : 43.4415
Gaus Width: 3.6574
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 49.92
Std Dev 3.127
Entry
ampl
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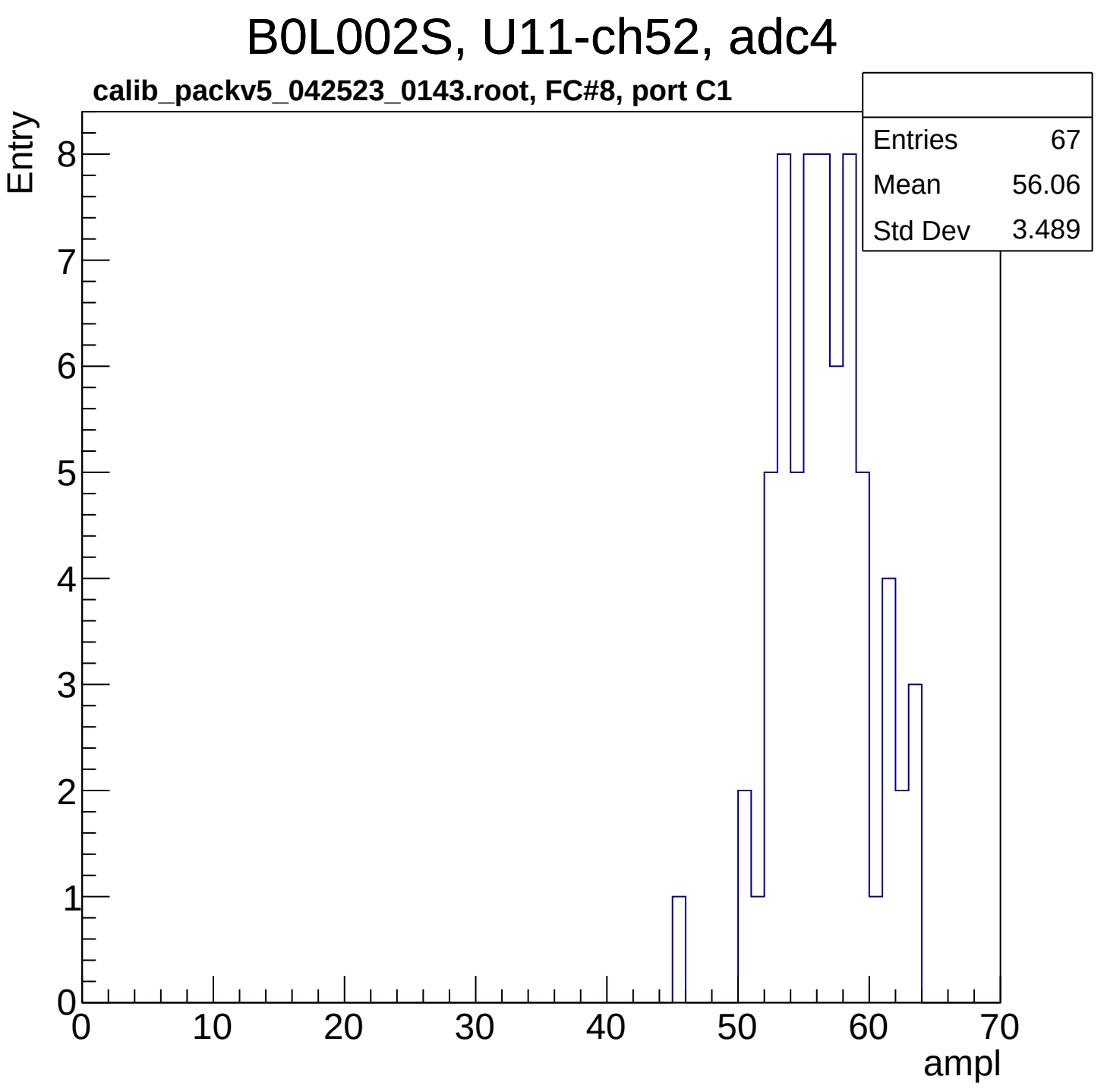
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.06
Std Dev 3.489
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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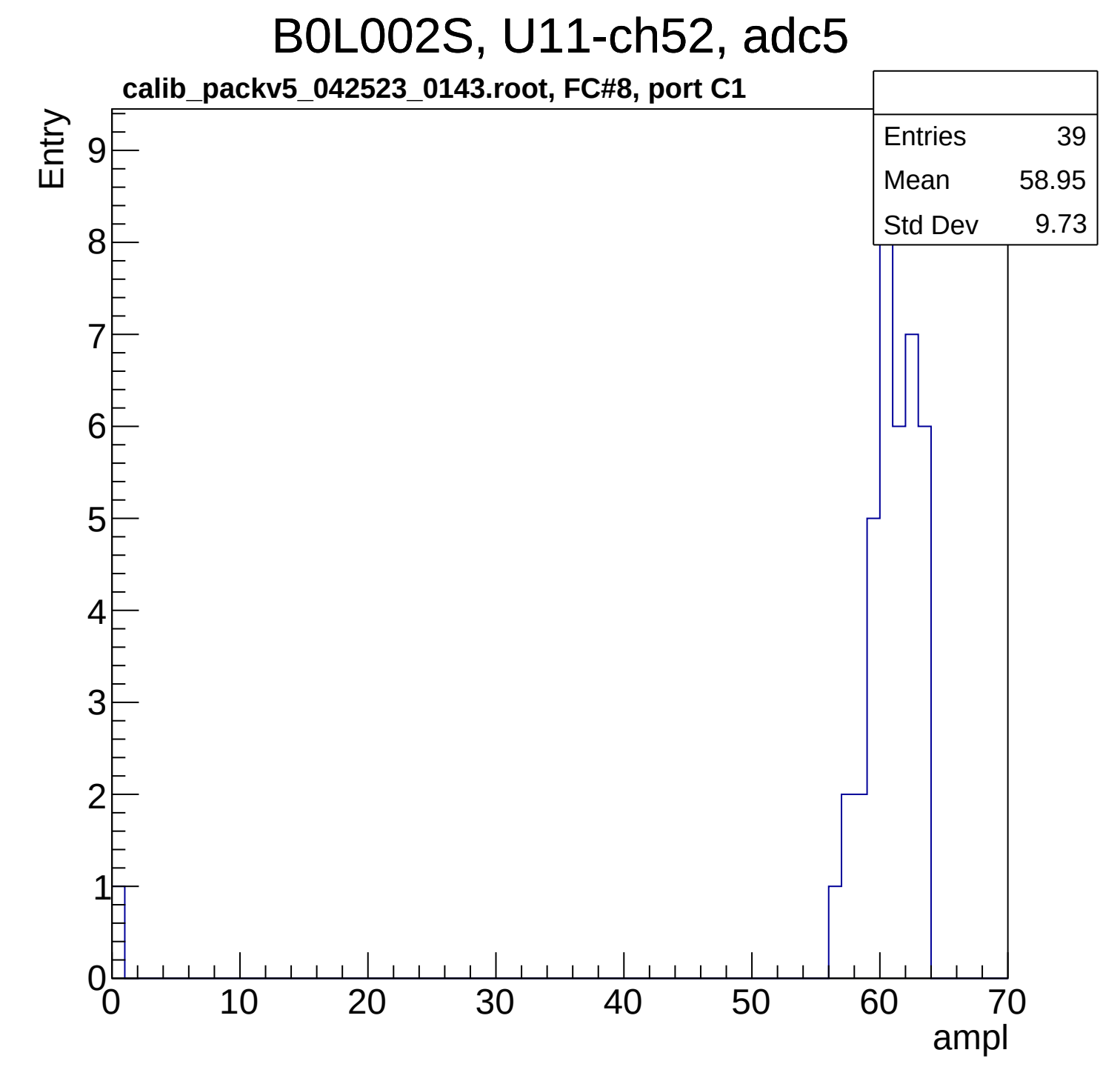
40
50
60
70


B0L002S, U11-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.95
Std Dev 9.73
Entry
ampl
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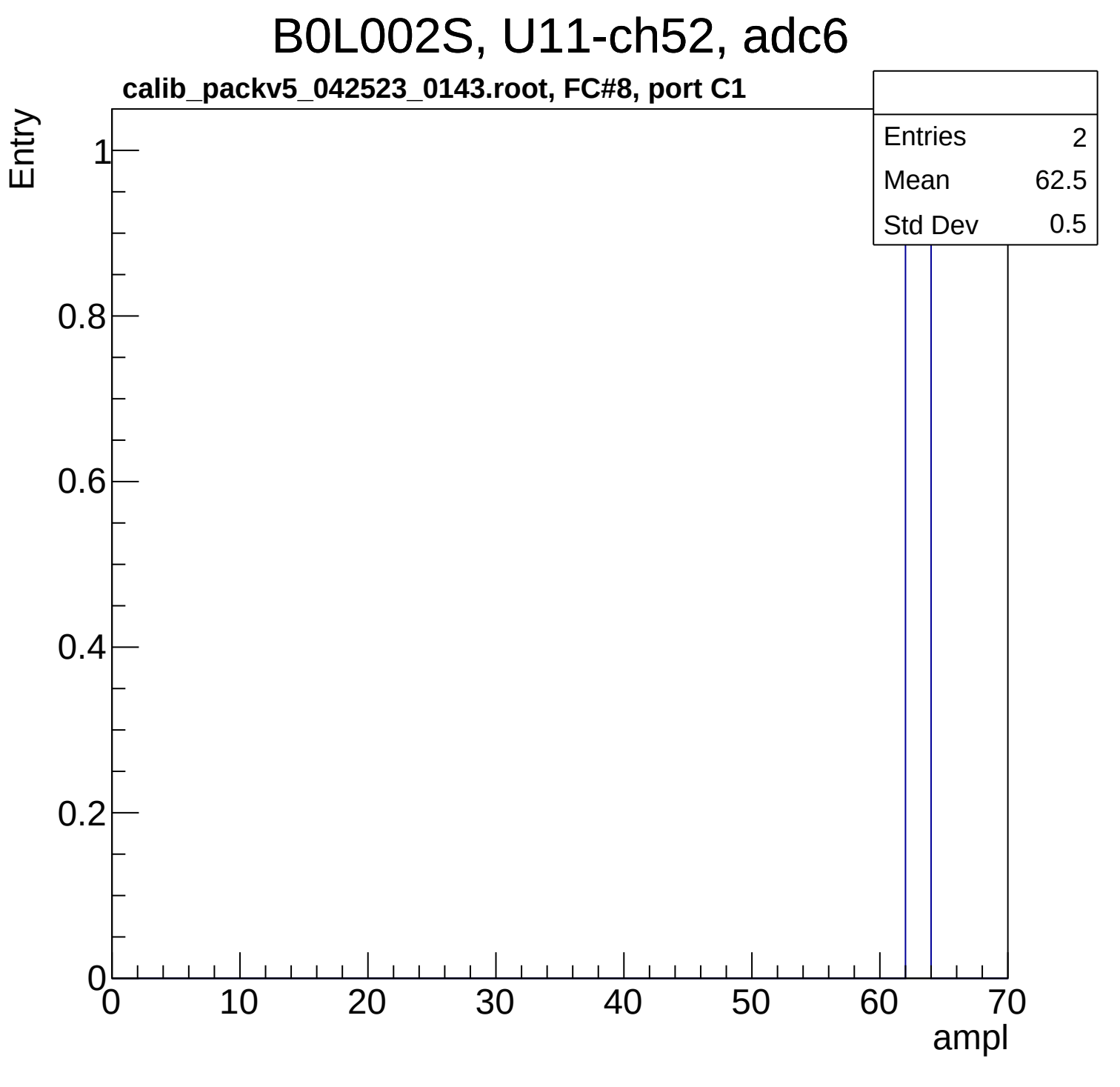
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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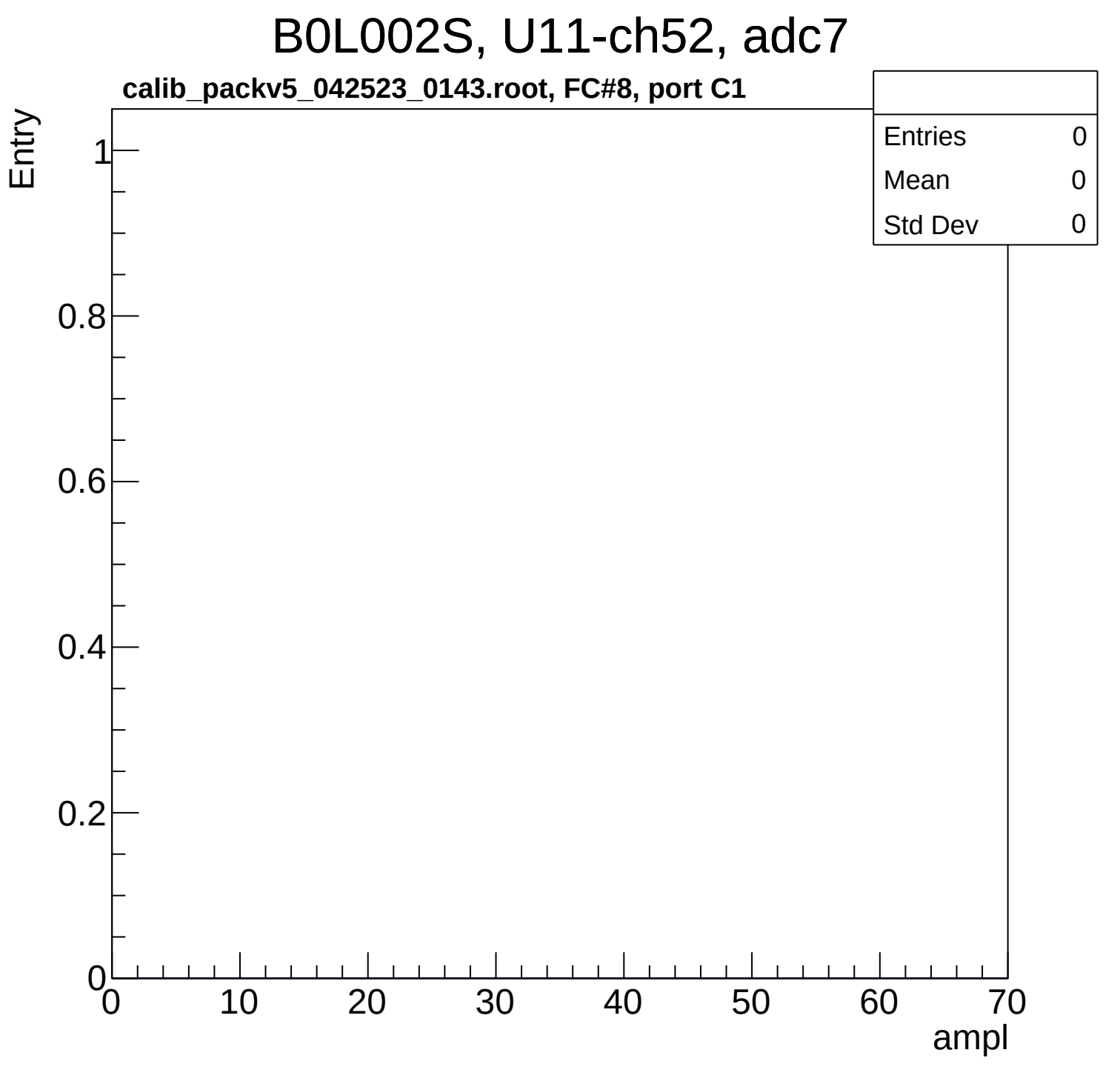
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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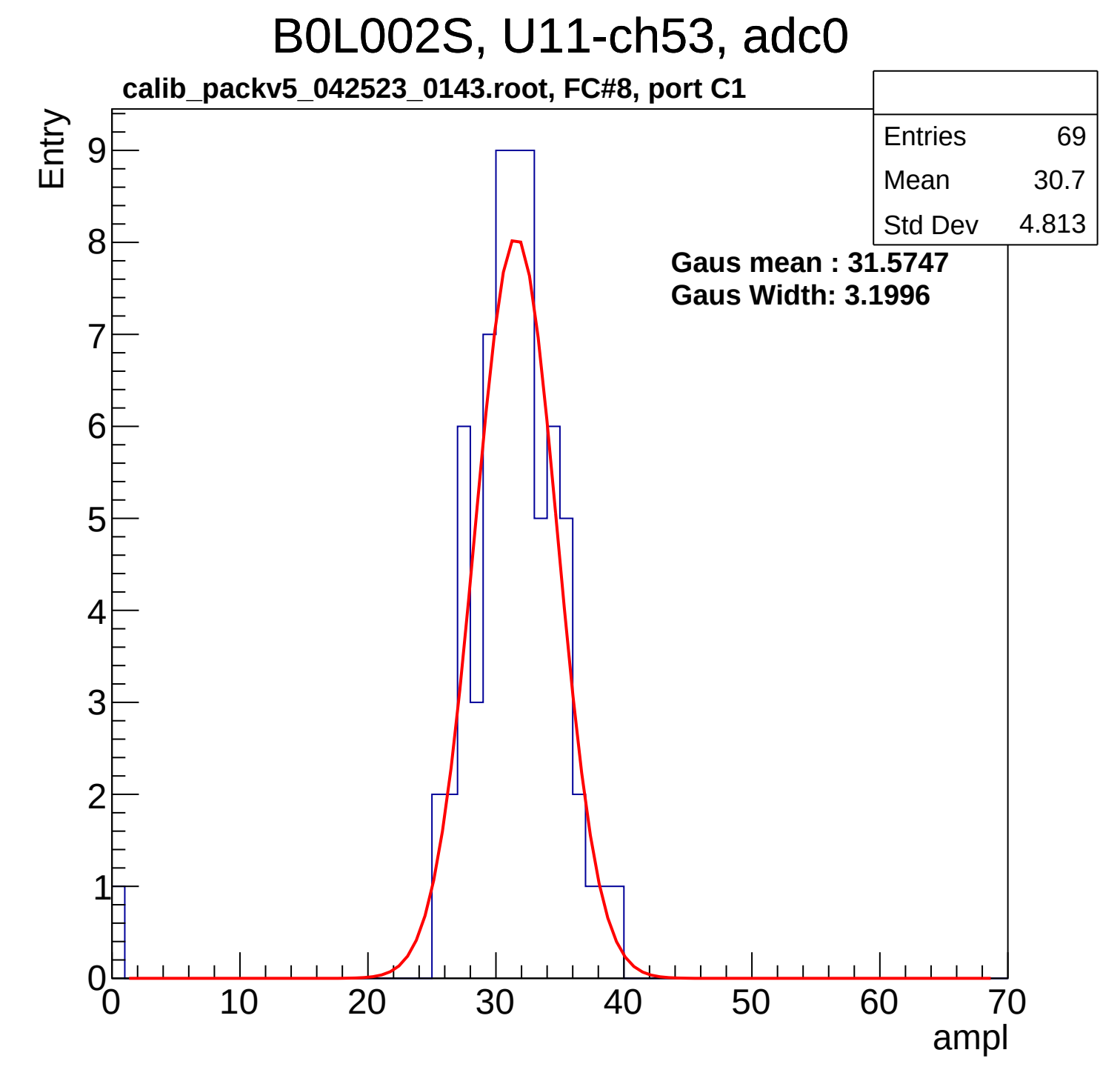
50
60
70
ampl


B0L002S, U11-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.7
Std Dev 4.813
Gaus mean : 31.5747
Gaus Width: 3.1996
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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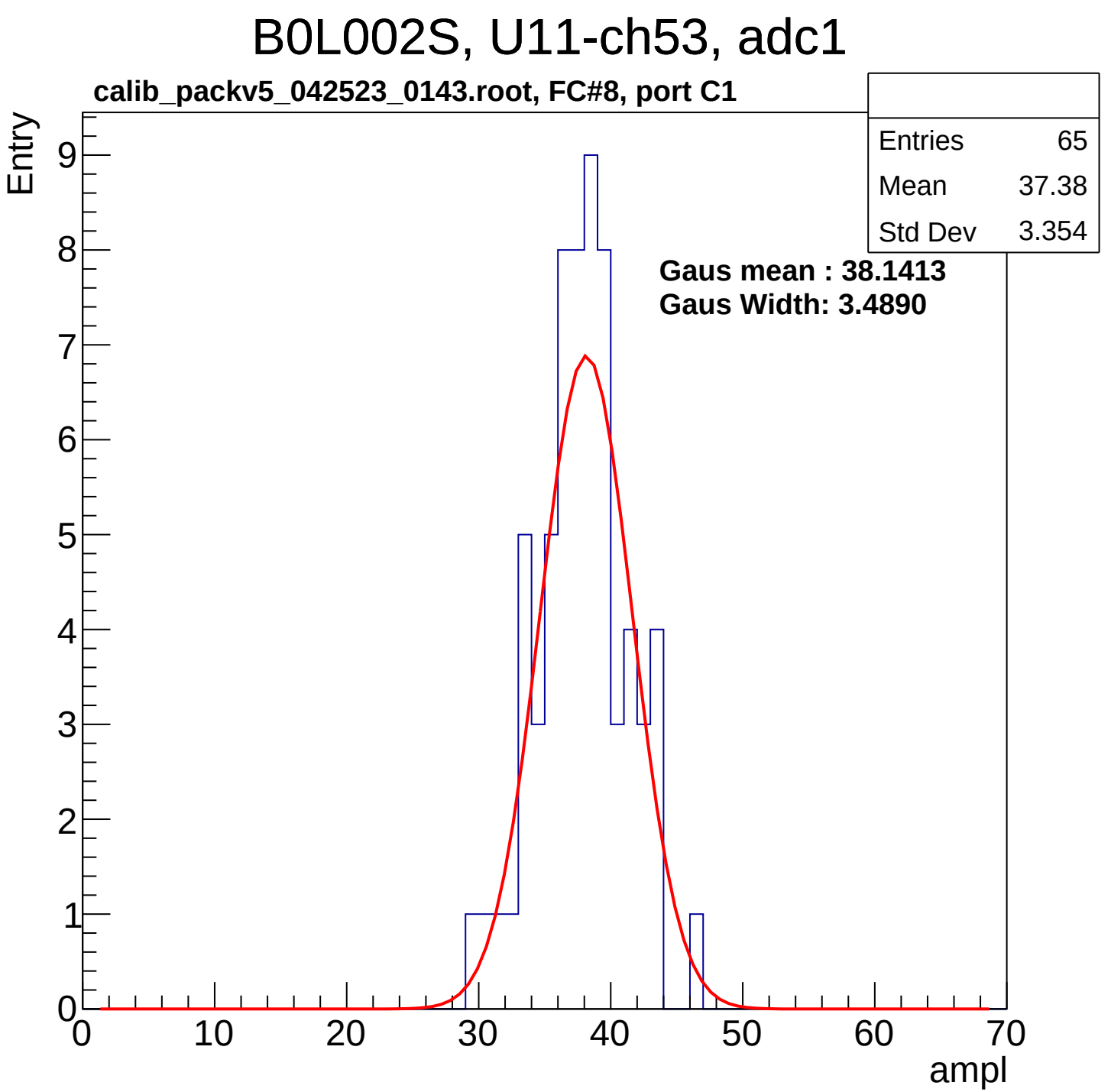
B0L002S, U11-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.38
Std Dev 3.354
Gaus mean : 38.1413
Gaus Width: 3.4890
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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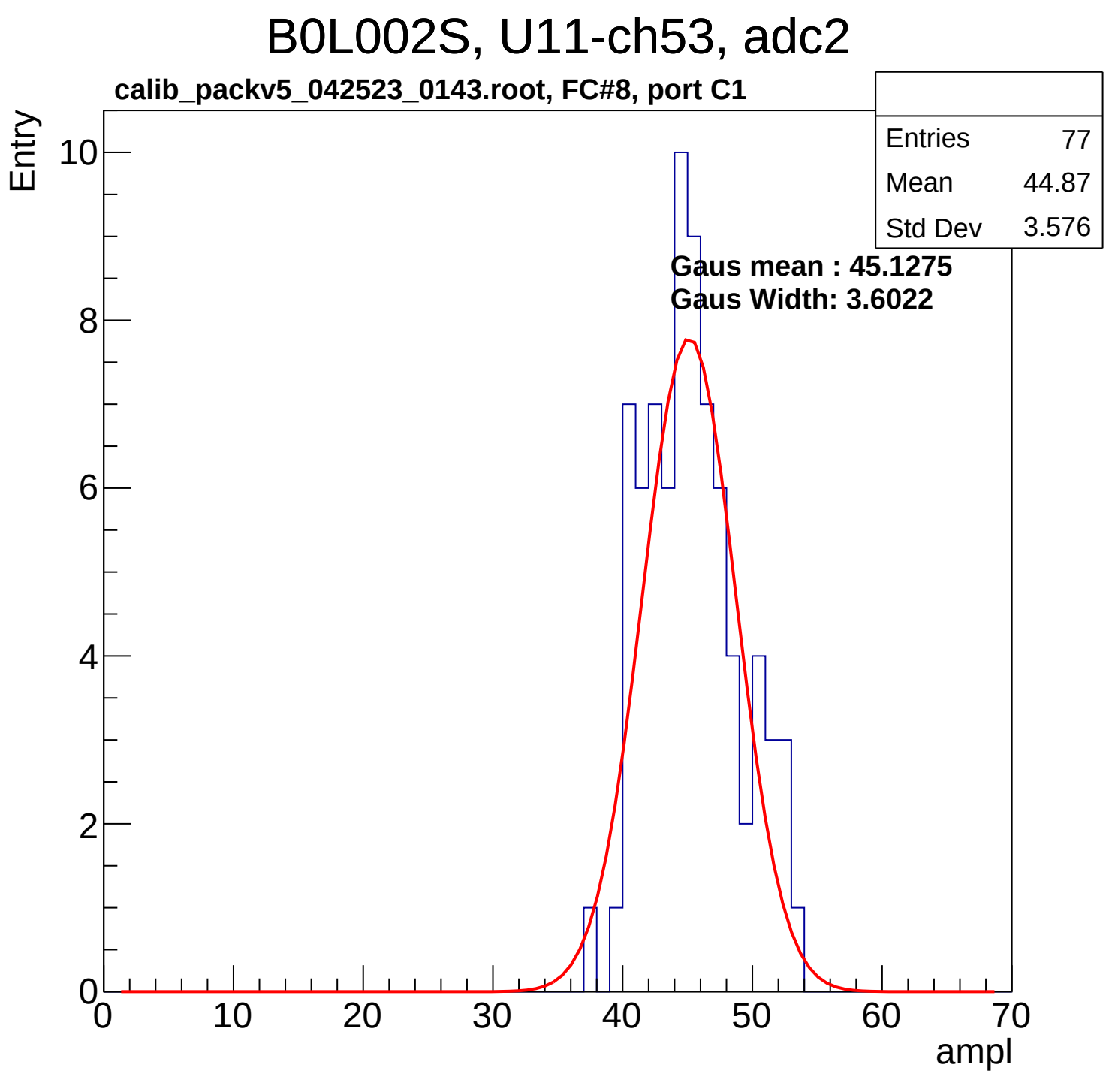
B0L002S, U11-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 44.87
Std Dev 3.576
Gaus mean : 45.1275
Gaus Width: 3.6022
Entry
ampl
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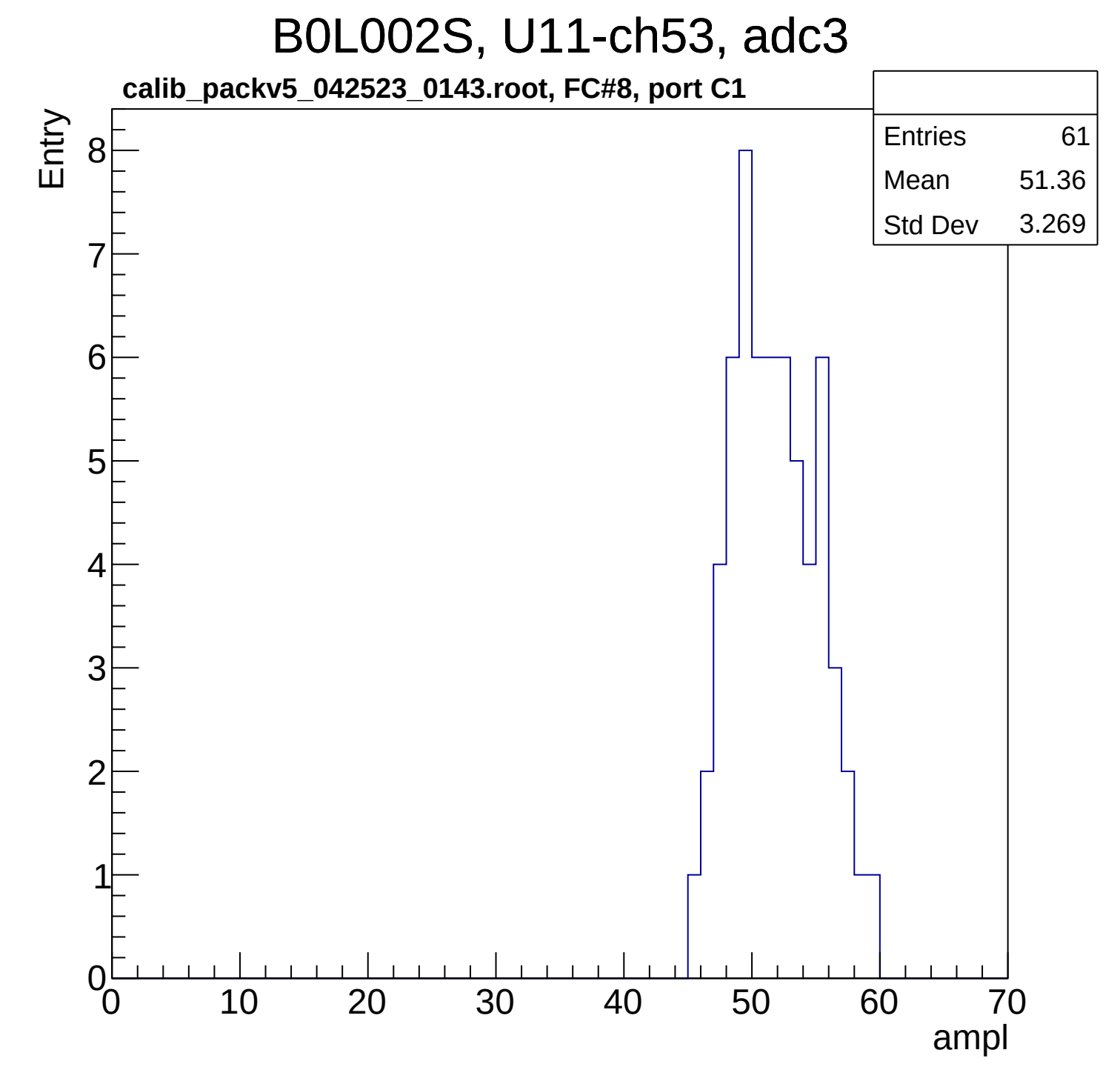
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.36
Std Dev 3.269
Entry
ampl
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0
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50
60
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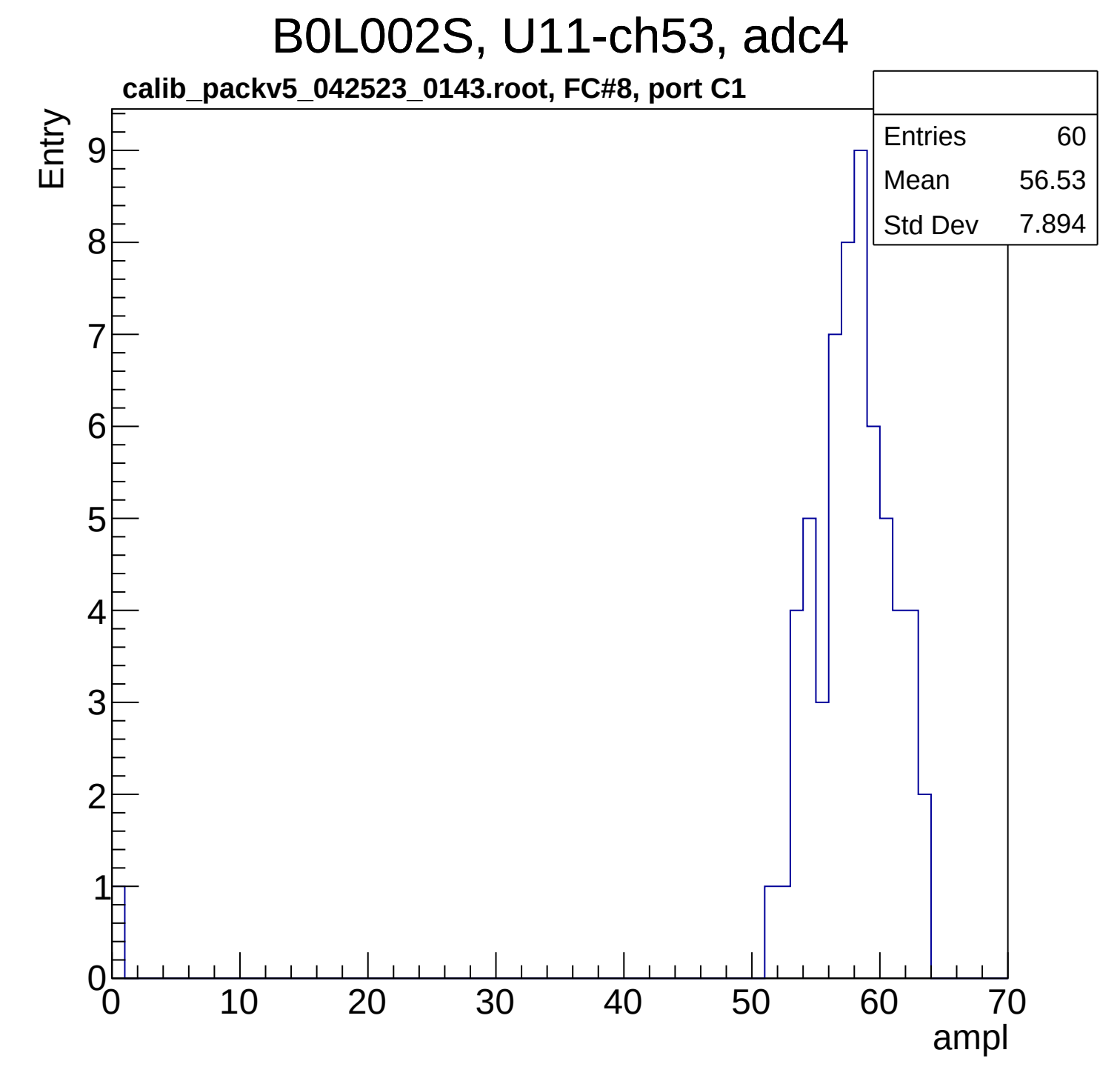
B0L002S, U11-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.53
Std Dev 7.894
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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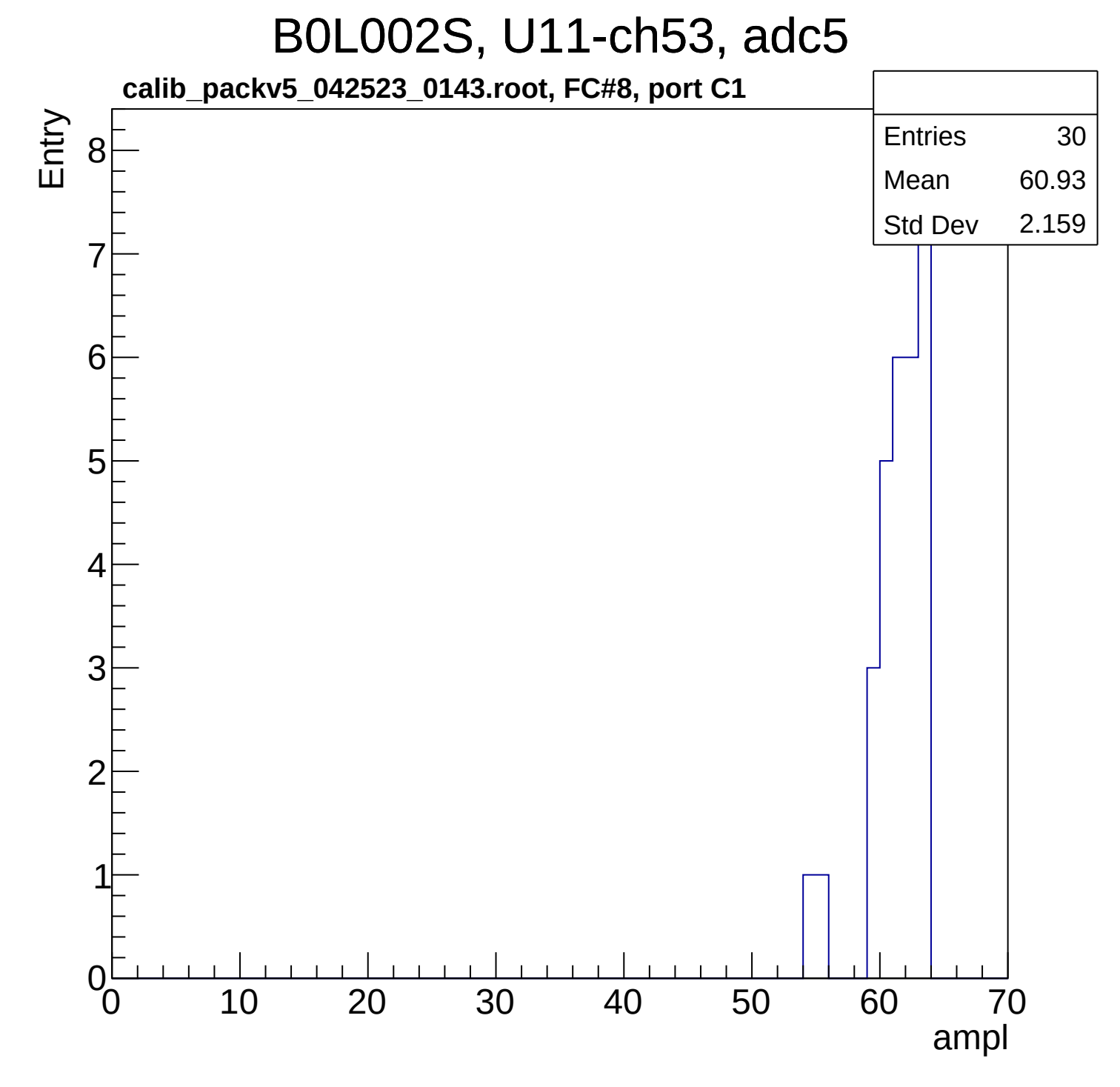
B0L002S, U11-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 60.93
Std Dev 2.159
Entry
ampl
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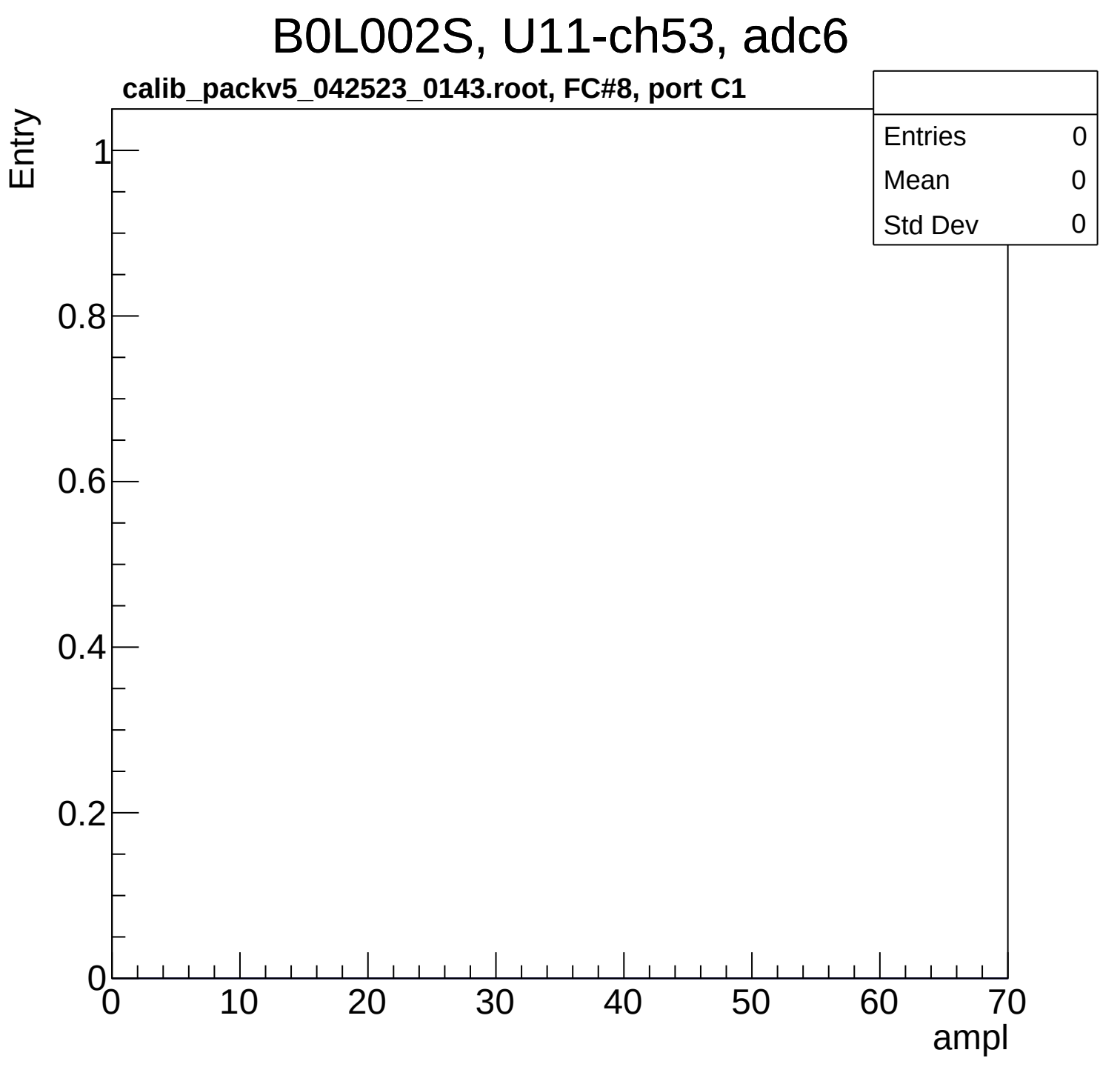
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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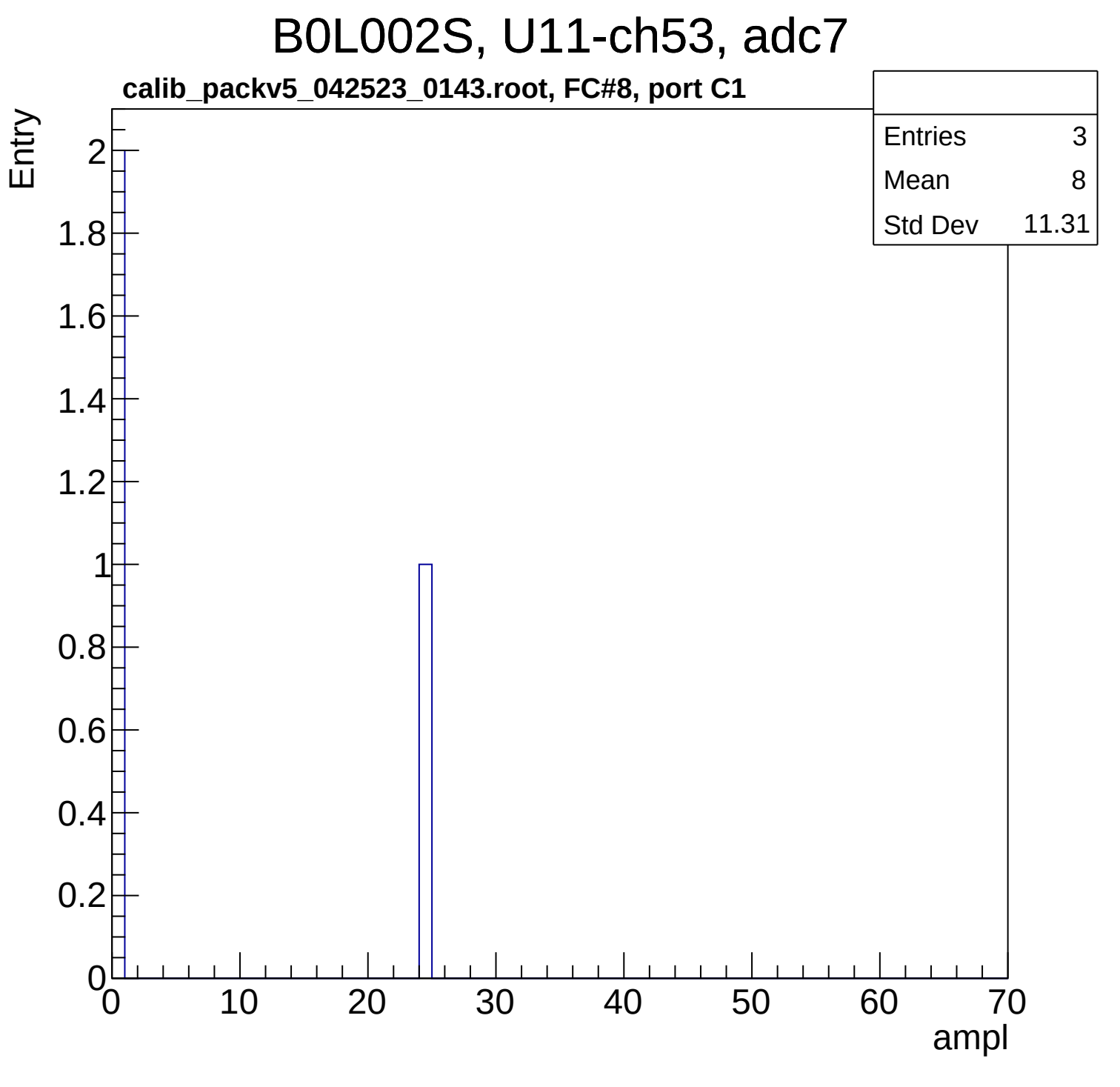
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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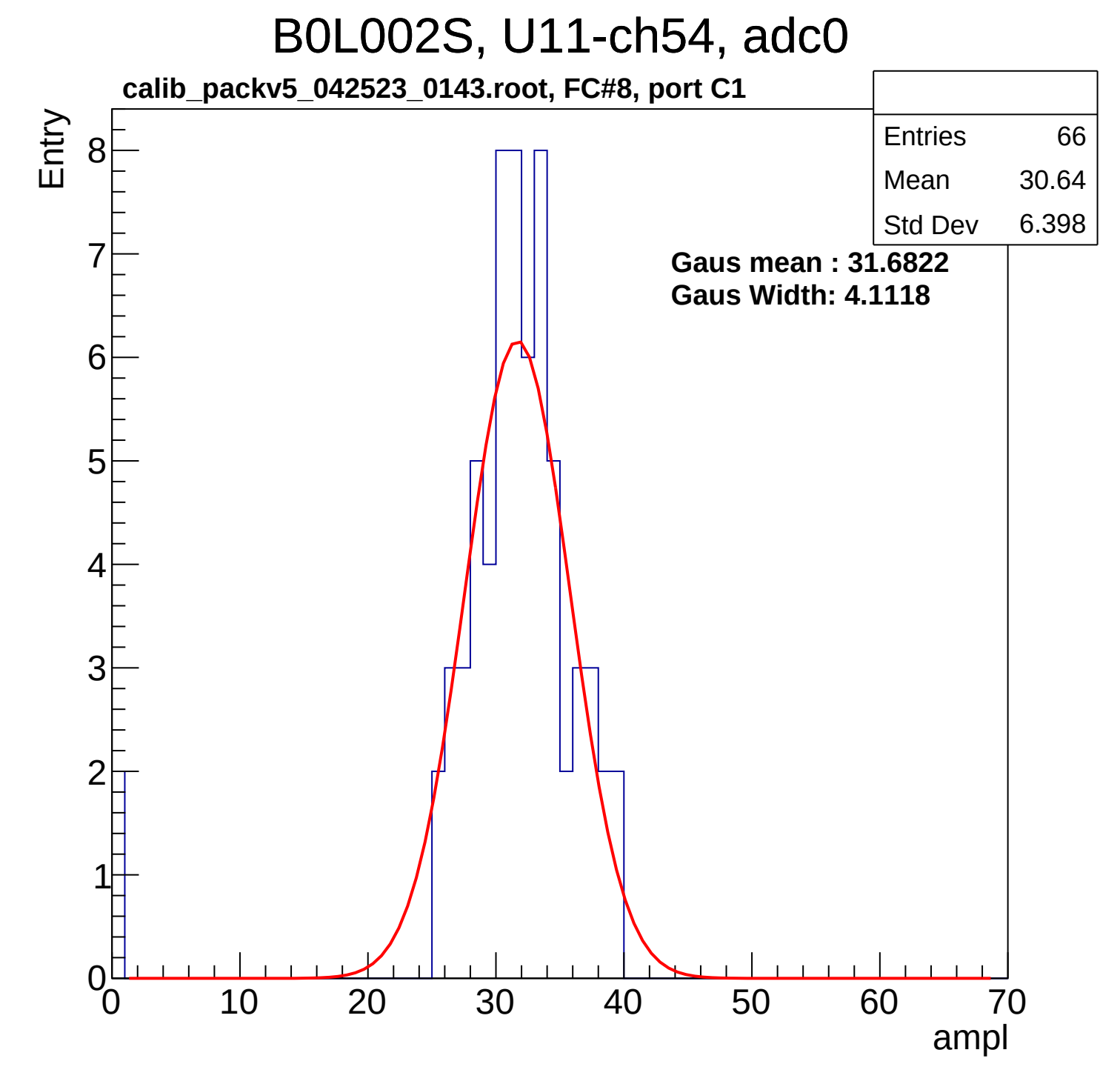
B0L002S, U11-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.64
Std Dev 6.398
Gaus mean : 31.6822
Gaus Width: 4.1118
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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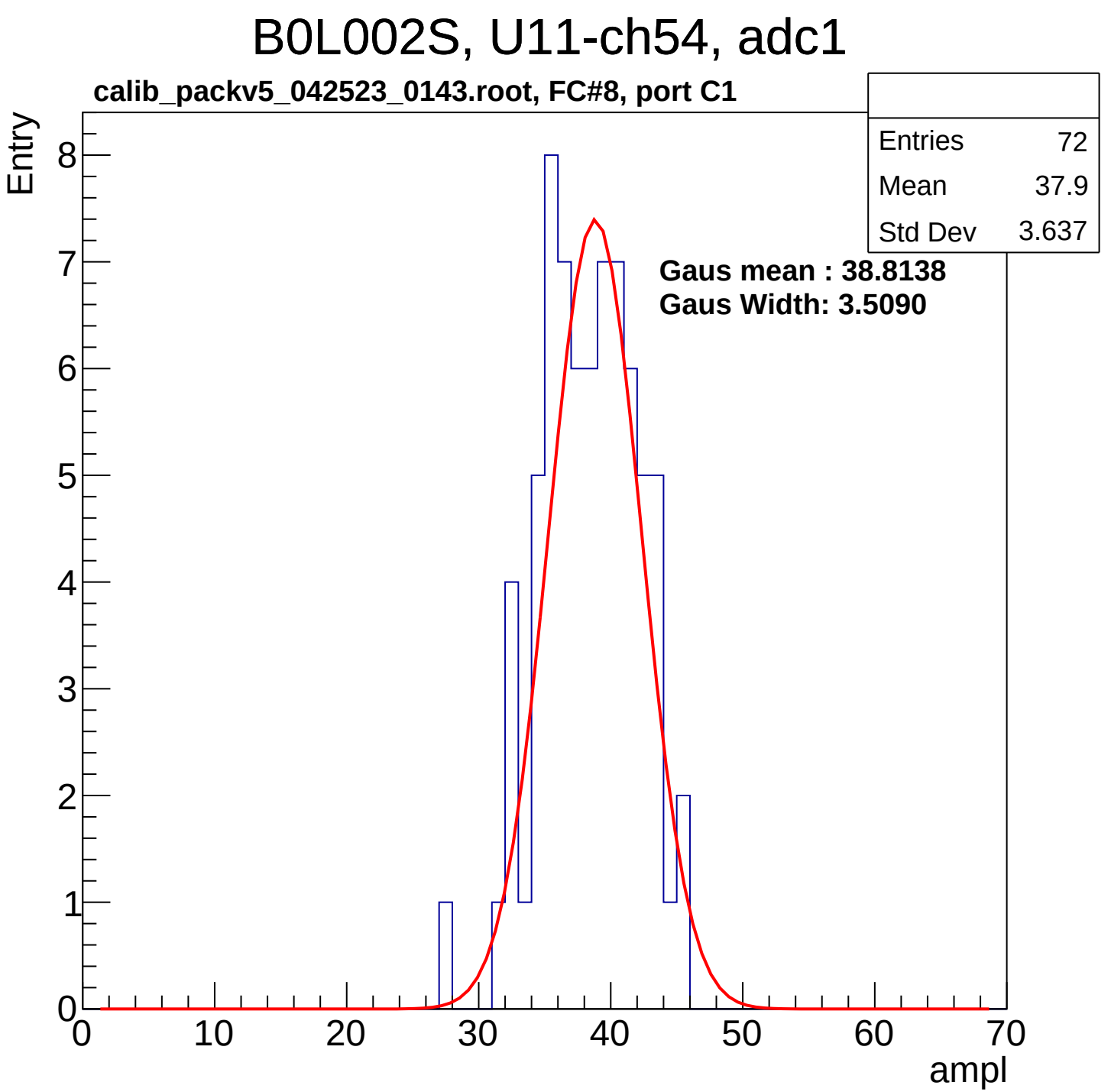
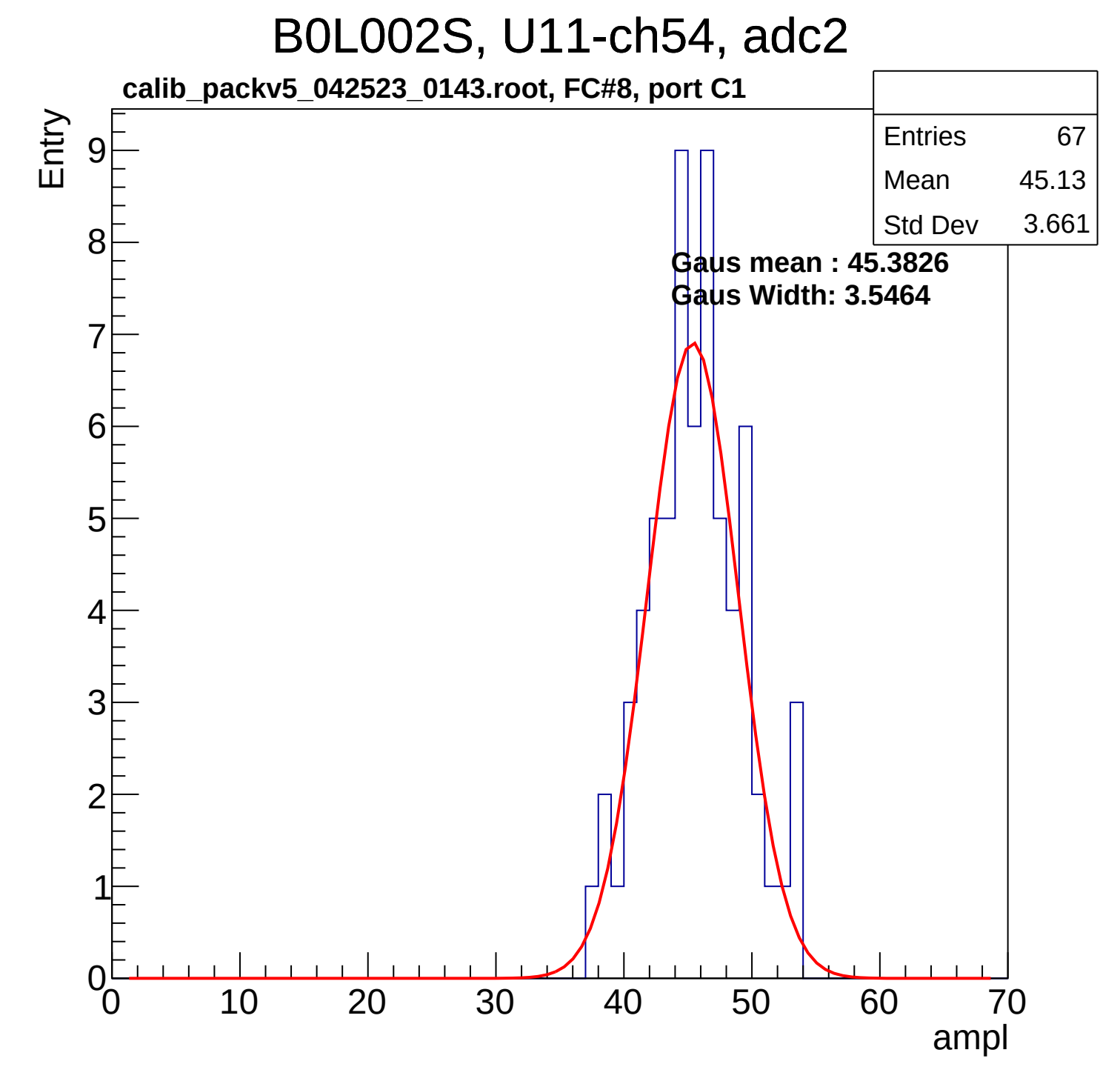

B0L002S, U11-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.9
Std Dev 3.637
Gaus mean : 38.8138
Gaus Width: 3.5090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.13
Std Dev 3.661
Gaus mean : 45.3826
Gaus Width: 3.5464
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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40
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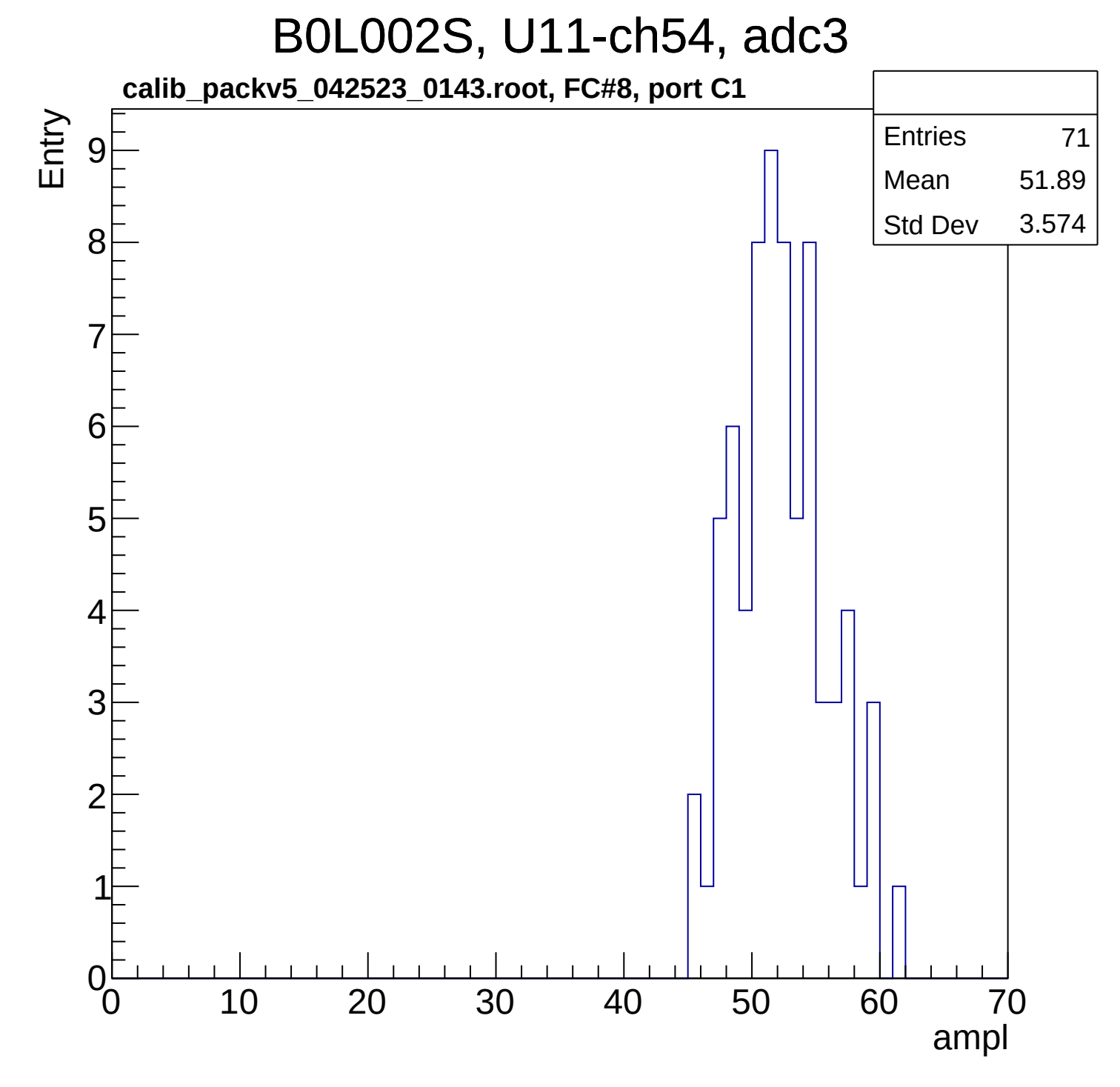
B0L002S, U11-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.89
Std Dev 3.574
Entry
ampl
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2
3
4
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6
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8
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0
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60
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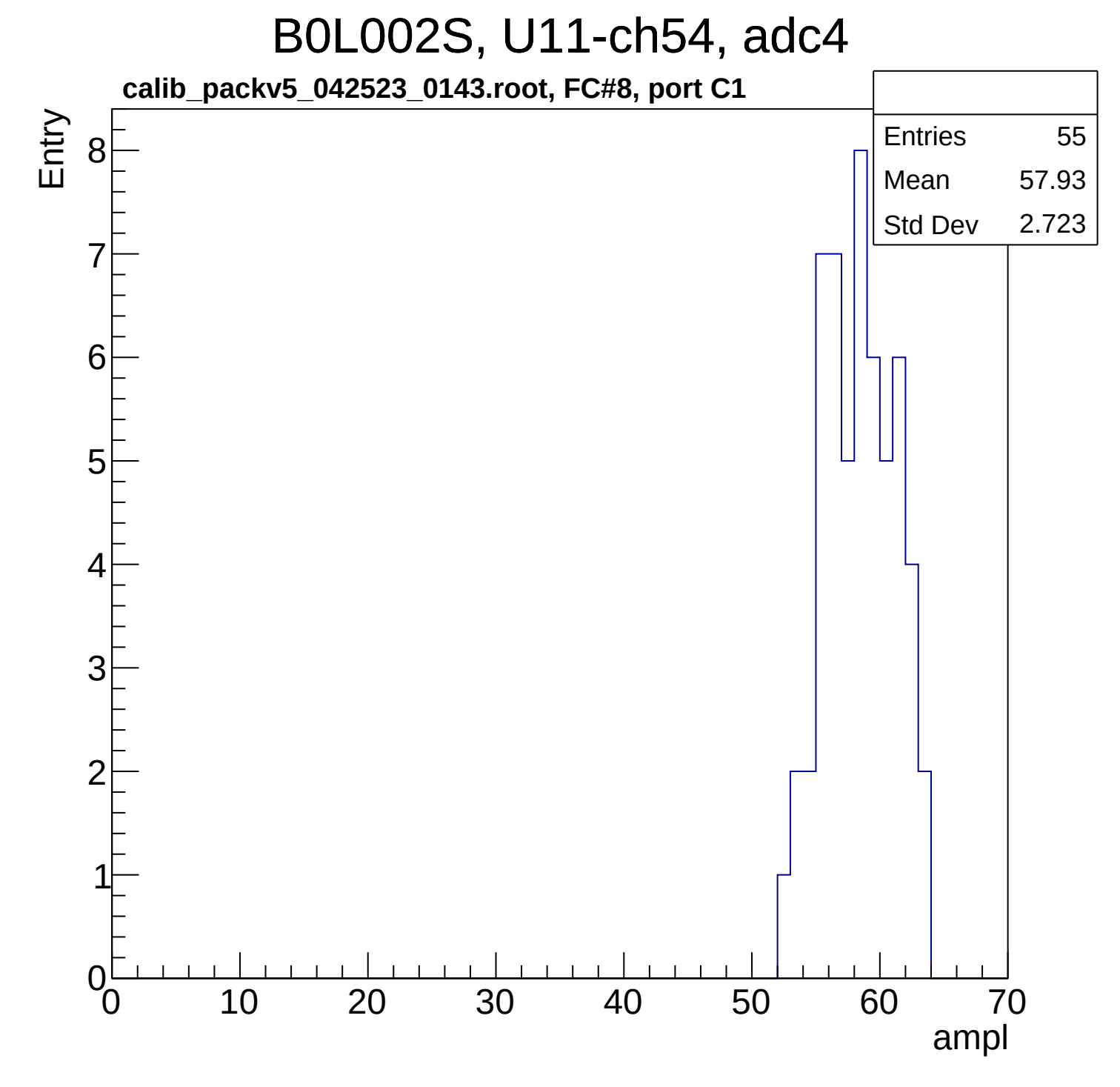
B0L002S, U11-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.93
Std Dev 2.723
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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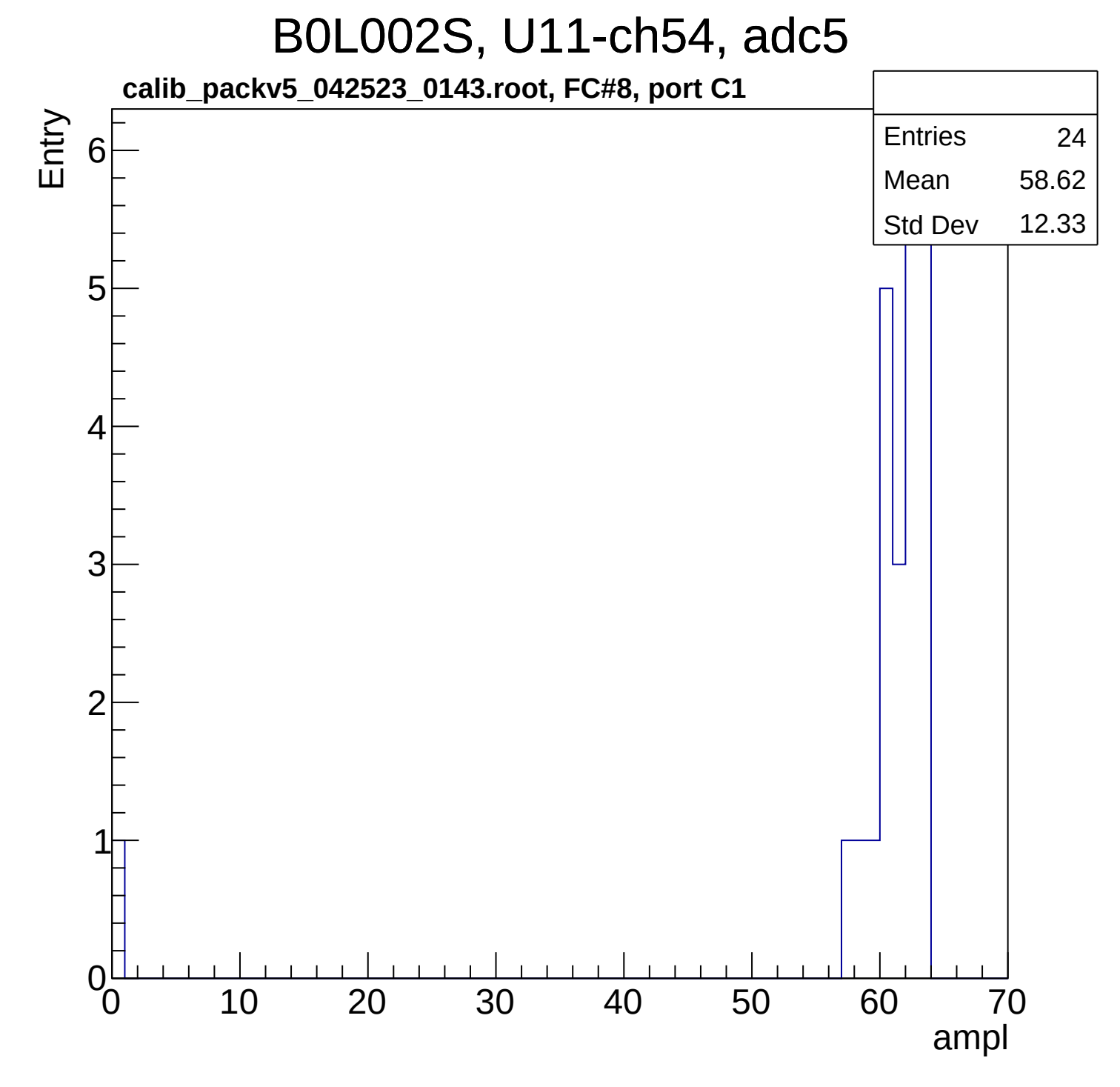
B0L002S, U11-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.62
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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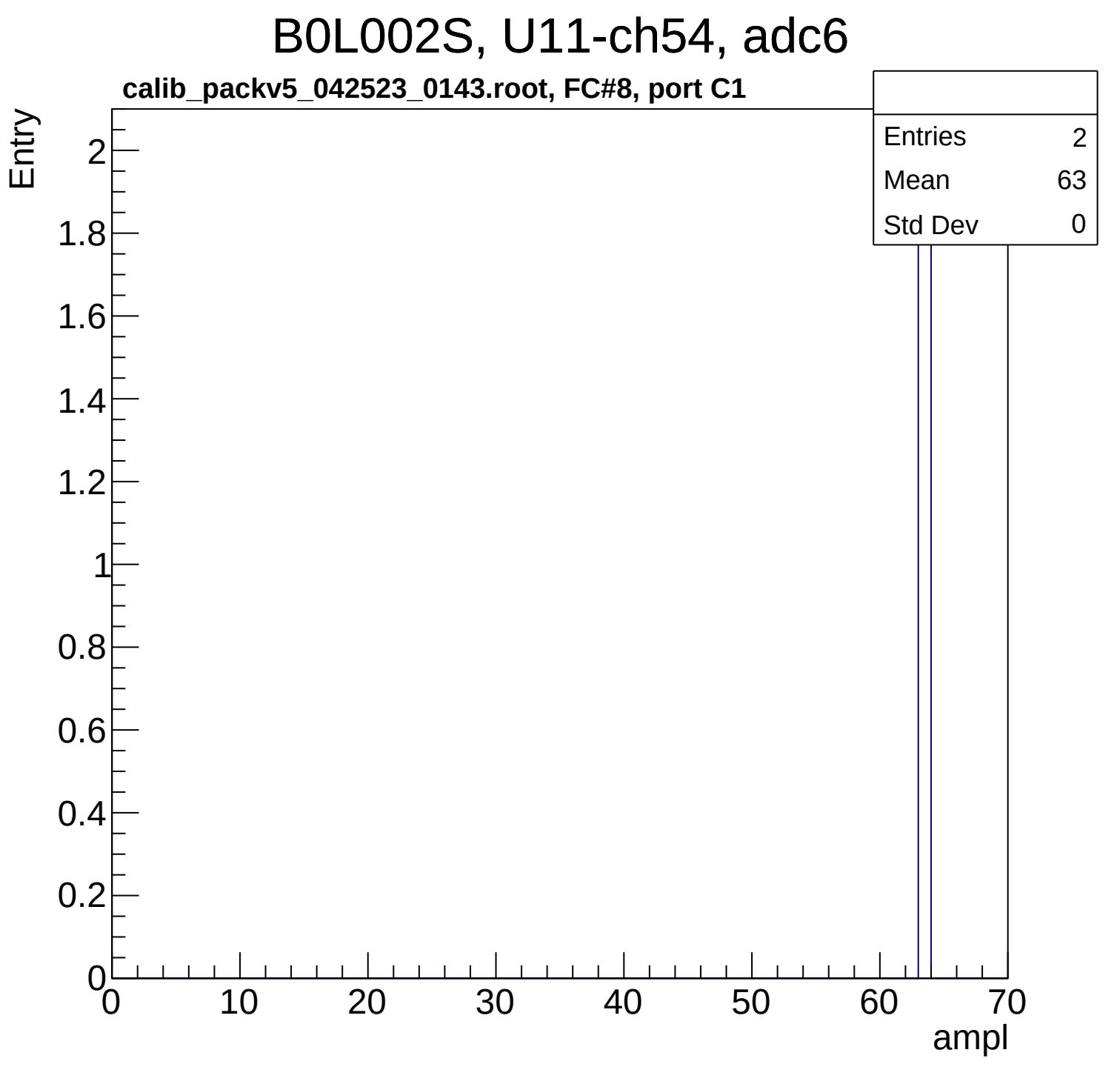
B0L002S, U11-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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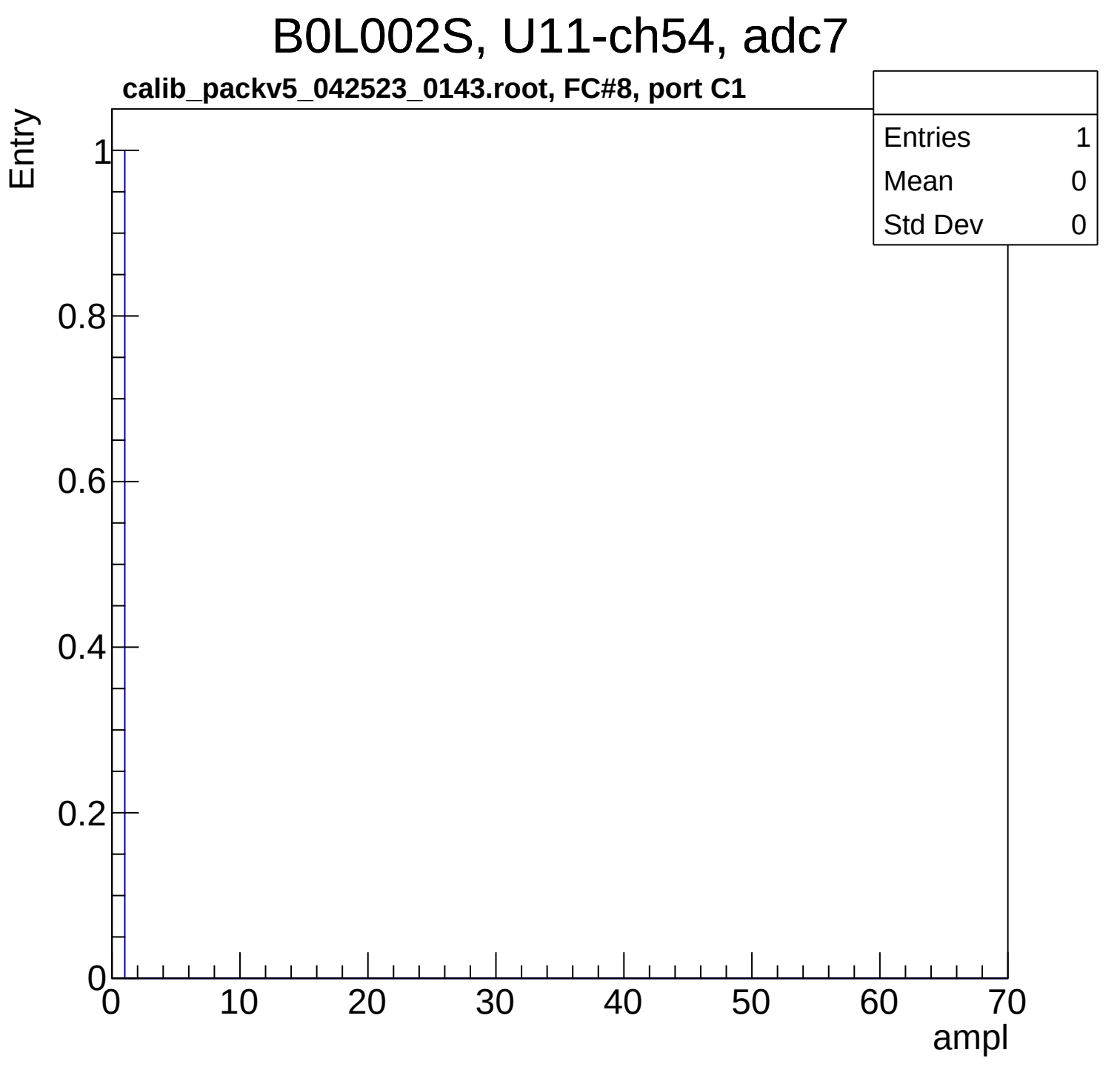
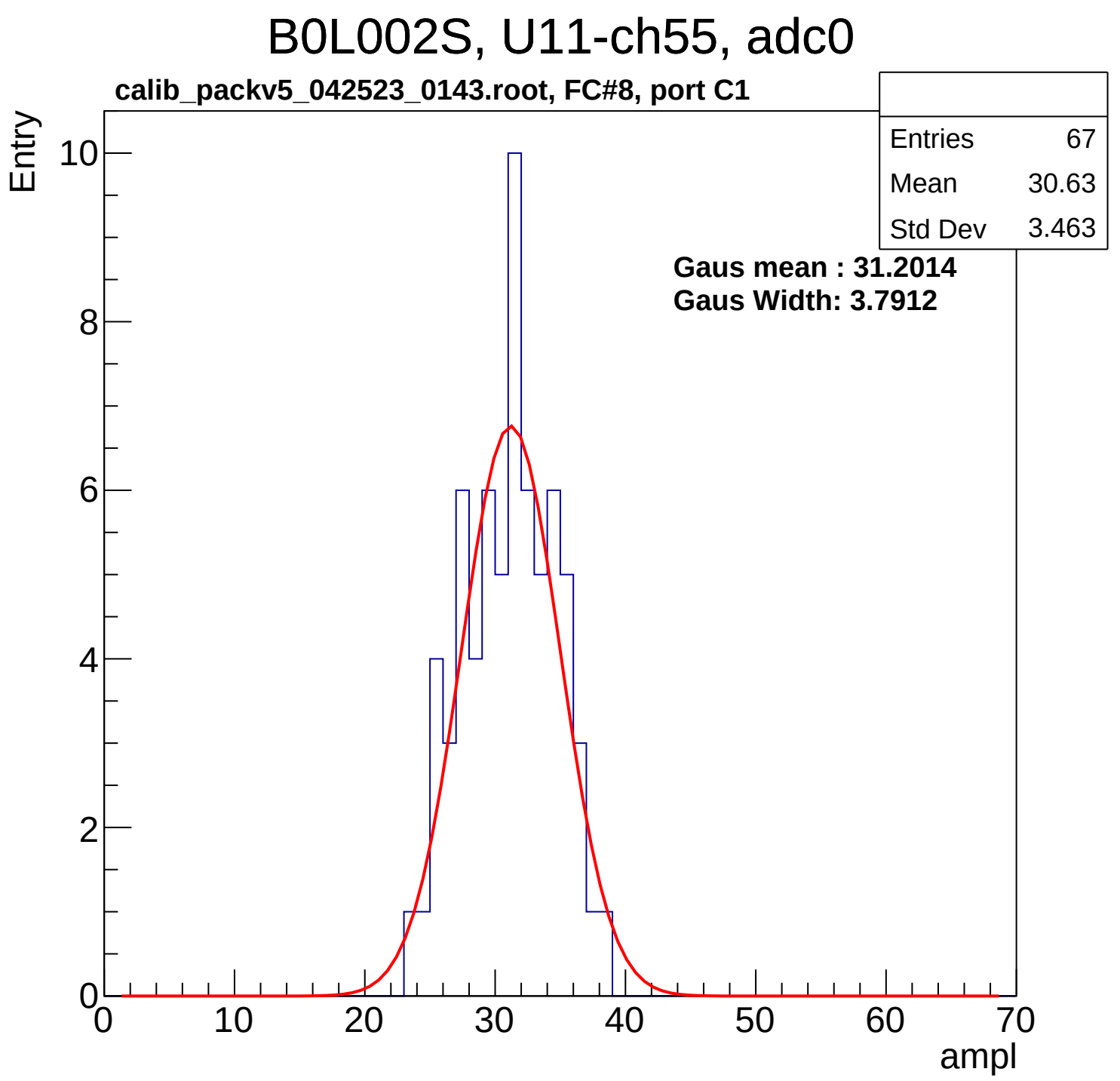

B0L002S, U11-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.63
Std Dev 3.463
Gaus mean : 31.2014
Gaus Width: 3.7912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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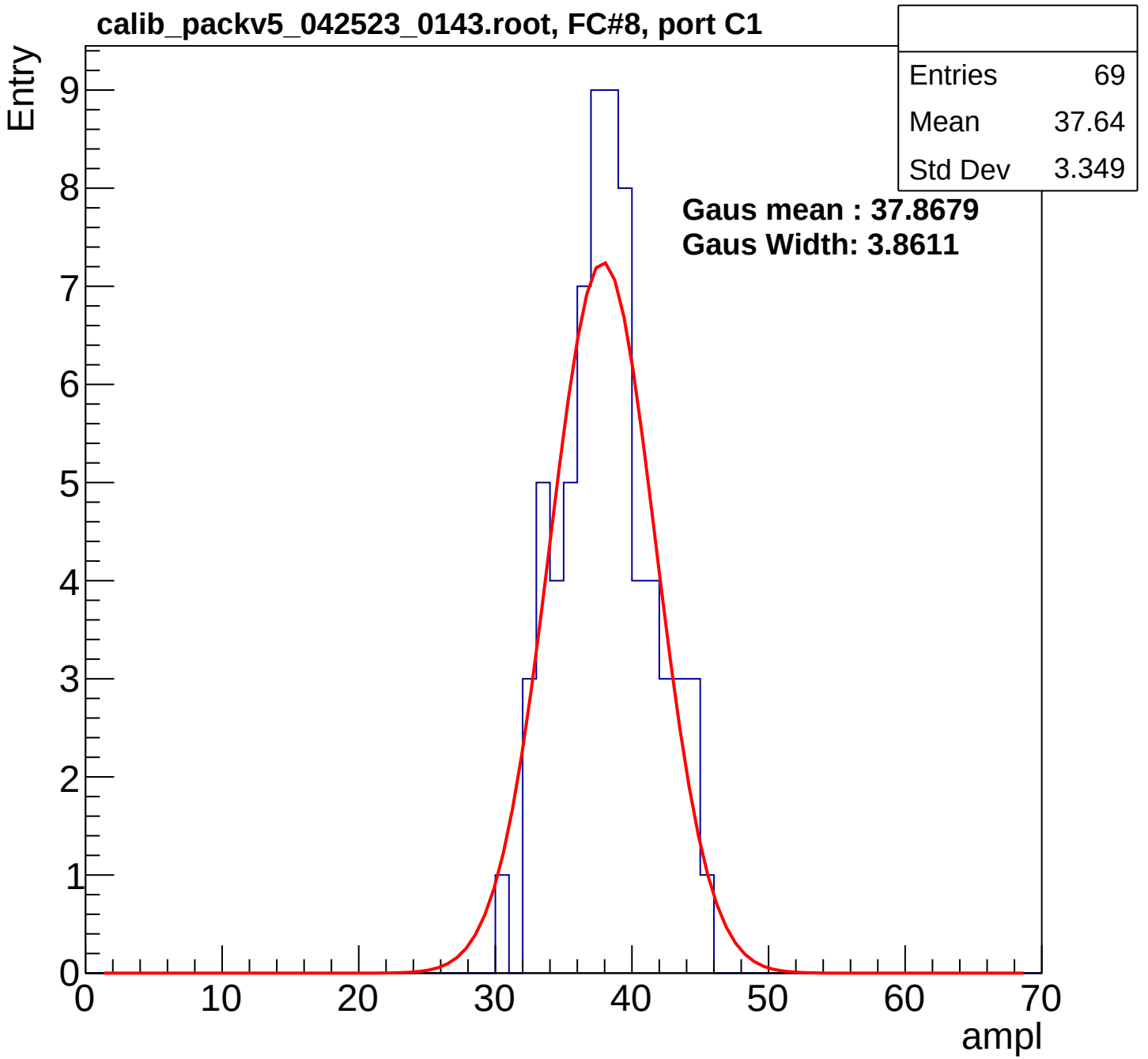
B0L002S, U11-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.64
Std Dev 3.349
Gaus mean : 37.8679
Gaus Width: 3.8611
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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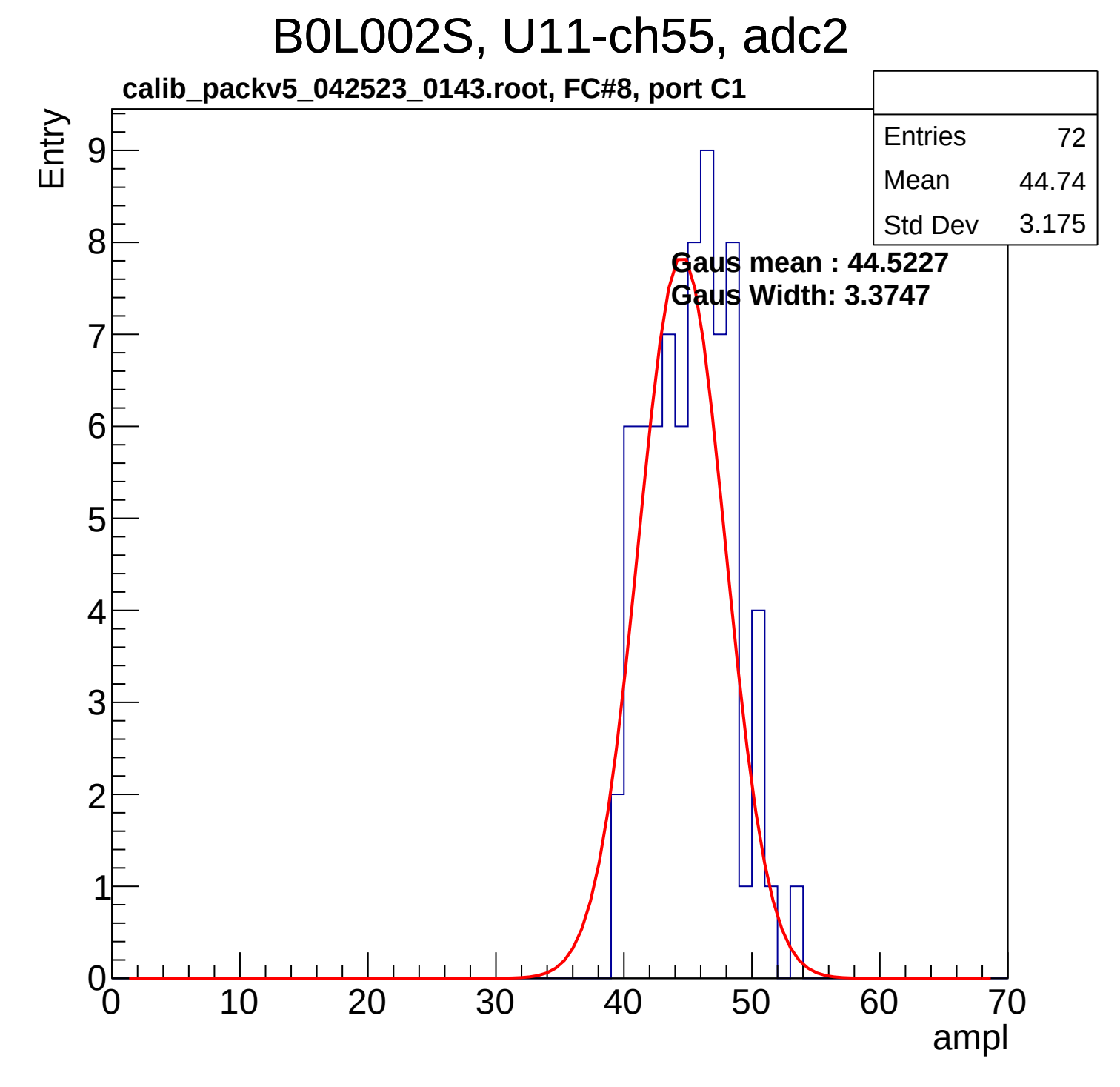
B0L002S, U11-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.74
Std Dev 3.175
Gaus mean : 44.5227
Gaus Width: 3.3747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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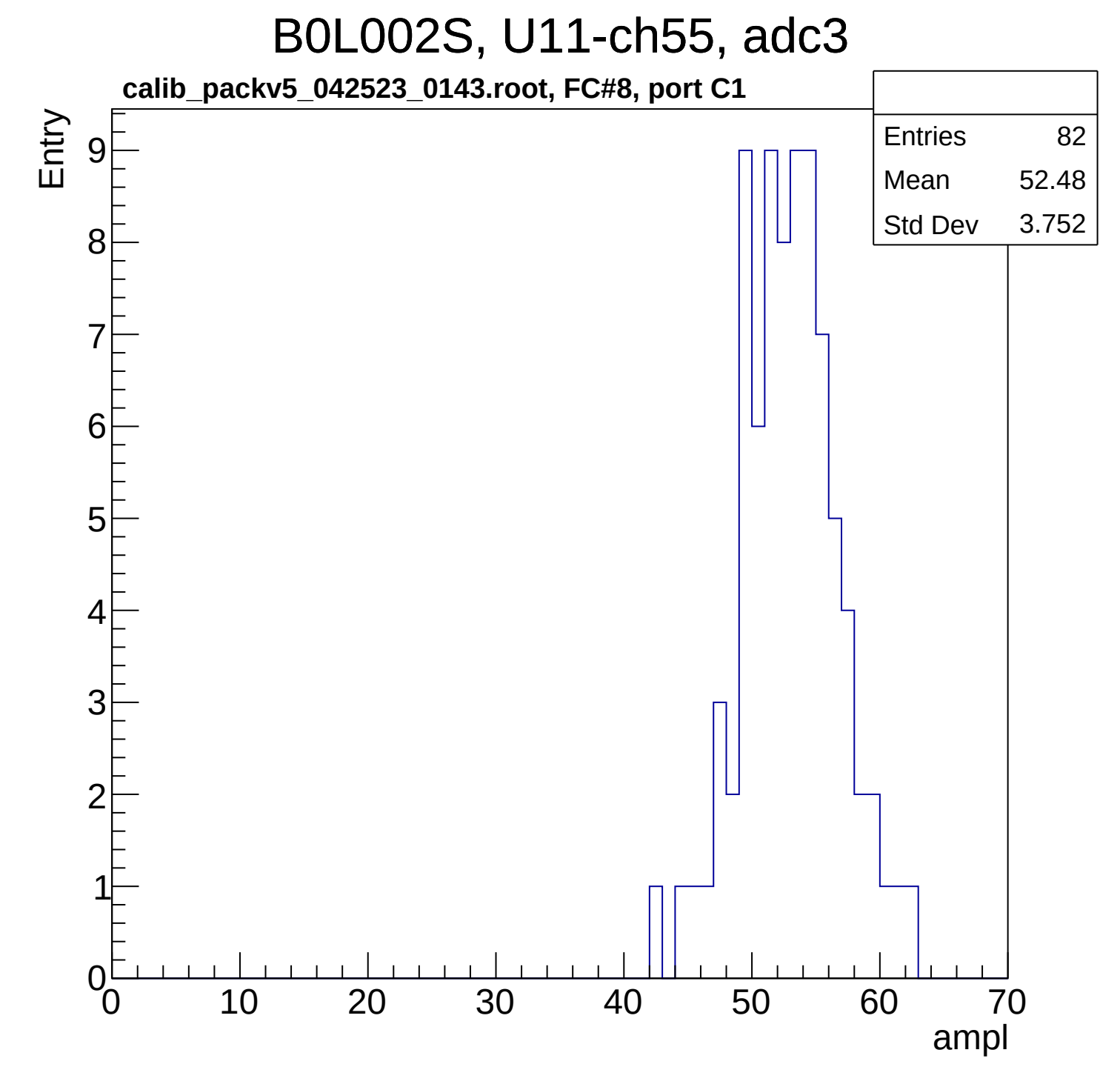
B0L002S, U11-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 52.48
Std Dev 3.752
Entry
ampl
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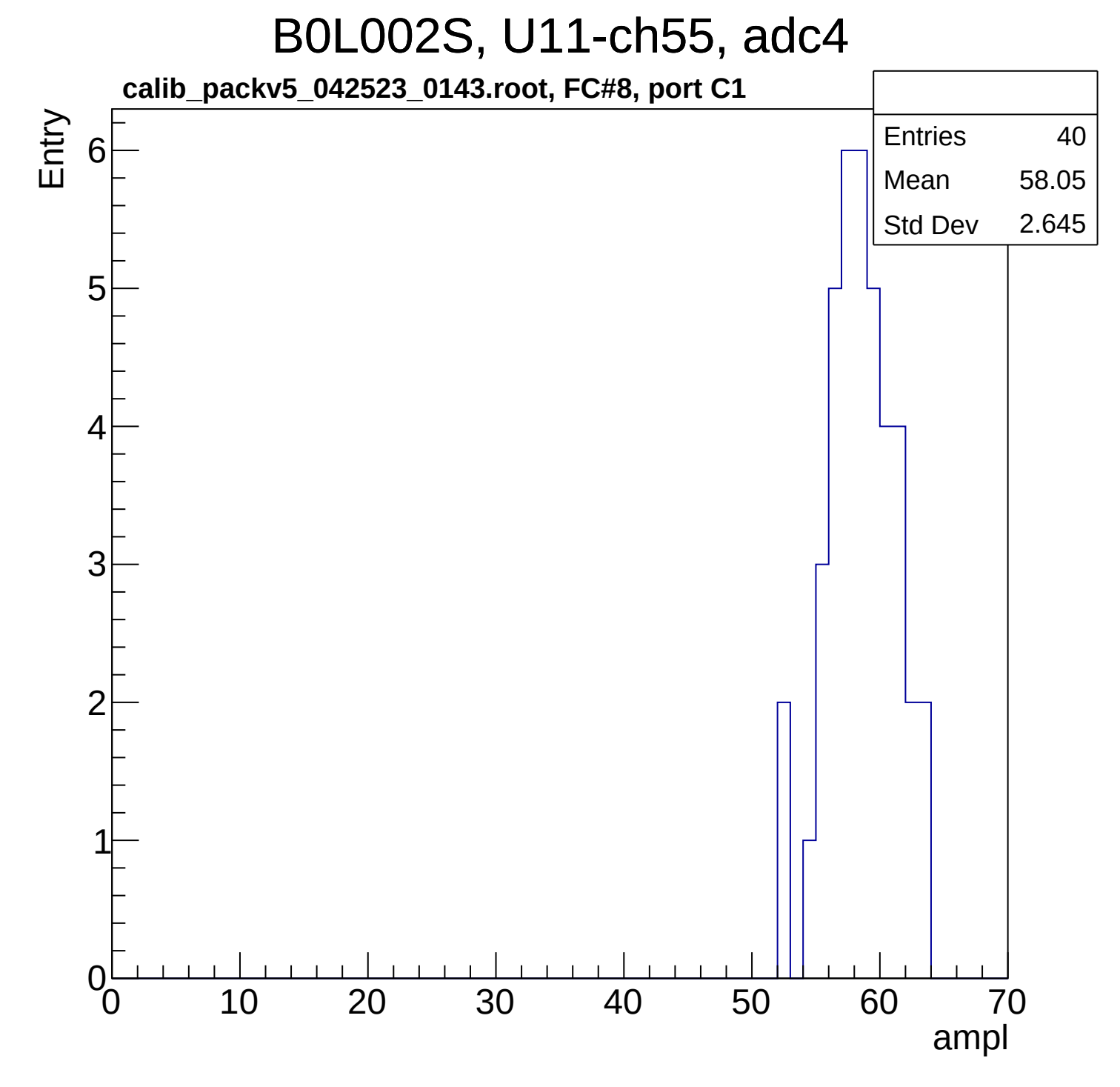
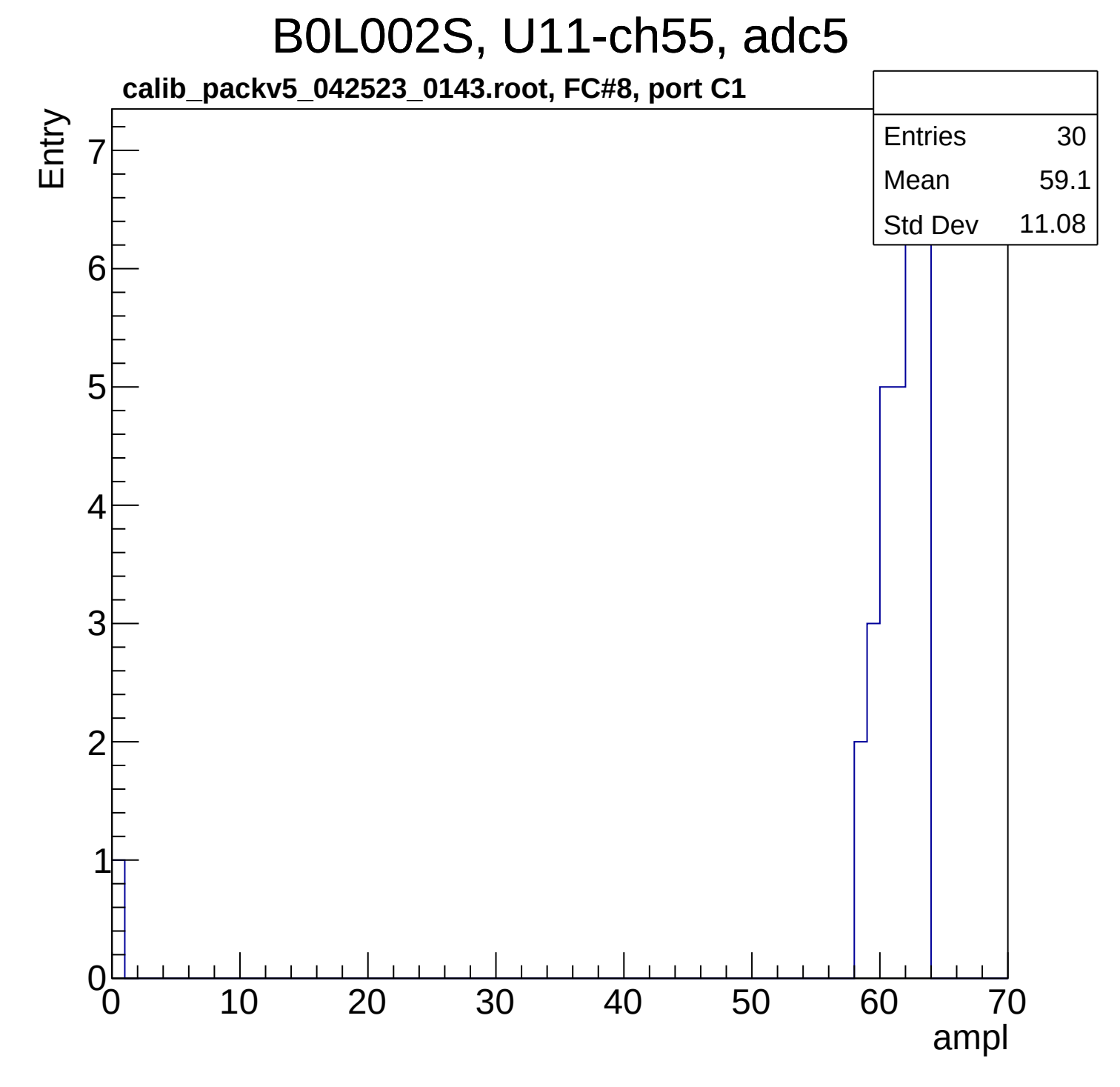
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.05
Std Dev 2.645
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.1
Std Dev 11.08
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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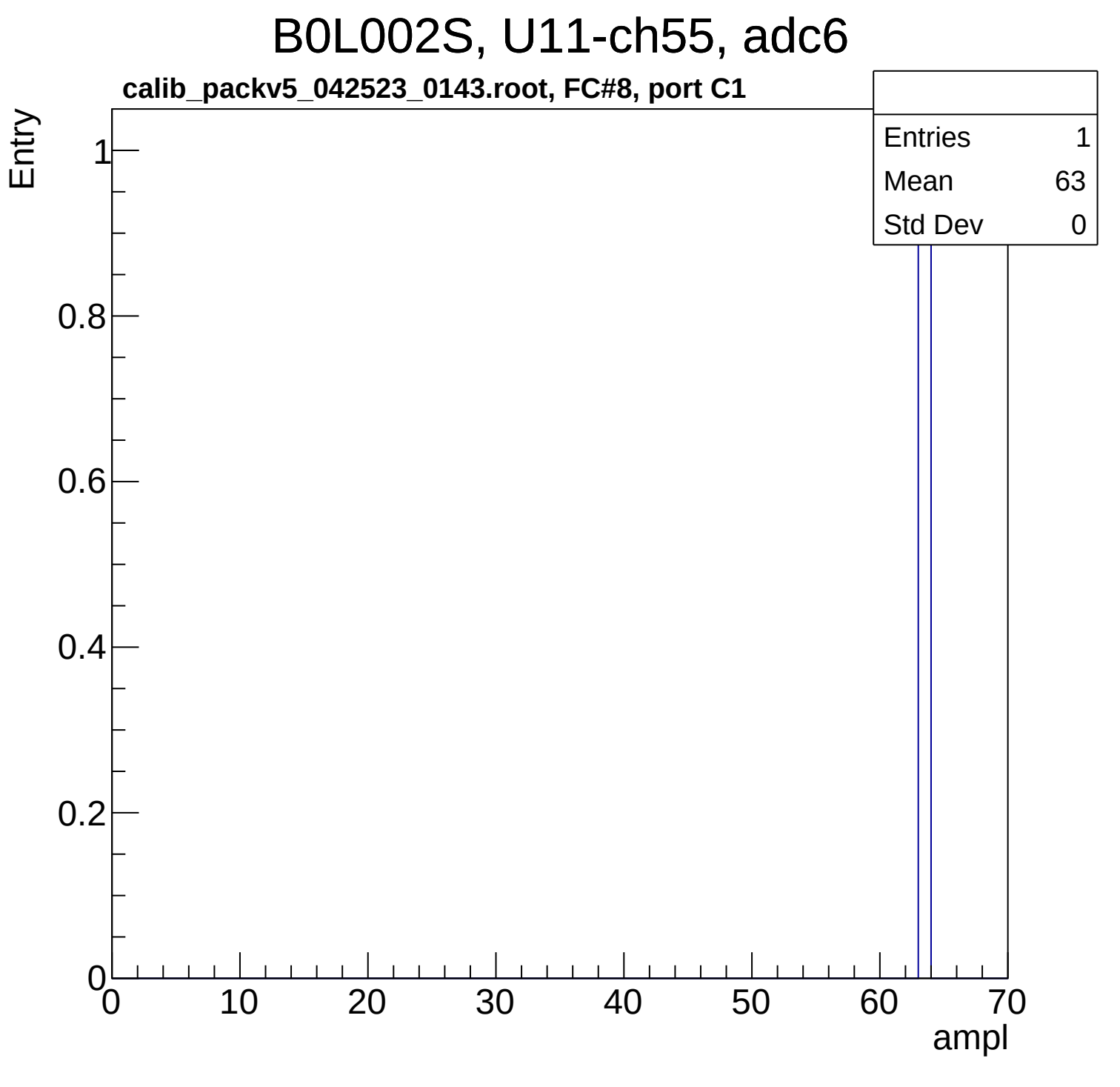
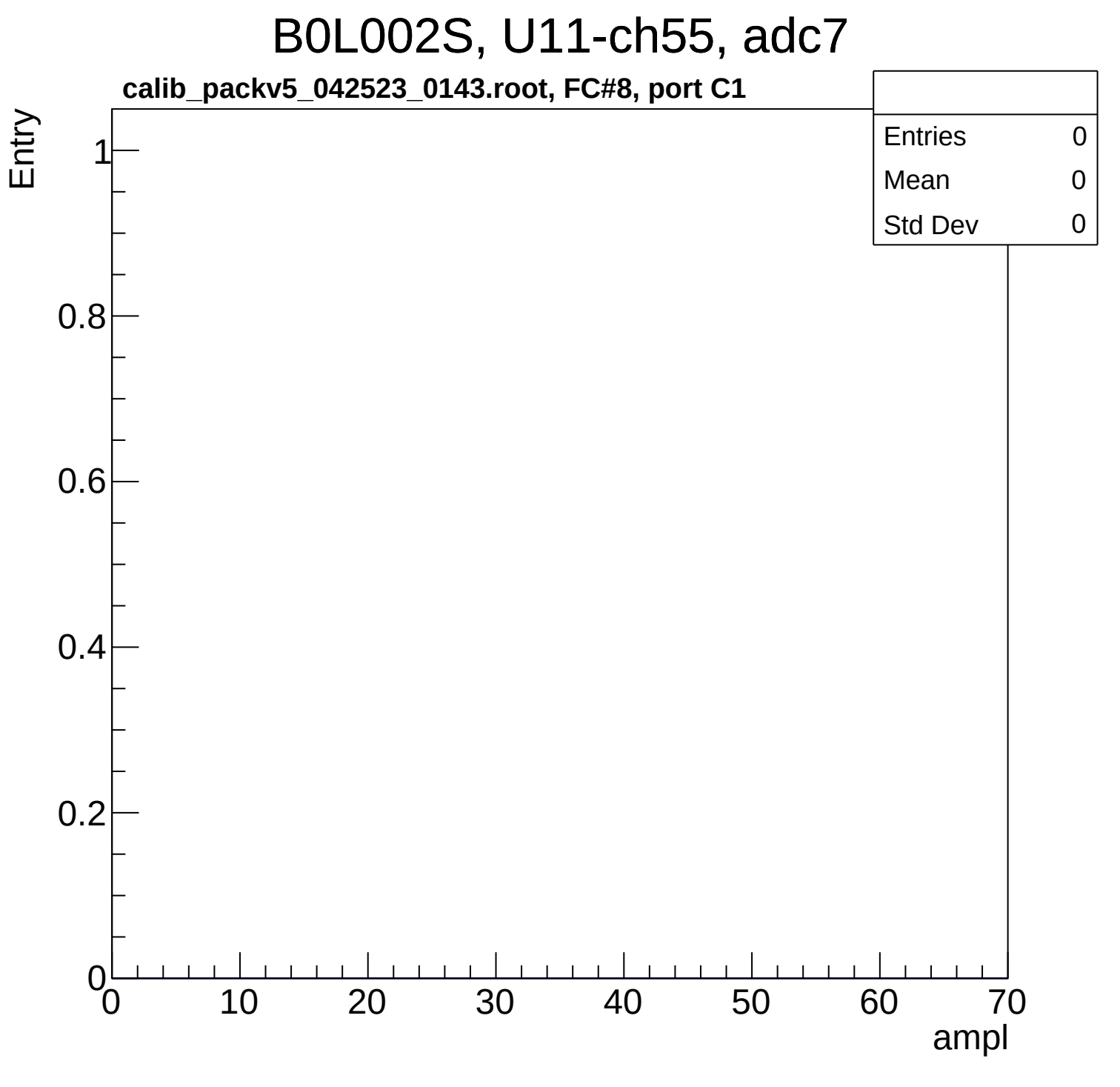

B0L002S, U11-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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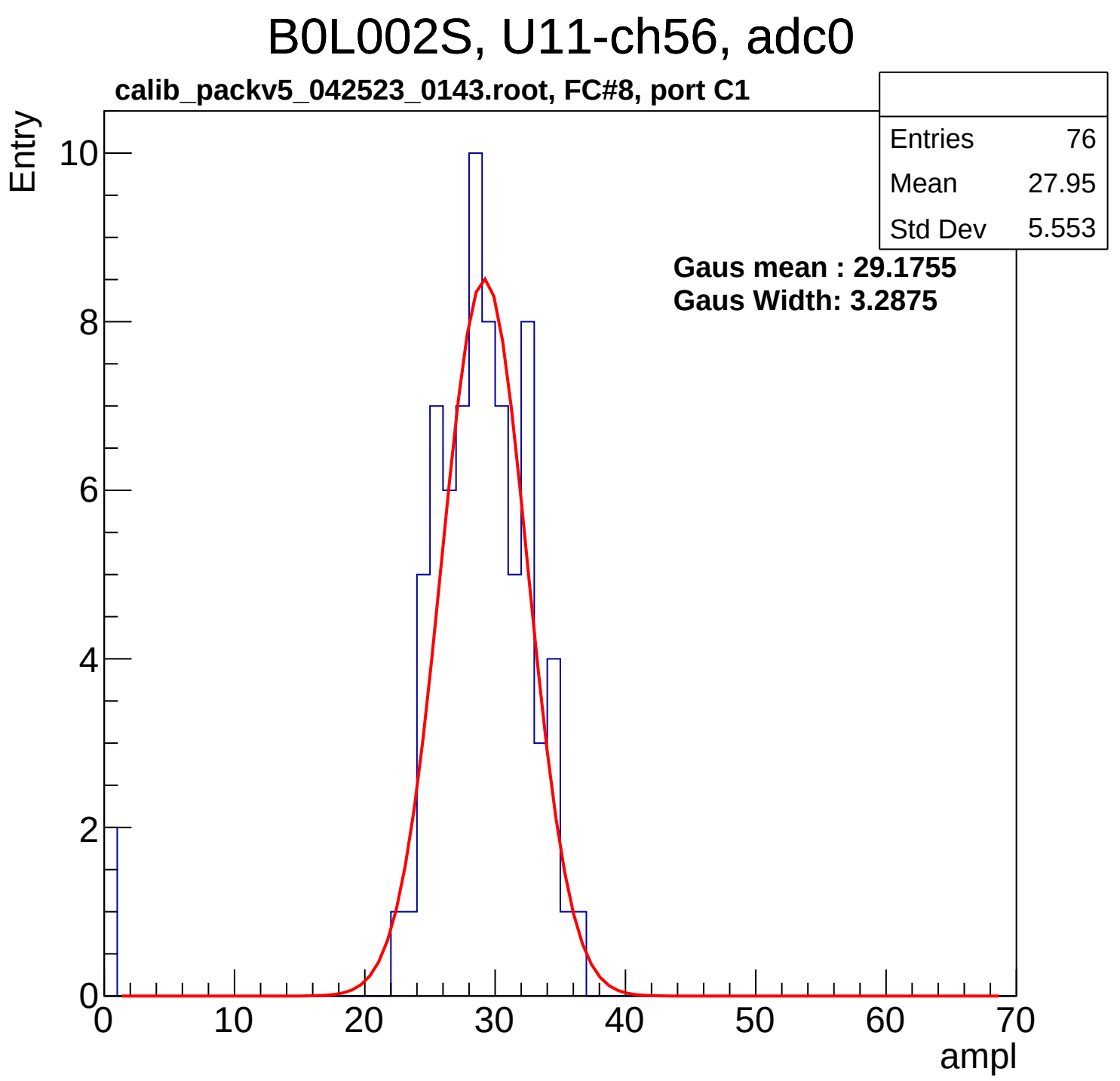
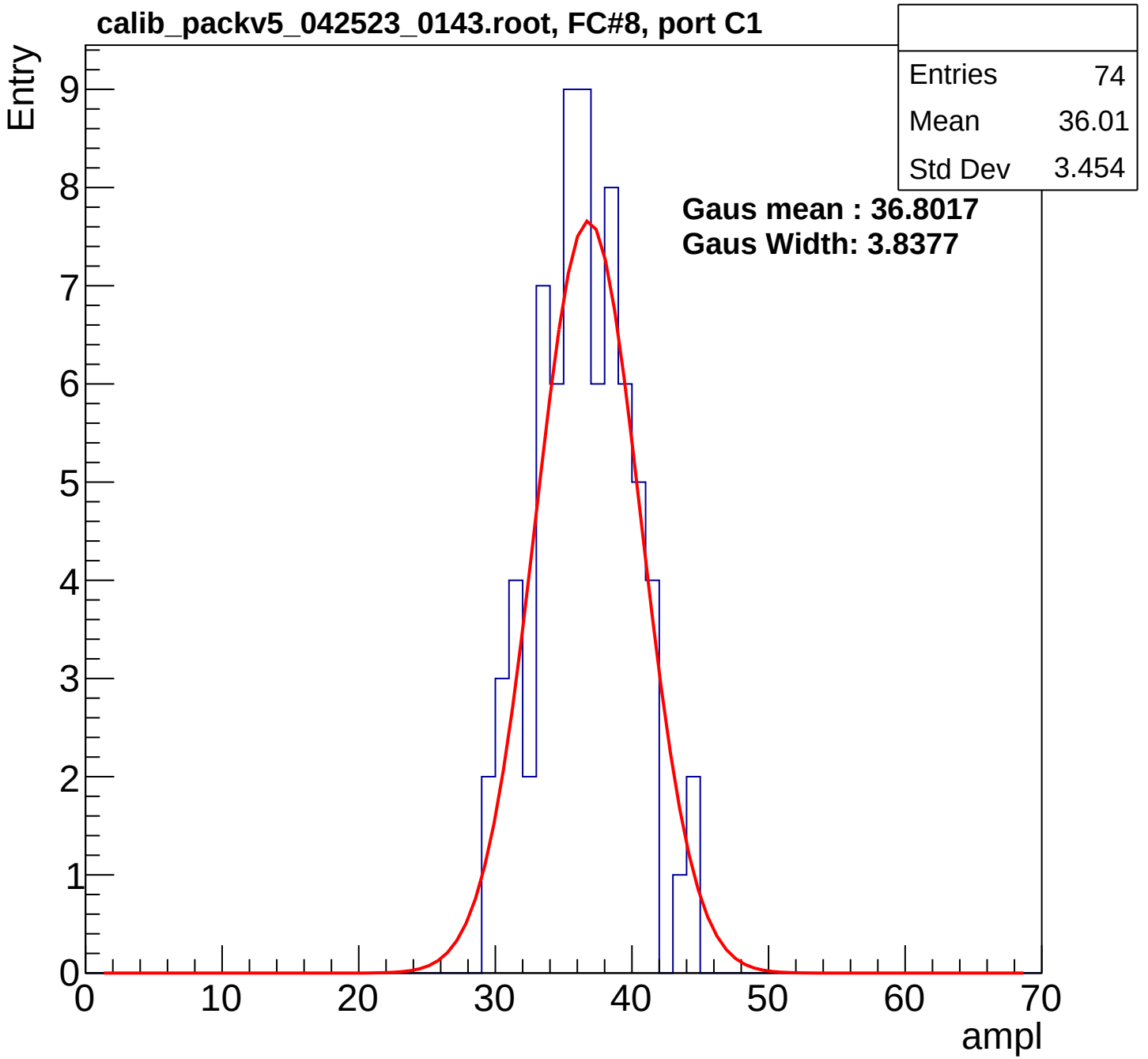
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 27.95
Std Dev 5.553
Gaus mean : 29.1755
Gaus Width: 3.2875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.01
Std Dev 3.454
Gaus mean : 36.8017
Gaus Width: 3.8377
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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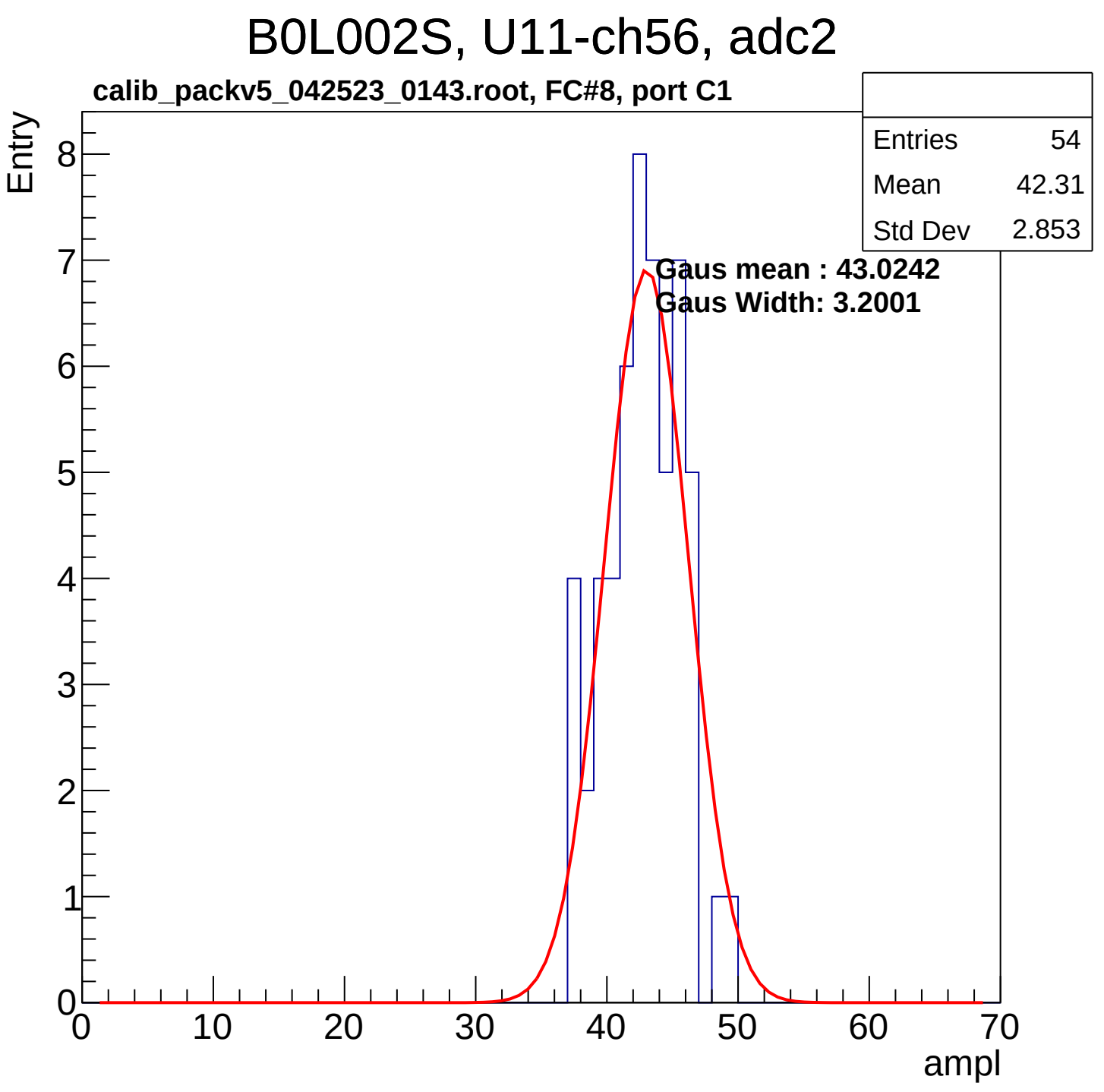
B0L002S, U11-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 42.31
Std Dev 2.853
Gaus mean : 43.0242
Gaus Width: 3.2001
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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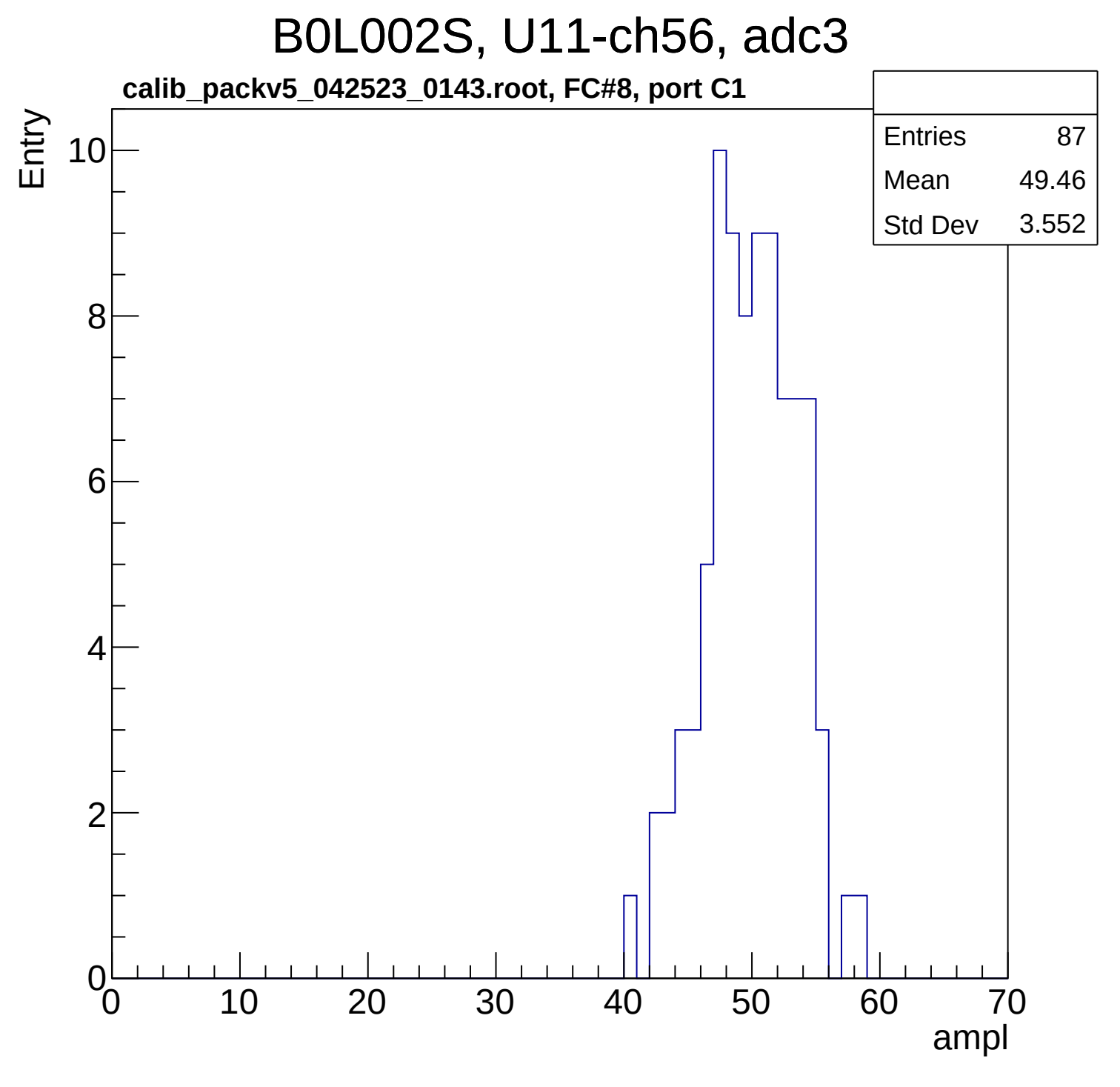
B0L002S, U11-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 49.46
Std Dev 3.552
Entry
ampl
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2
4
6
8
10
0
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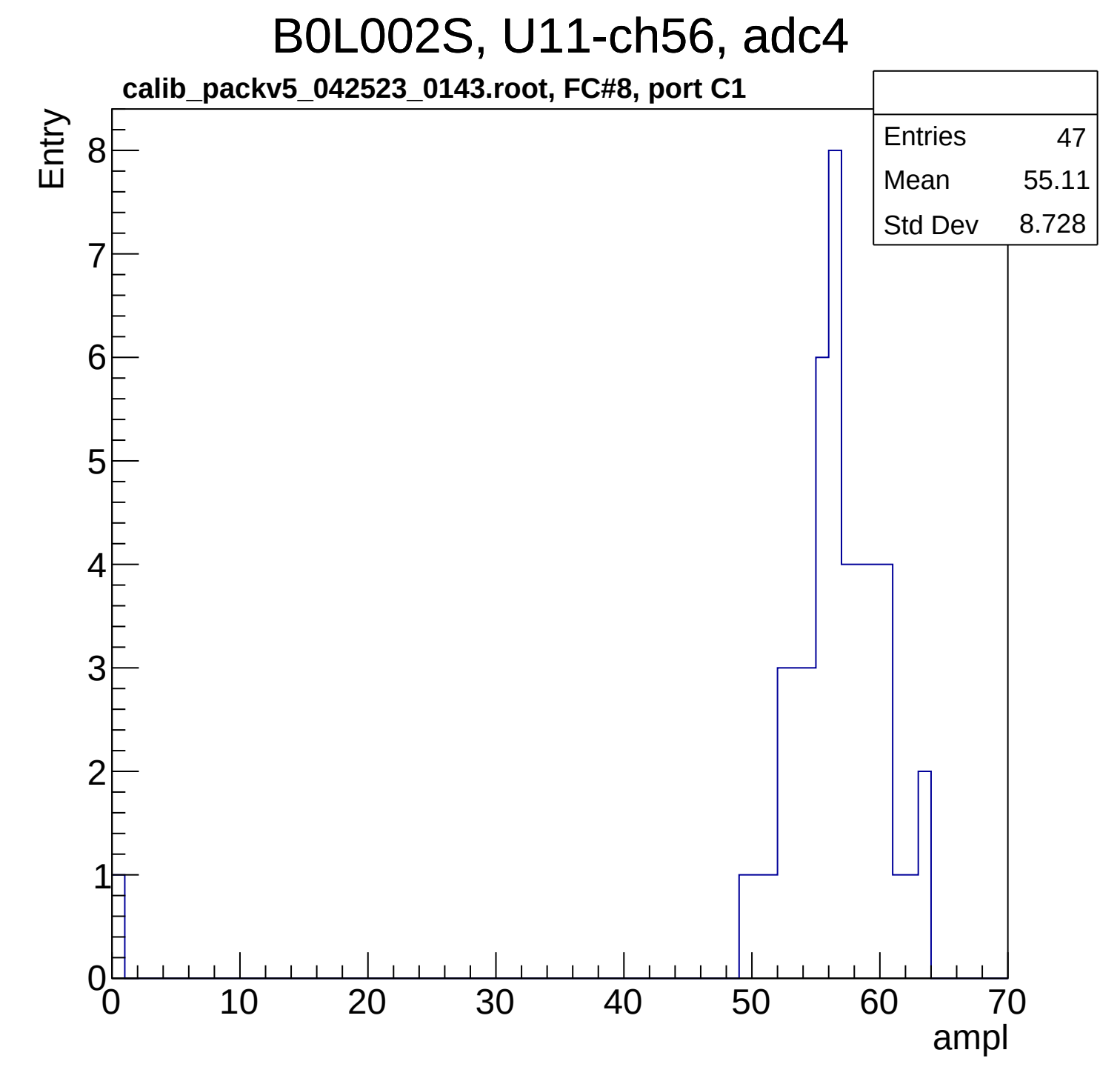
B0L002S, U11-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 55.11
Std Dev 8.728
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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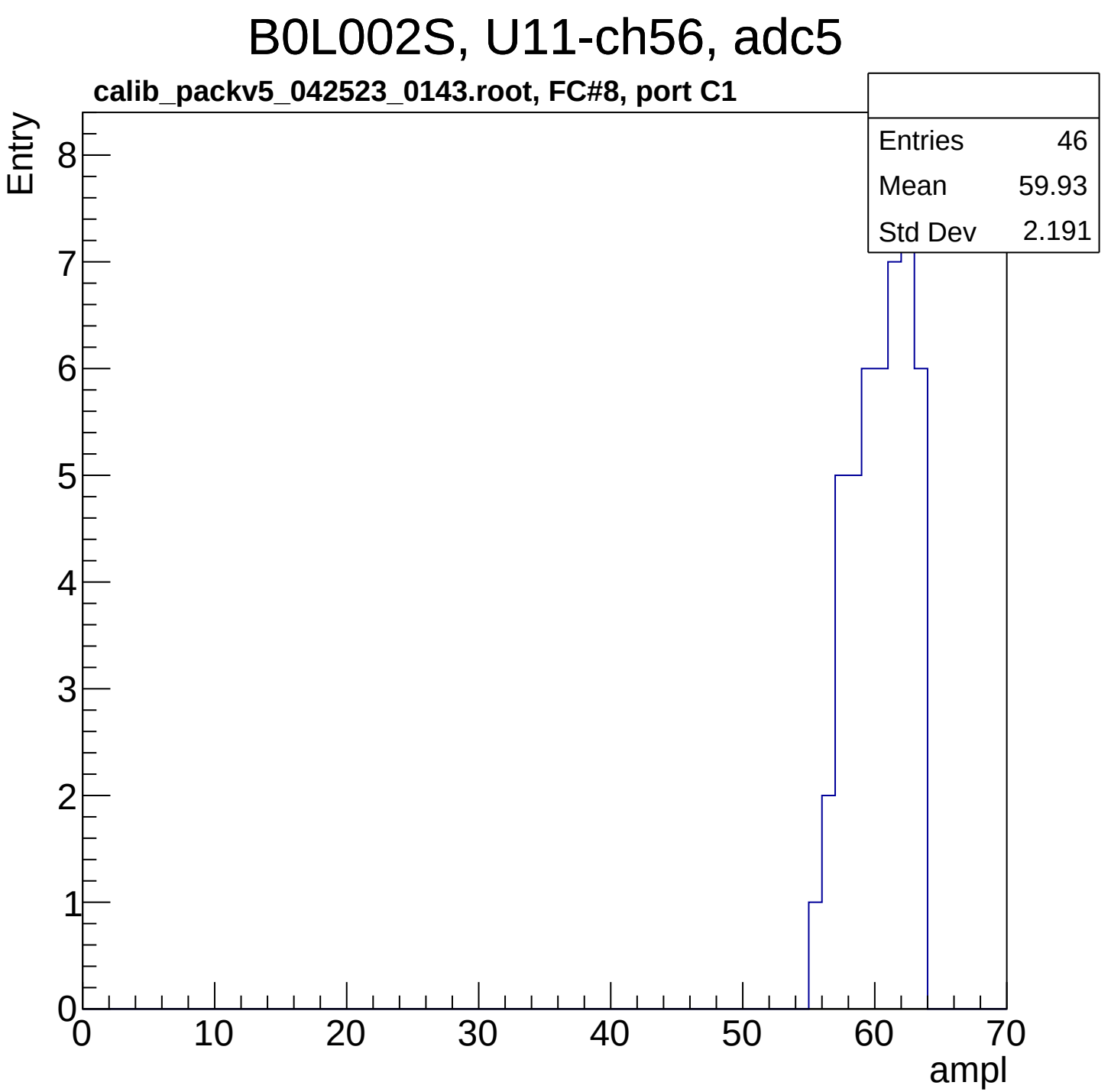
B0L002S, U11-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.93
Std Dev 2.191
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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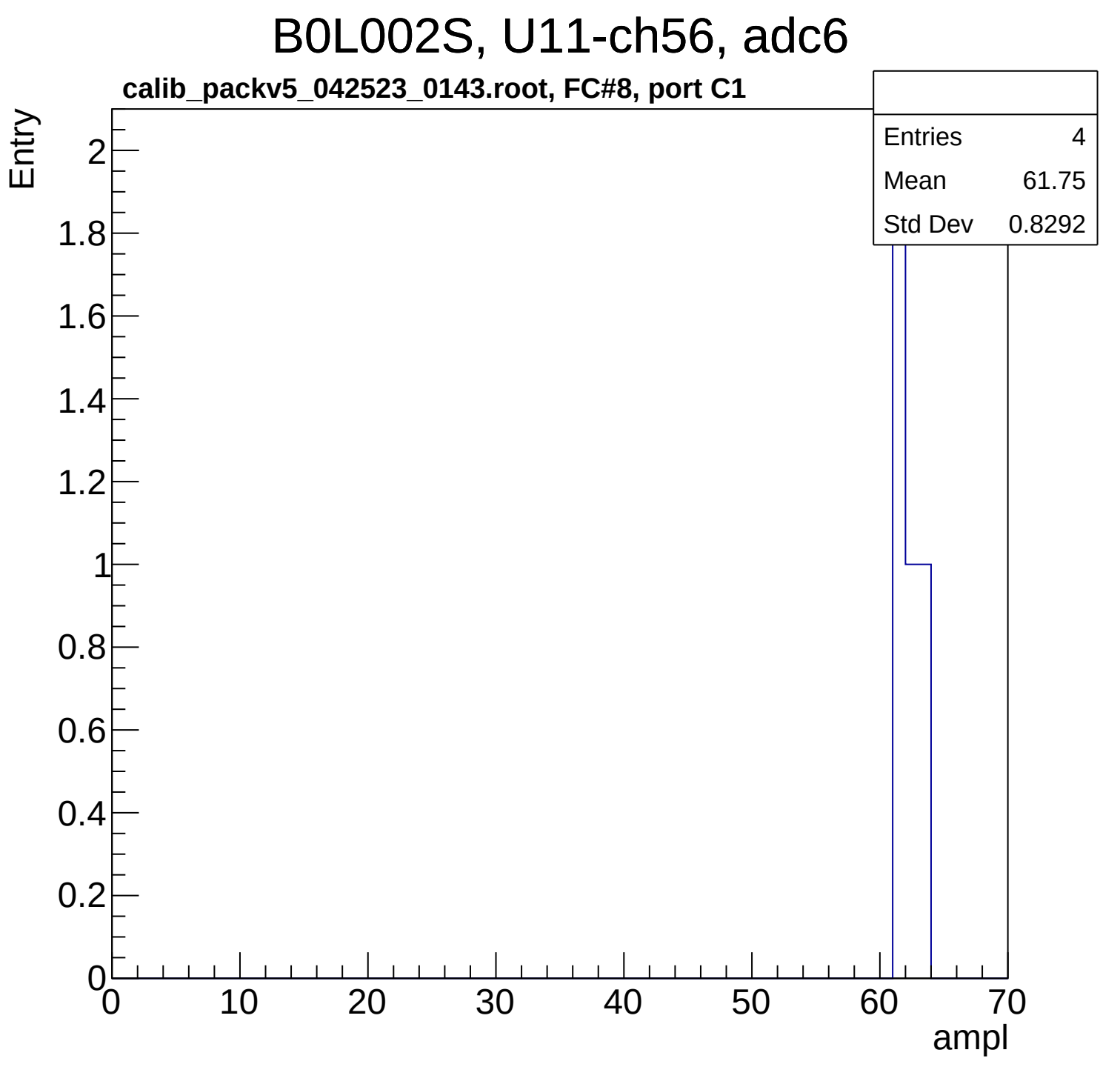
B0L002S, U11-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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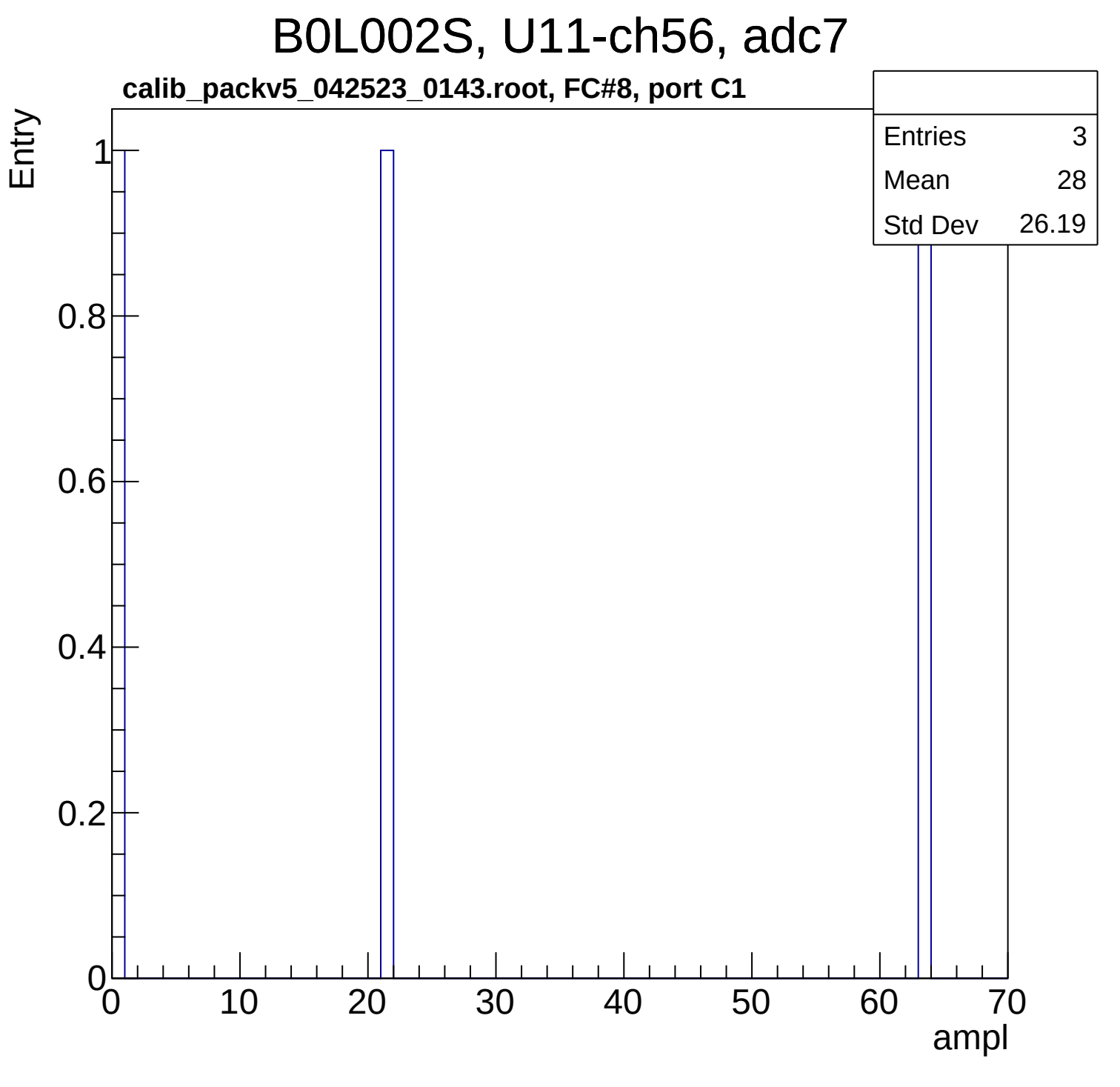
B0L002S, U11-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 28
Std Dev 26.19
Entry
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0.8
0.6
0.4
0.2
0
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10
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30
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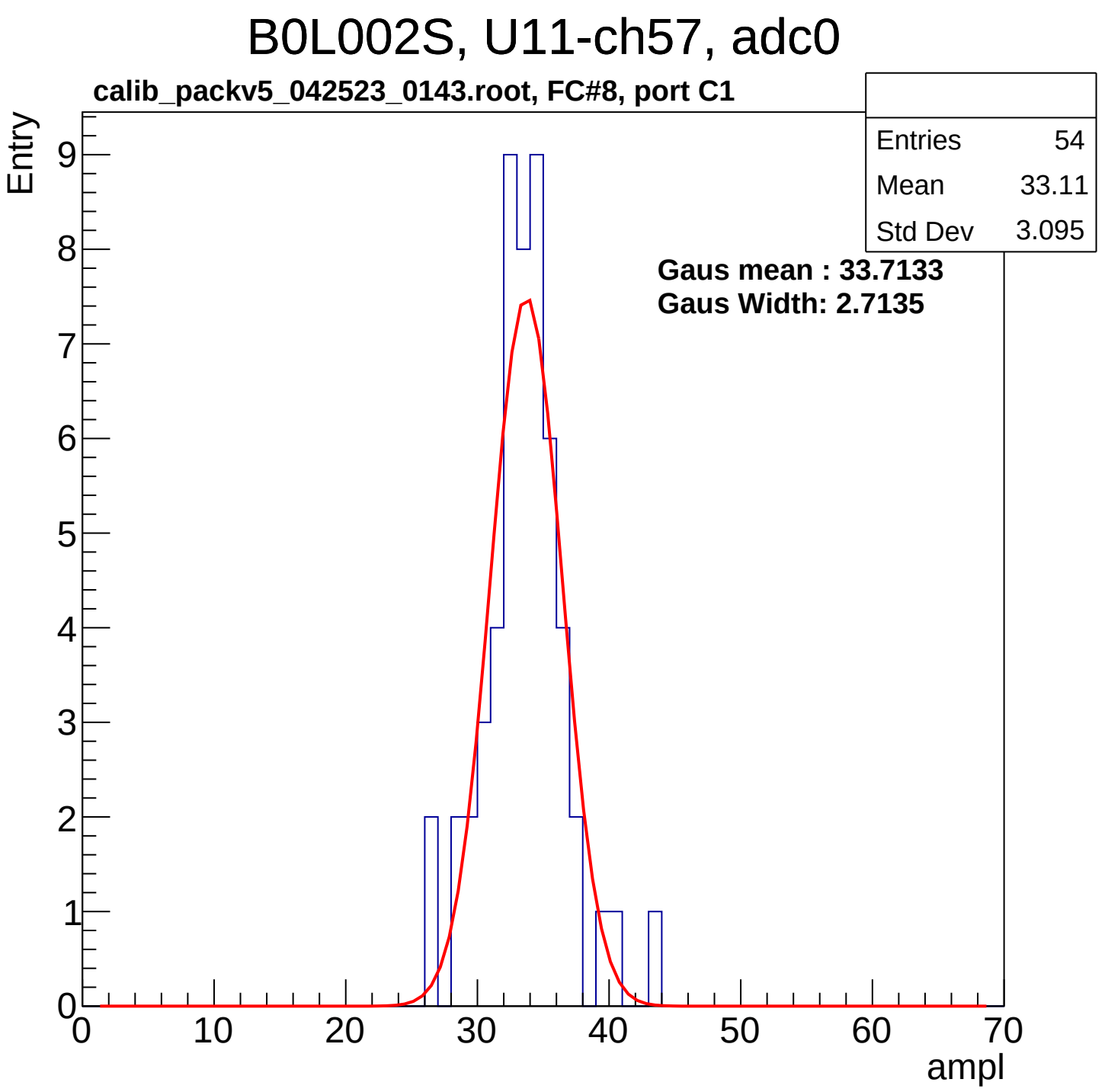
50
60
70
ampl


B0L002S, U11-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 33.11
Std Dev 3.095
Gaus mean : 33.7133
Gaus Width: 2.7135
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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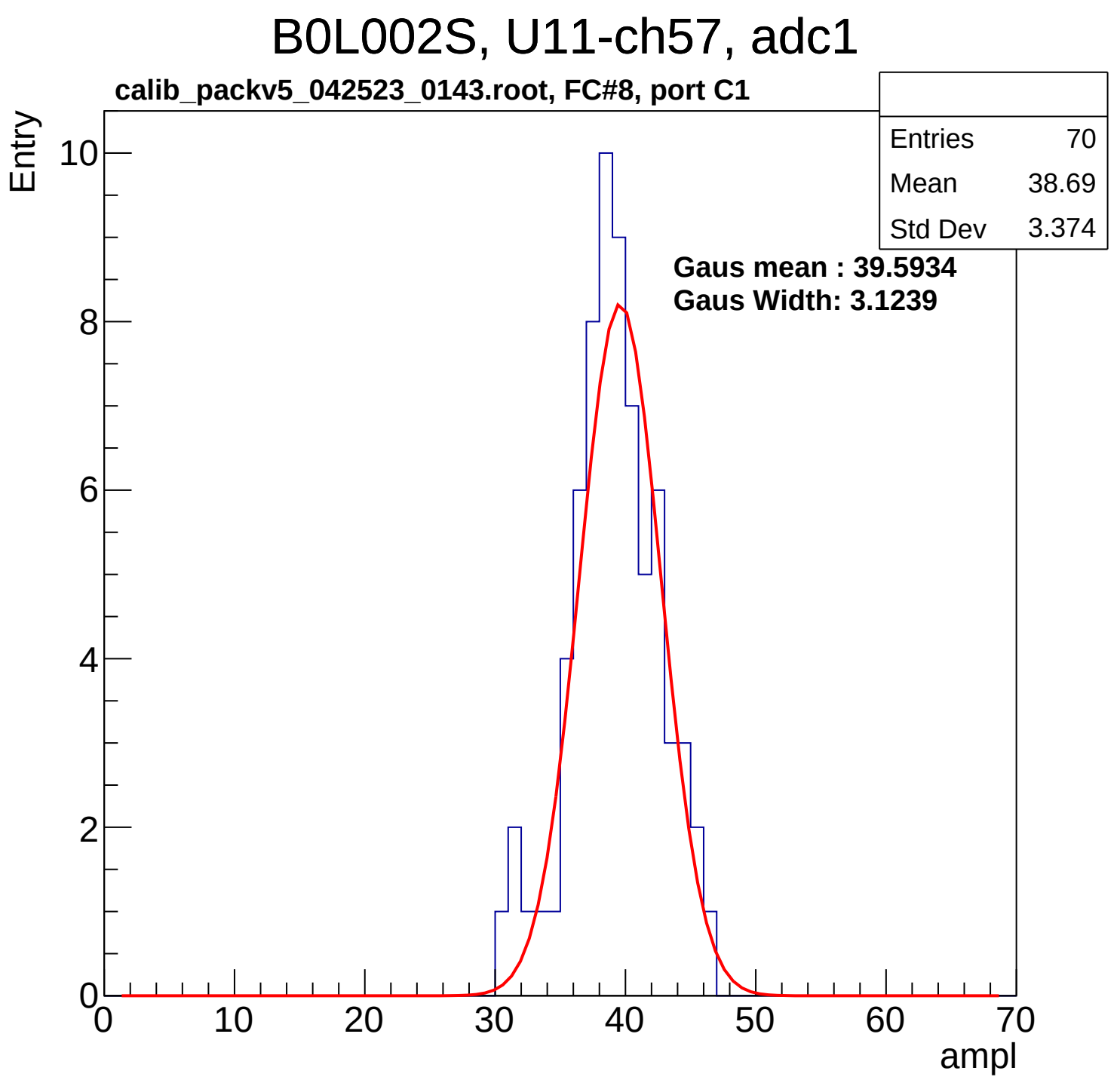
B0L002S, U11-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.69
Std Dev 3.374
Gaus mean : 39.5934
Gaus Width: 3.1239
Entry
ampl
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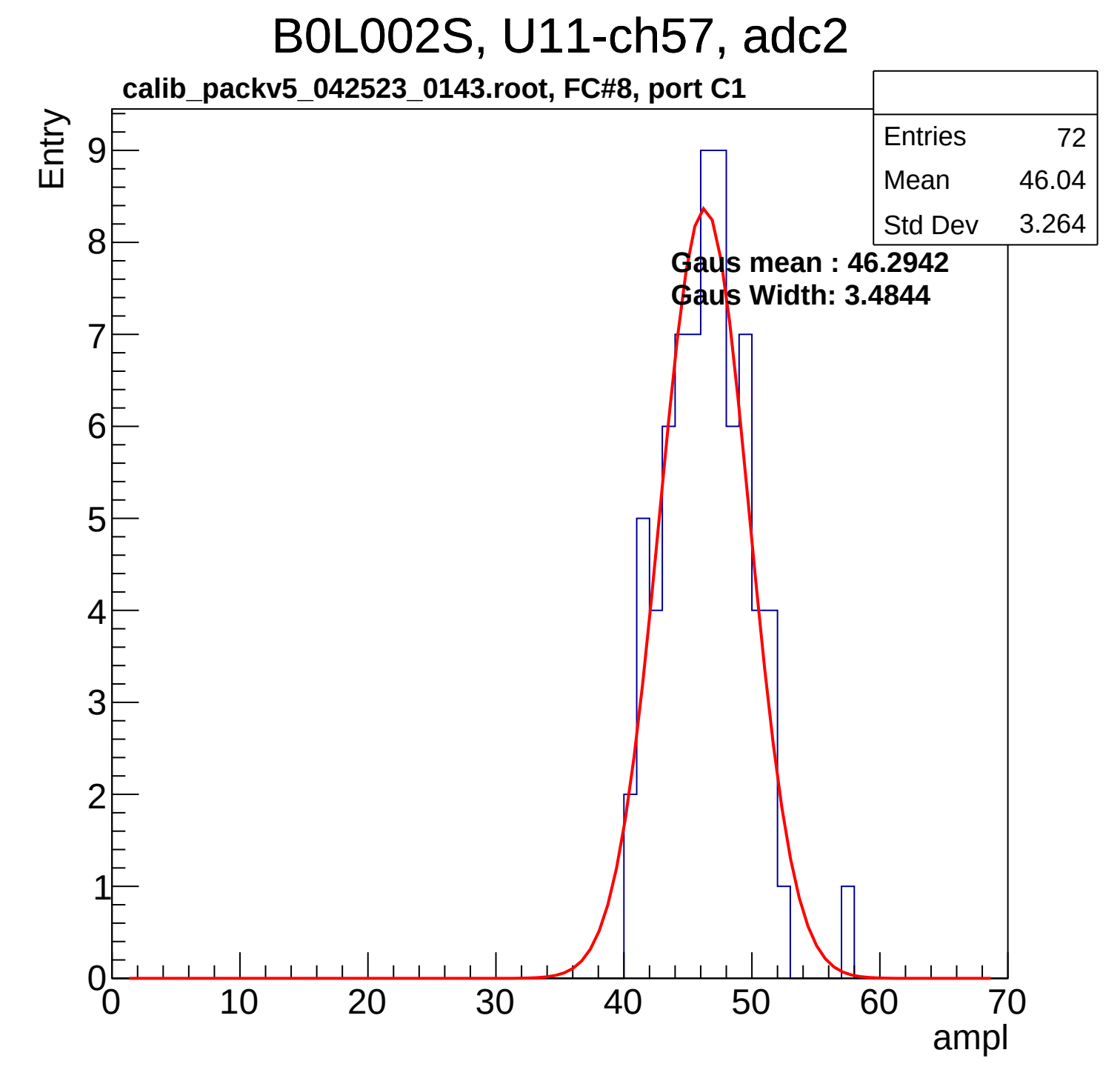
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 46.04
Std Dev 3.264
Gaus mean : 46.2942
Gaus Width: 3.4844
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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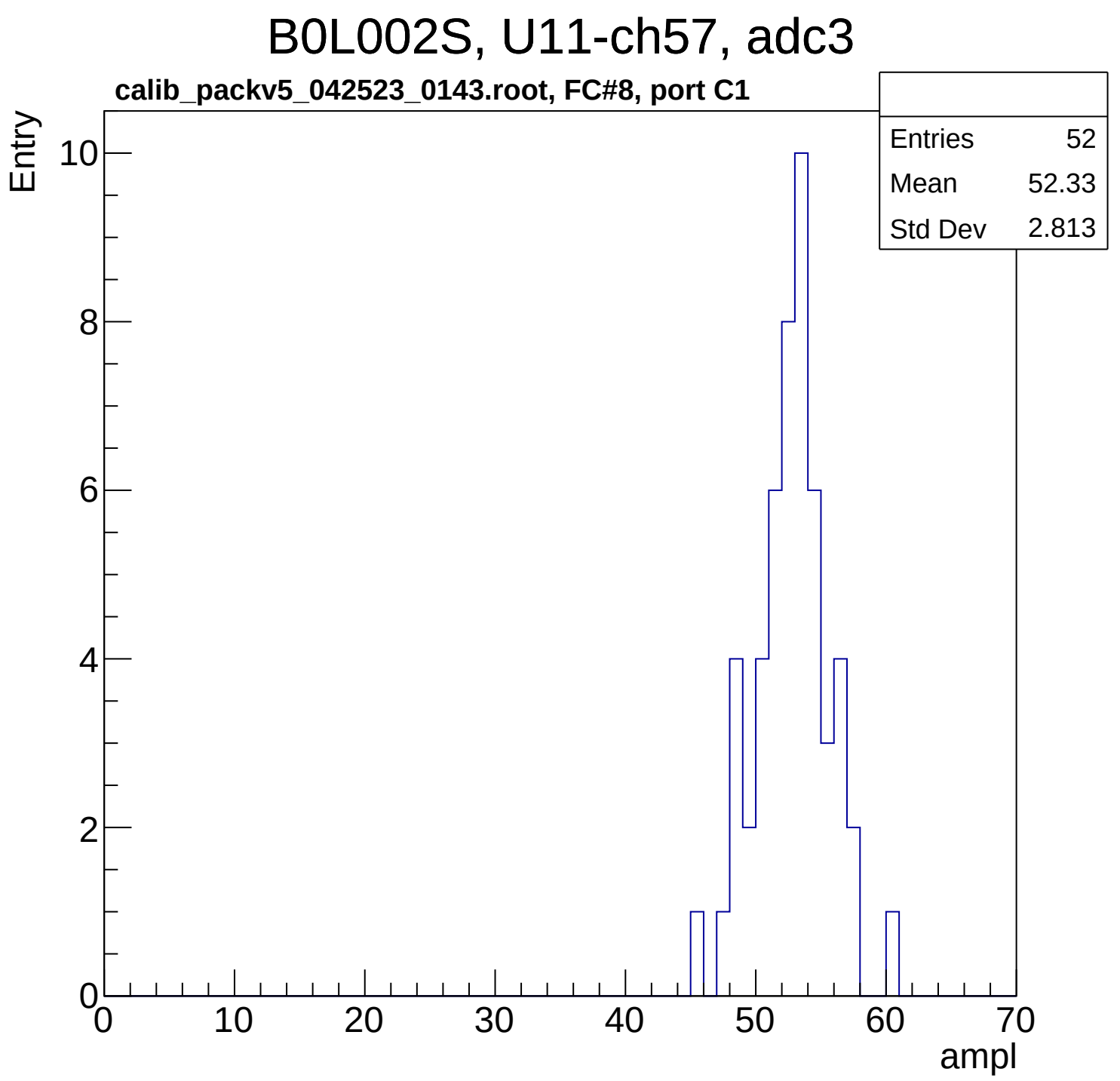
B0L002S, U11-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 52.33
Std Dev 2.813
Entry
ampl
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60
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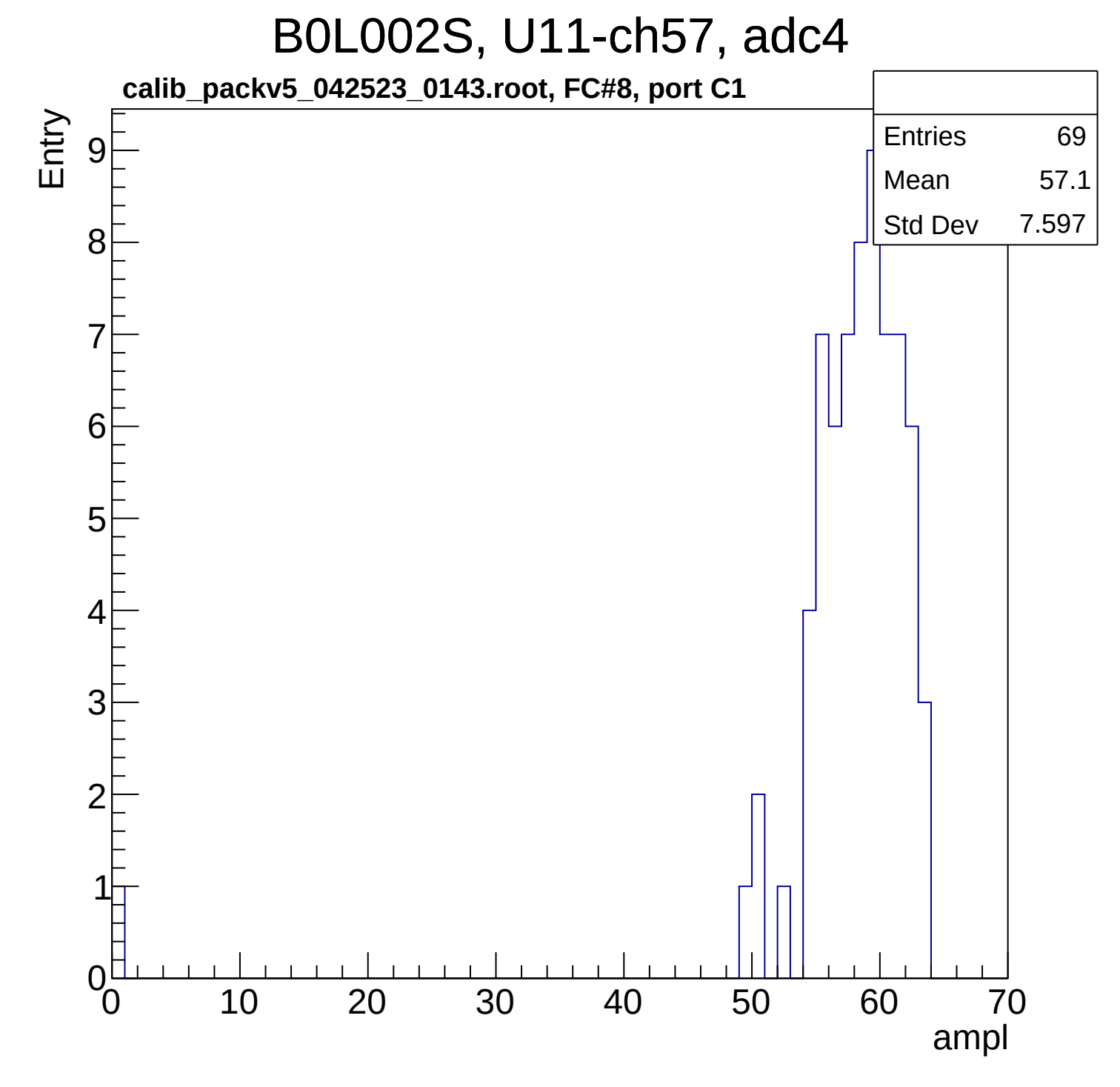
B0L002S, U11-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 57.1
Std Dev 7.597
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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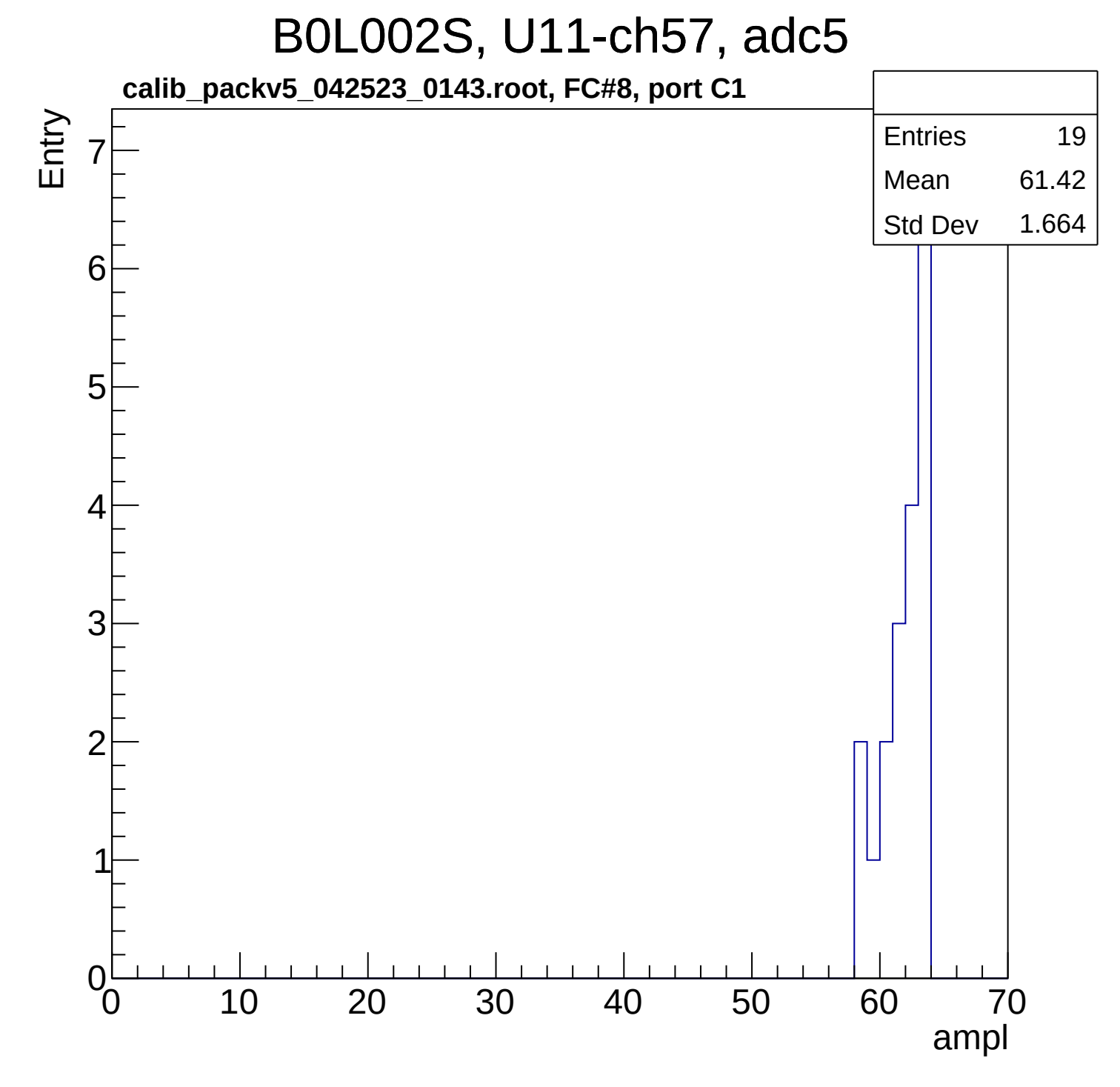
B0L002S, U11-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.42
Std Dev 1.664
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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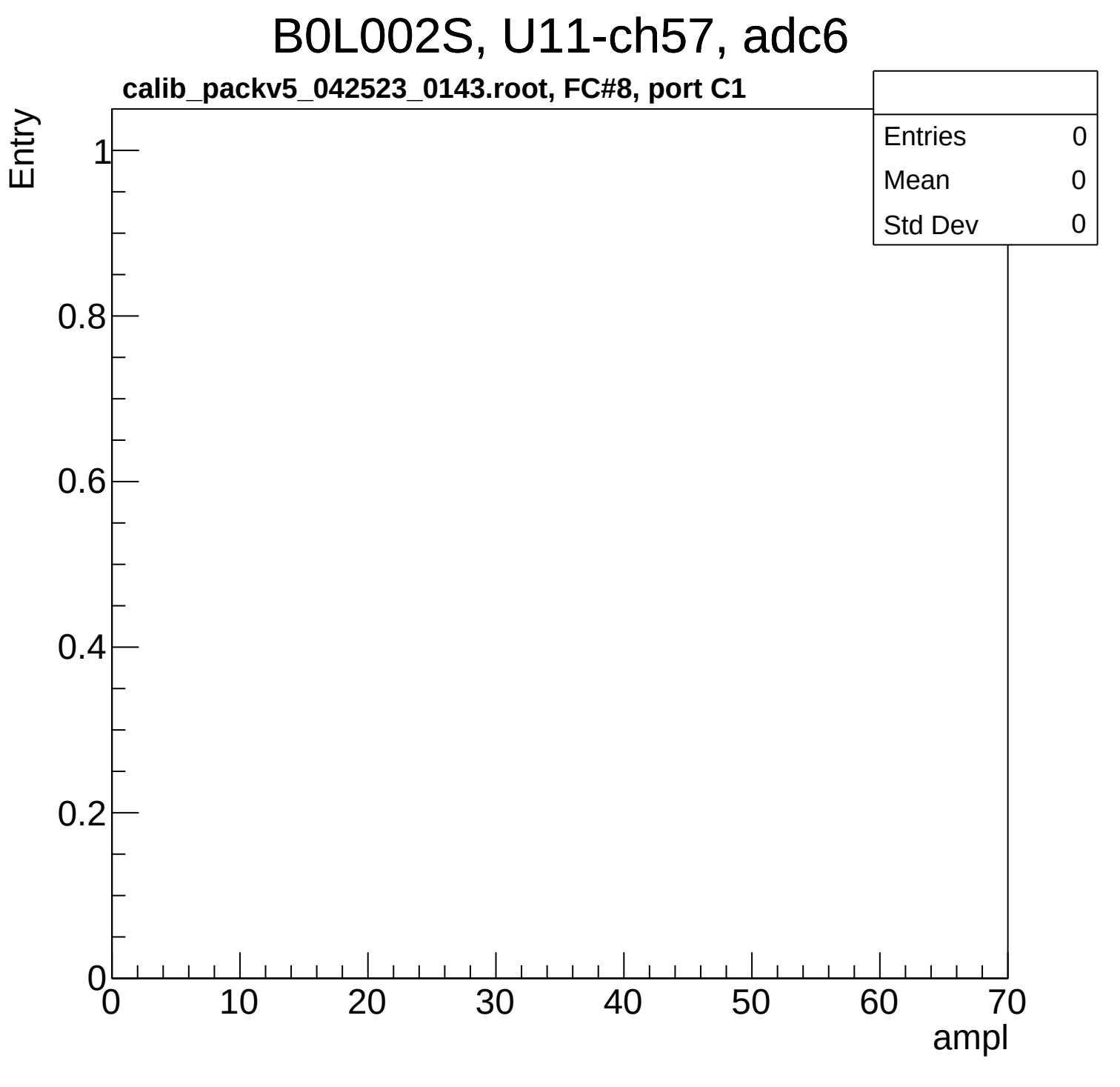
B0L002S, U11-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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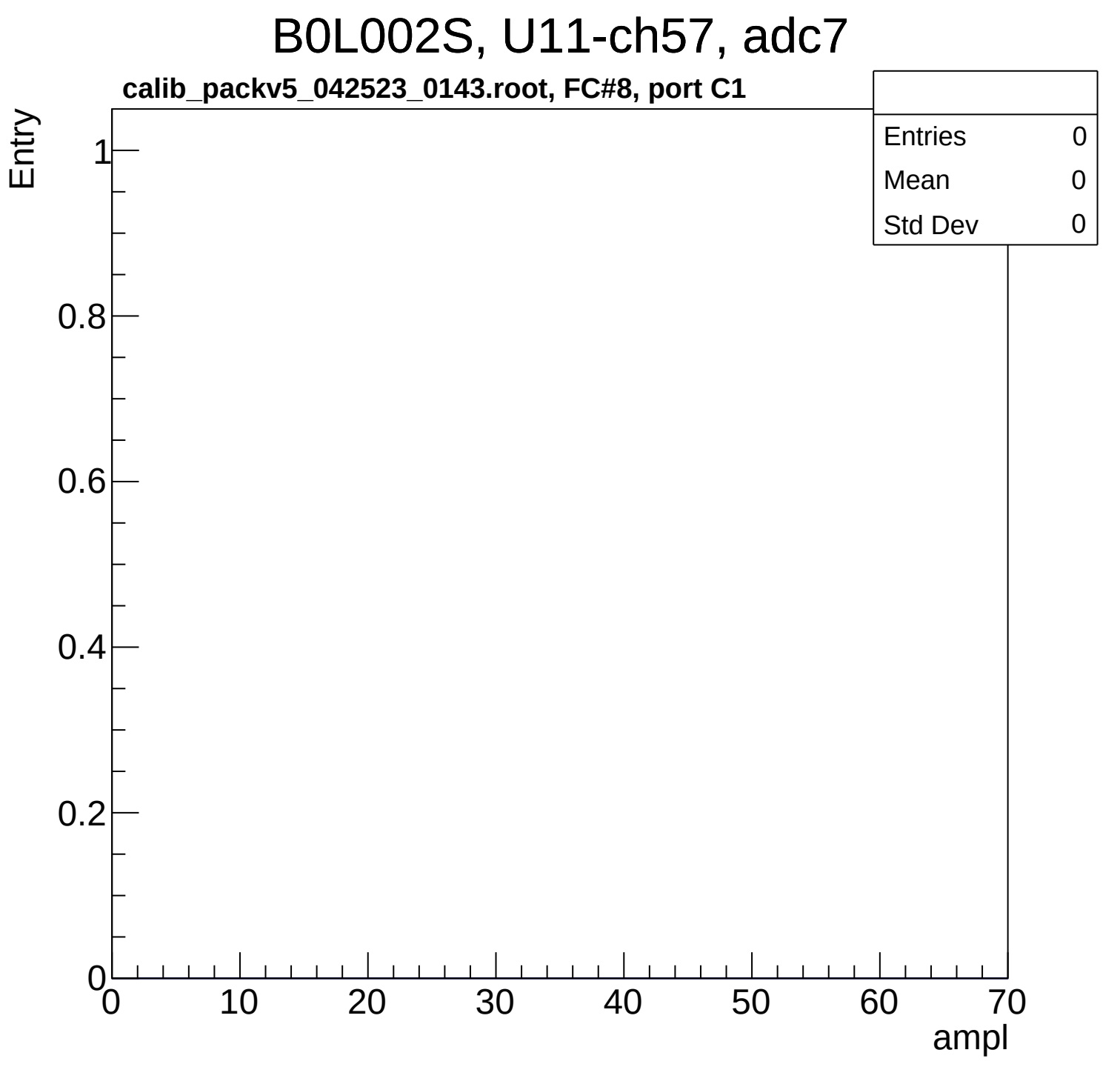
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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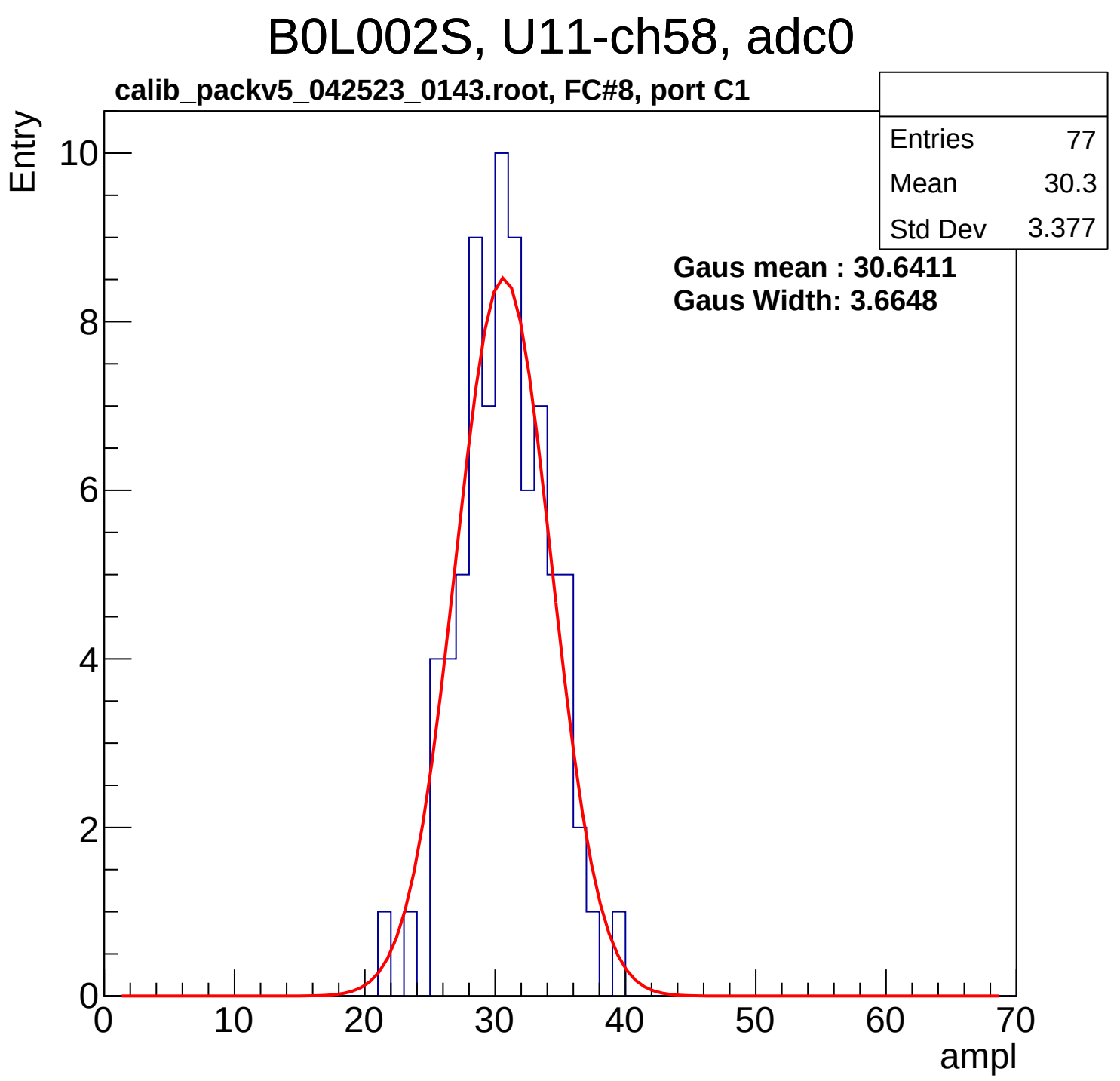
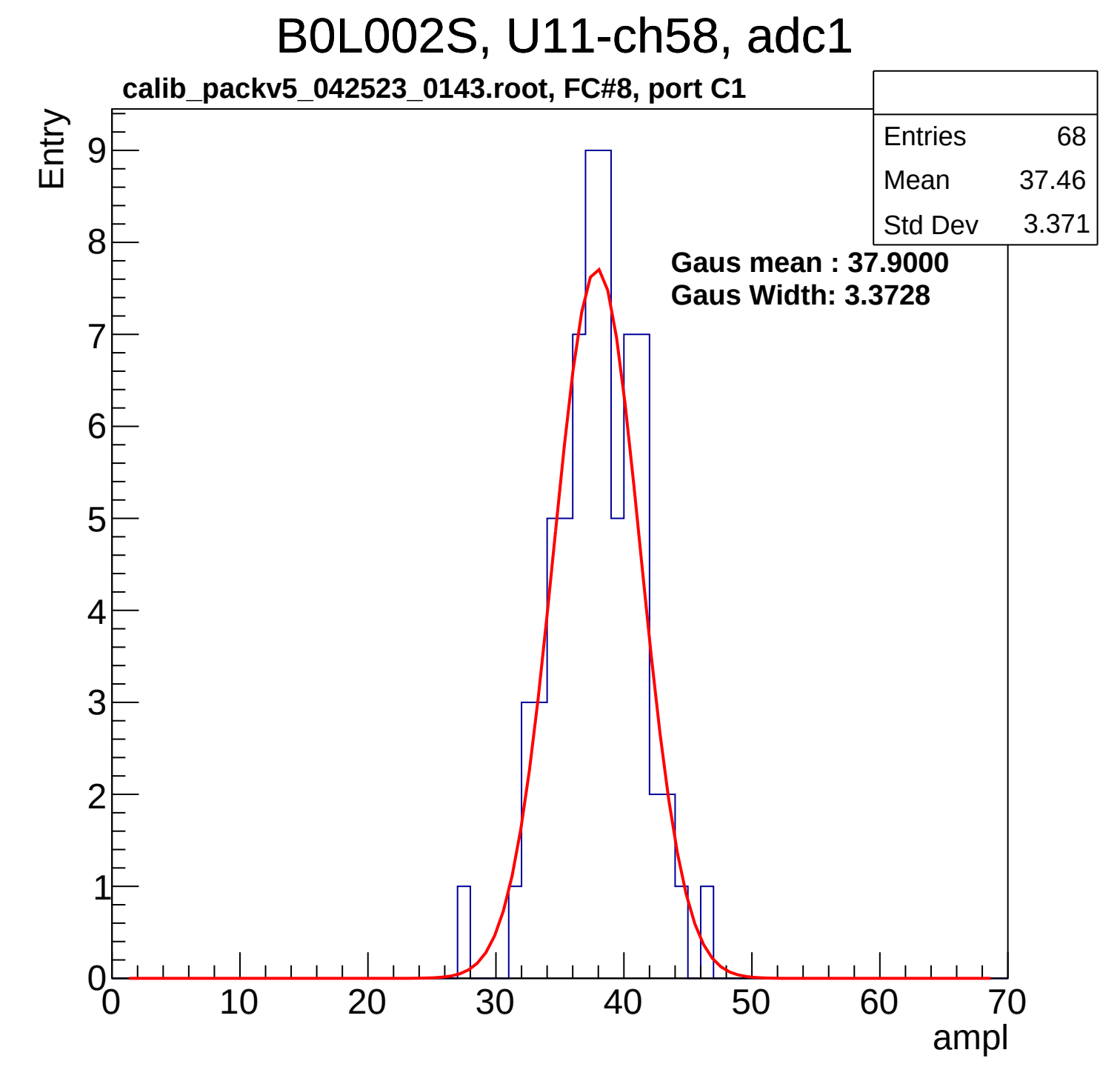
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.3
Std Dev 3.377
Gaus mean : 30.6411
Gaus Width: 3.6648
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.46
Std Dev 3.371
Gaus mean : 37.9000
Gaus Width: 3.3728
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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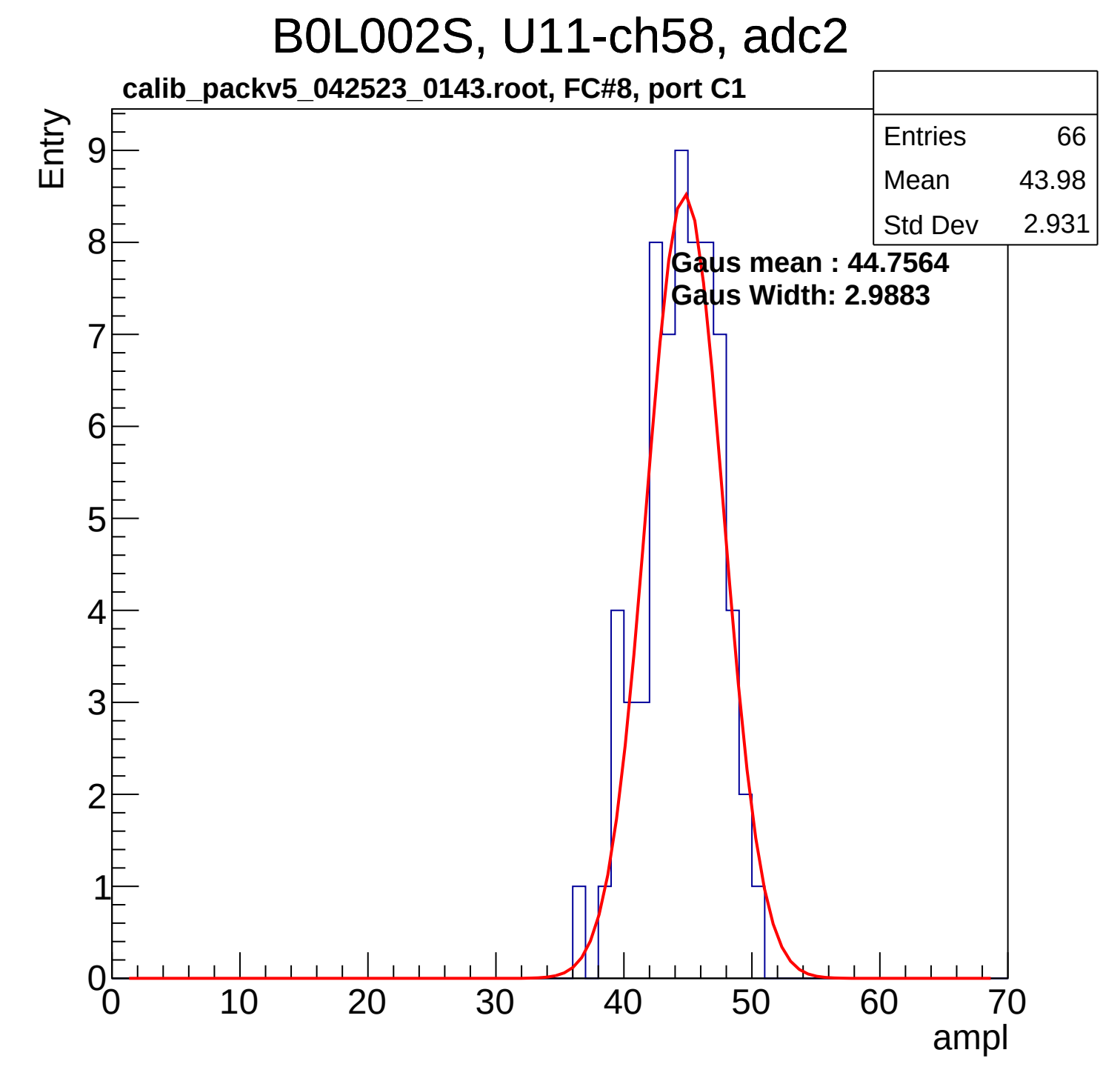
B0L002S, U11-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.98
Std Dev 2.931
Gaus mean : 44.7564
Gaus Width: 2.9883
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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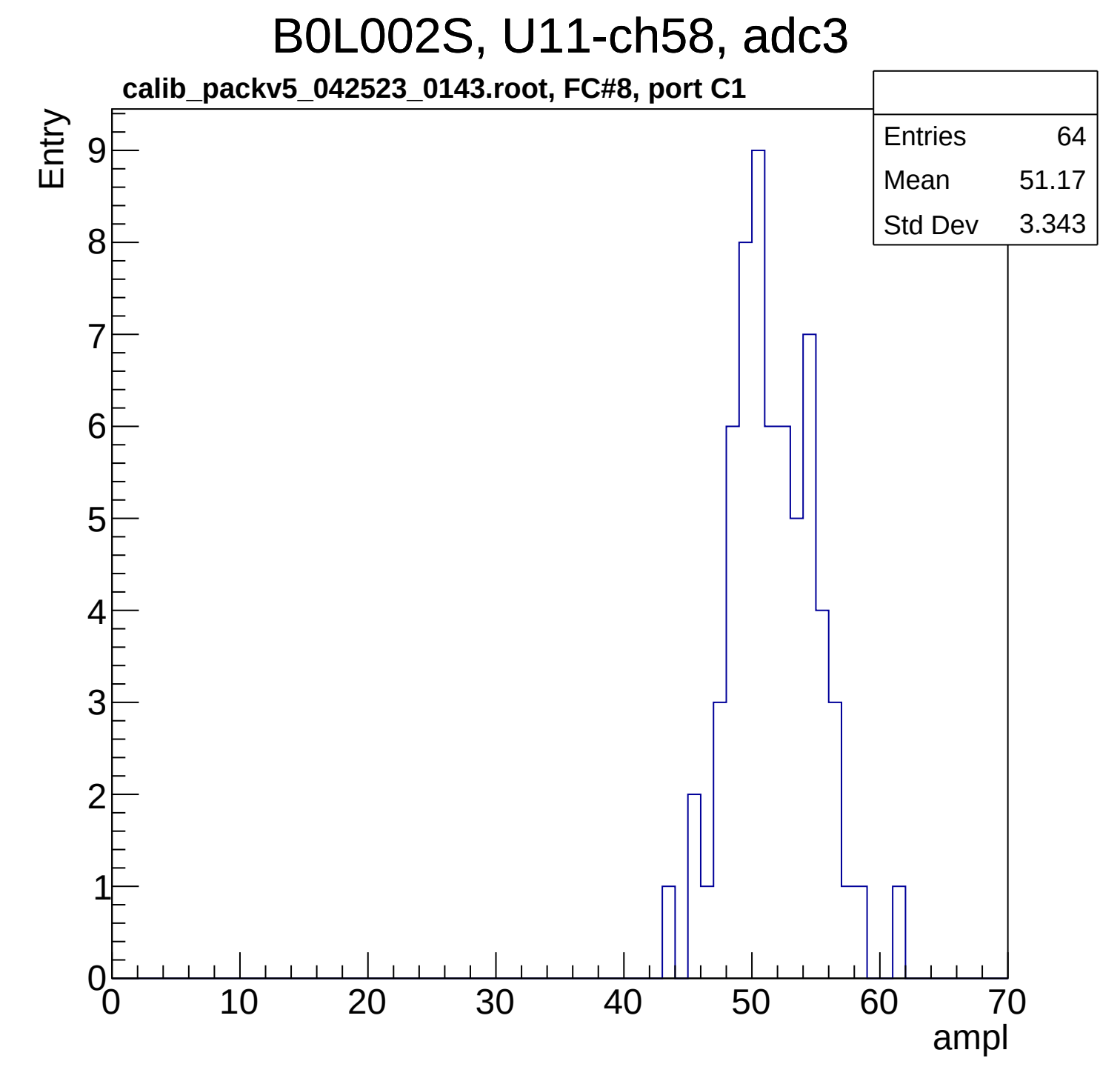
B0L002S, U11-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.17
Std Dev 3.343
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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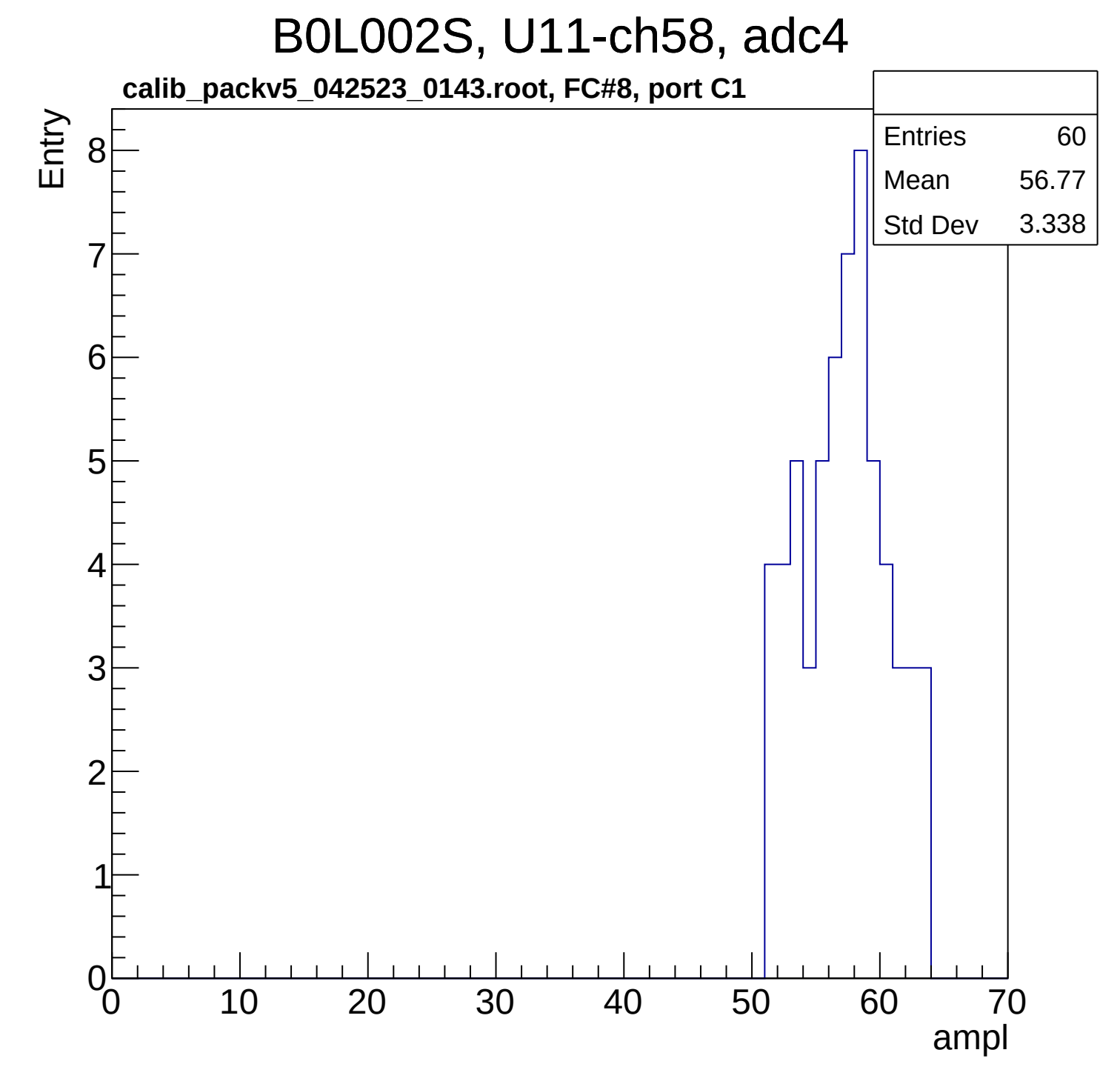
B0L002S, U11-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.77
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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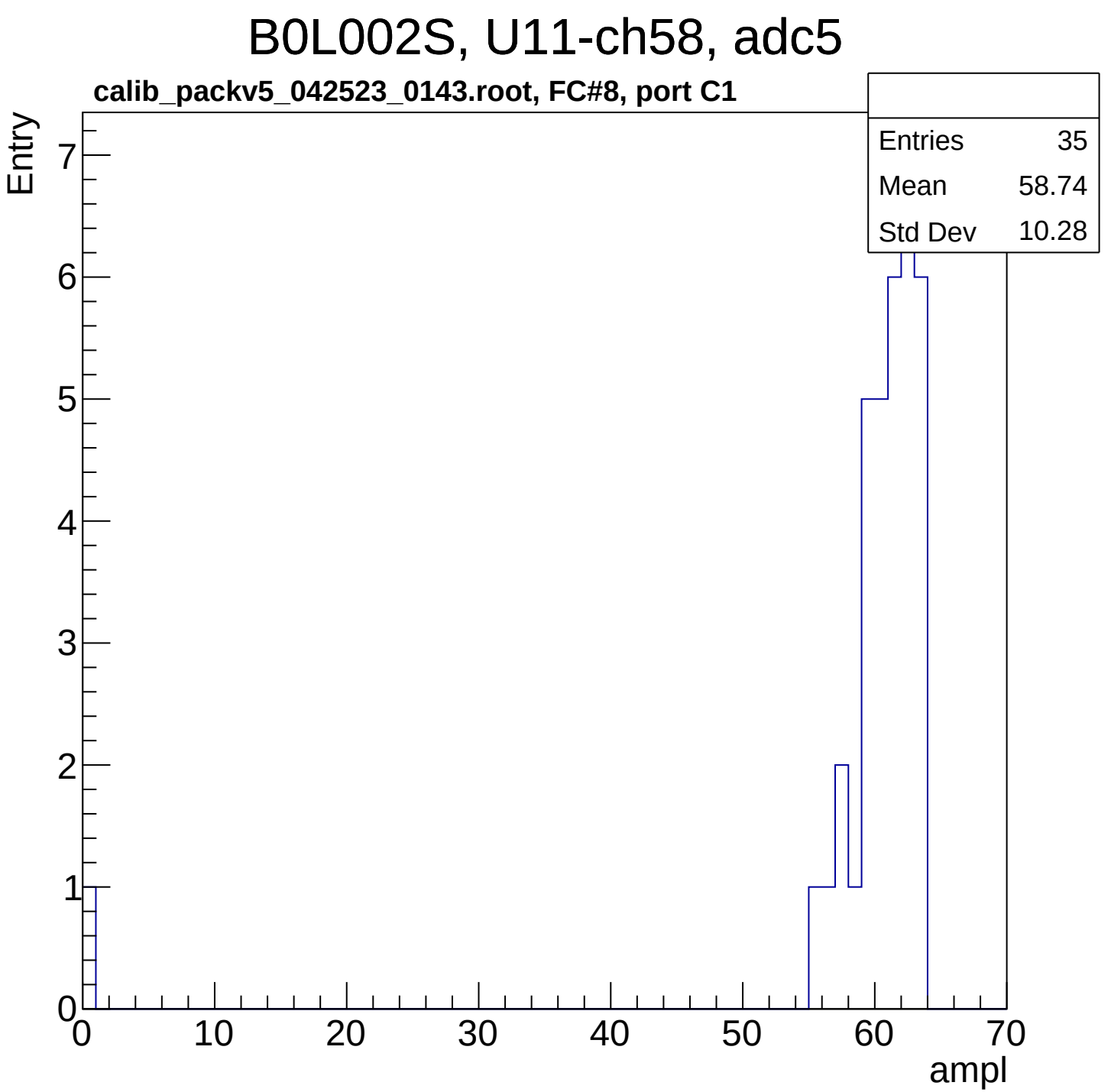
B0L002S, U11-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.74
Std Dev 10.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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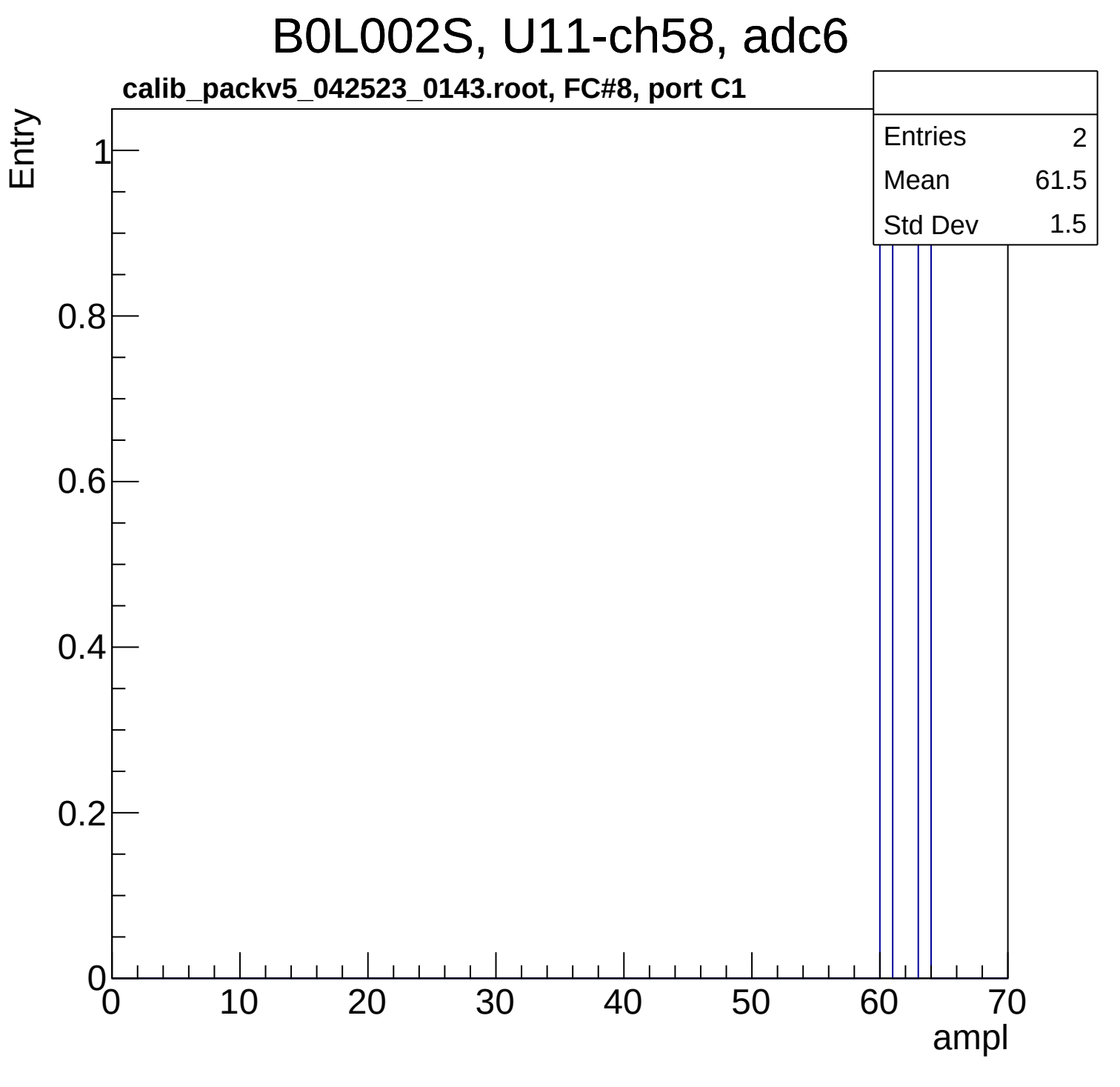
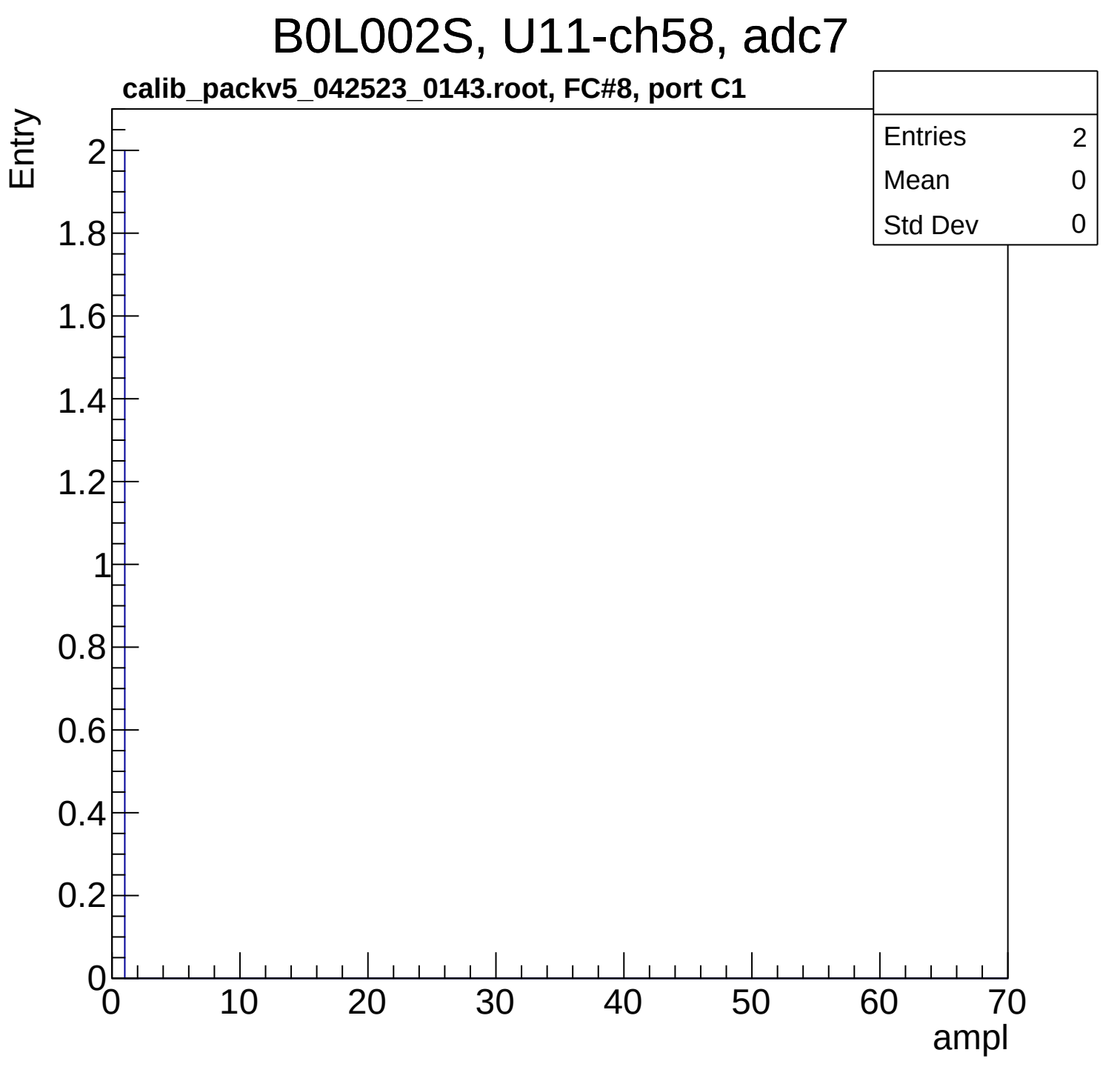
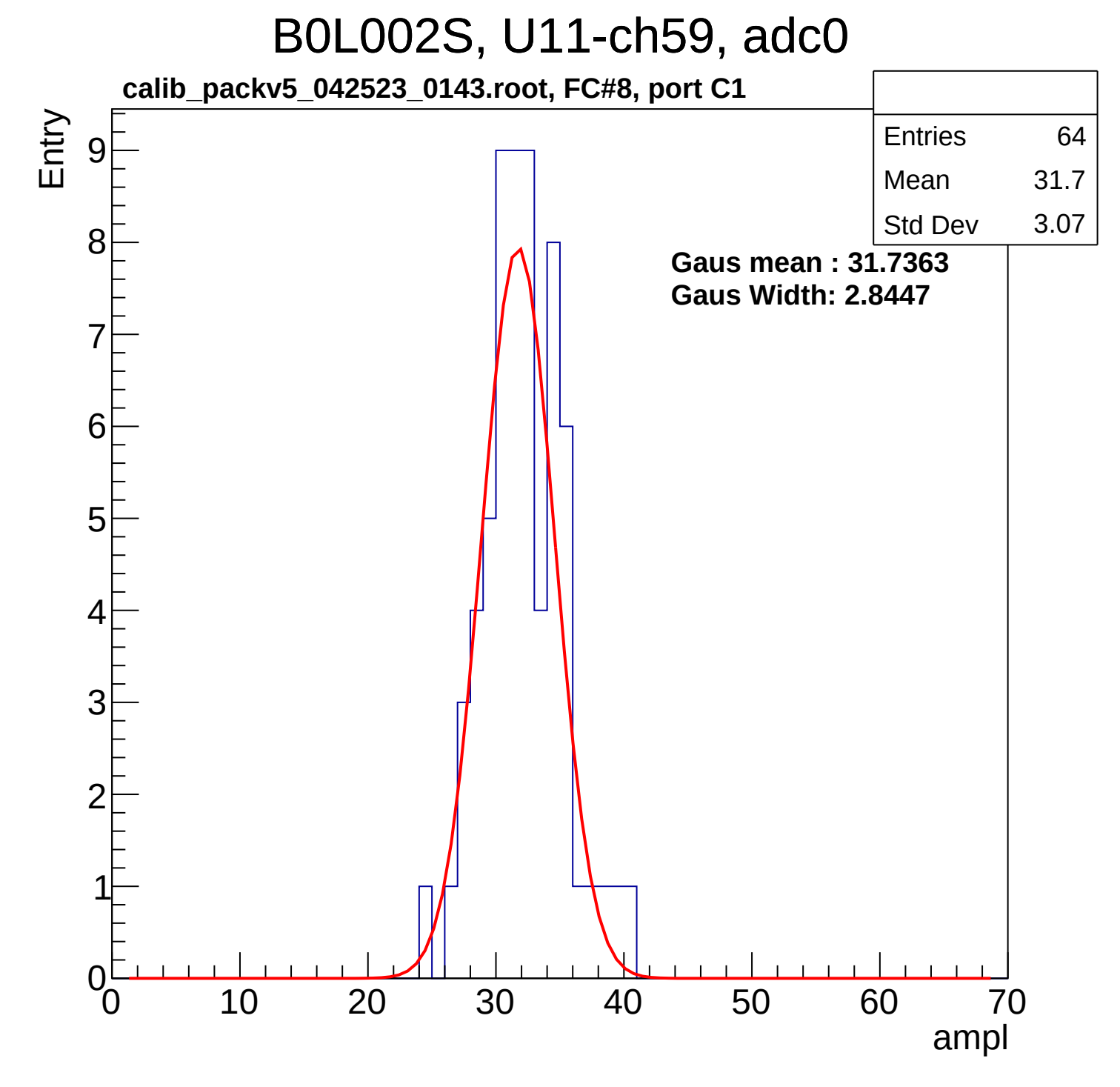

B0L002S, U11-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.7
Std Dev 3.07
Gaus mean : 31.7363
Gaus Width: 2.8447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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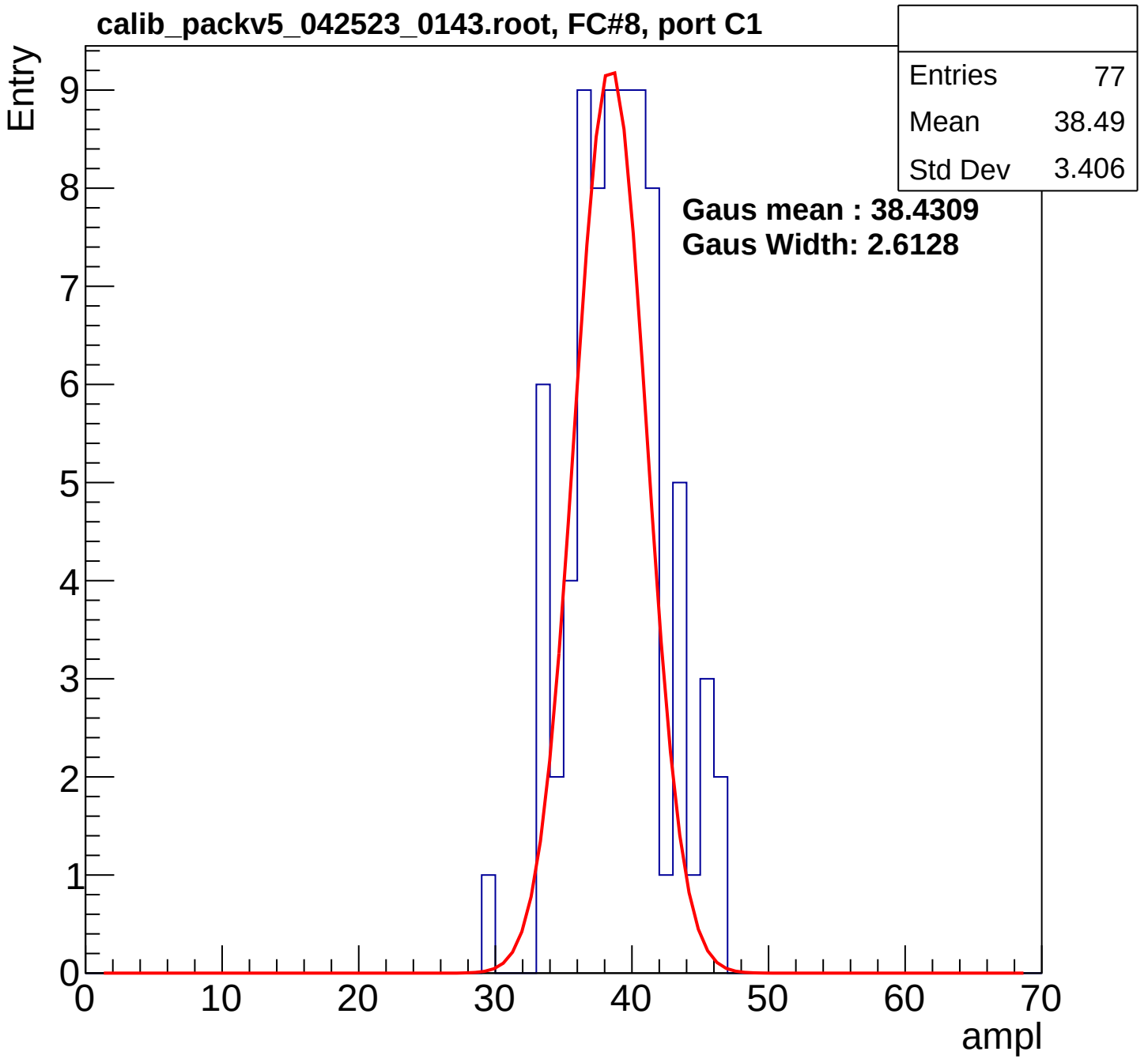
60
70


B0L002S, U11-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.49
Std Dev 3.406
Gaus mean : 38.4309
Gaus Width: 2.6128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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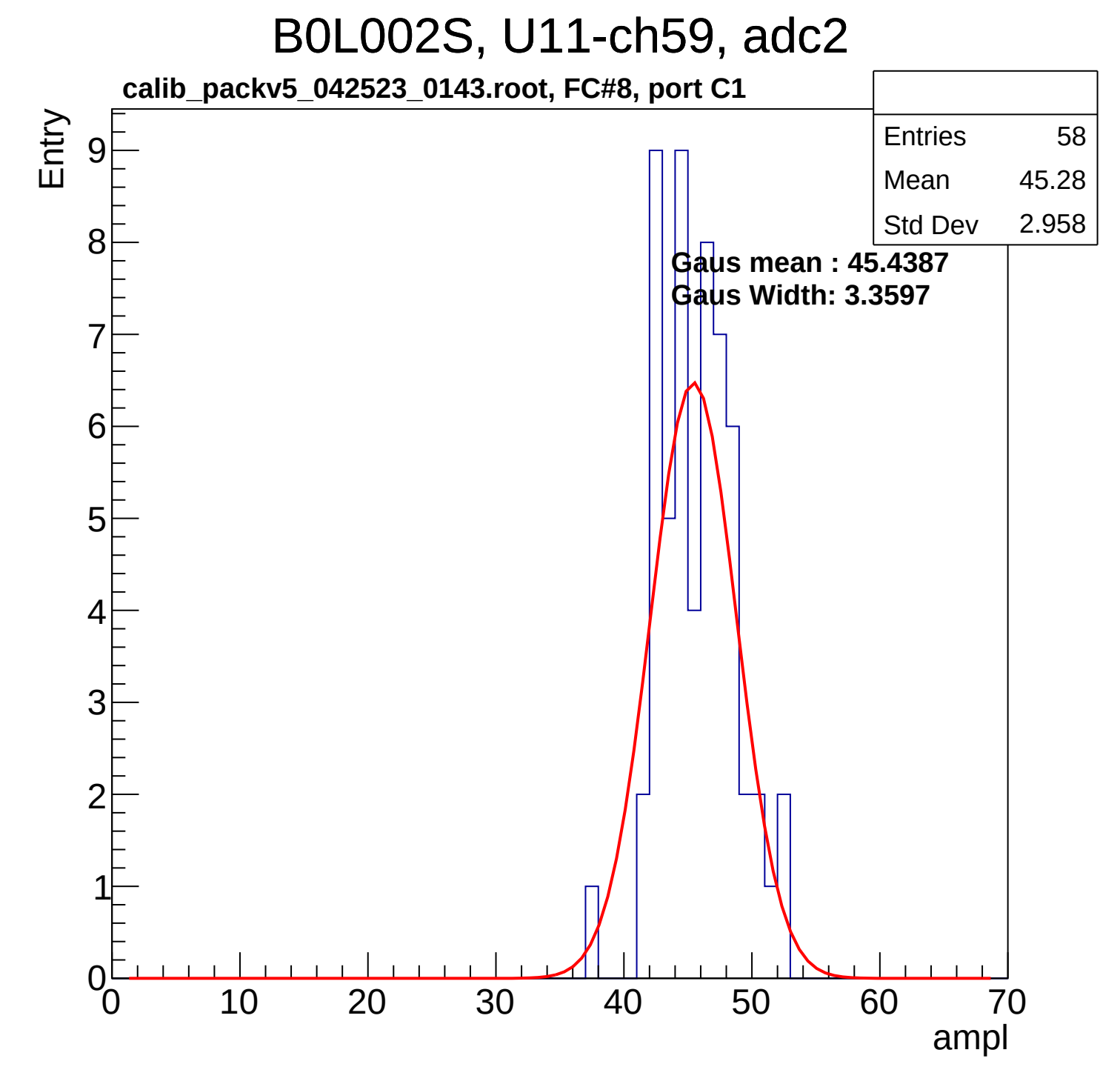
B0L002S, U11-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 45.28
Std Dev 2.958
Gaus mean : 45.4387
Gaus Width: 3.3597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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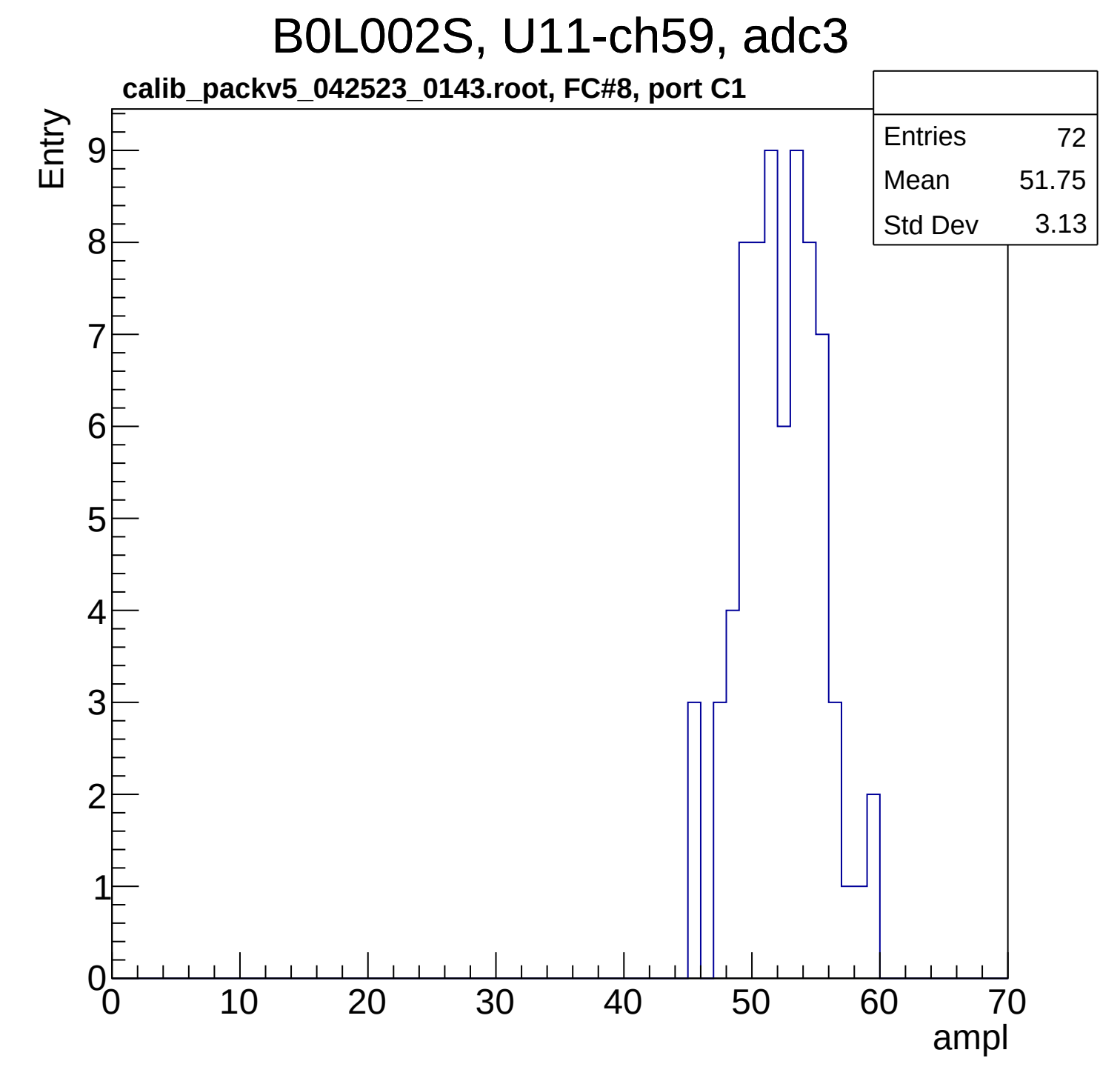
B0L002S, U11-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.75
Std Dev 3.13
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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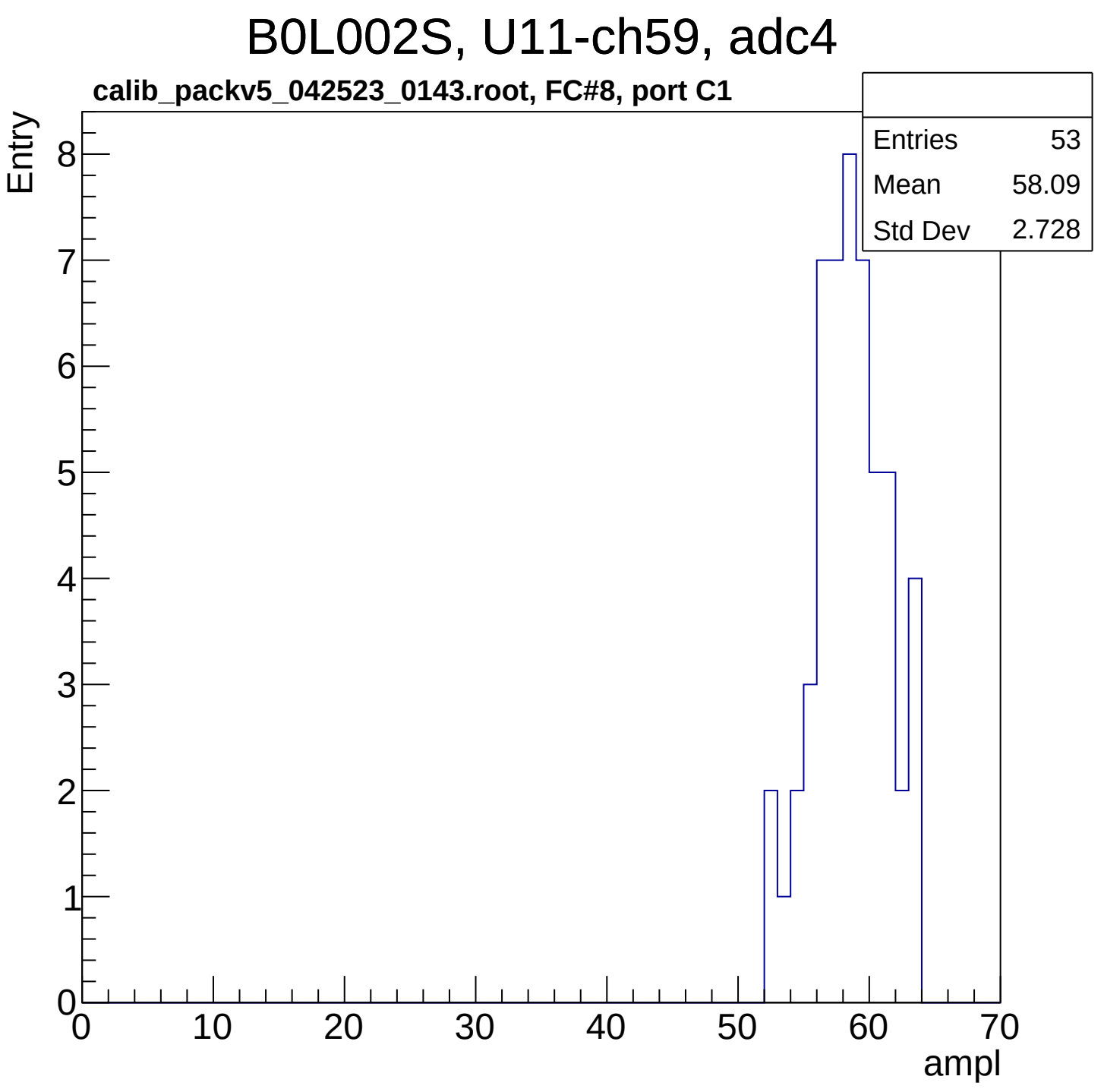
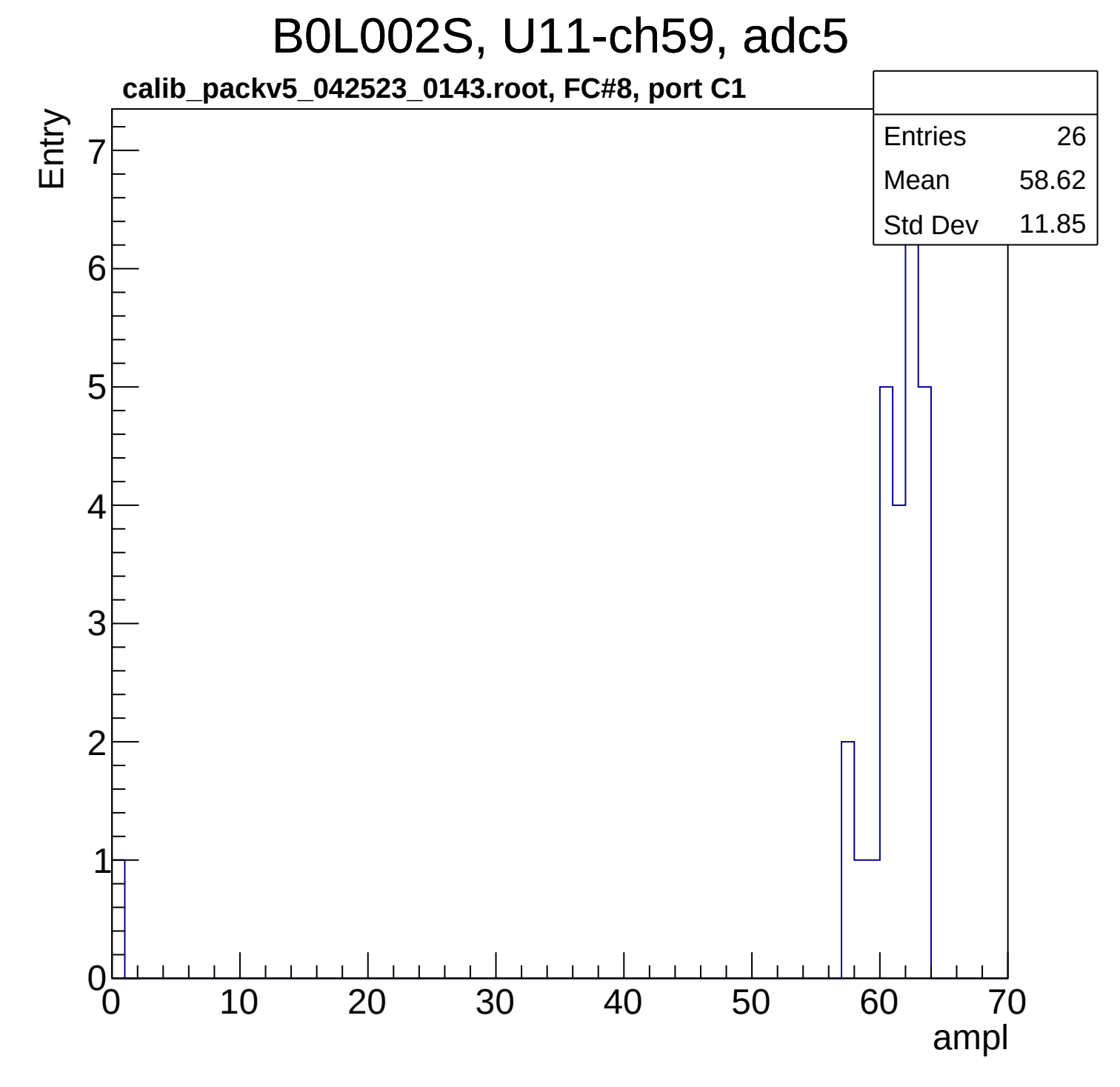
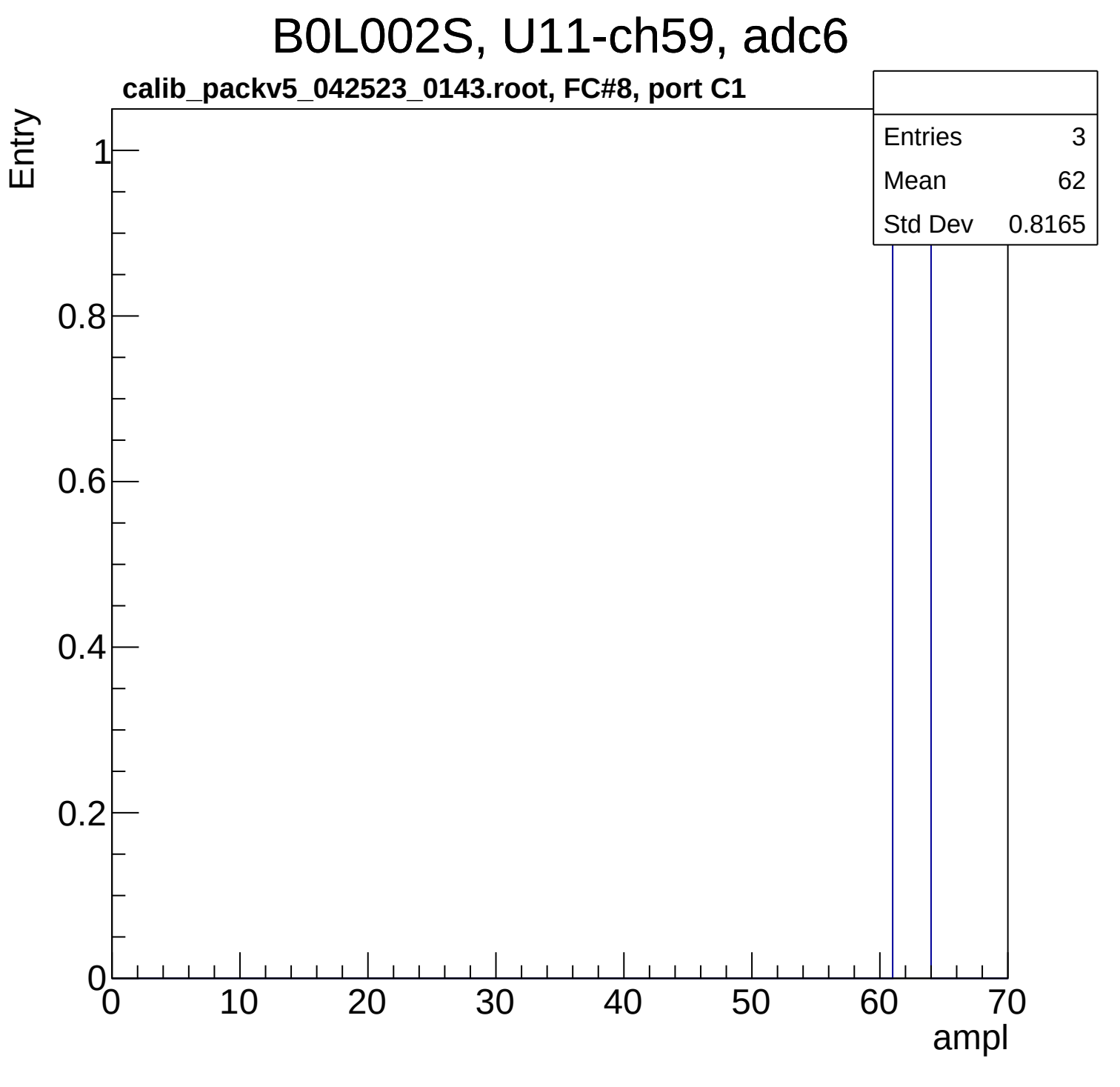
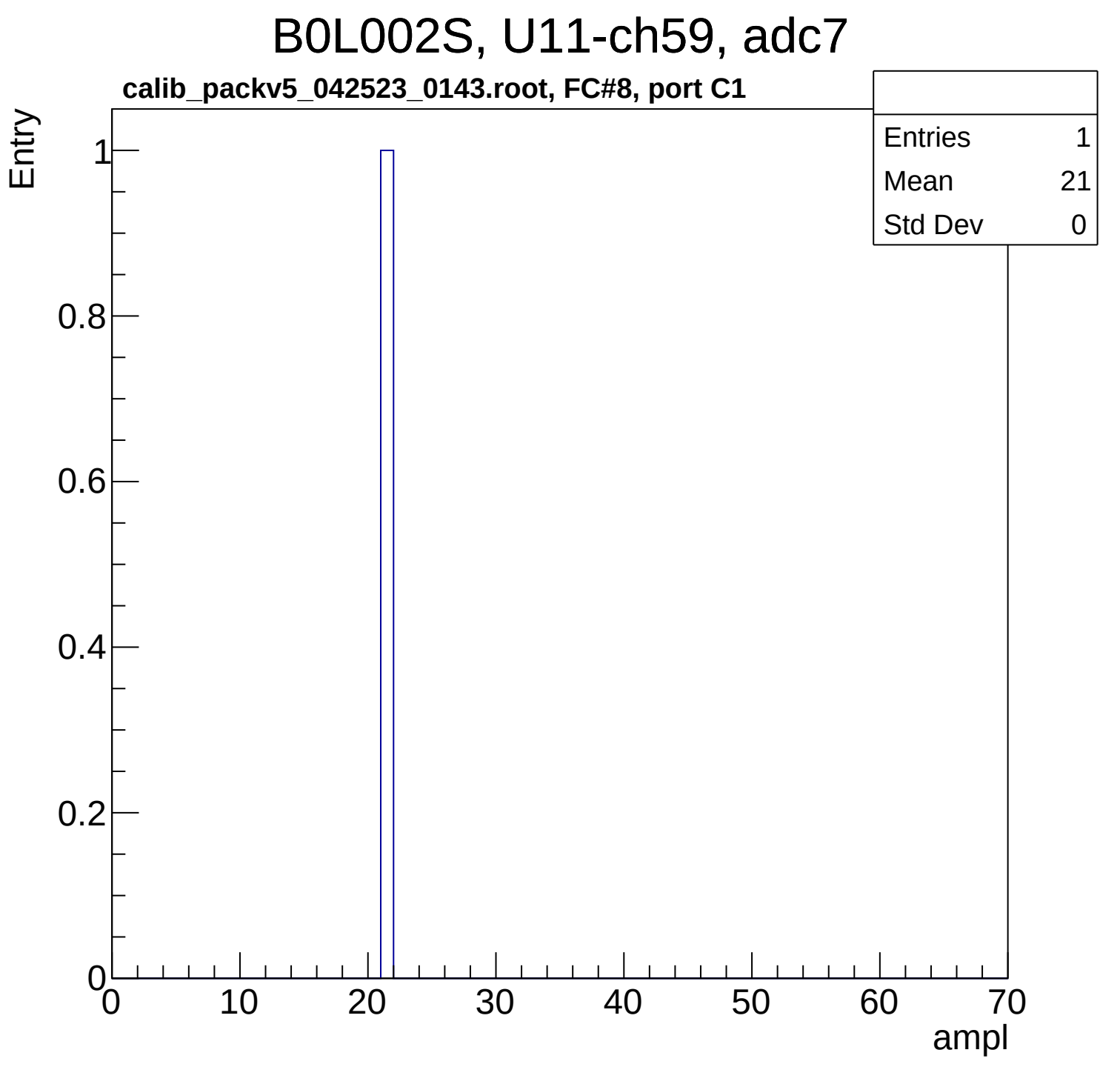

B0L002S, U11-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 58.09
Std Dev 2.728
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.62
Std Dev 11.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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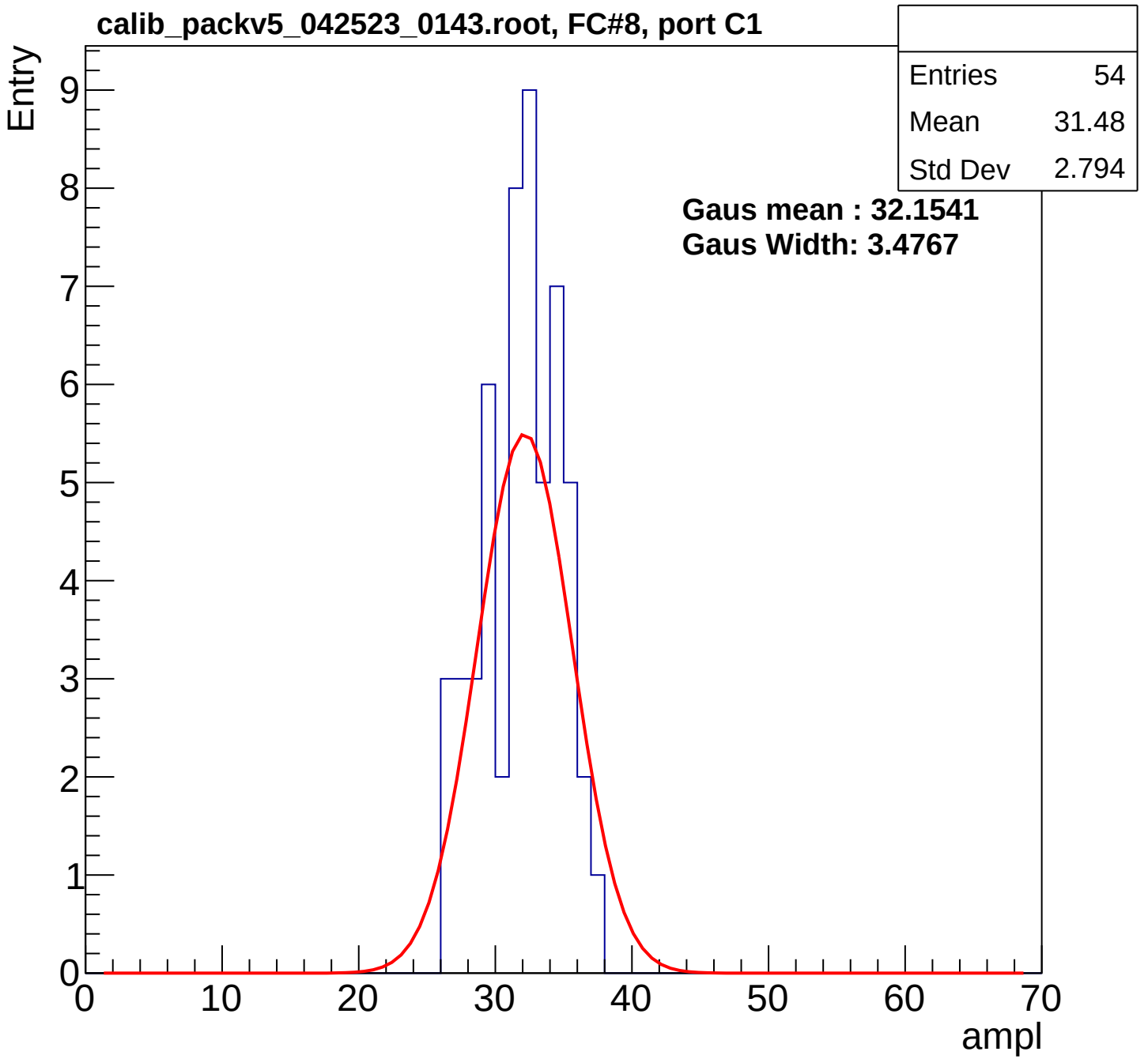
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 31.48
Std Dev 2.794
Gaus mean : 32.1541
Gaus Width: 3.4767
Entry
ampl
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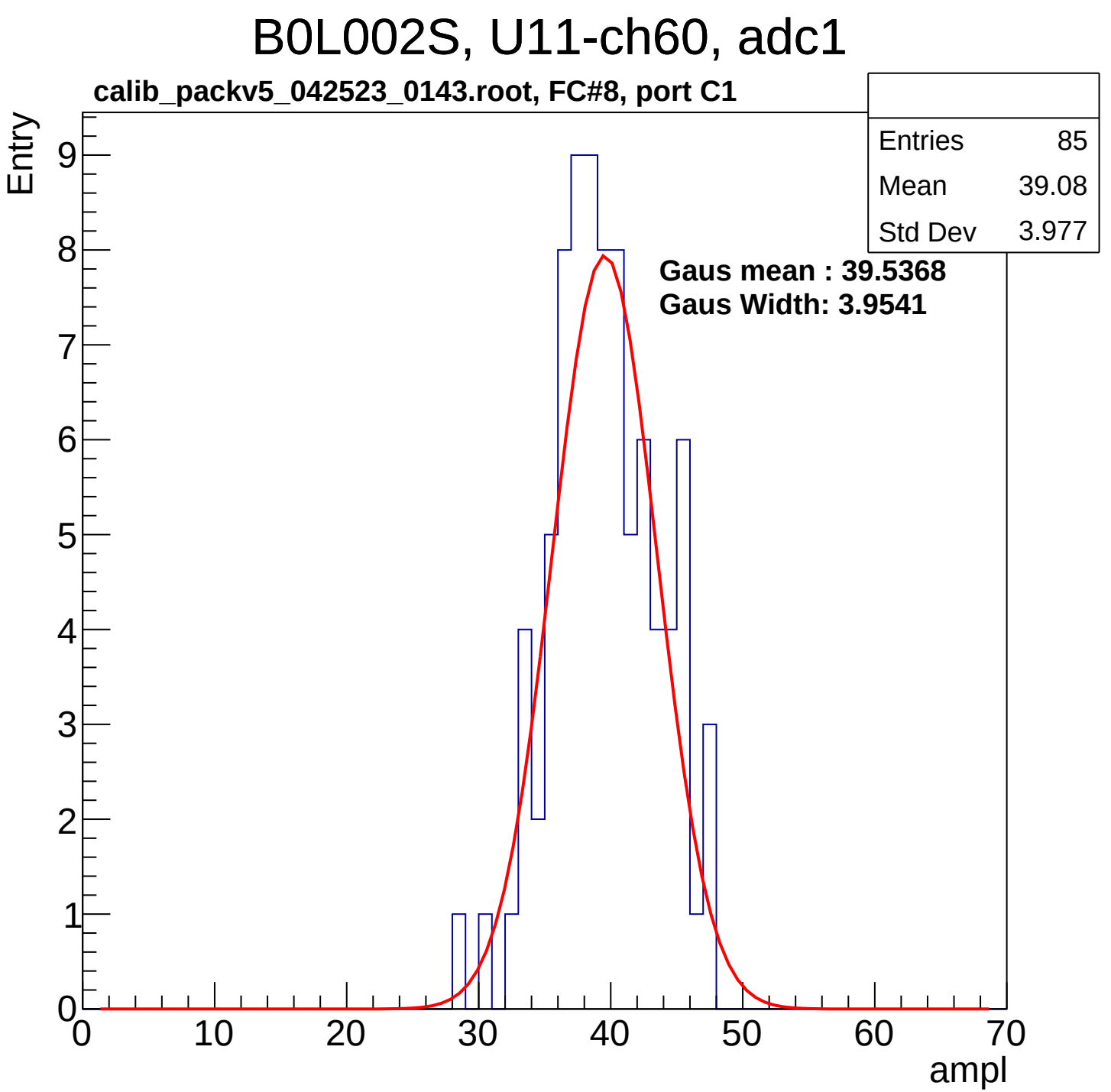
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 39.08
Std Dev 3.977
Gaus mean : 39.5368
Gaus Width: 3.9541
Entry
ampl
0
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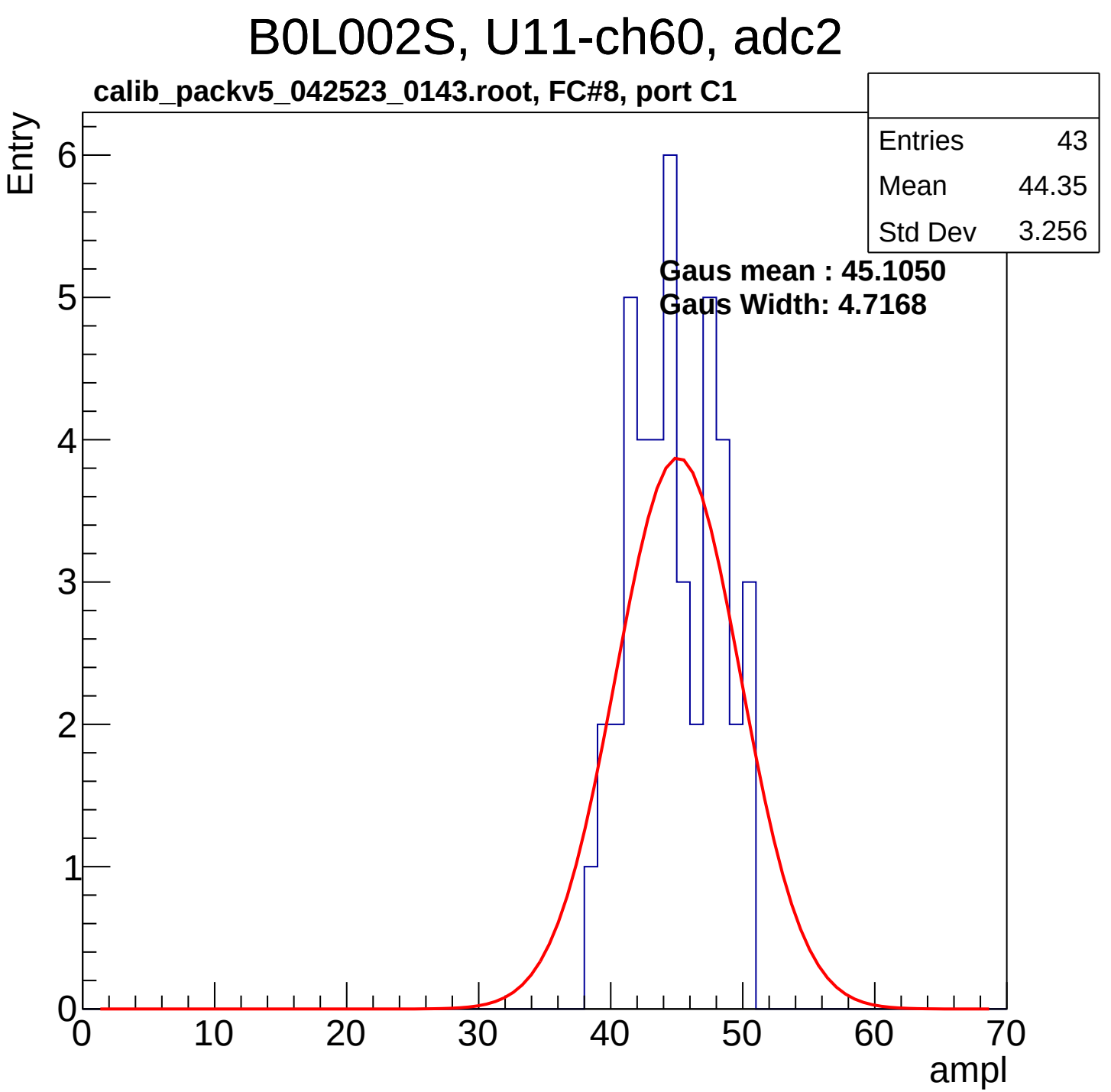
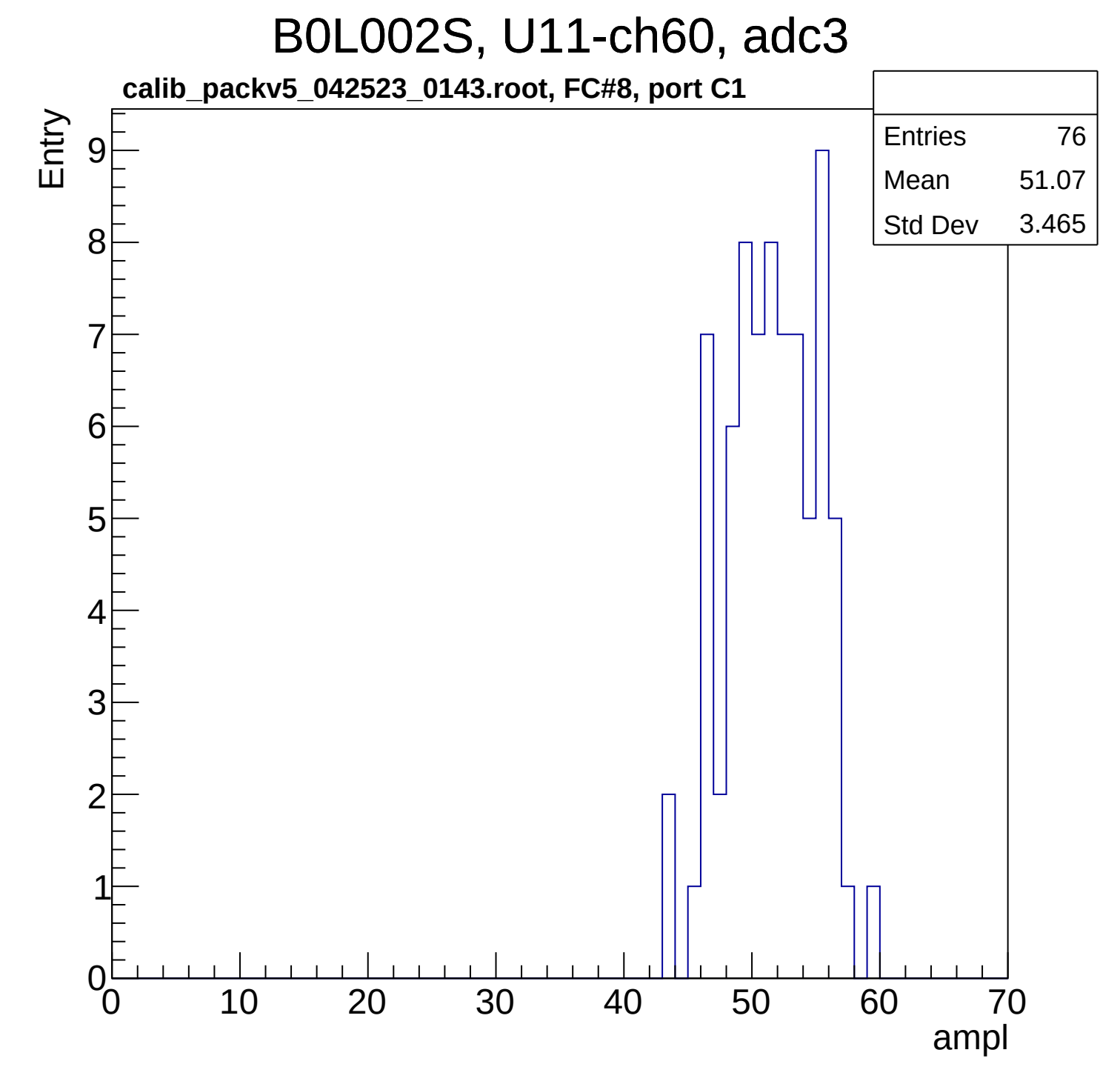
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 44.35
Std Dev 3.256
Gaus mean : 45.1050
Gaus Width: 4.7168
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U11-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.07
Std Dev 3.465
Entry
ampl
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3
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5
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0
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60
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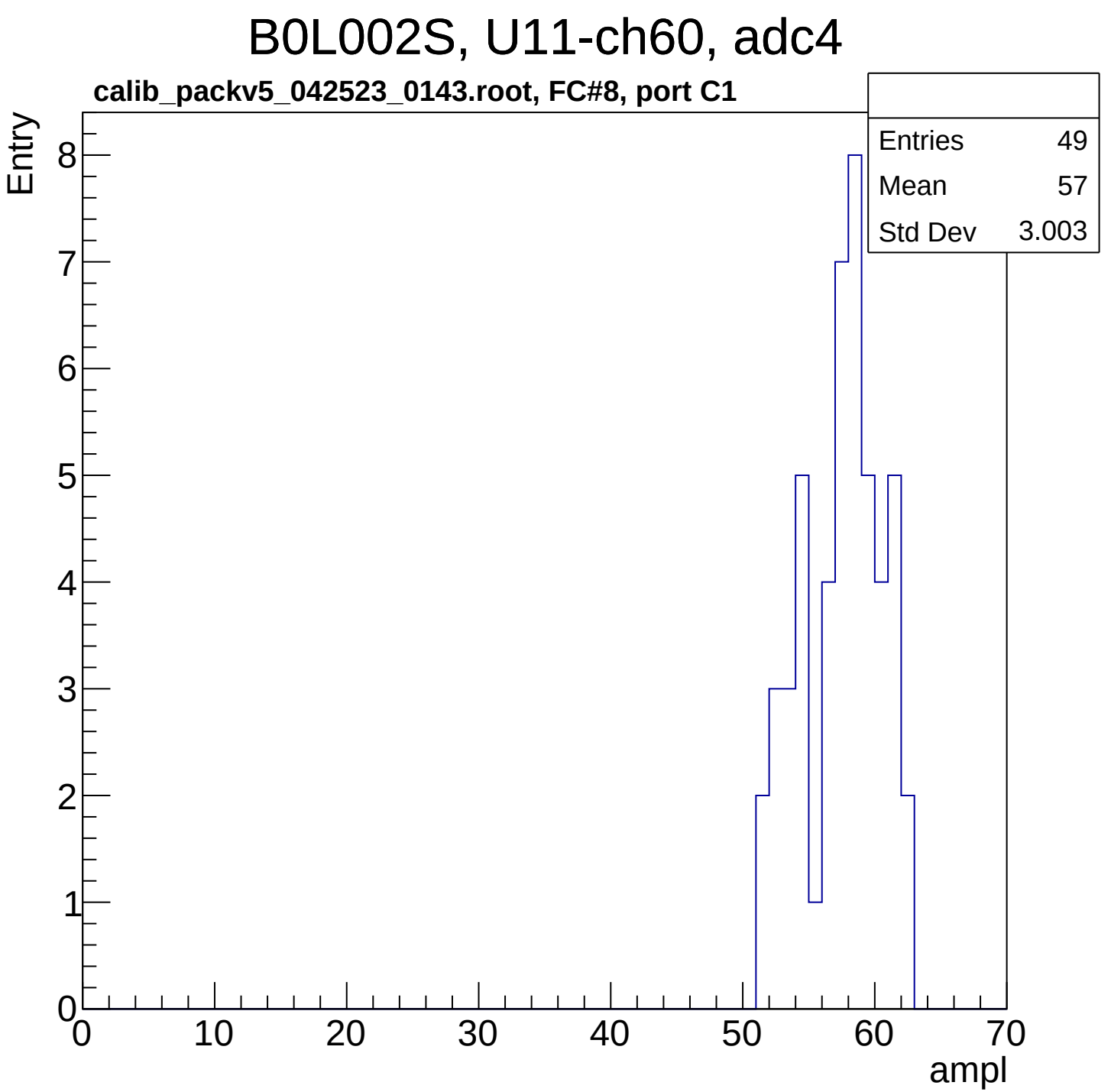
B0L002S, U11-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57
Std Dev 3.003
Entry
ampl
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7
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0
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60
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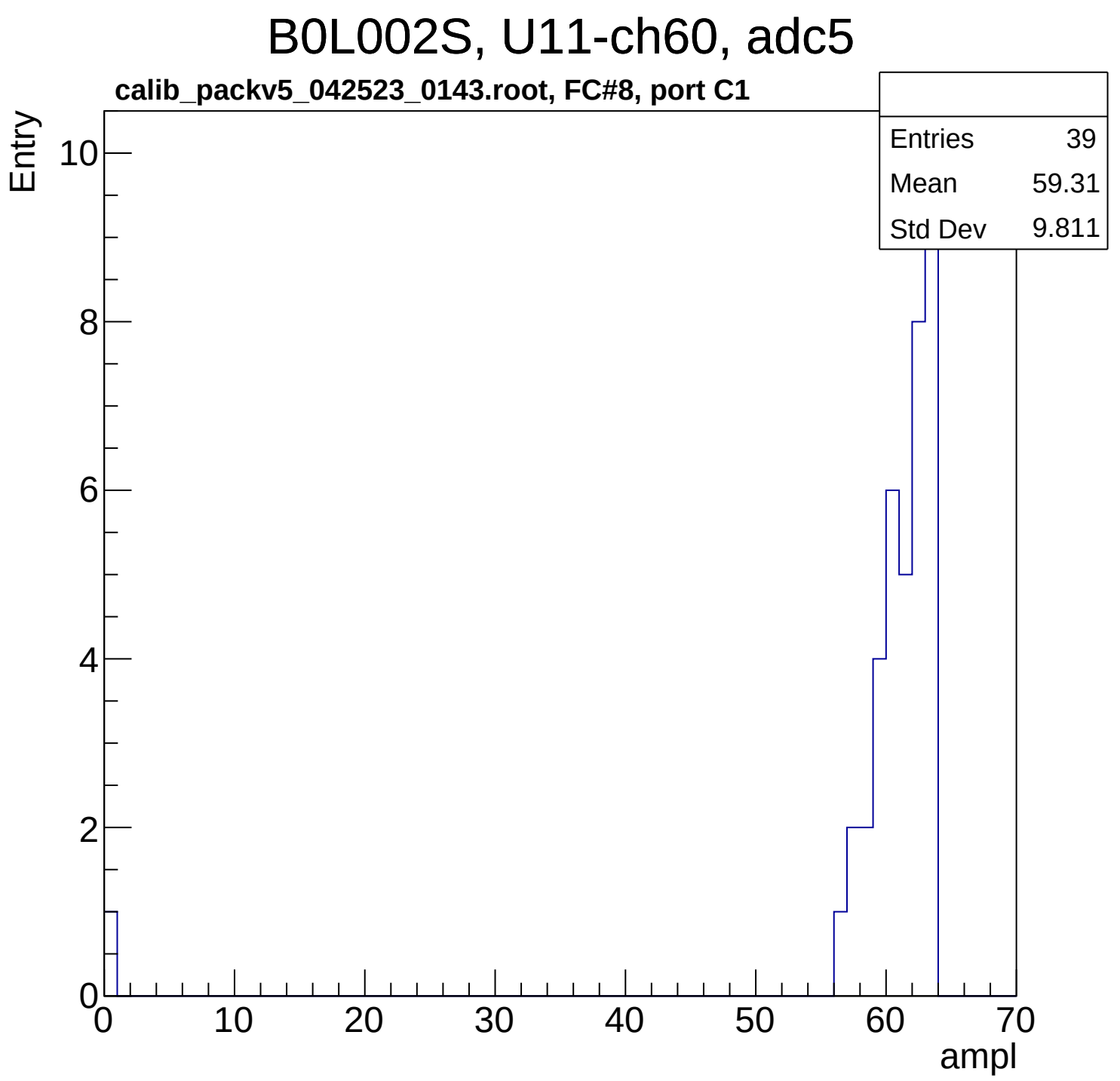
B0L002S, U11-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.31
Std Dev 9.811
Entry
ampl
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0
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60
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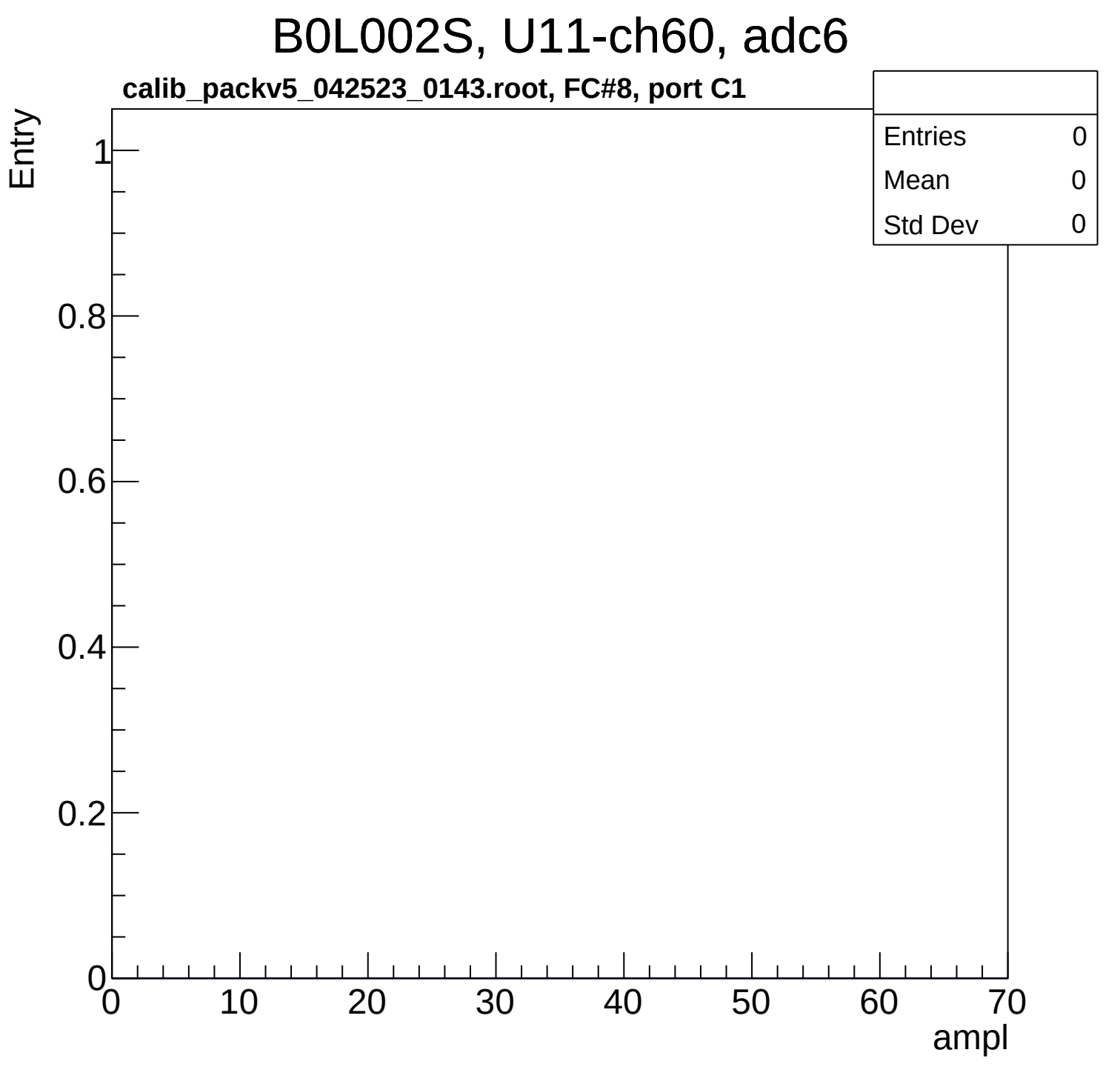
B0L002S, U11-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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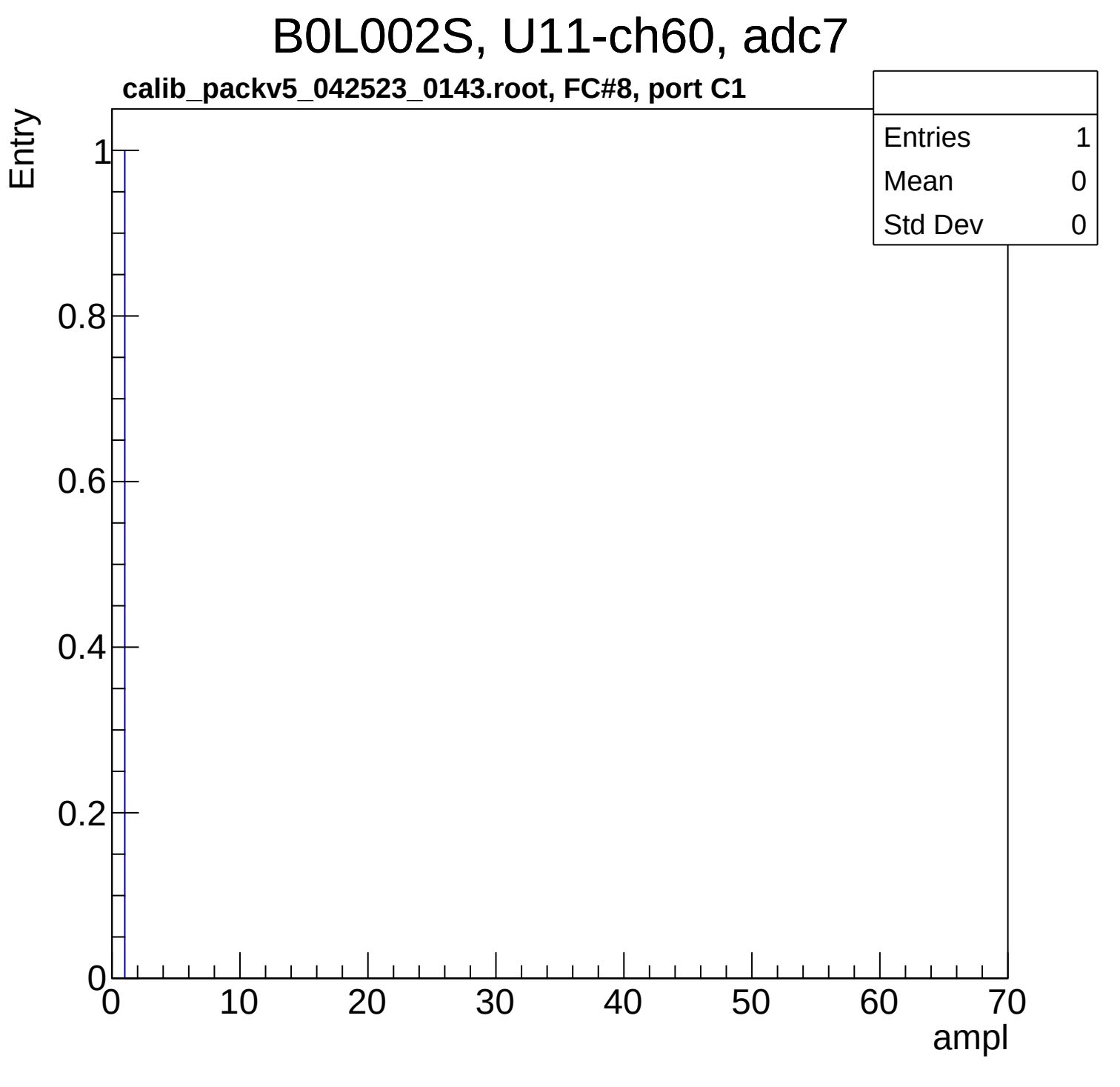
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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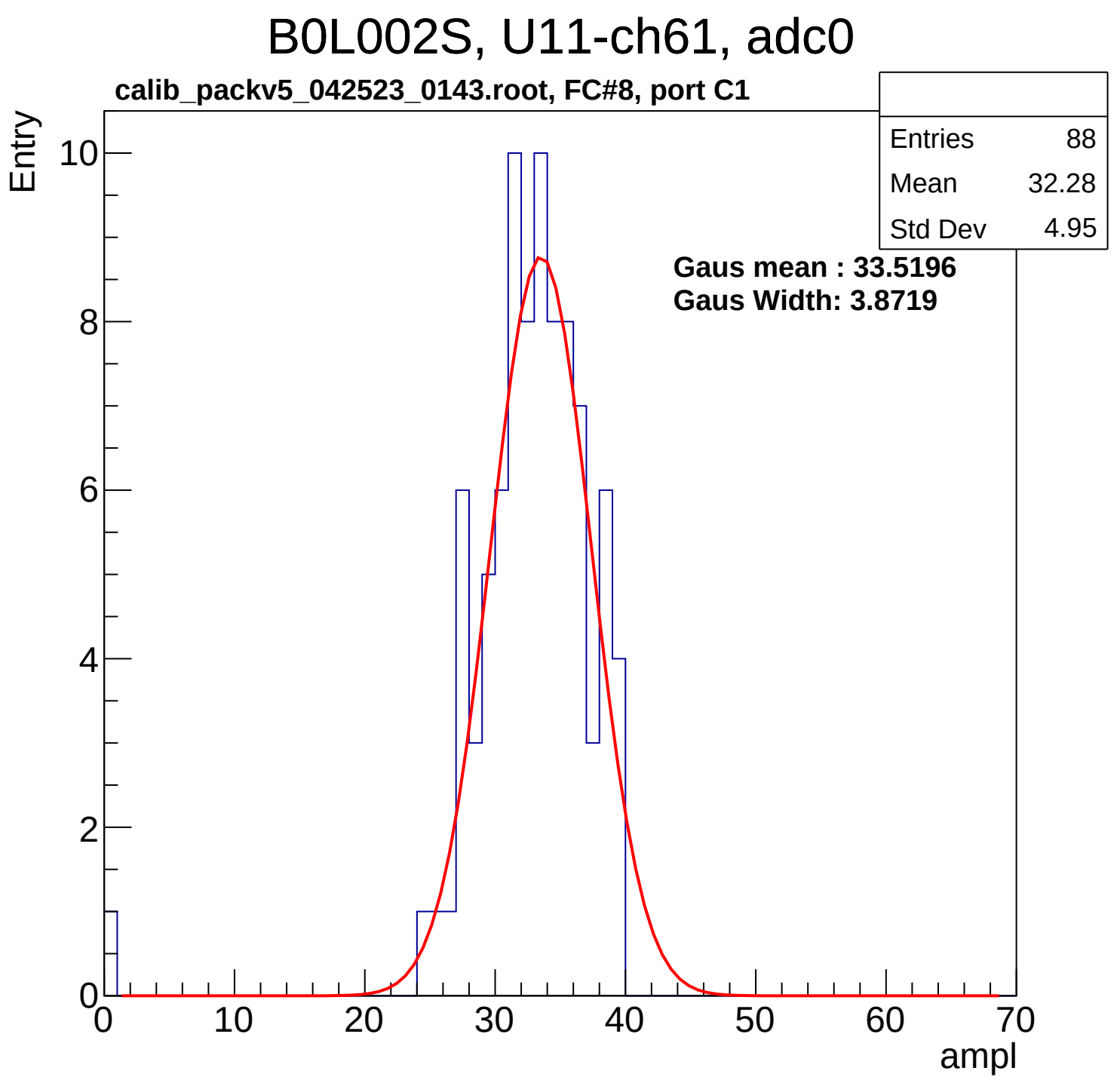
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 32.28
Std Dev 4.95
Gaus mean : 33.5196
Gaus Width: 3.8719
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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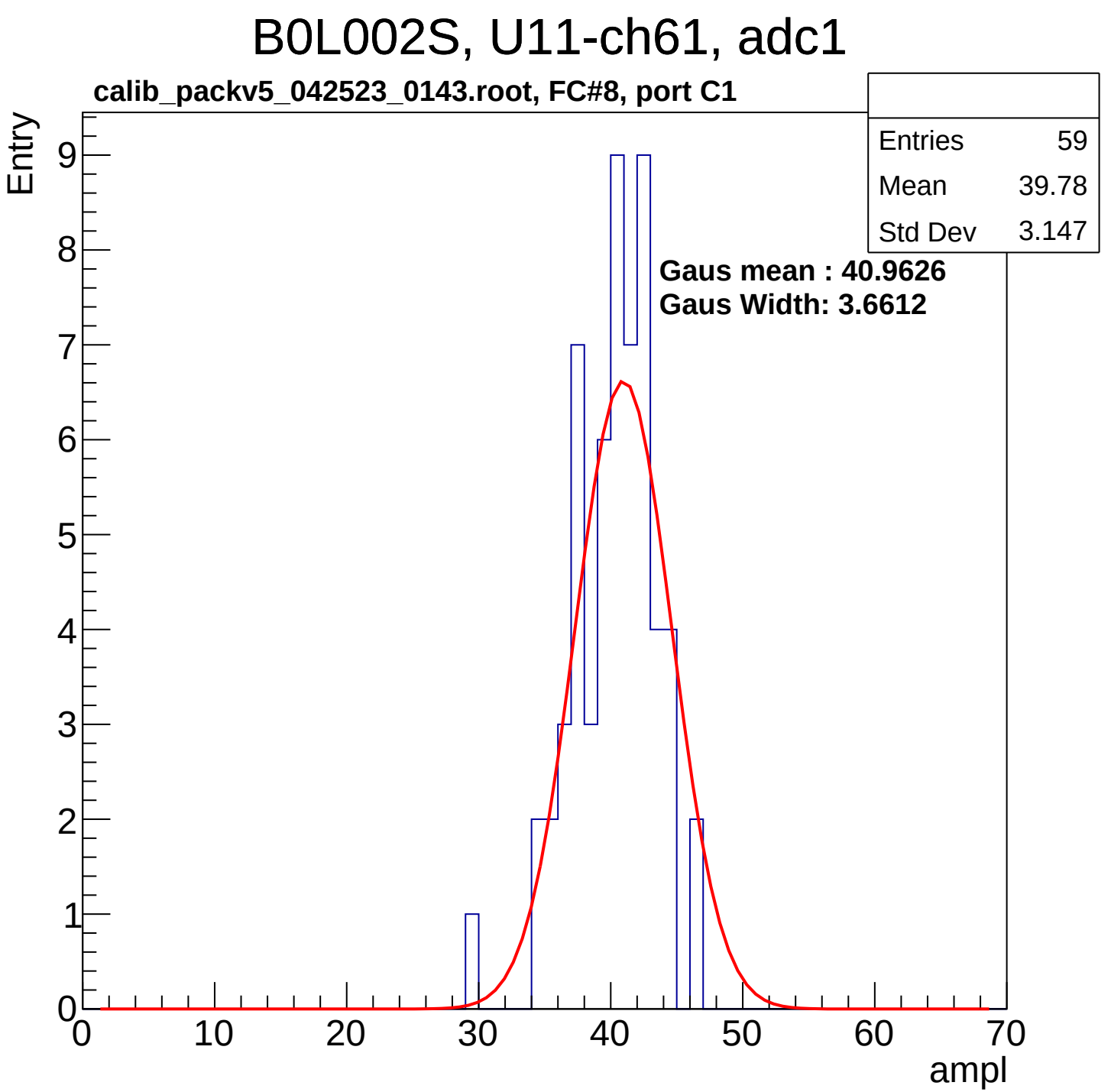
B0L002S, U11-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 39.78
Std Dev 3.147
Gaus mean : 40.9626
Gaus Width: 3.6612
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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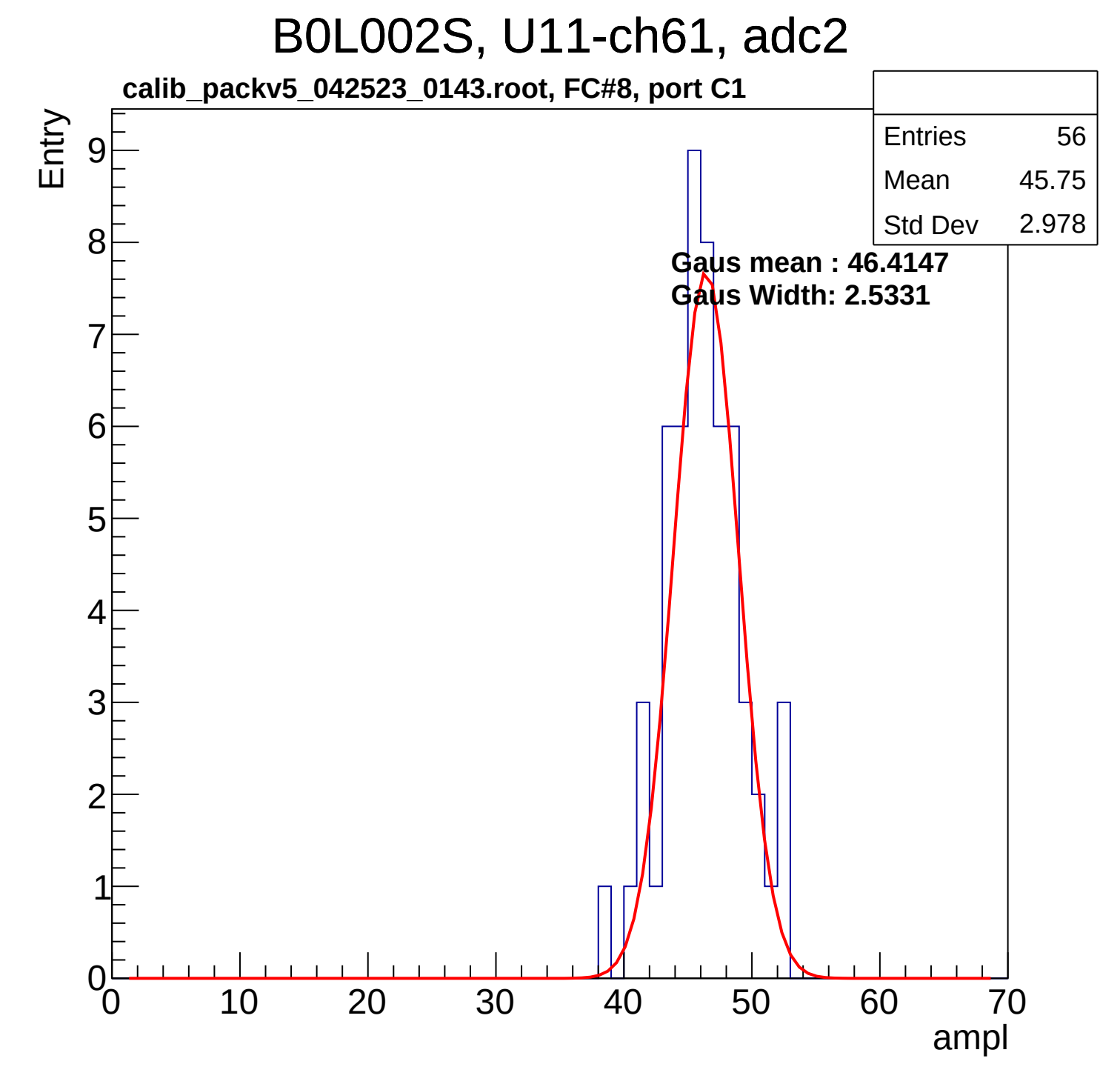
B0L002S, U11-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.75
Std Dev 2.978
Gaus mean : 46.4147
Gaus Width: 2.5331
Entry
ampl
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2
3
4
5
6
7
8
9
0
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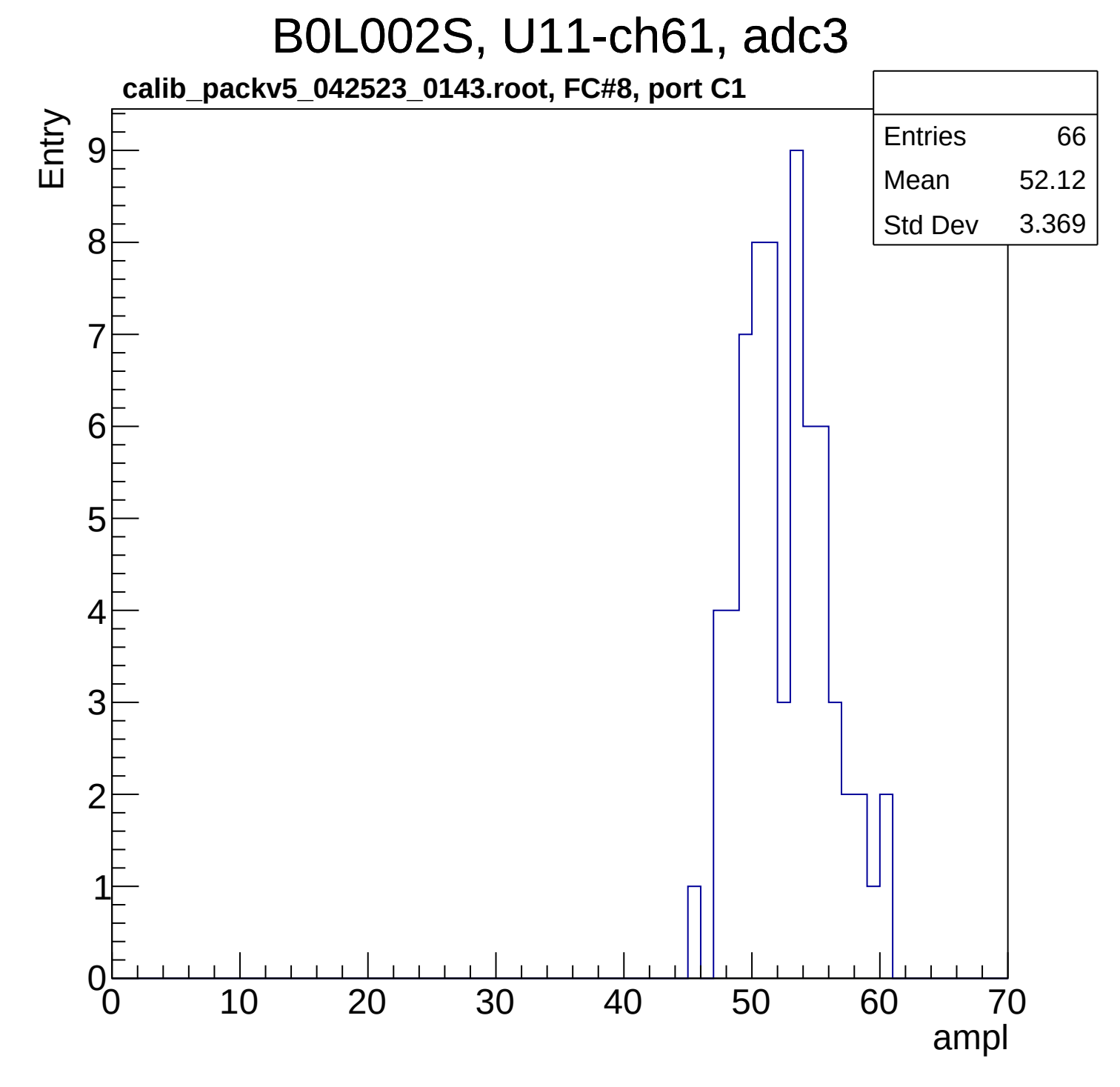
B0L002S, U11-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 52.12
Std Dev 3.369
Entry
ampl
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4
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6
7
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0
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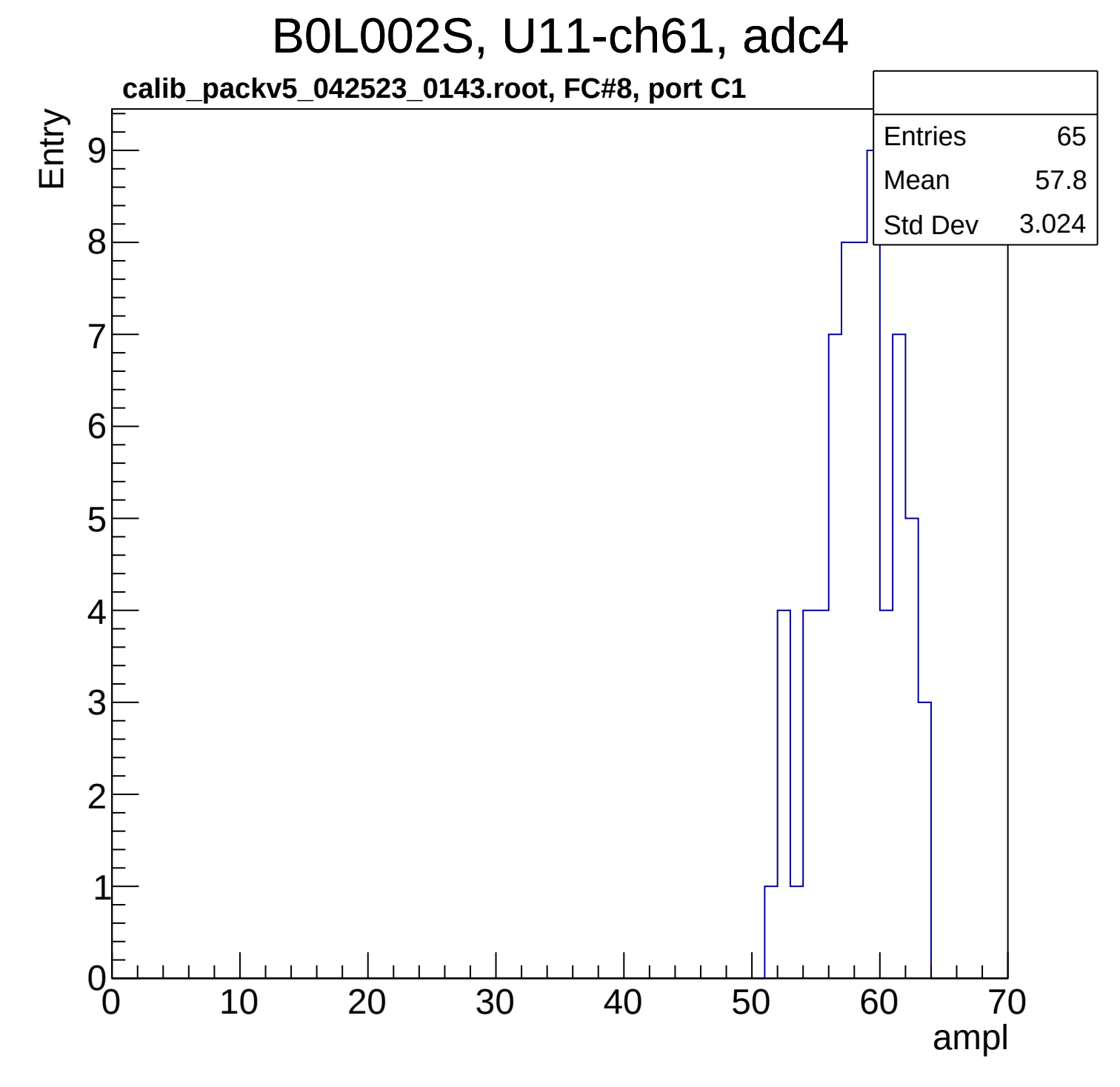
B0L002S, U11-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.8
Std Dev 3.024
Entry
ampl
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3
4
5
6
7
8
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0
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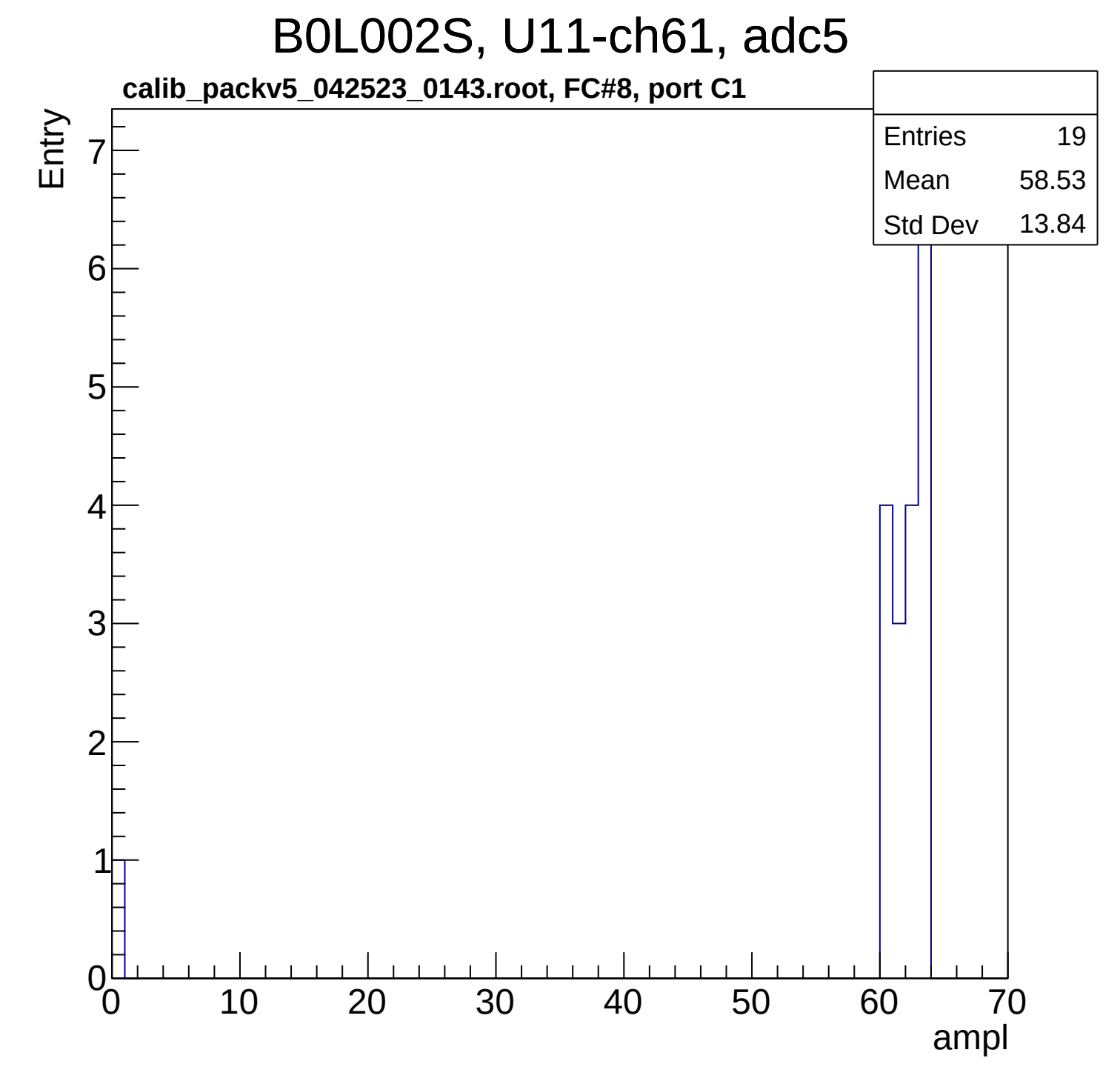
B0L002S, U11-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 58.53
Std Dev 13.84
Entry
ampl
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4
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0
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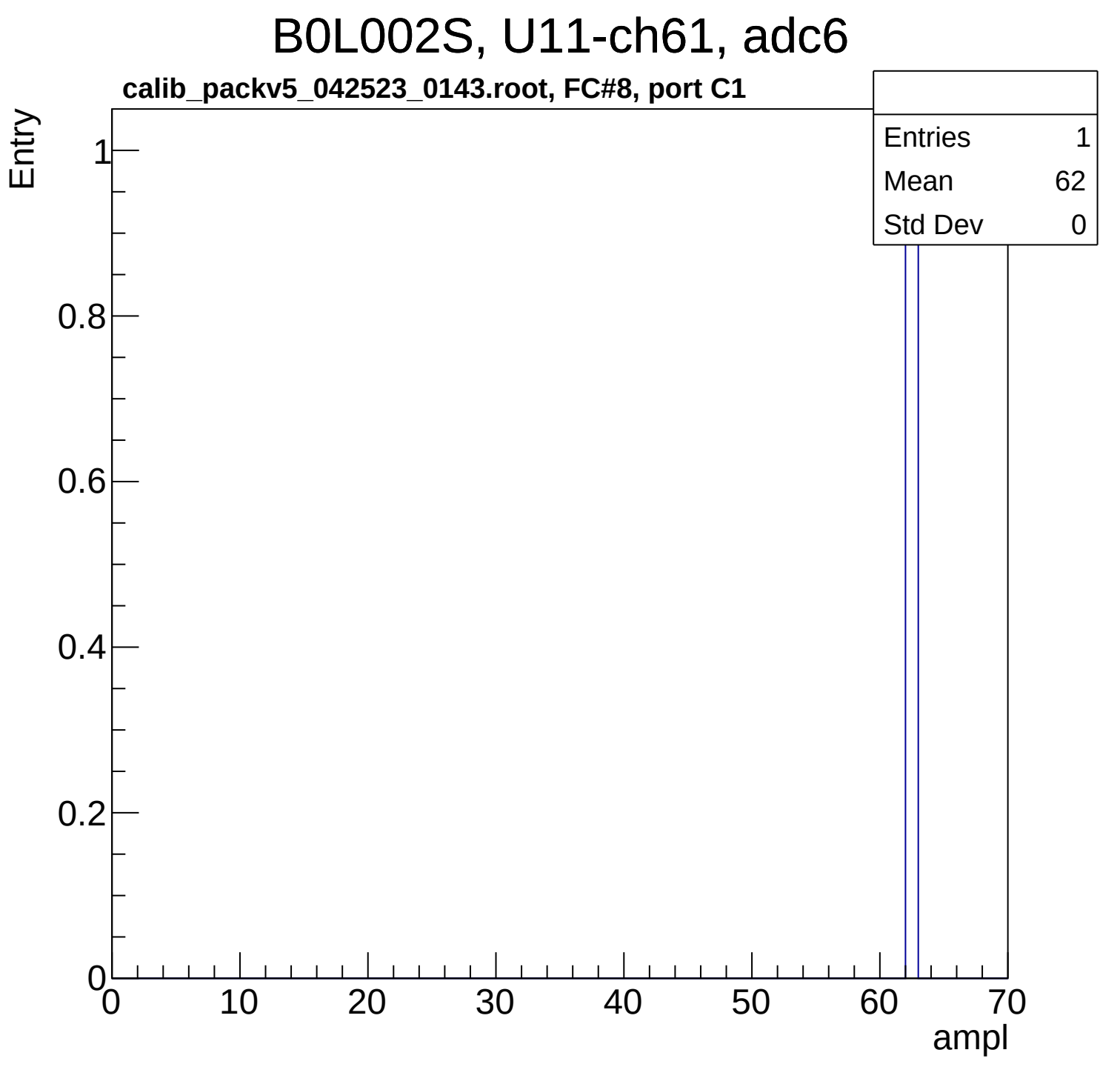
B0L002S, U11-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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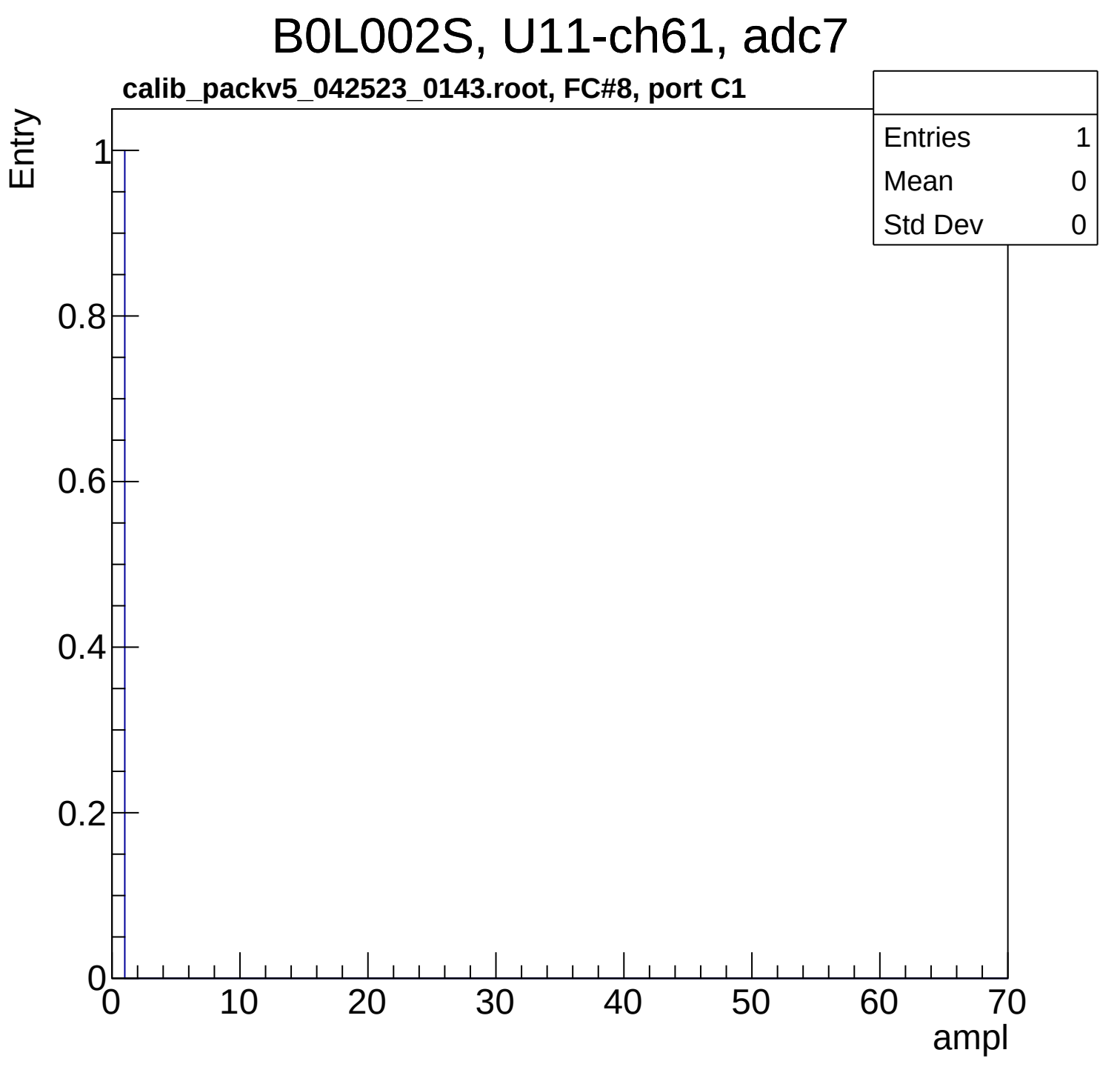
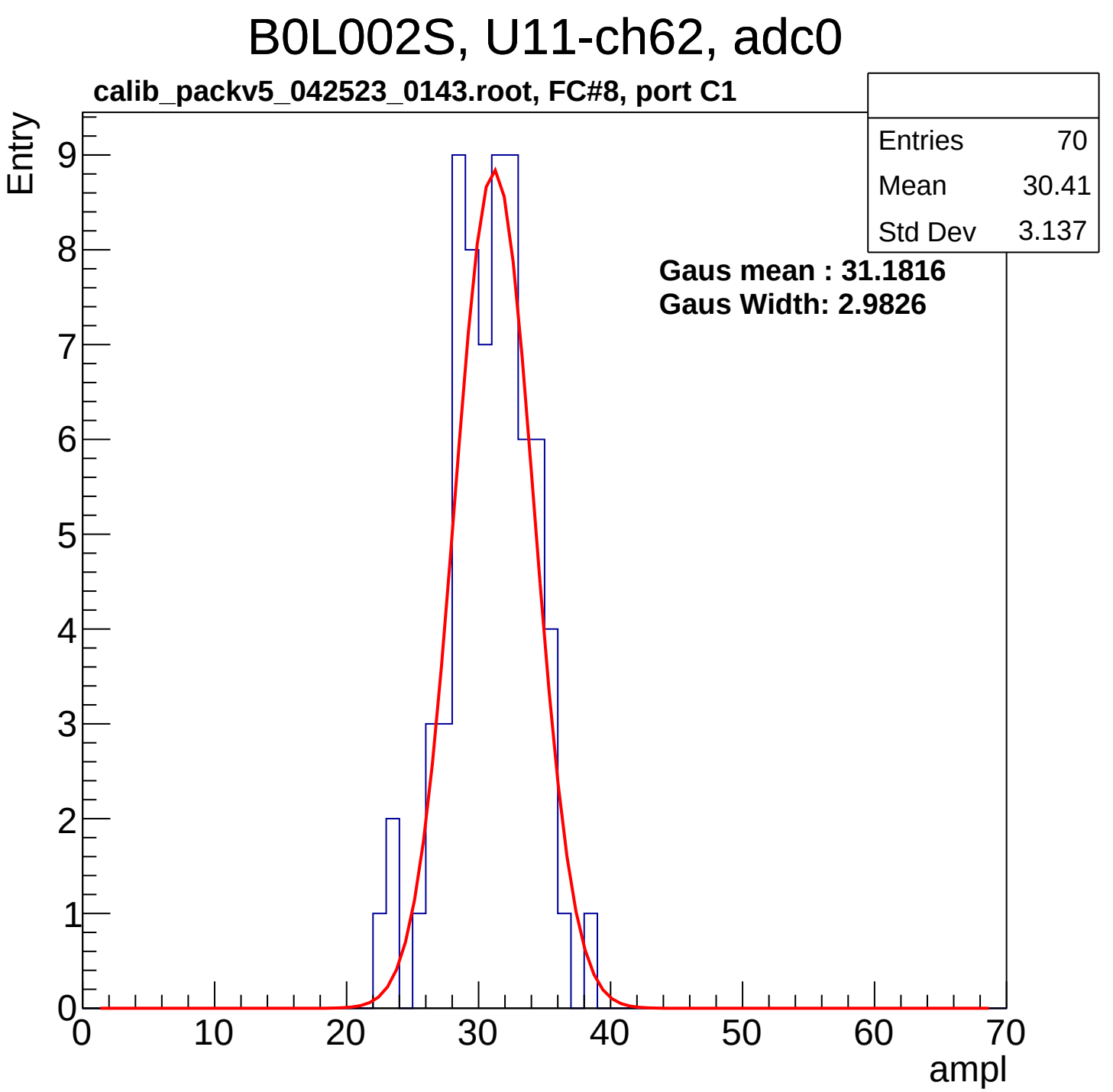
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.41
Std Dev 3.137
Gaus mean : 31.1816
Gaus Width: 2.9826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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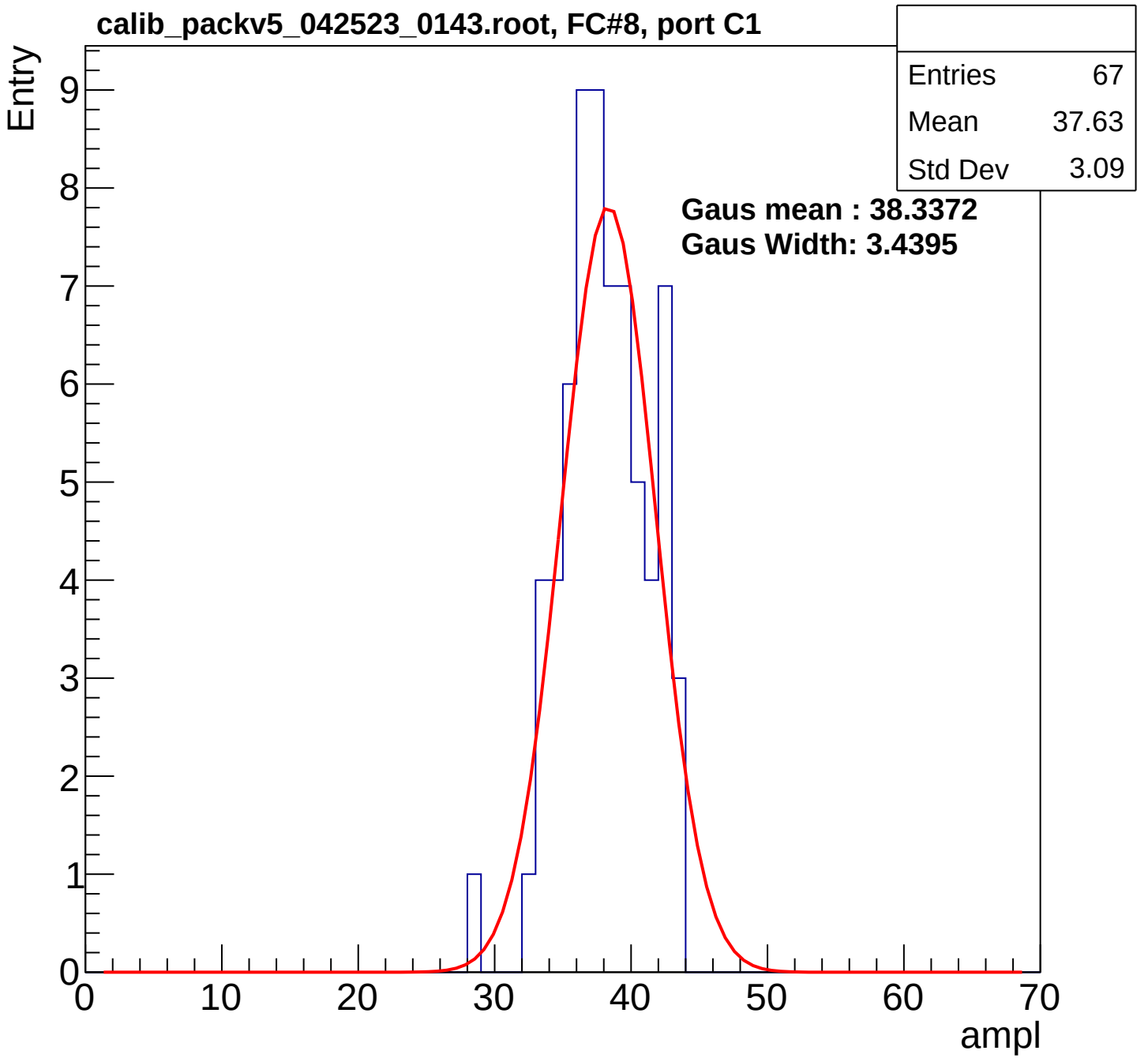
B0L002S, U11-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.63
Std Dev 3.09
Gaus mean : 38.3372
Gaus Width: 3.4395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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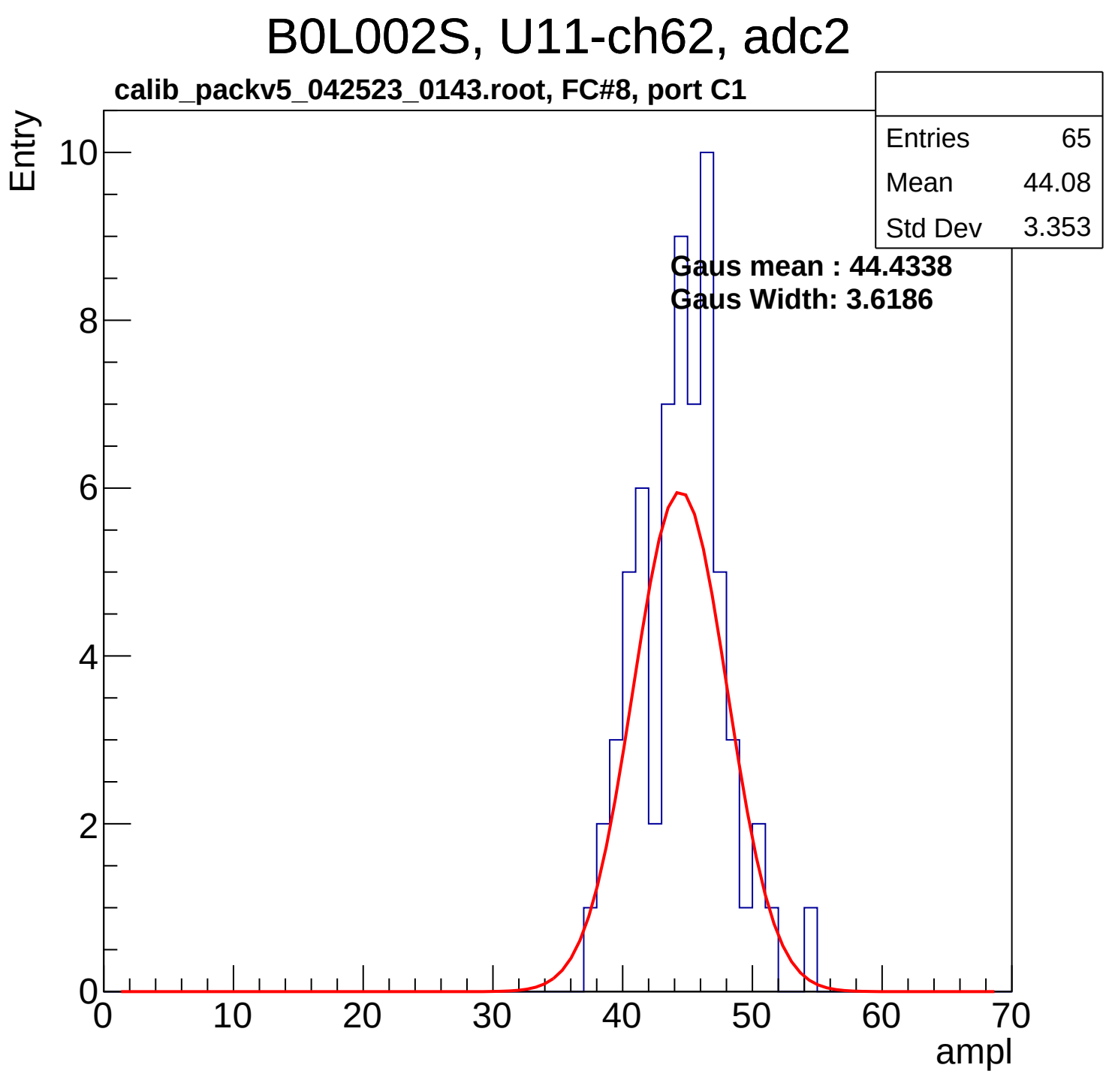
B0L002S, U11-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.08
Std Dev 3.353
Gaus mean : 44.4338
Gaus Width: 3.6186
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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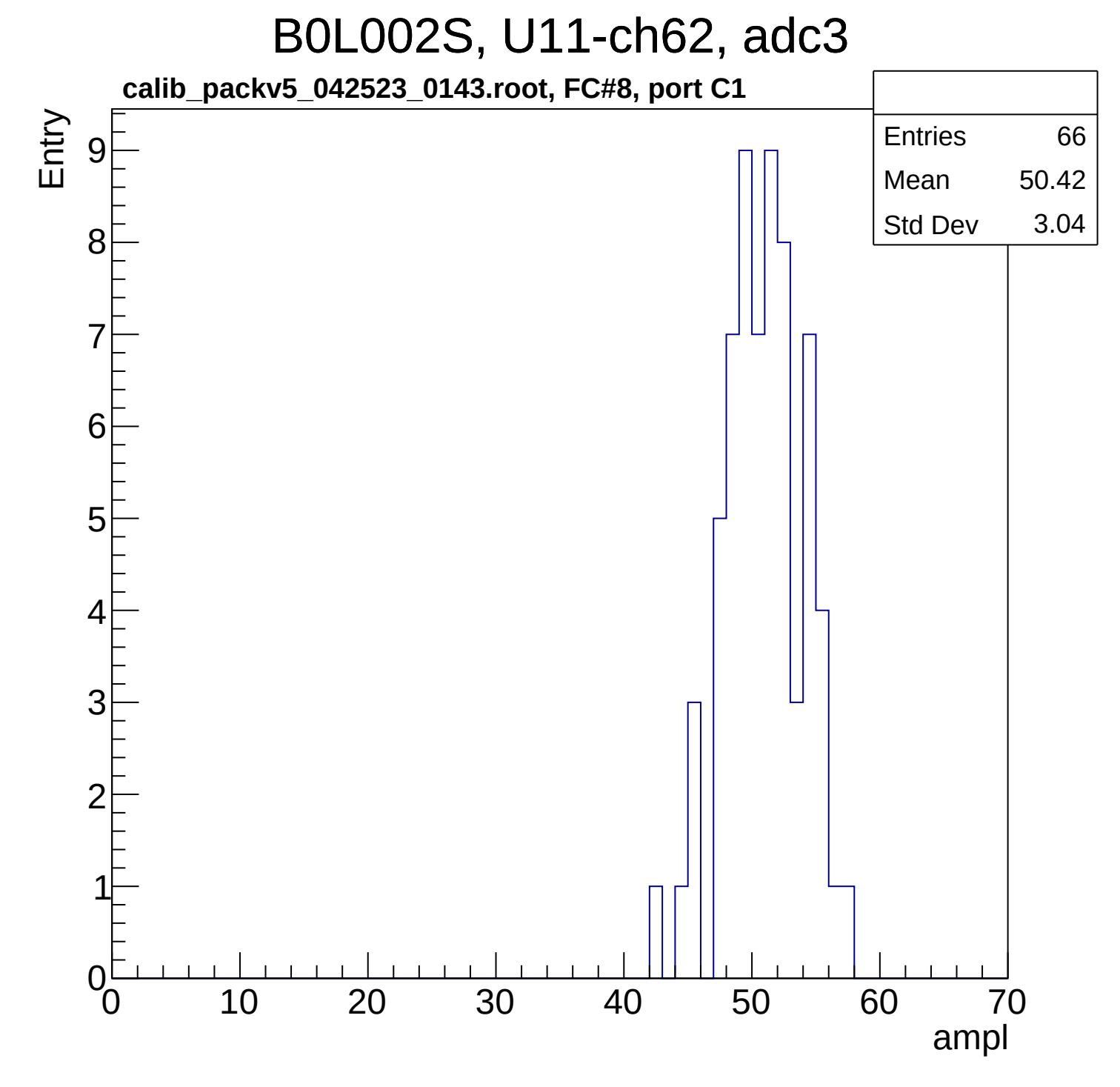
B0L002S, U11-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.42
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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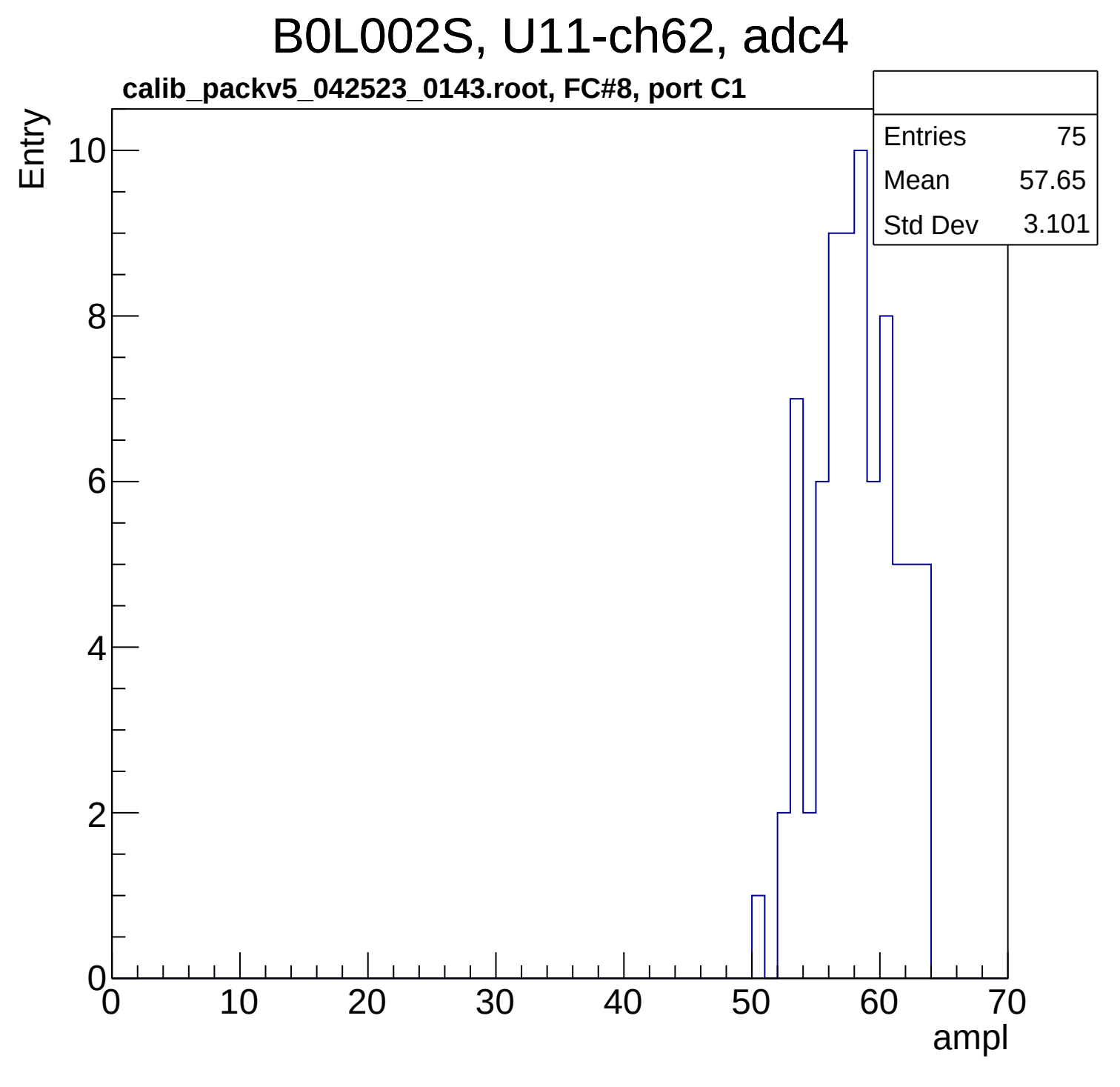
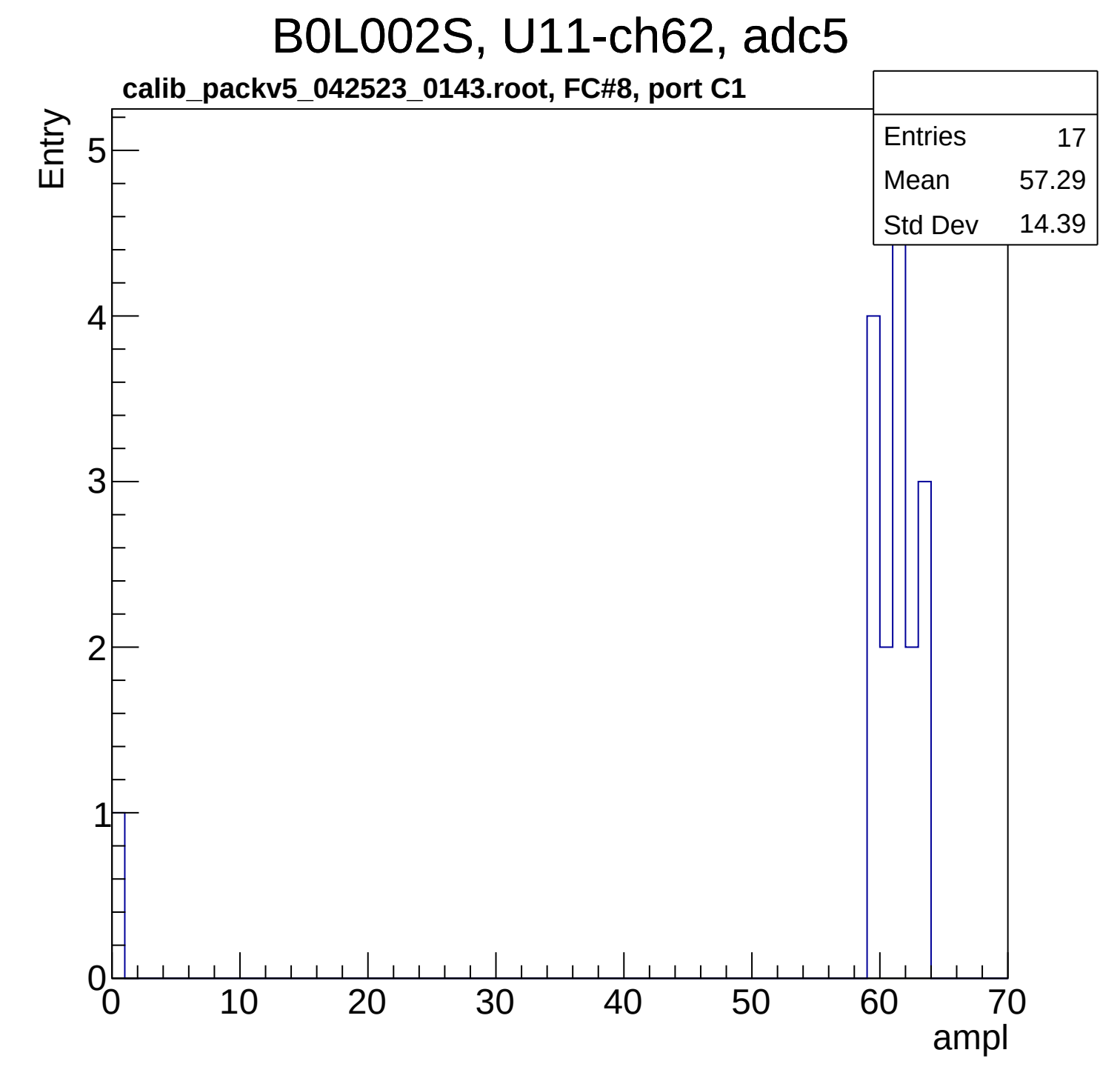
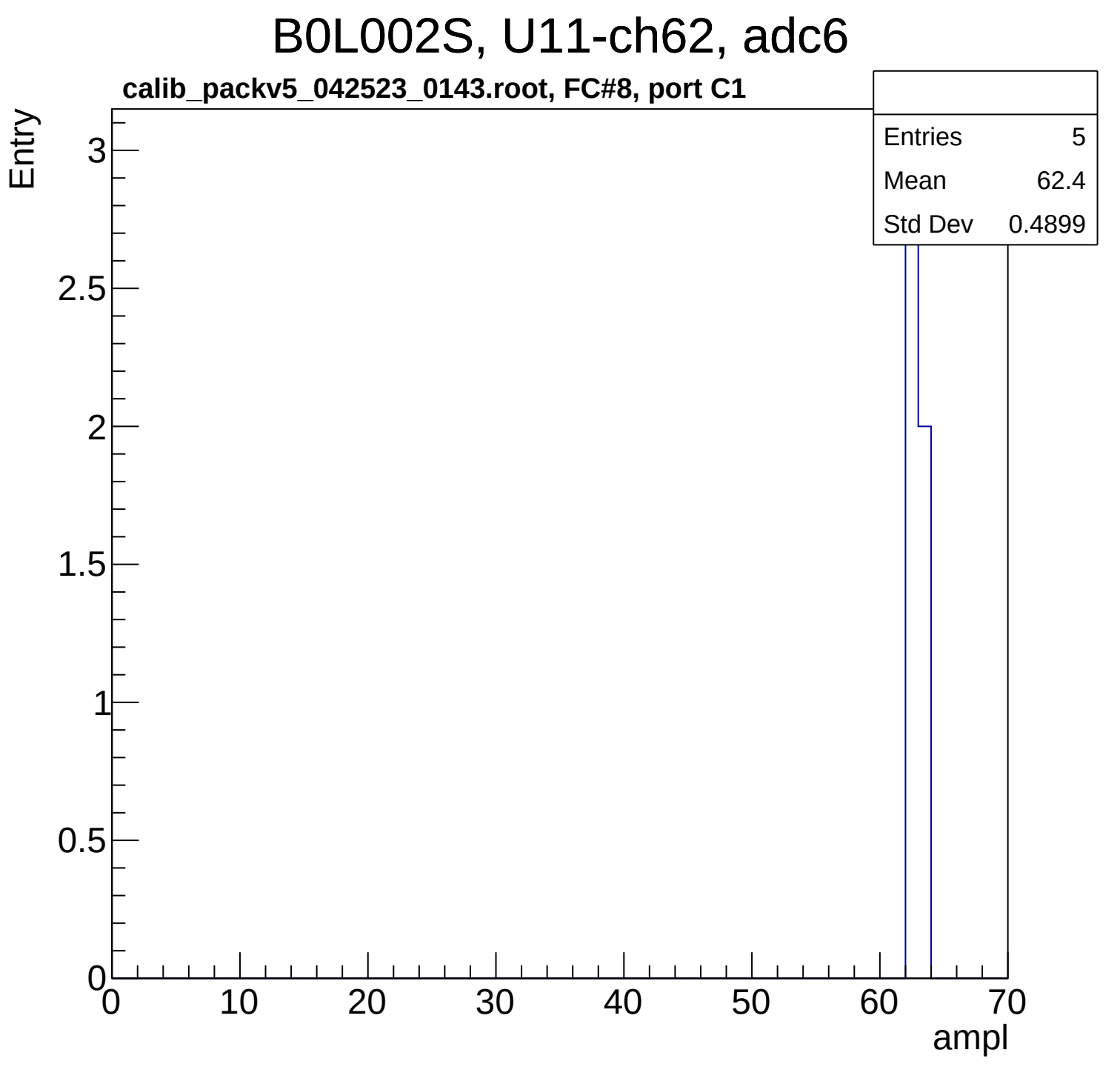

B0L002S, U11-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 57.65
Std Dev 3.101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 57.29
Std Dev 14.39
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
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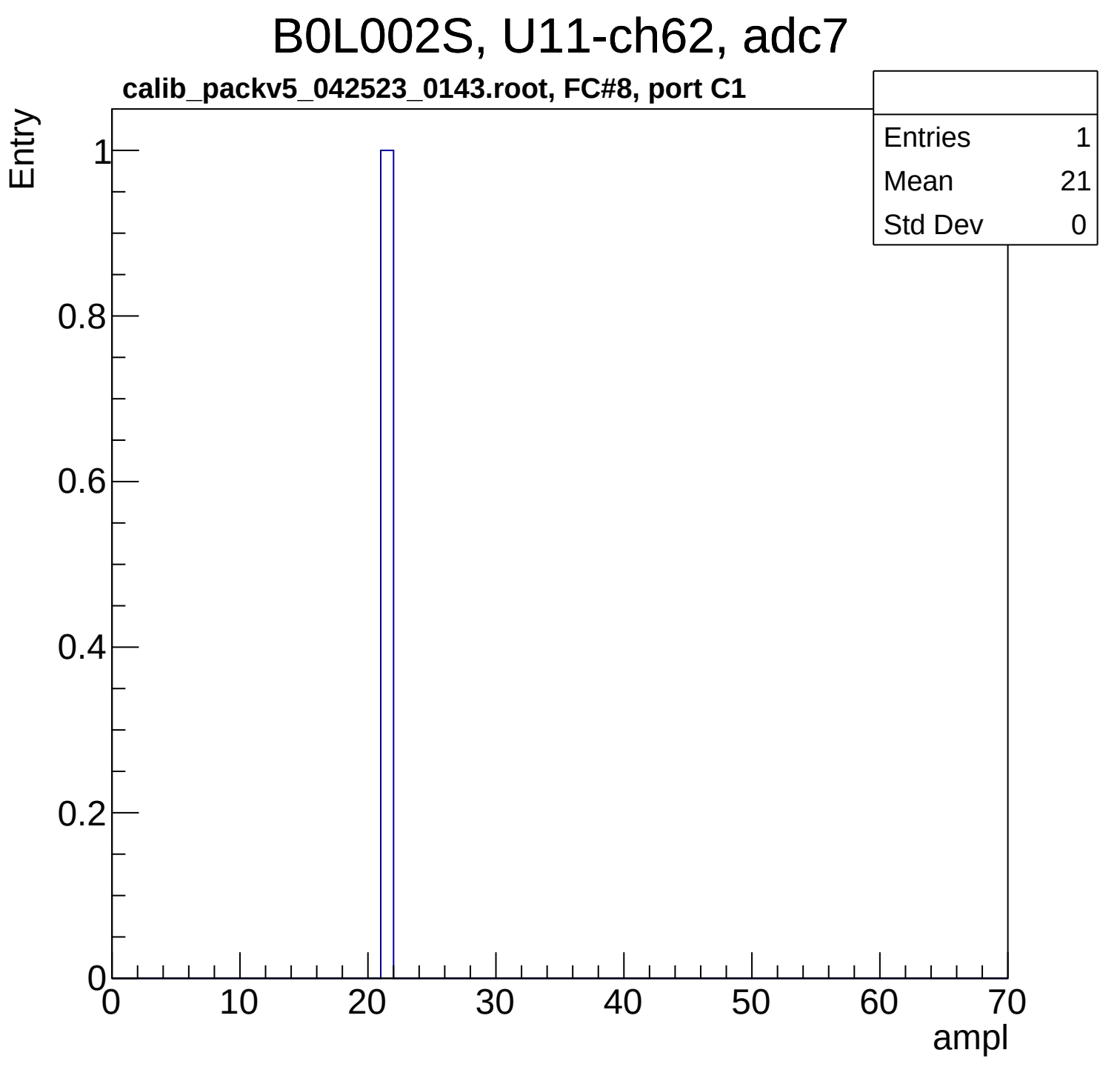
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U11-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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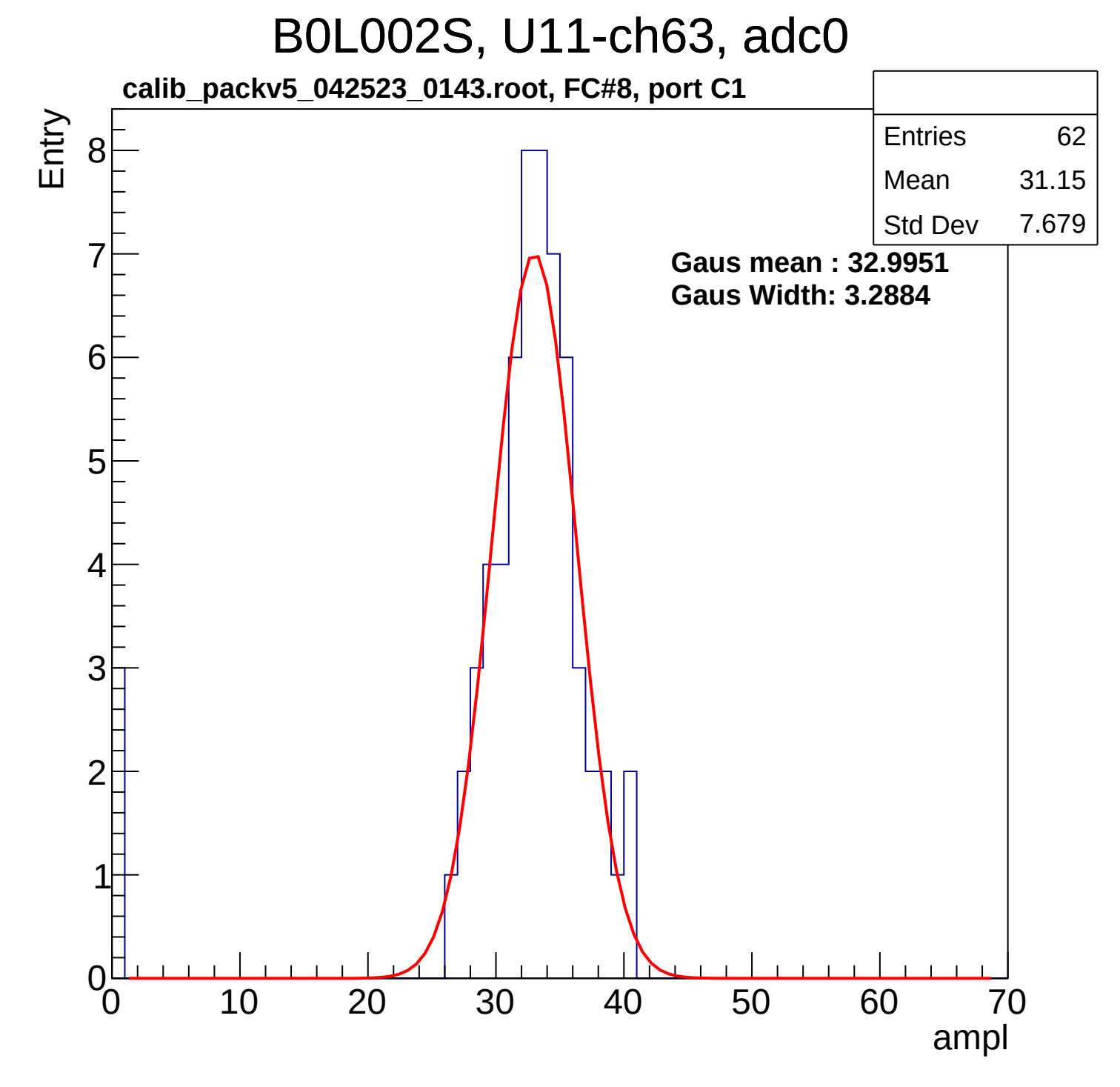
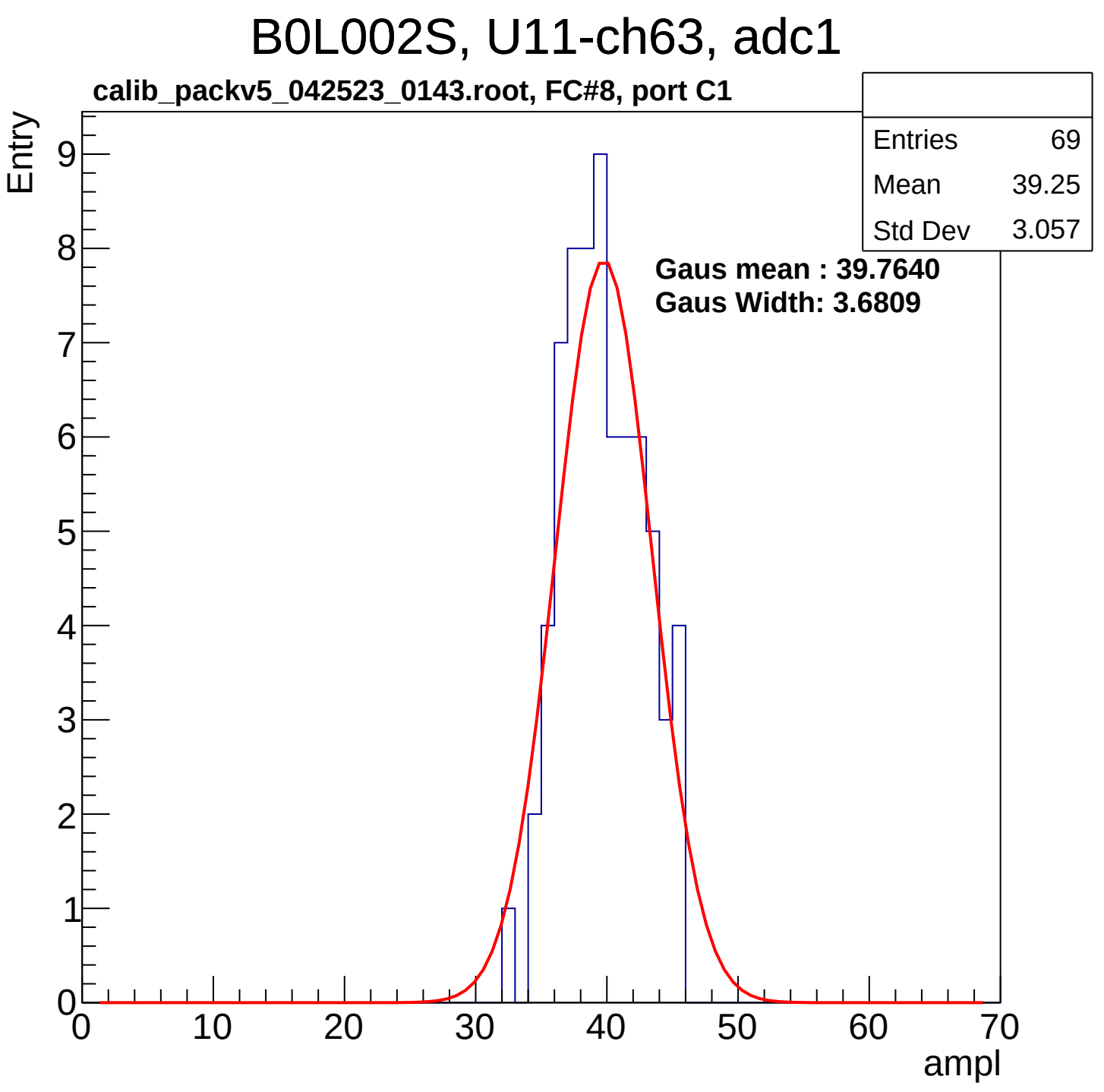
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.15
Std Dev 7.679
Gaus mean : 32.9951
Gaus Width: 3.2884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 39.25
Std Dev 3.057
Gaus mean : 39.7640
Gaus Width: 3.6809
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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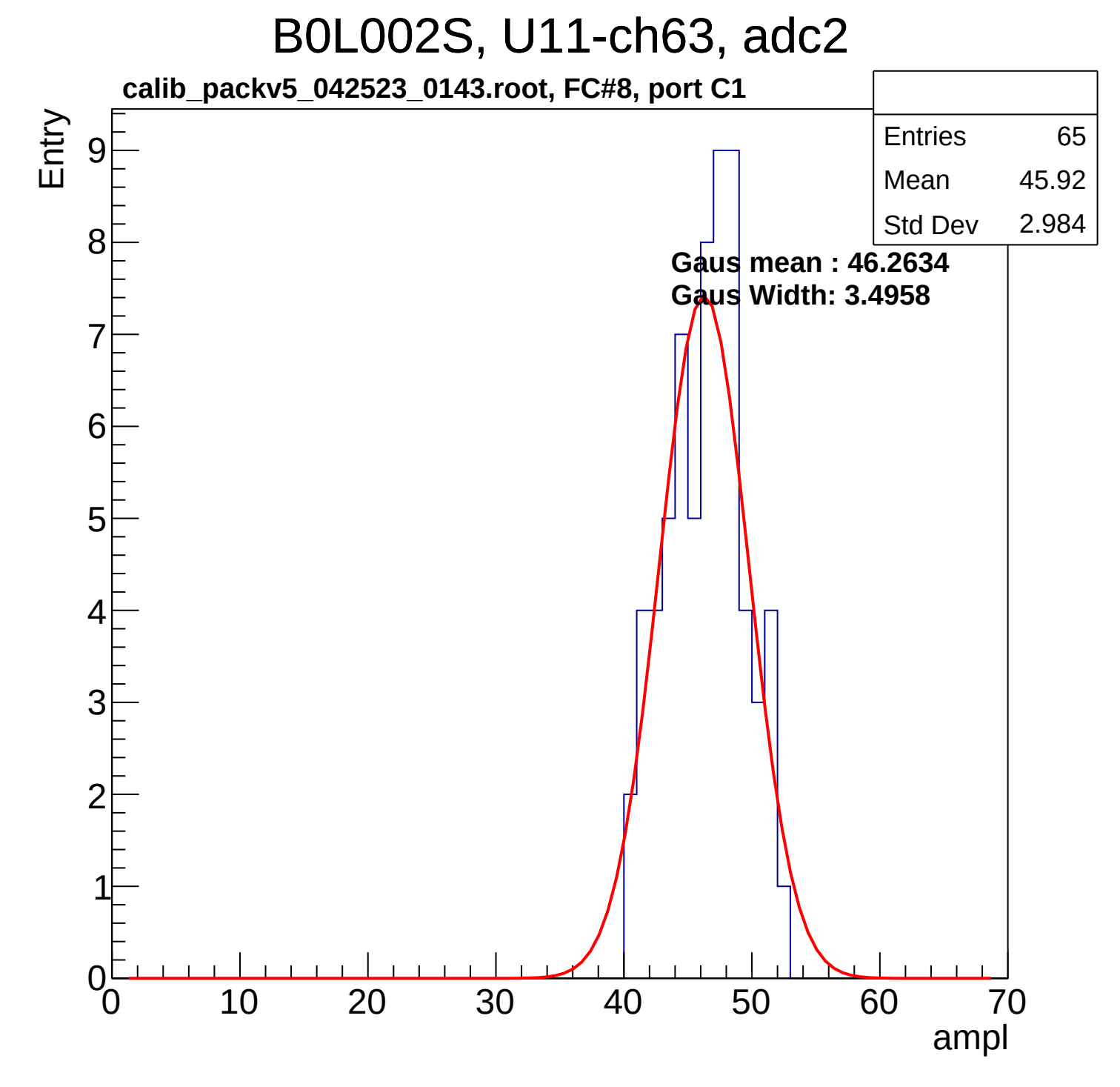
B0L002S, U11-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.92
Std Dev 2.984
Gaus mean : 46.2634
Gaus Width: 3.4958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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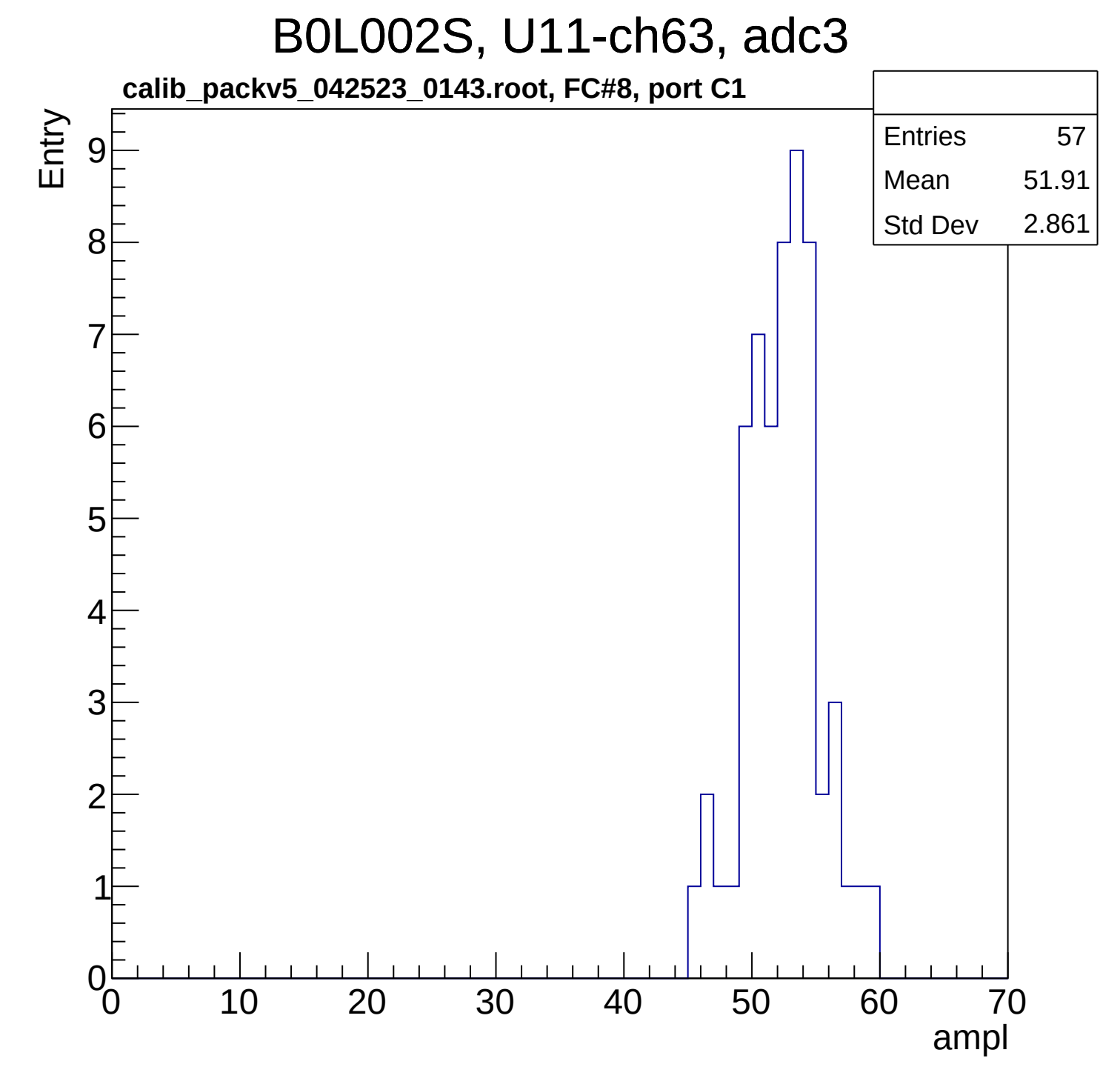
B0L002S, U11-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 51.91
Std Dev 2.861
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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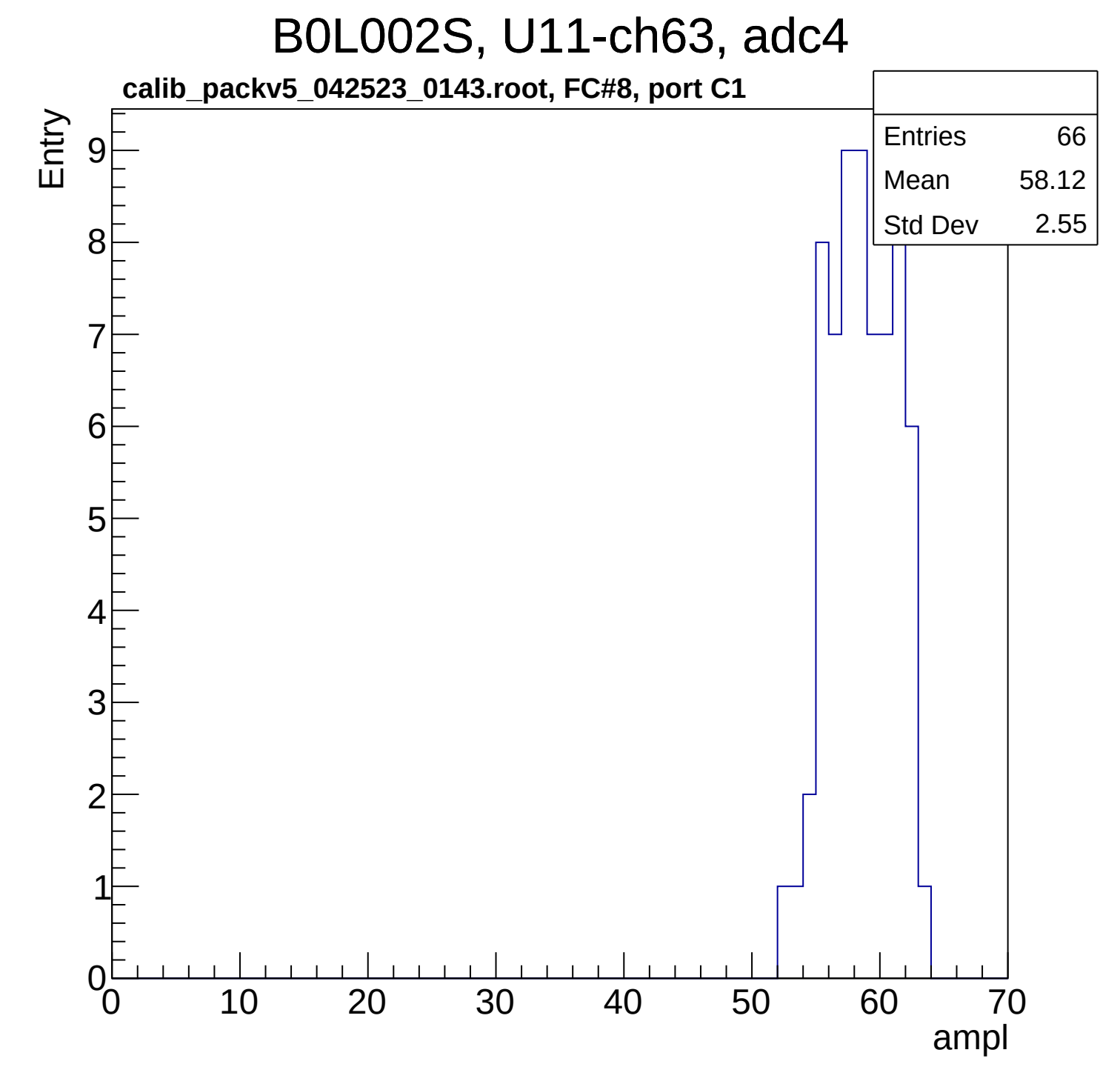
B0L002S, U11-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 58.12
Std Dev 2.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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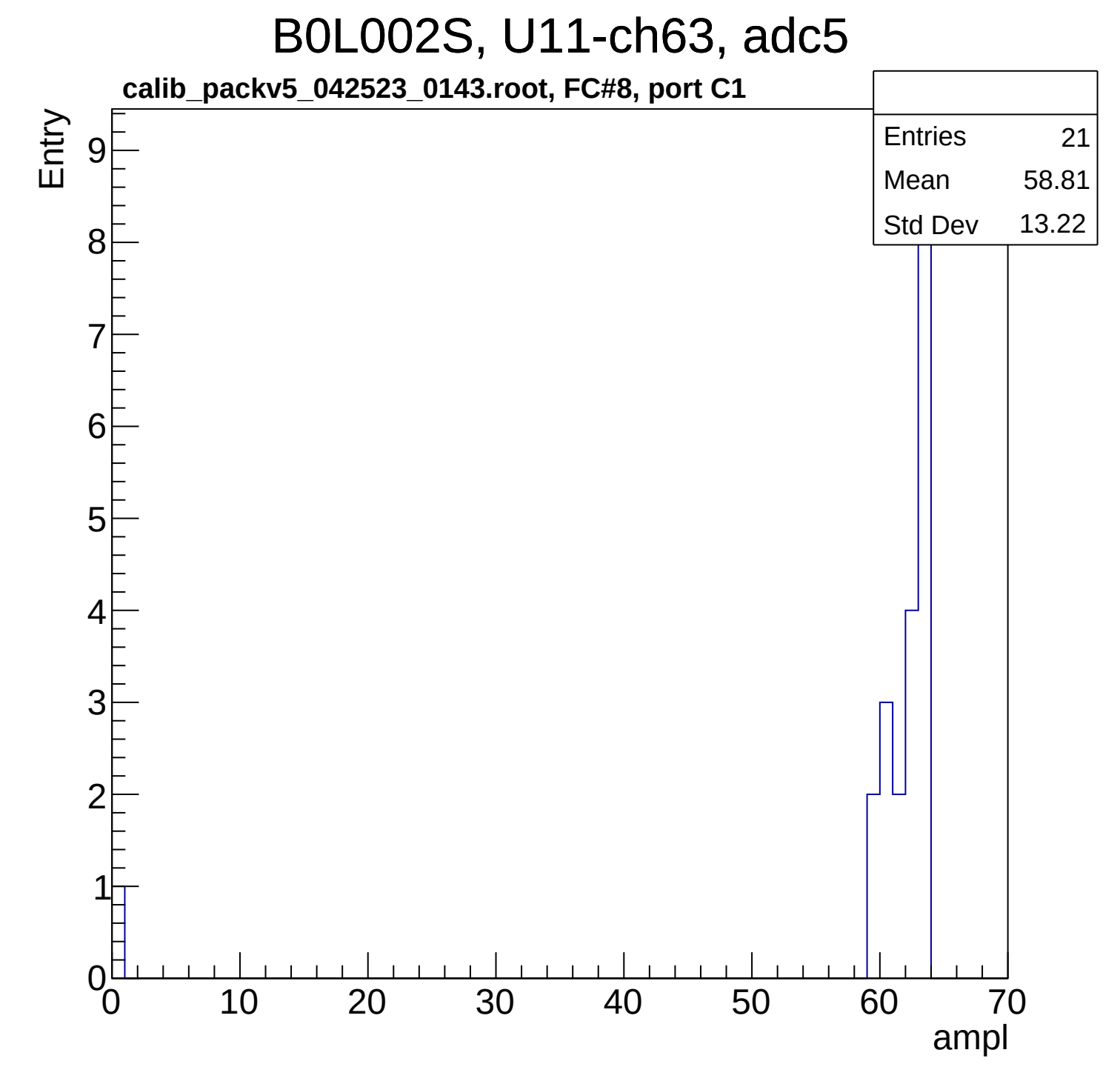
B0L002S, U11-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.81
Std Dev 13.22
Entry
ampl
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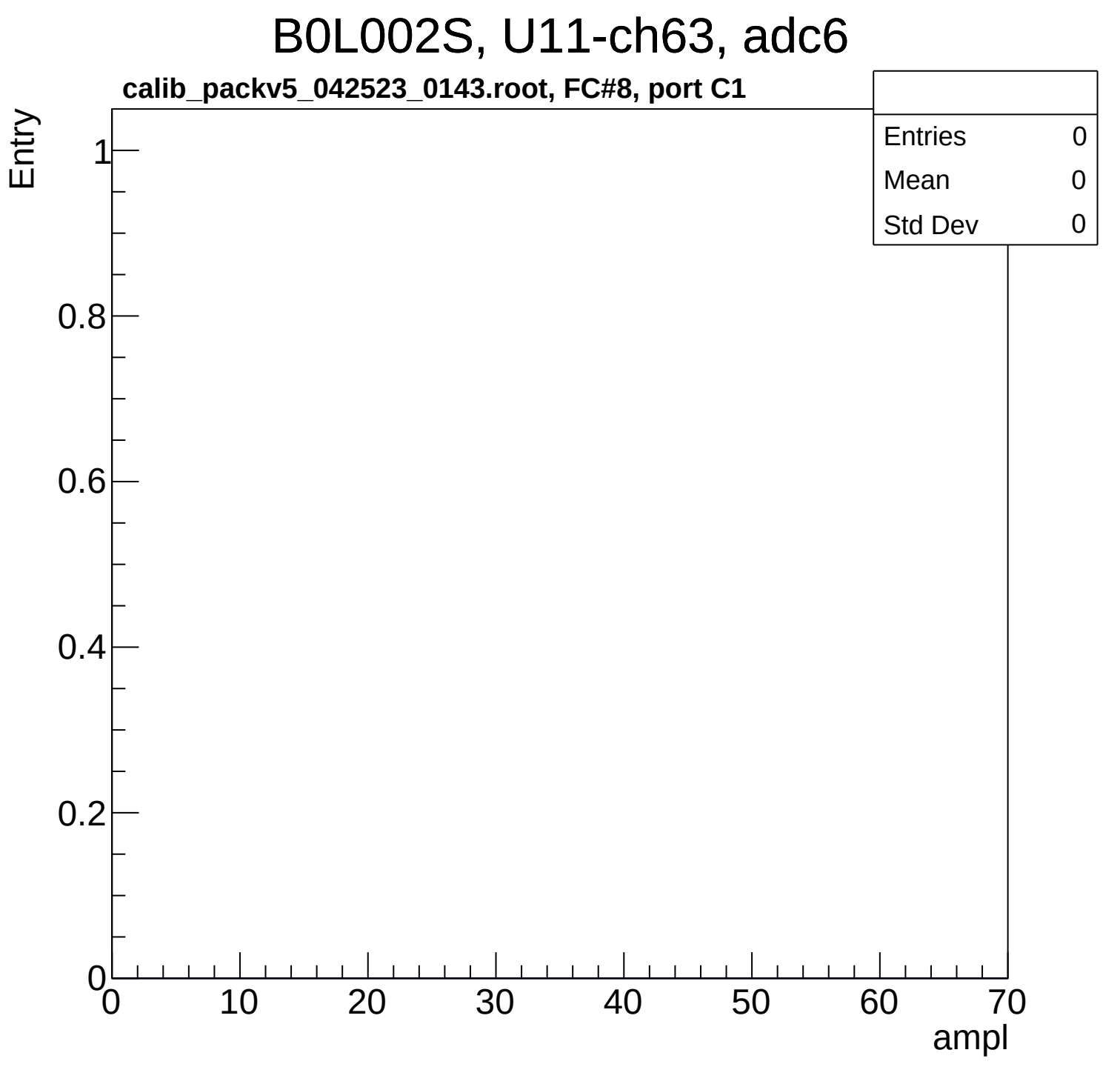
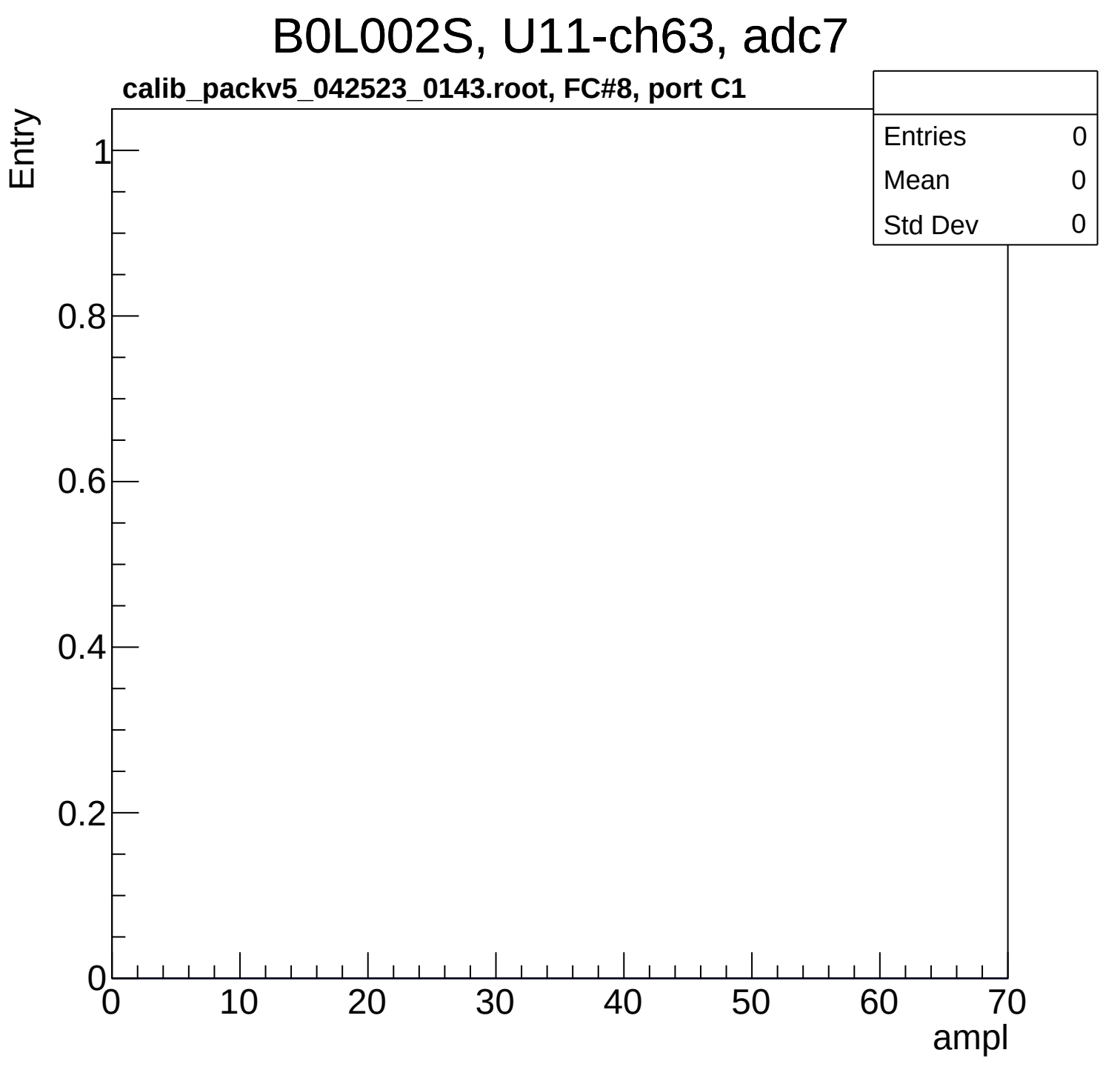
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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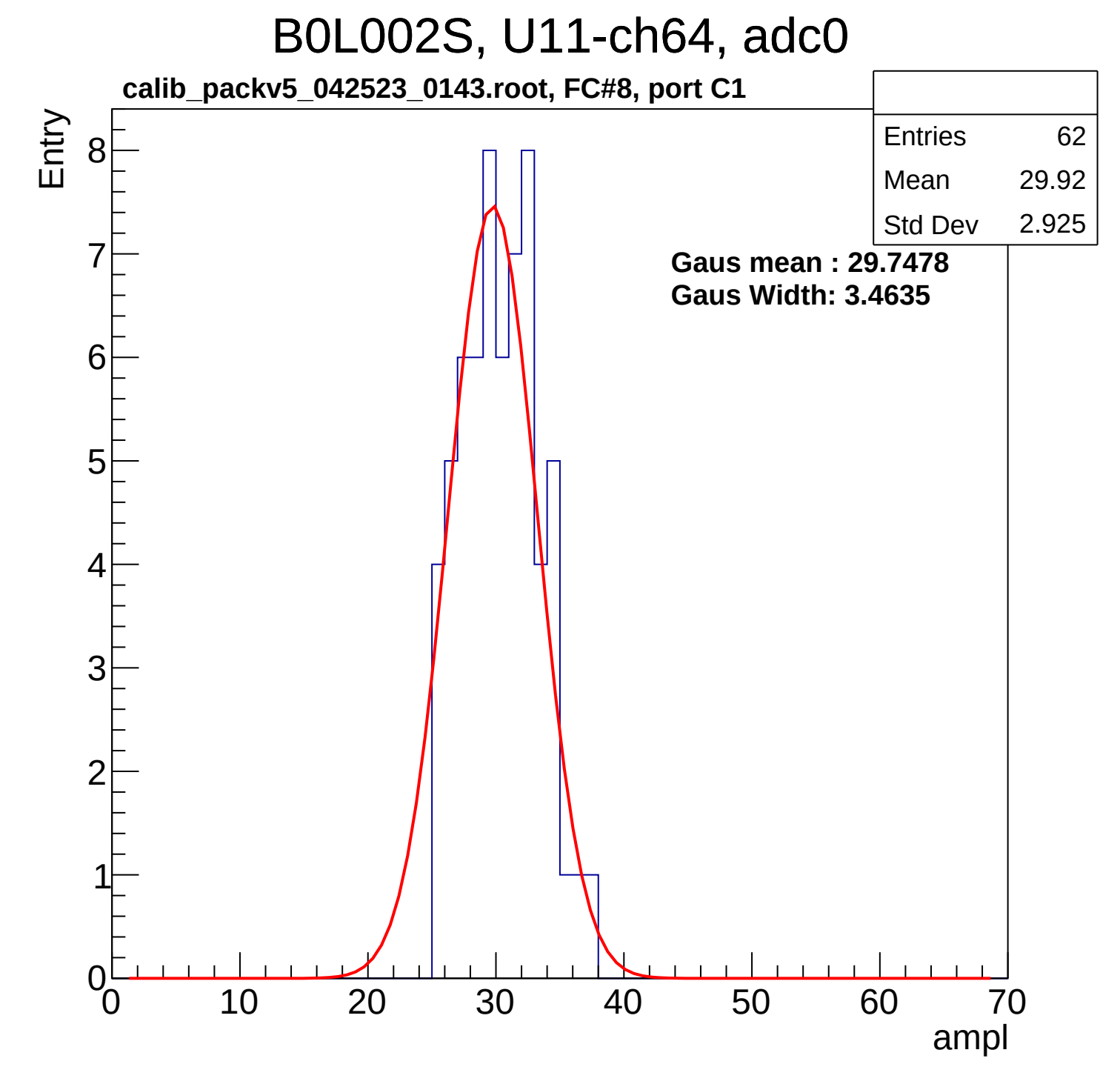
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 29.92
Std Dev 2.925
Gaus mean : 29.7478
Gaus Width: 3.4635
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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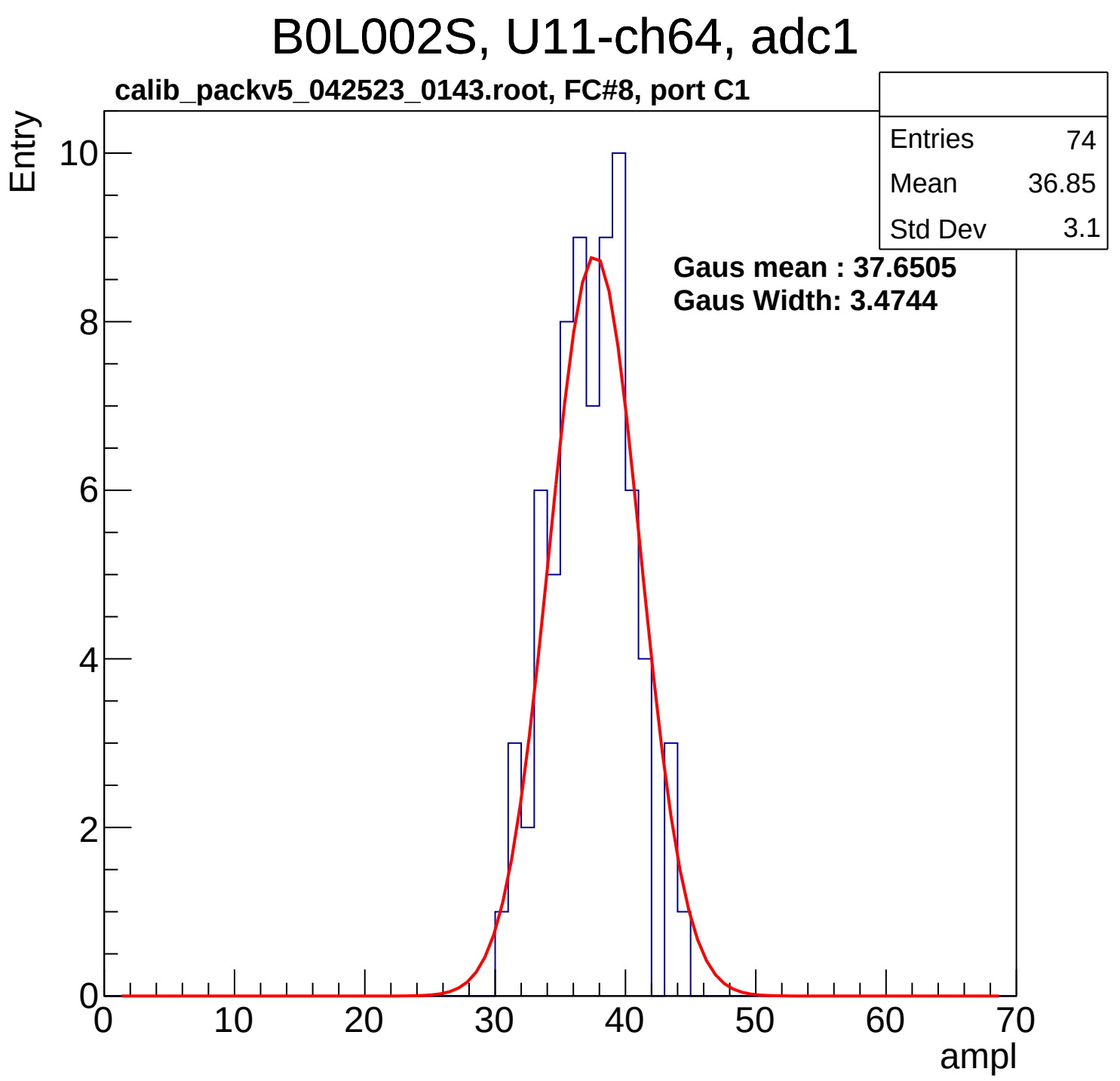
B0L002S, U11-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.85
Std Dev 3.1
Gaus mean : 37.6505
Gaus Width: 3.4744
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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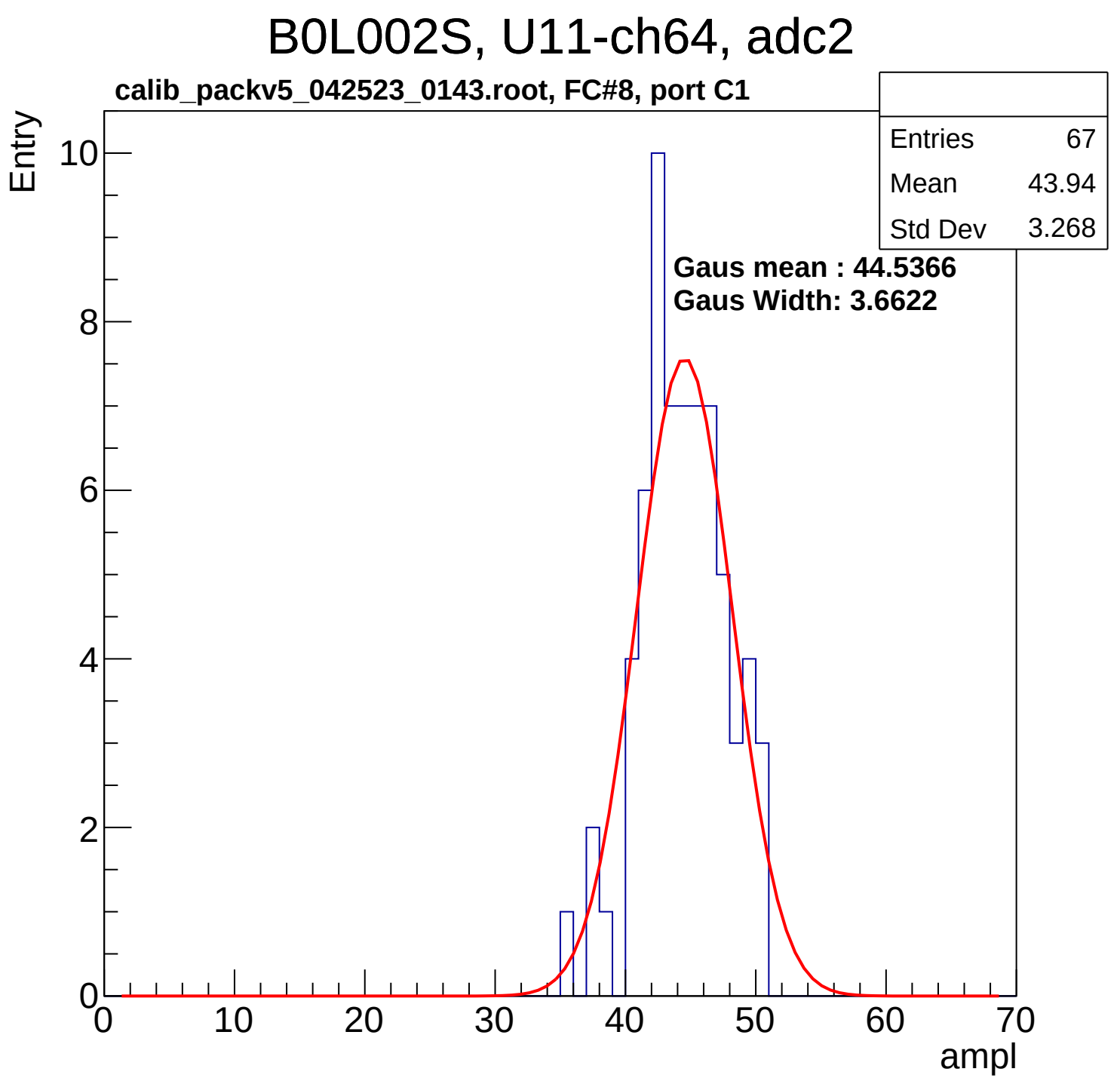
B0L002S, U11-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.94
Std Dev 3.268
Gaus mean : 44.5366
Gaus Width: 3.6622
Entry
ampl
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4
6
8
10
0
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60
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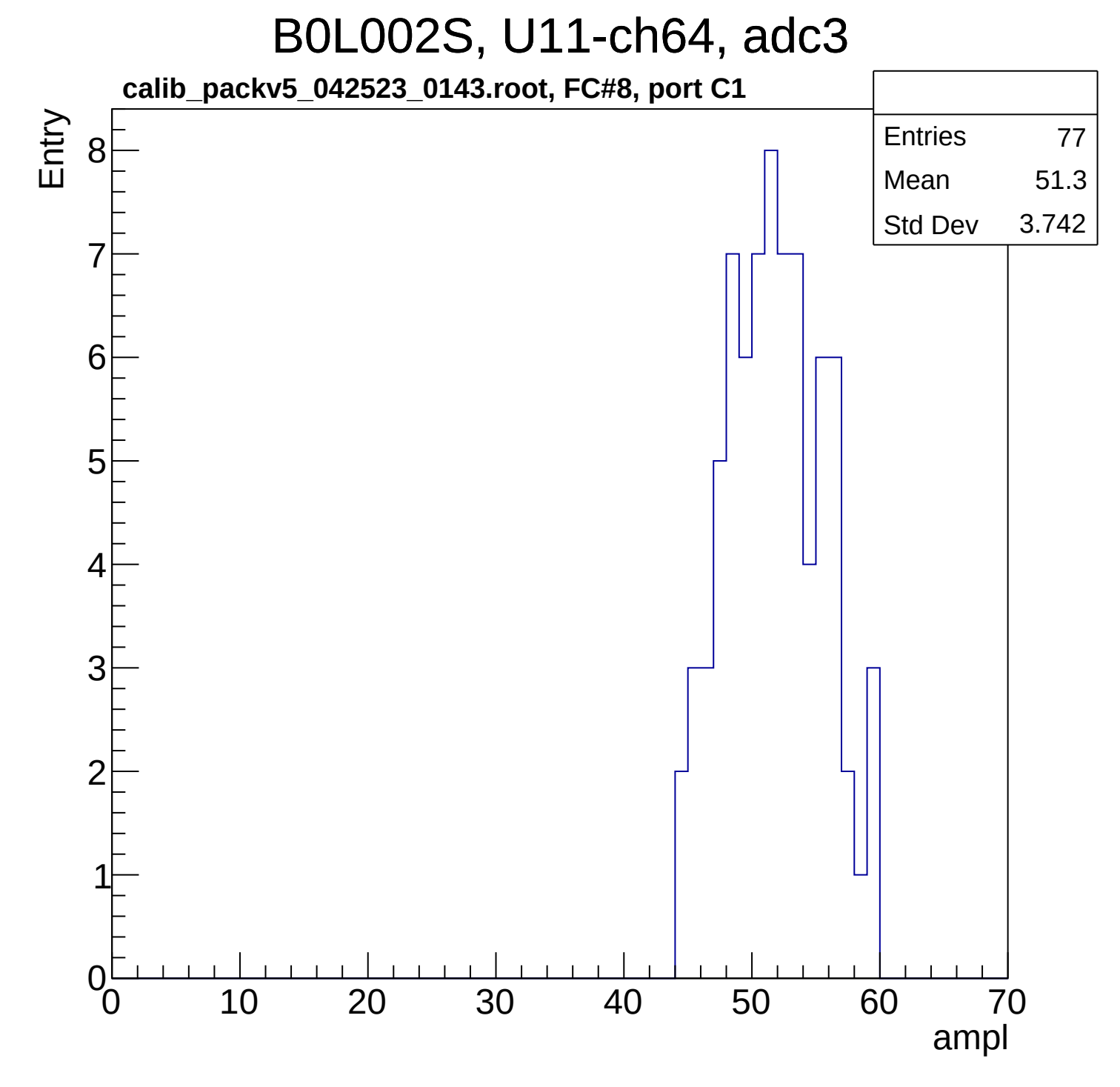
B0L002S, U11-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.3
Std Dev 3.742
Entry
ampl
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2
3
4
5
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7
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60
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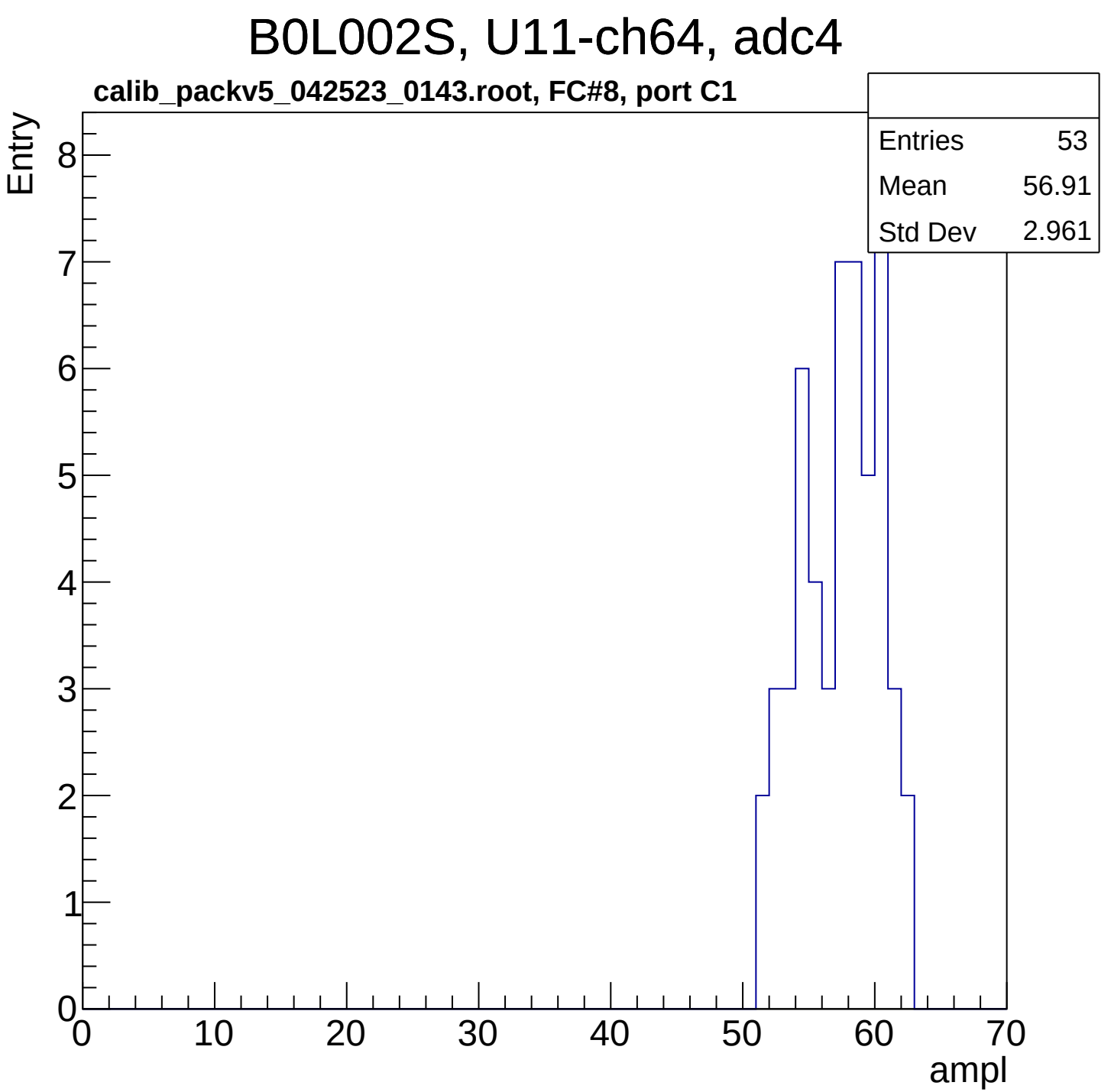
B0L002S, U11-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.91
Std Dev 2.961
Entry
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3
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5
6
7
8
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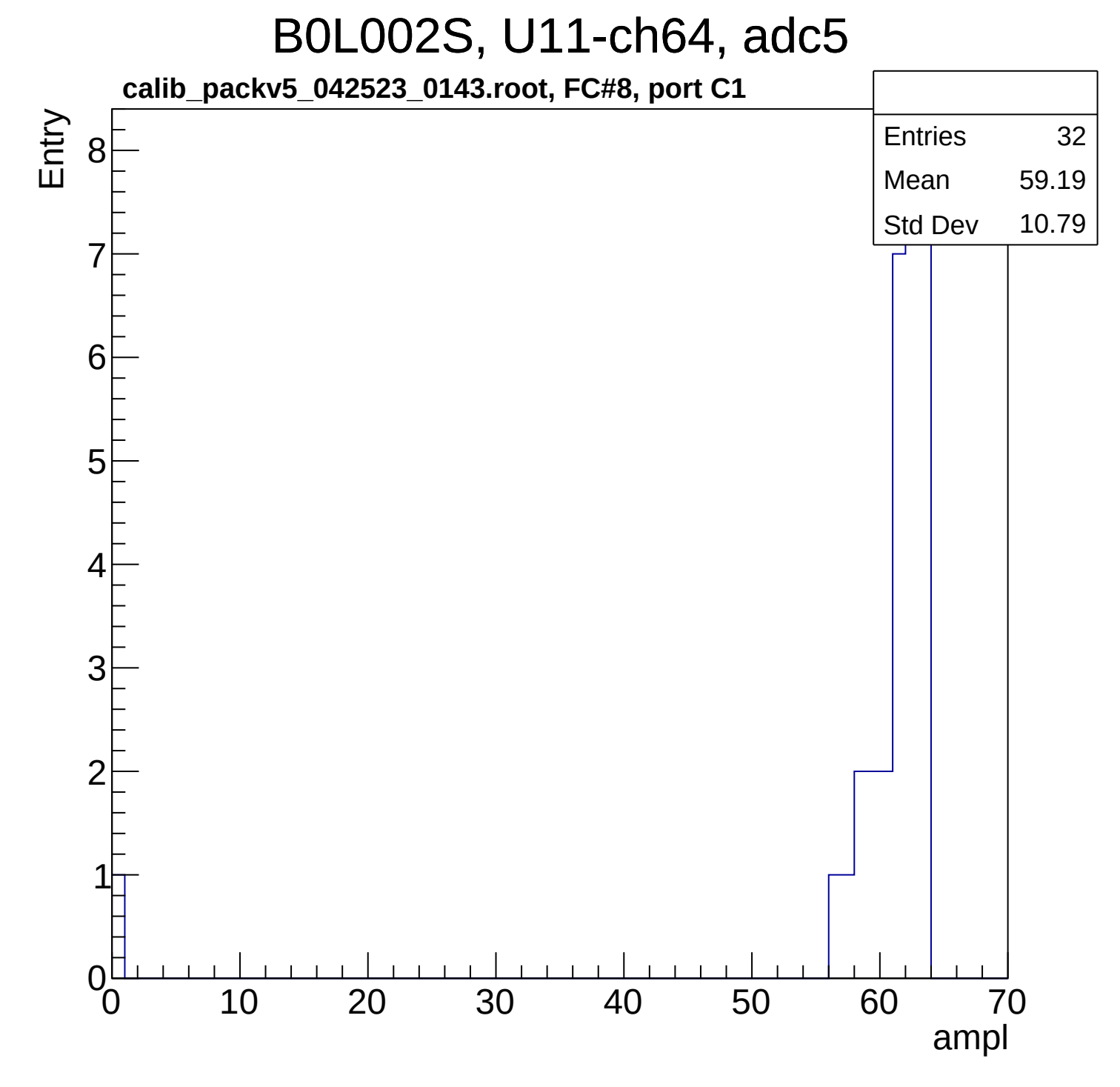
50
60
70
ampl


B0L002S, U11-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.19
Std Dev 10.79
Entry
ampl
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2
3
4
5
6
7
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0
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60
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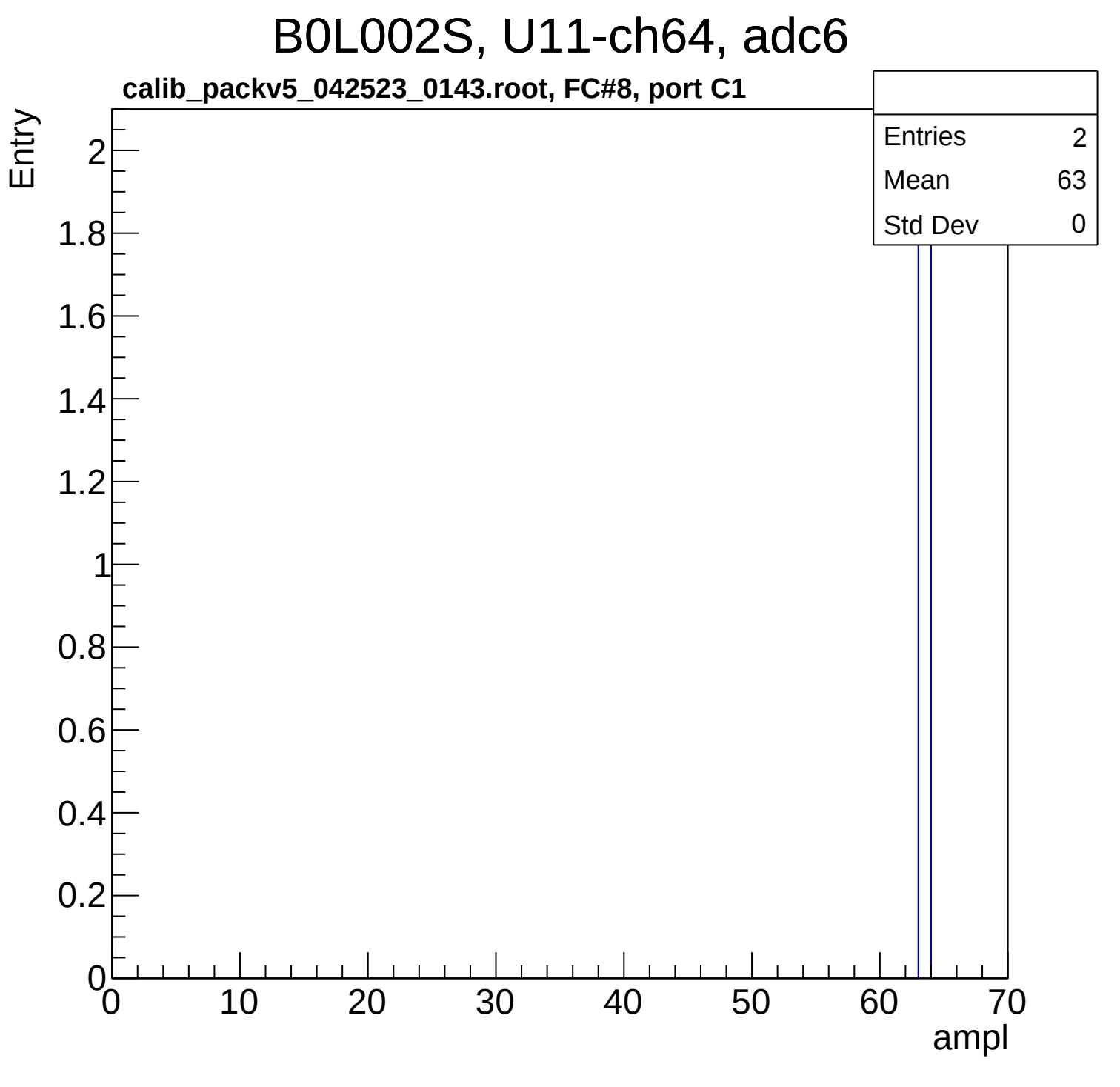
B0L002S, U11-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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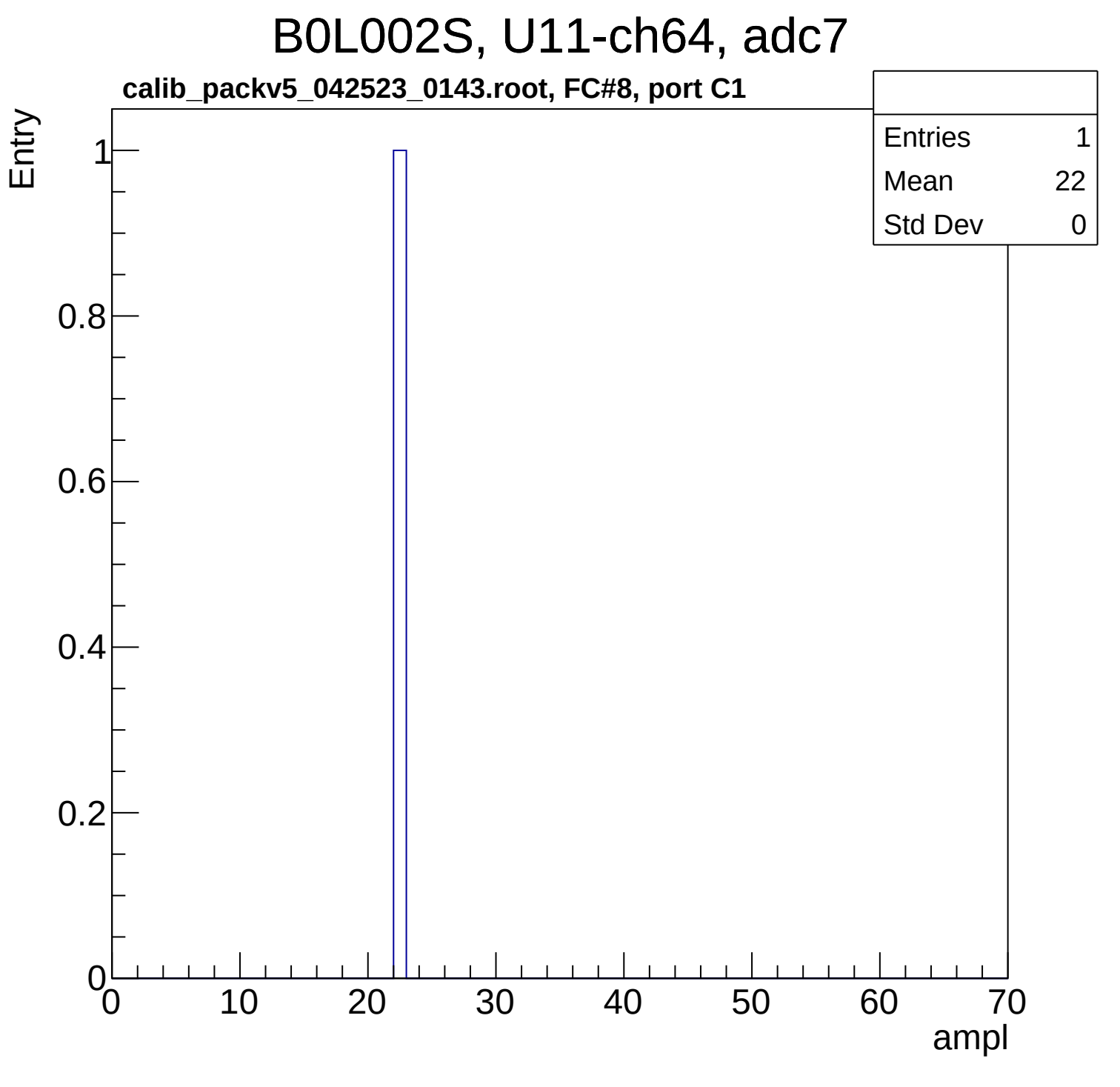
B0L002S, U11-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
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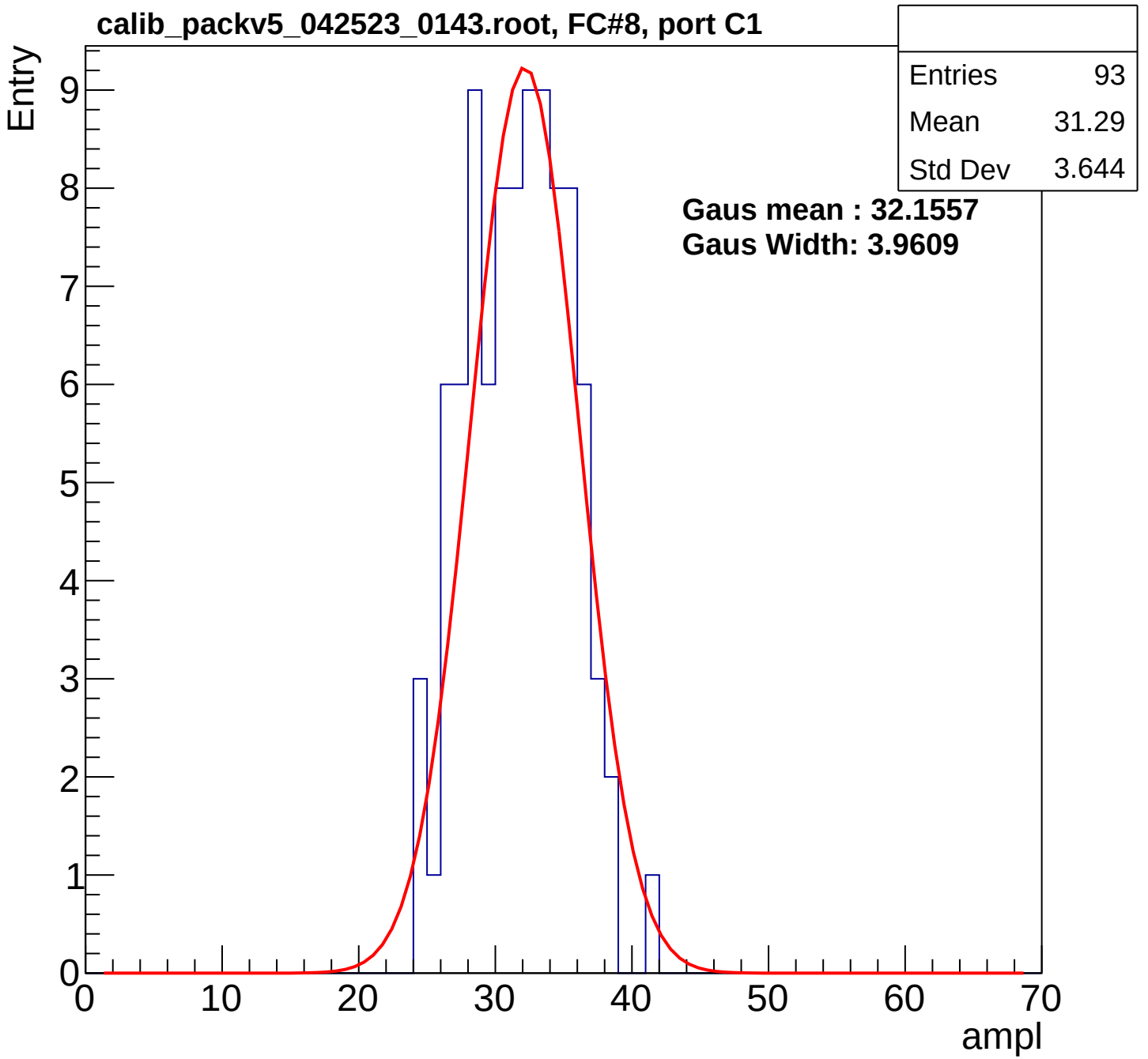
50
60
70
ampl


B0L002S, U11-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 93
Mean 31.29
Std Dev 3.644
Gaus mean : 32.1557
Gaus Width: 3.9609
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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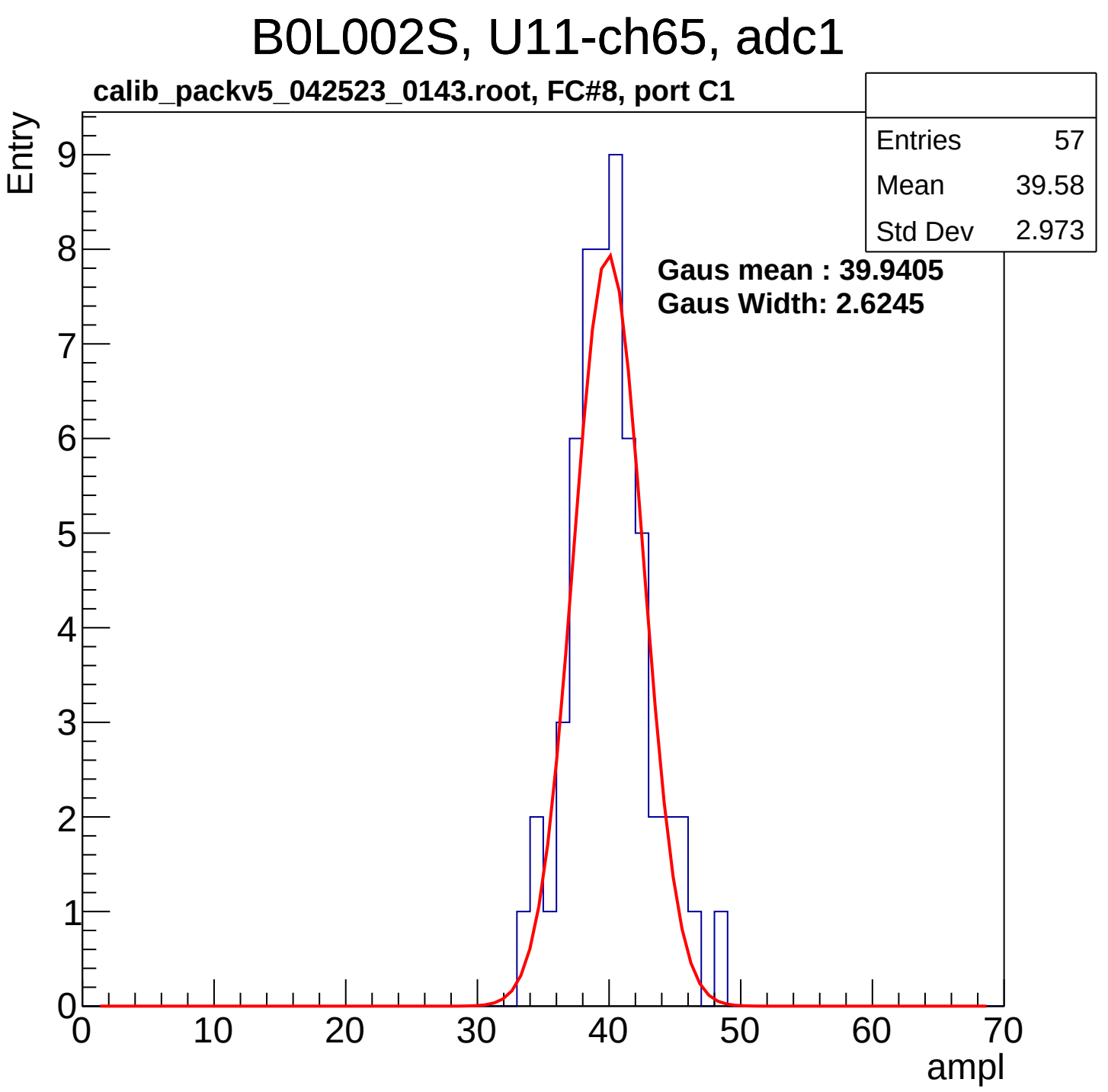
B0L002S, U11-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 39.58
Std Dev 2.973
Gaus mean : 39.9405
Gaus Width: 2.6245
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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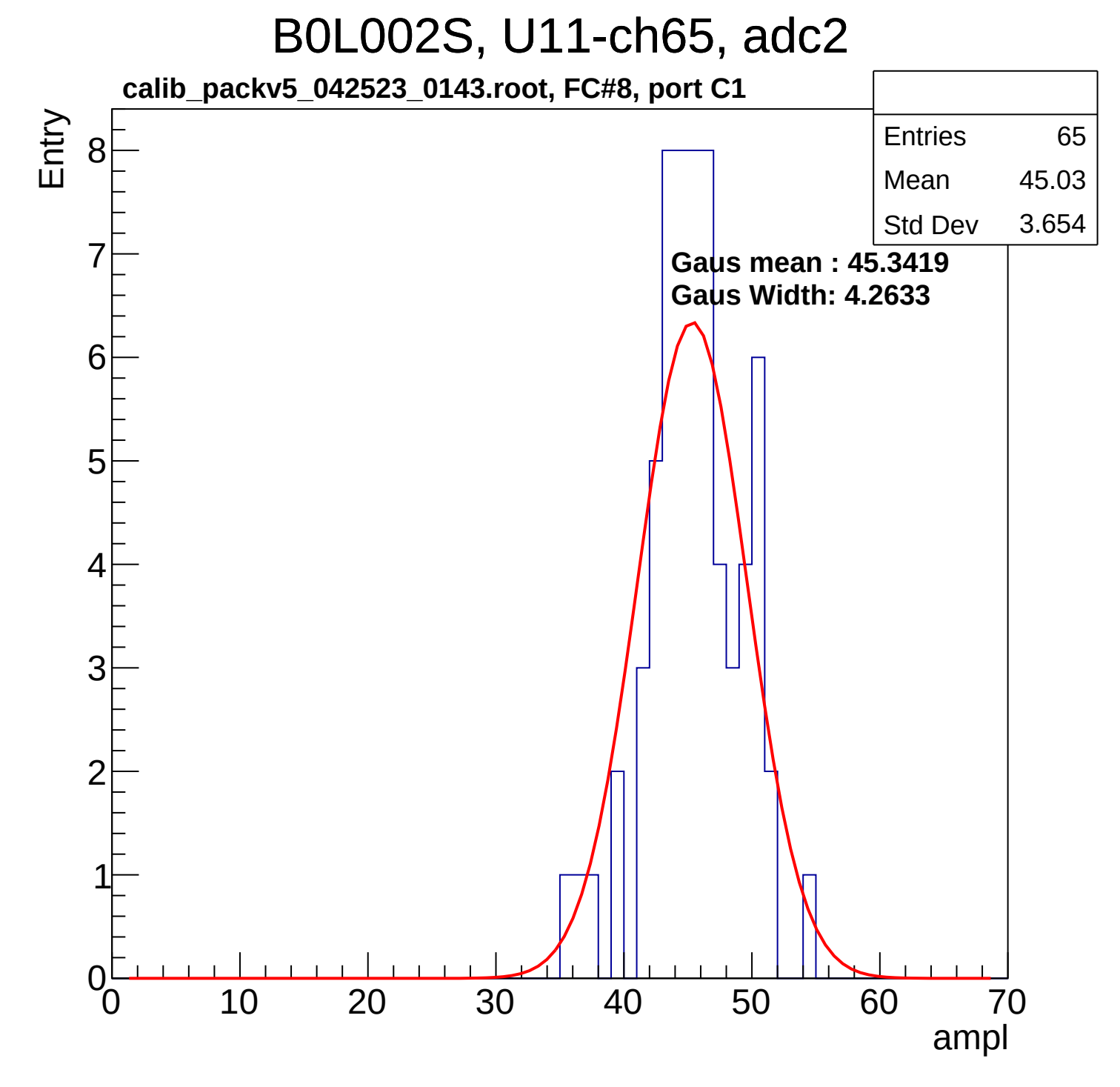
B0L002S, U11-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.03
Std Dev 3.654
Gaus mean : 45.3419
Gaus Width: 4.2633
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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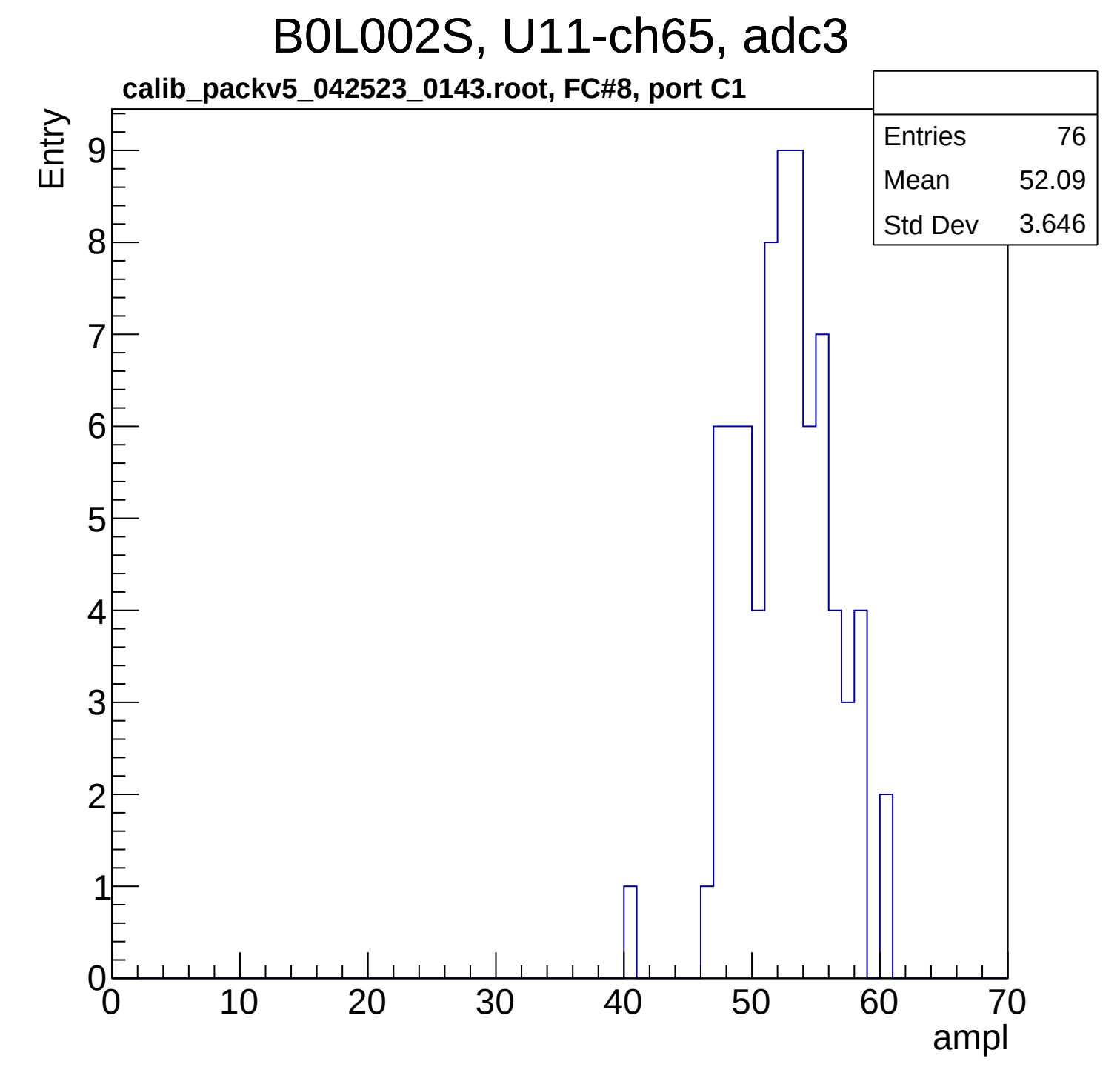
B0L002S, U11-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.09
Std Dev 3.646
Entry
ampl
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3
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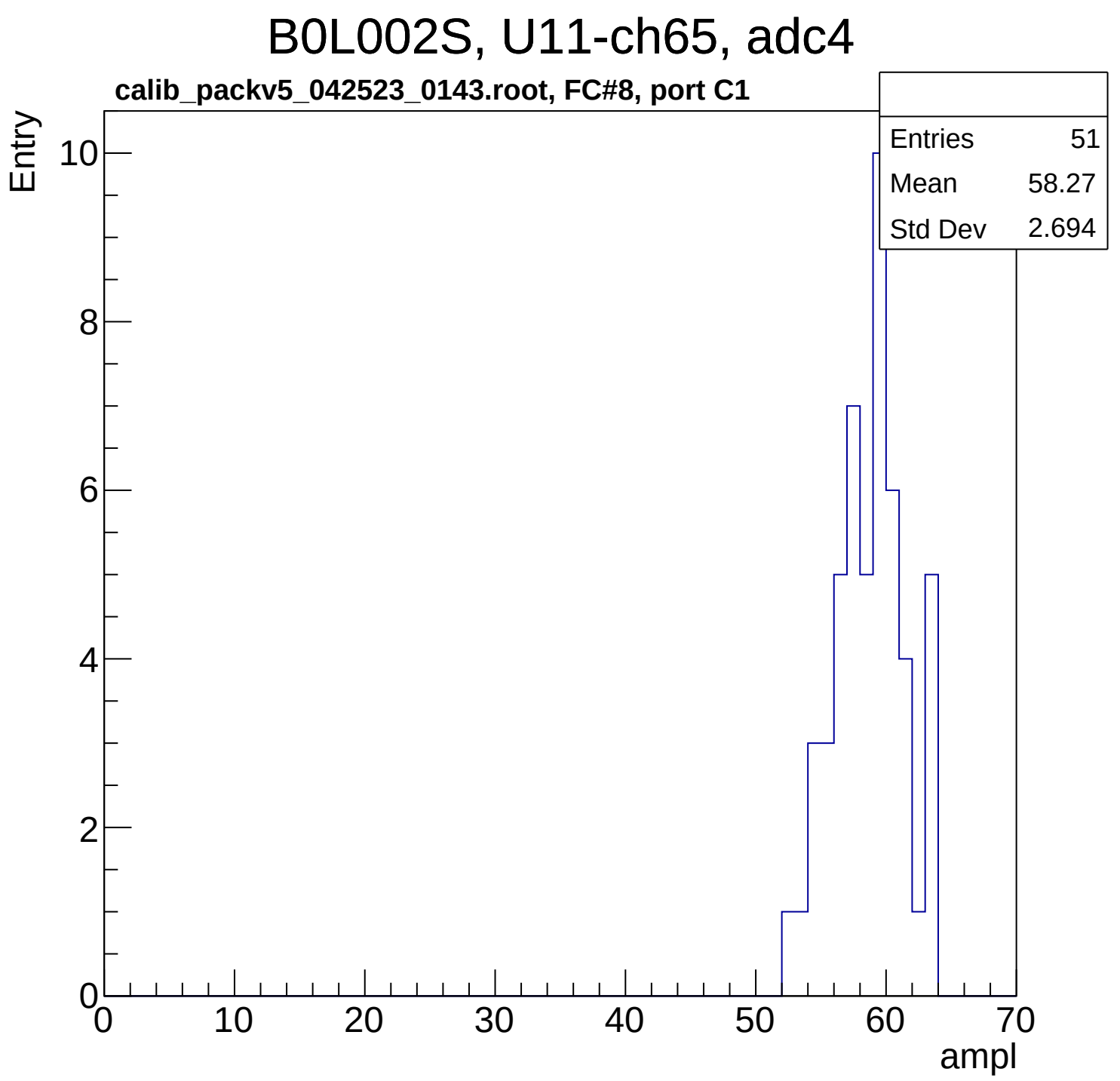
B0L002S, U11-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.27
Std Dev 2.694
Entry
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4
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10
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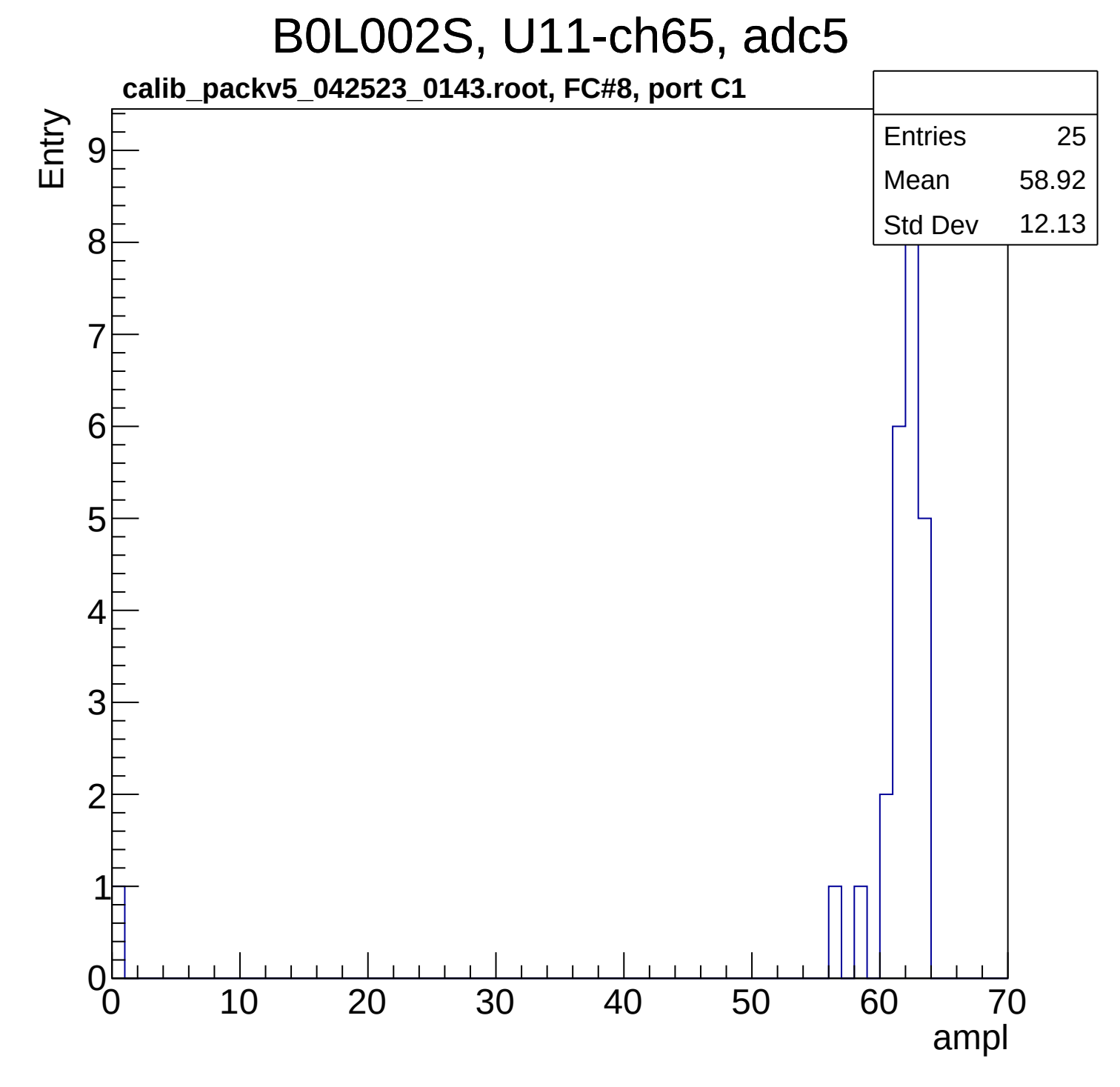
50
60
70
ampl


B0L002S, U11-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.92
Std Dev 12.13
Entry
ampl
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3
4
5
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7
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9
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60
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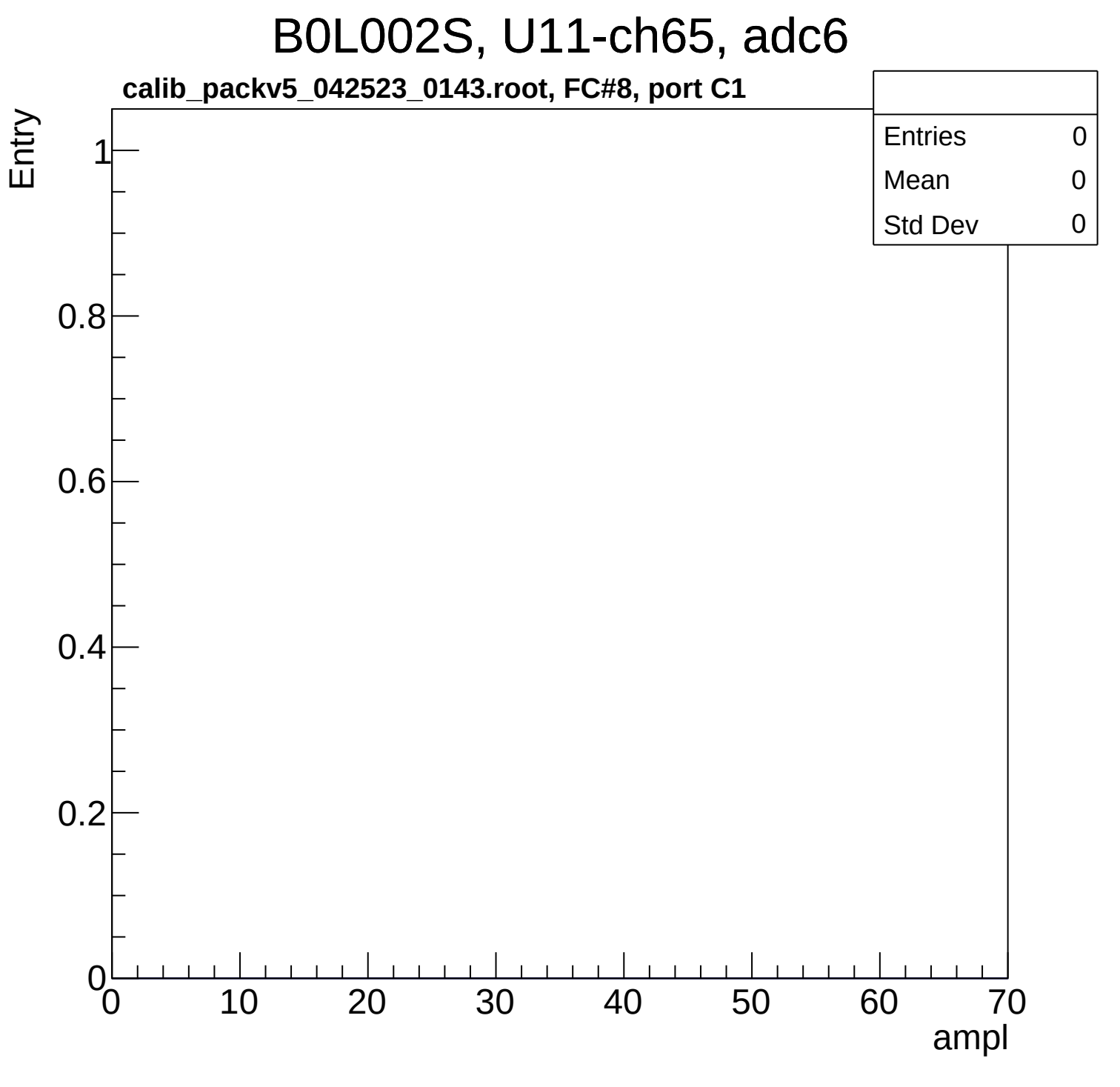
B0L002S, U11-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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30
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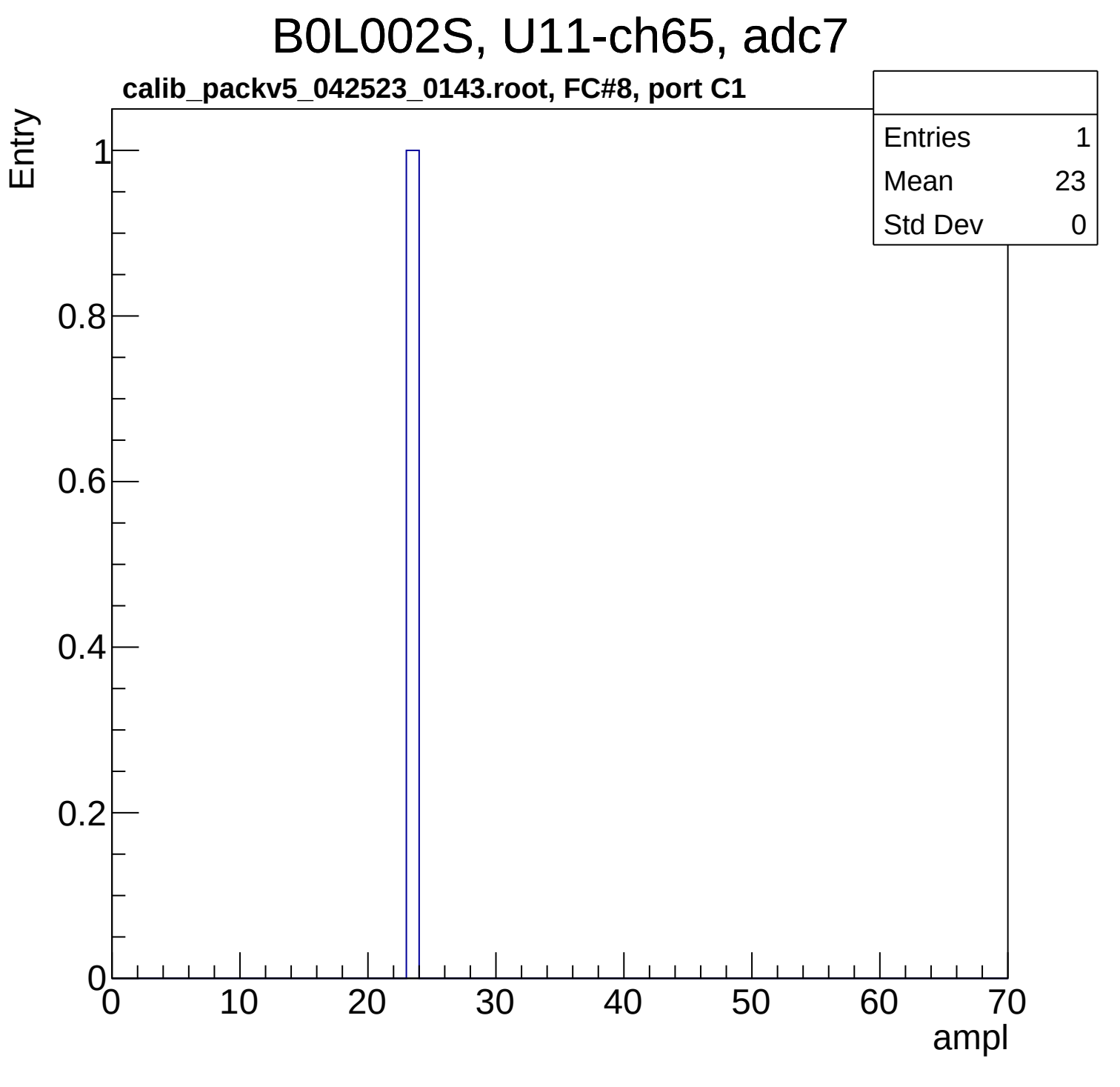
50
60
70
ampl


B0L002S, U11-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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0.4
0.2
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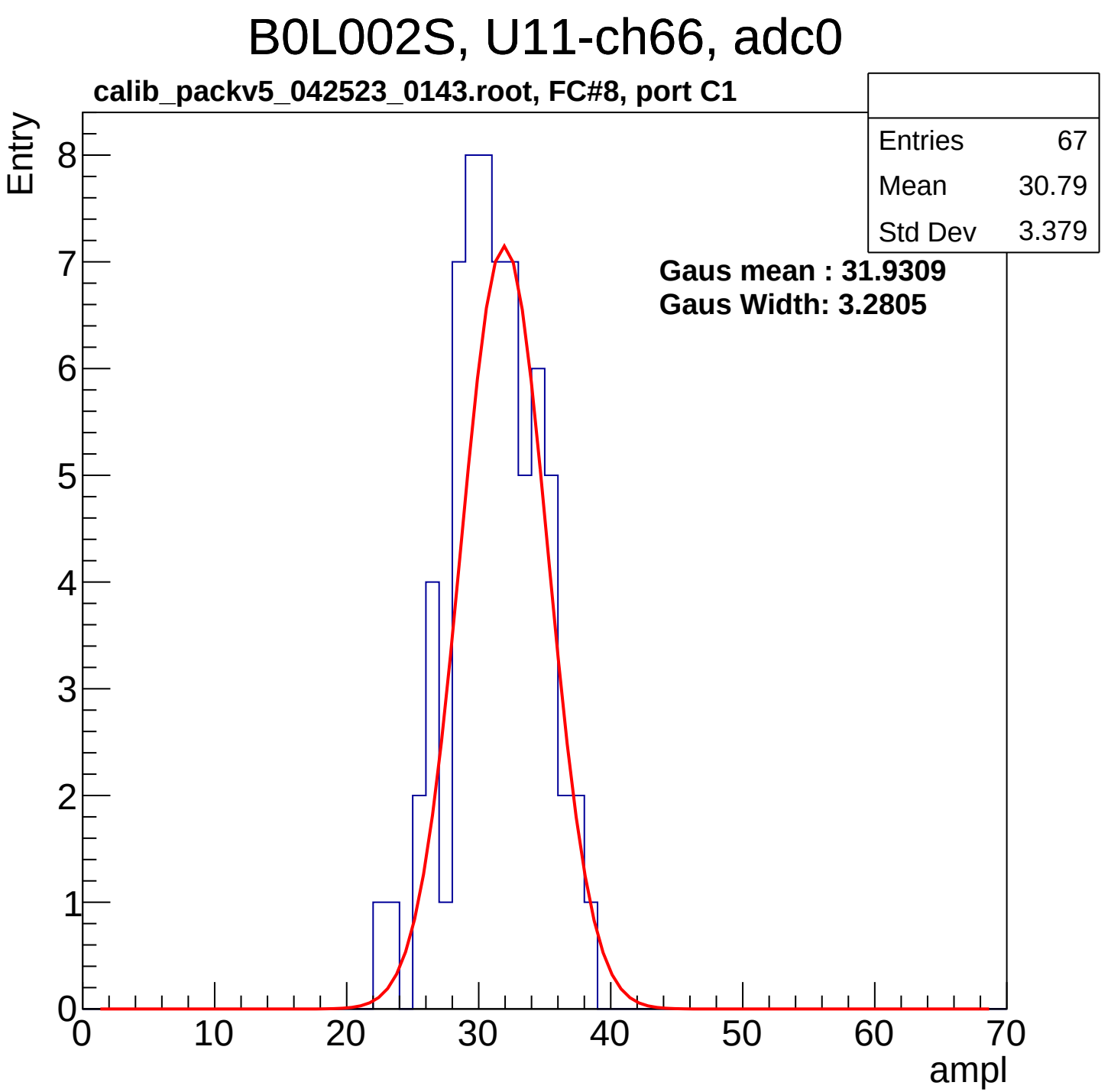
ampl


B0L002S, U11-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.79
Std Dev 3.379
Gaus mean : 31.9309
Gaus Width: 3.2805
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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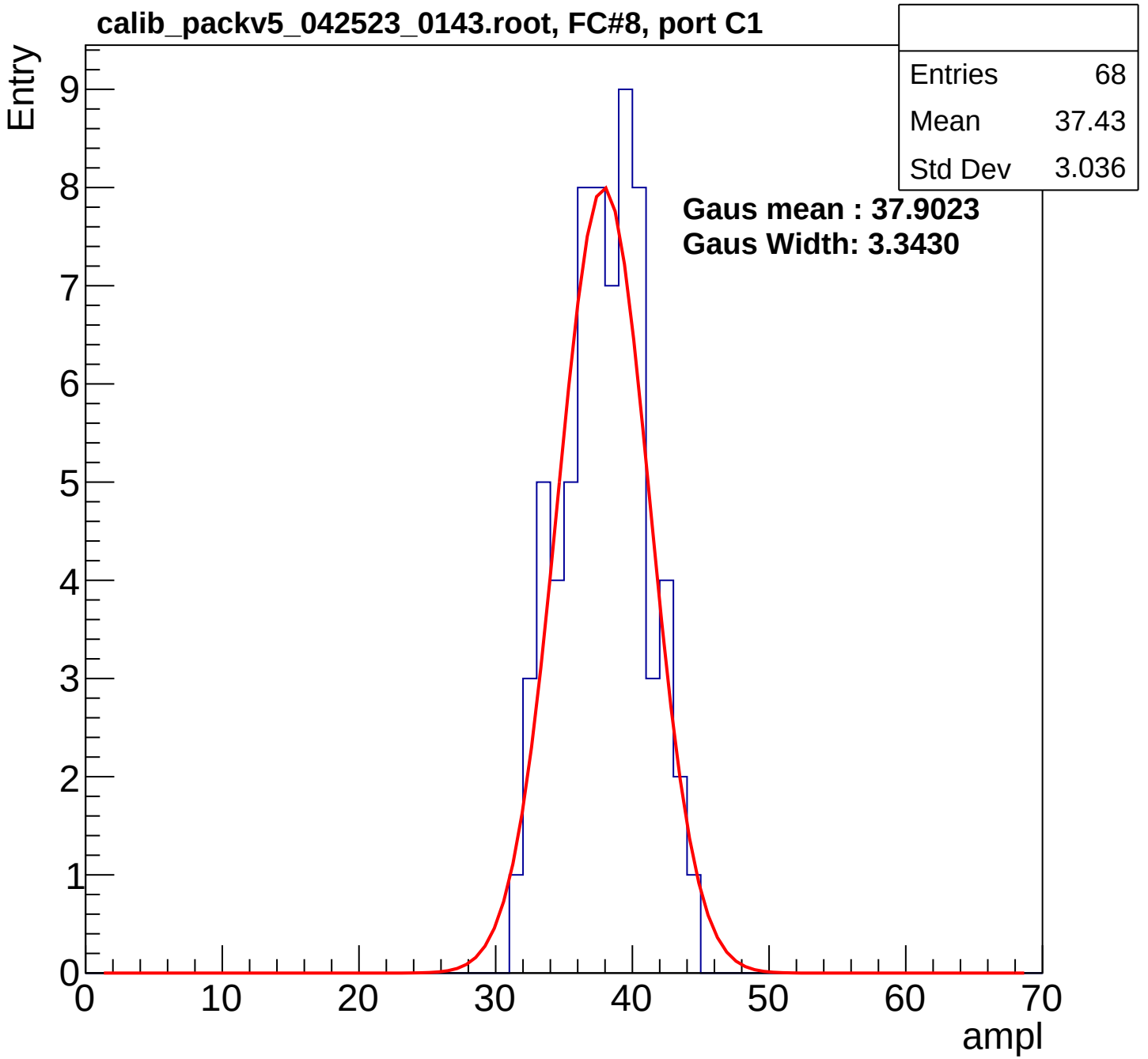
B0L002S, U11-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.43
Std Dev 3.036
Gaus mean : 37.9023
Gaus Width: 3.3430
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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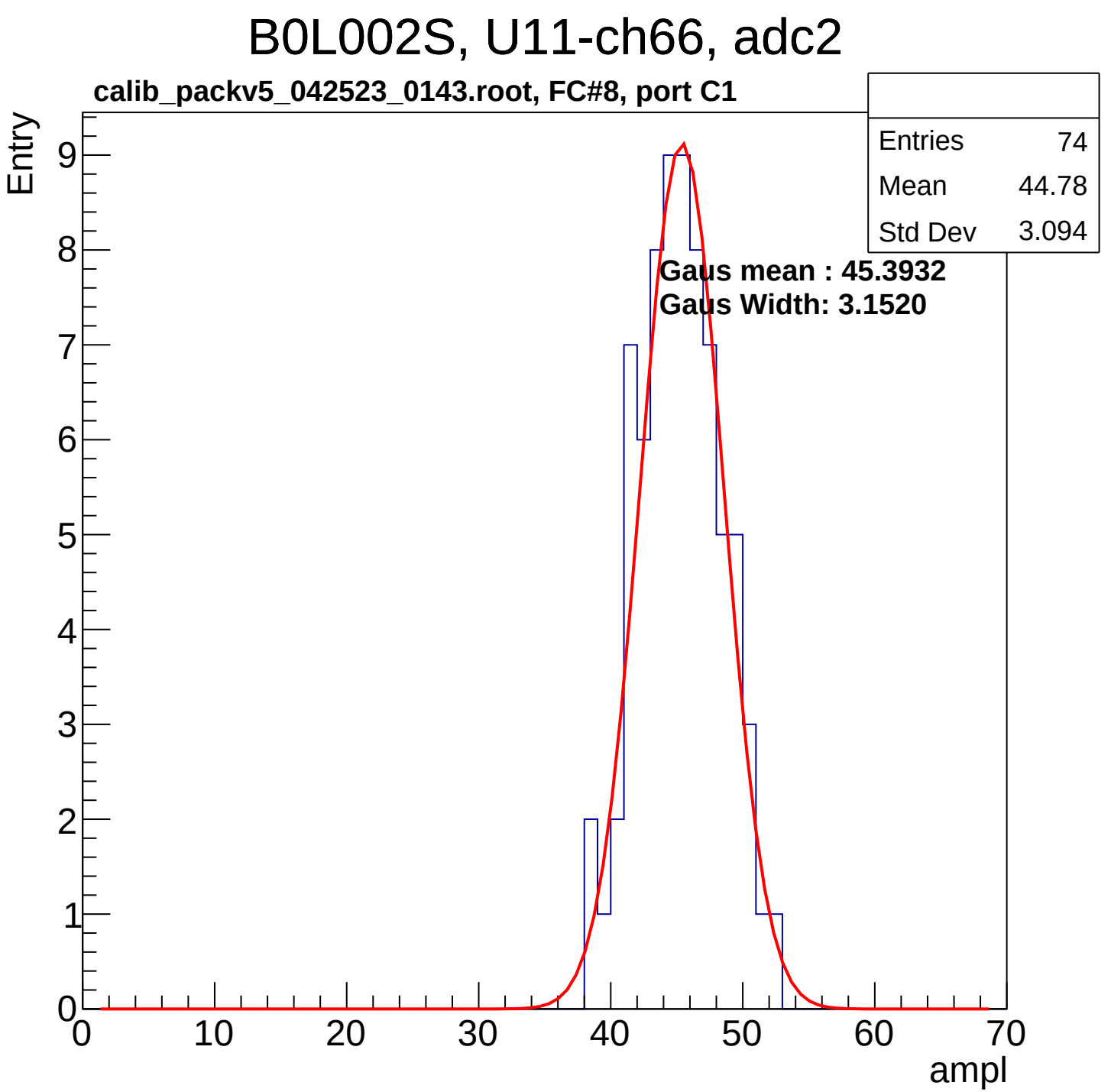
B0L002S, U11-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 44.78
Std Dev 3.094
Gaus mean : 45.3932
Gaus Width: 3.1520
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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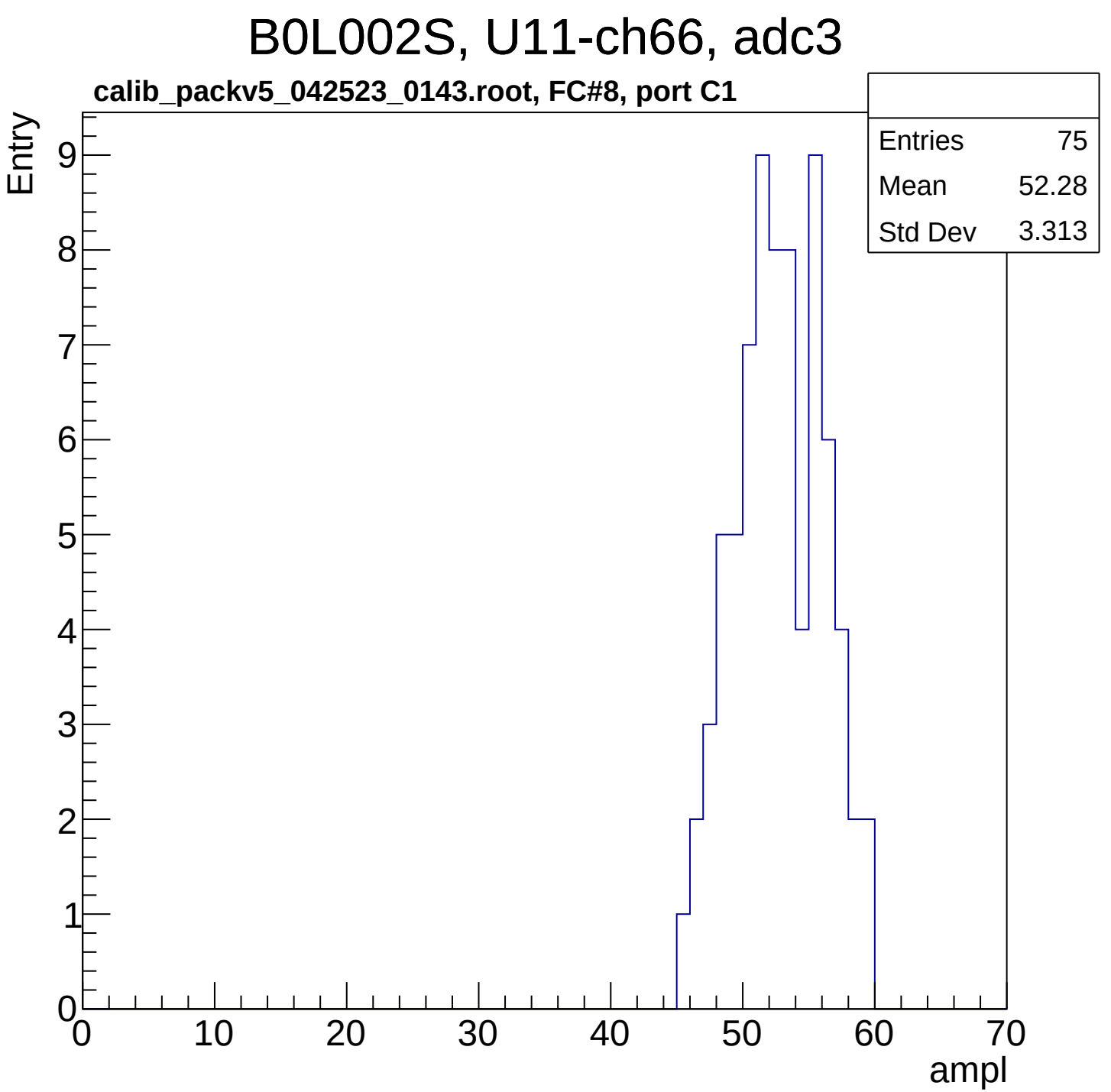
B0L002S, U11-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 52.28
Std Dev 3.313
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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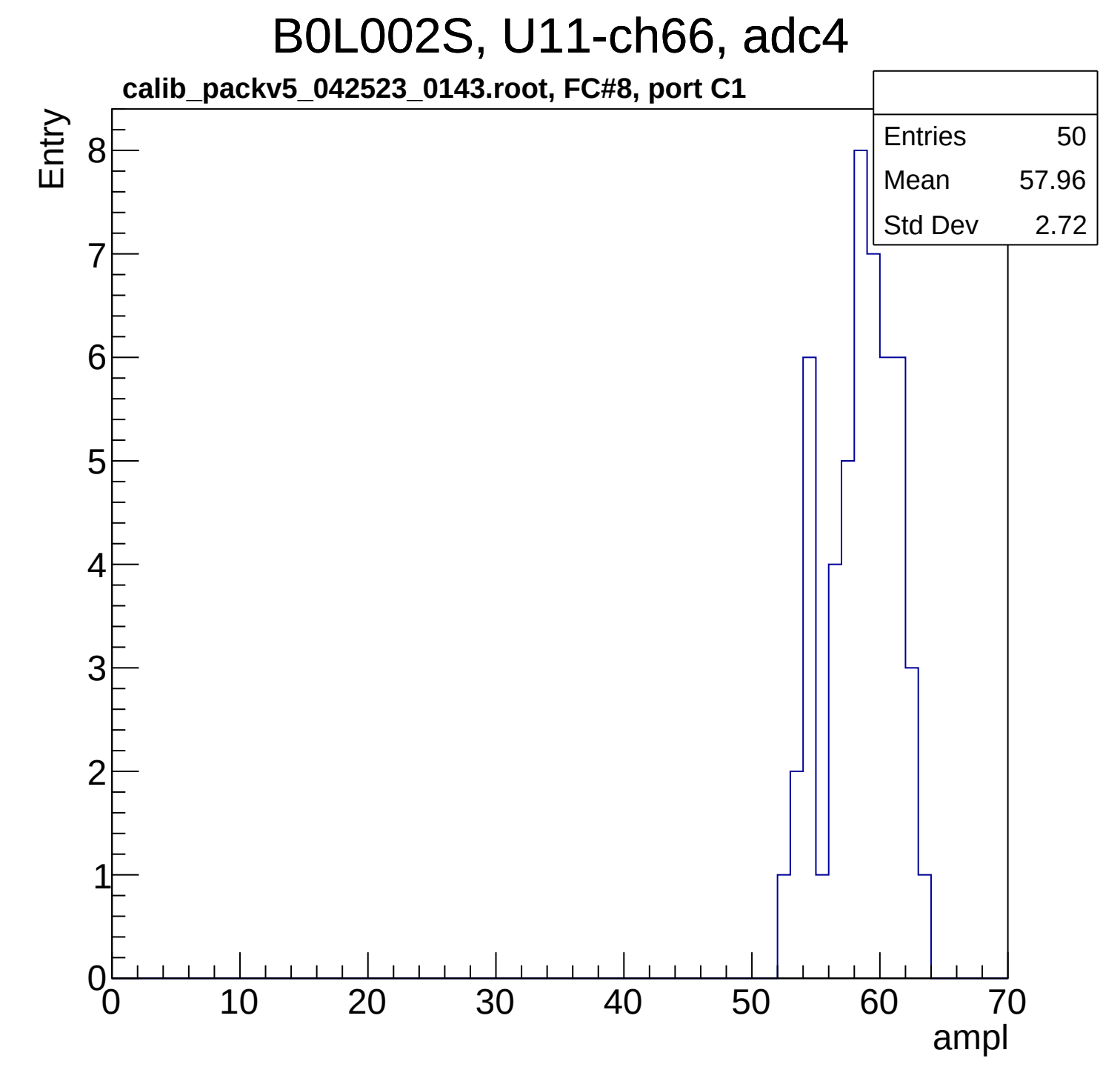
B0L002S, U11-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.96
Std Dev 2.72
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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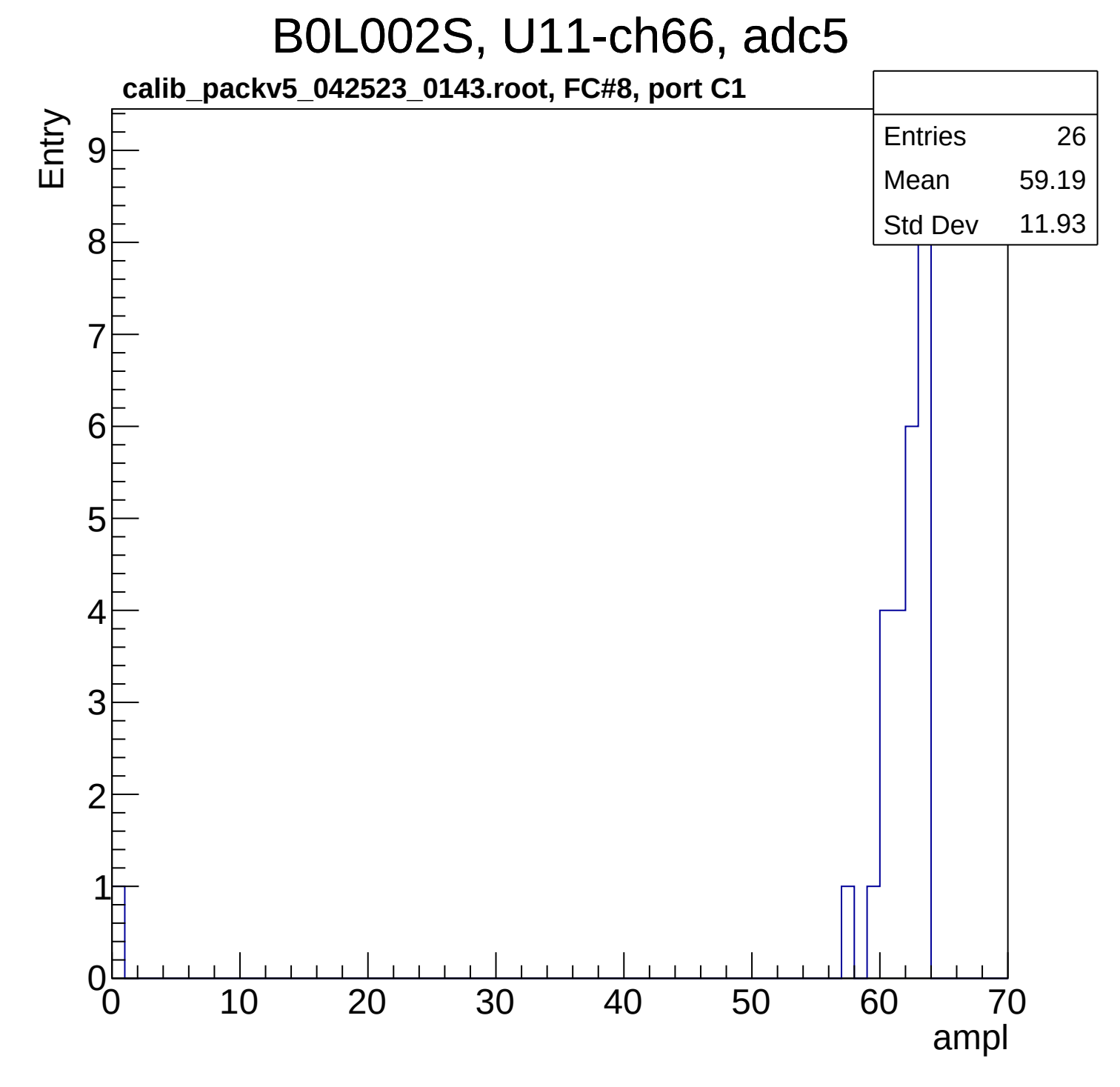
B0L002S, U11-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59.19
Std Dev 11.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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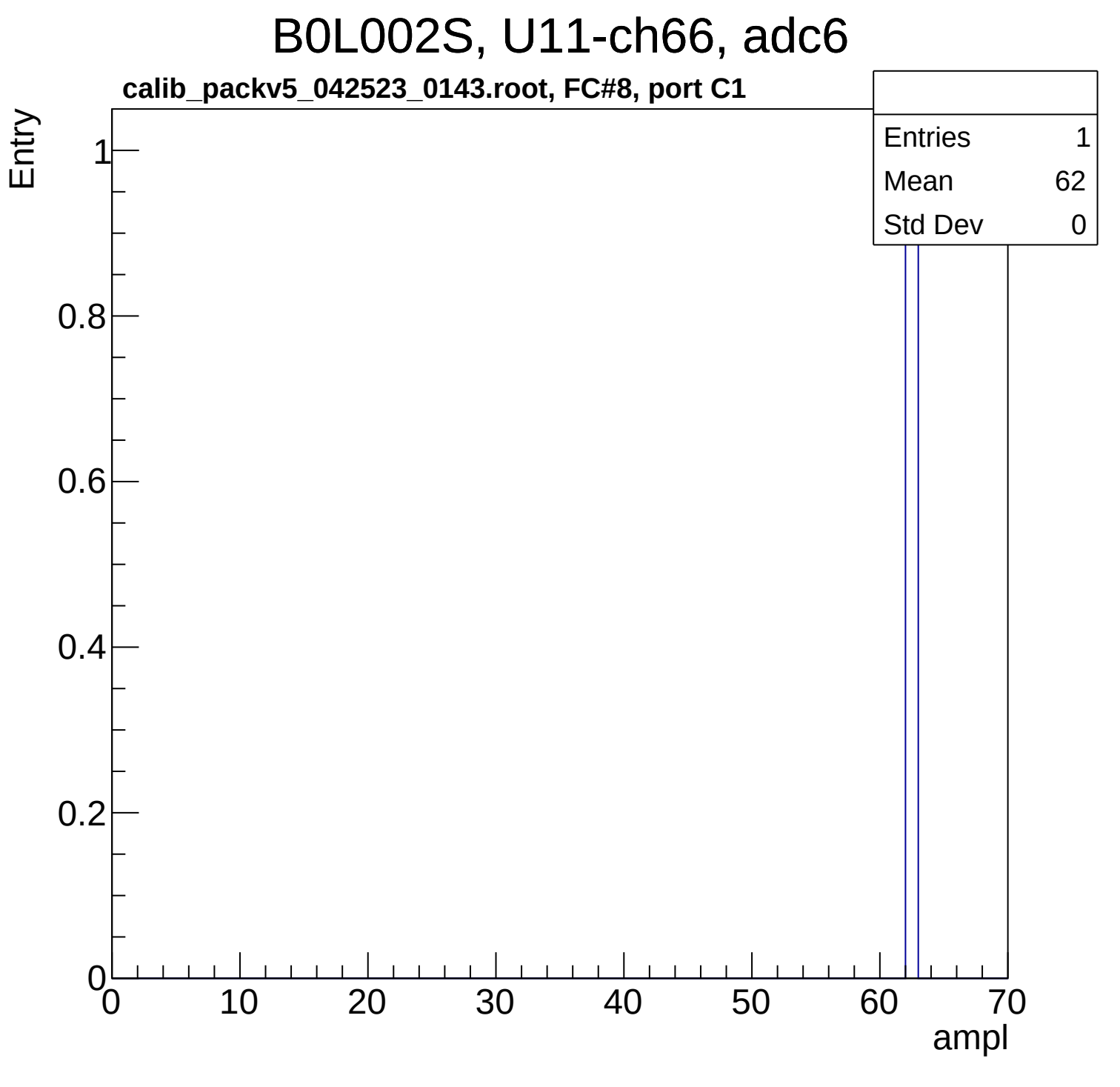
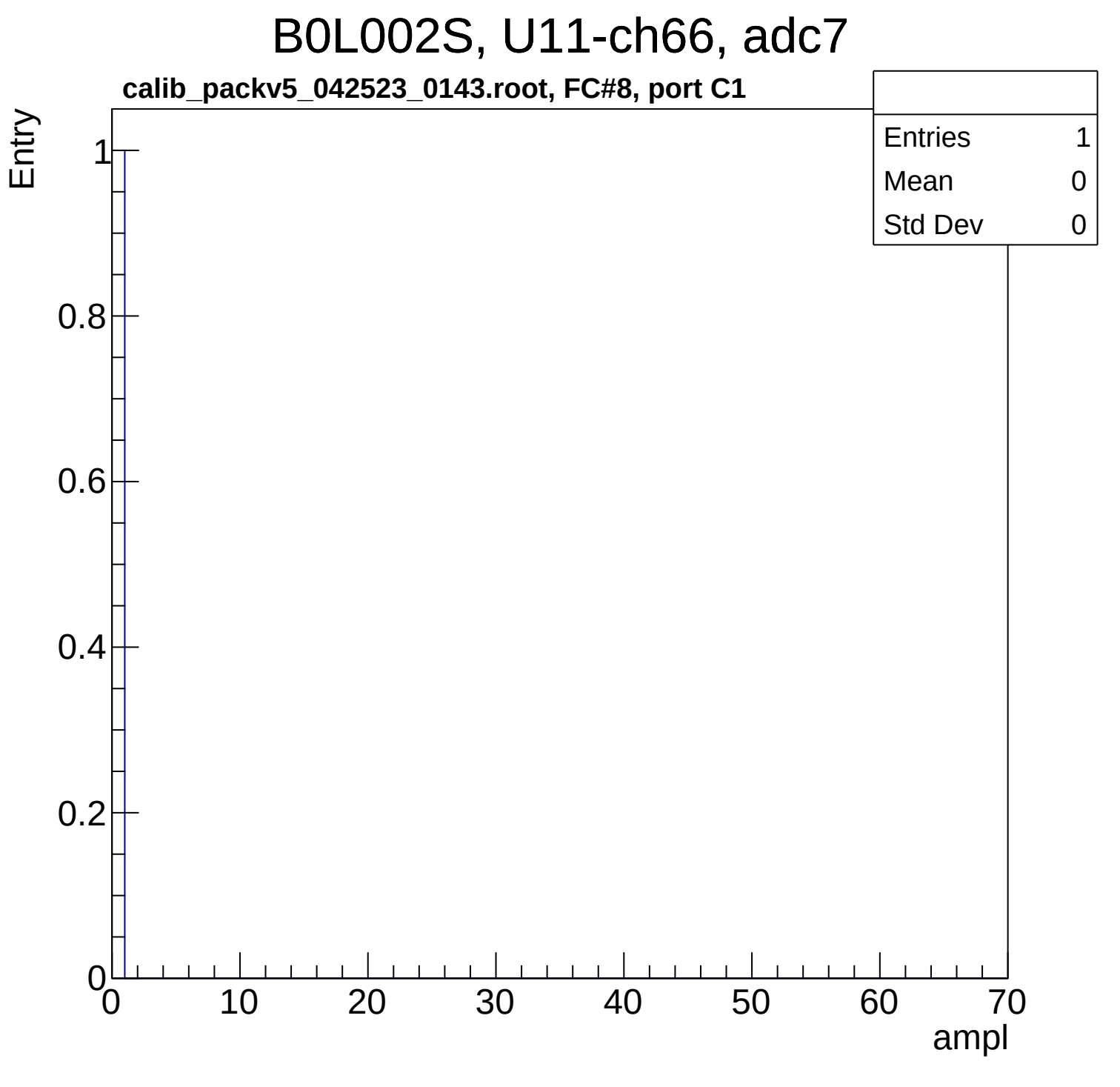
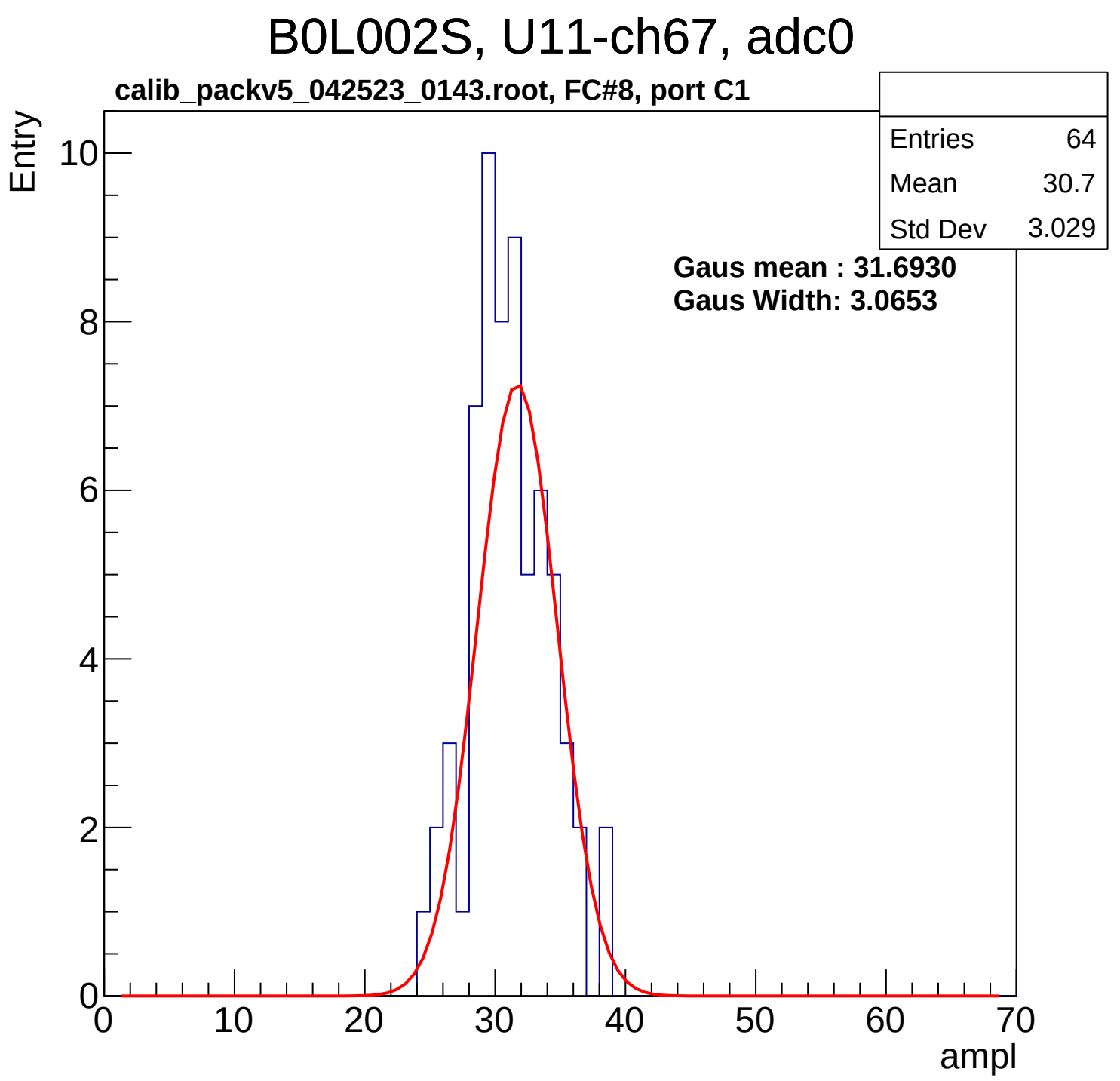
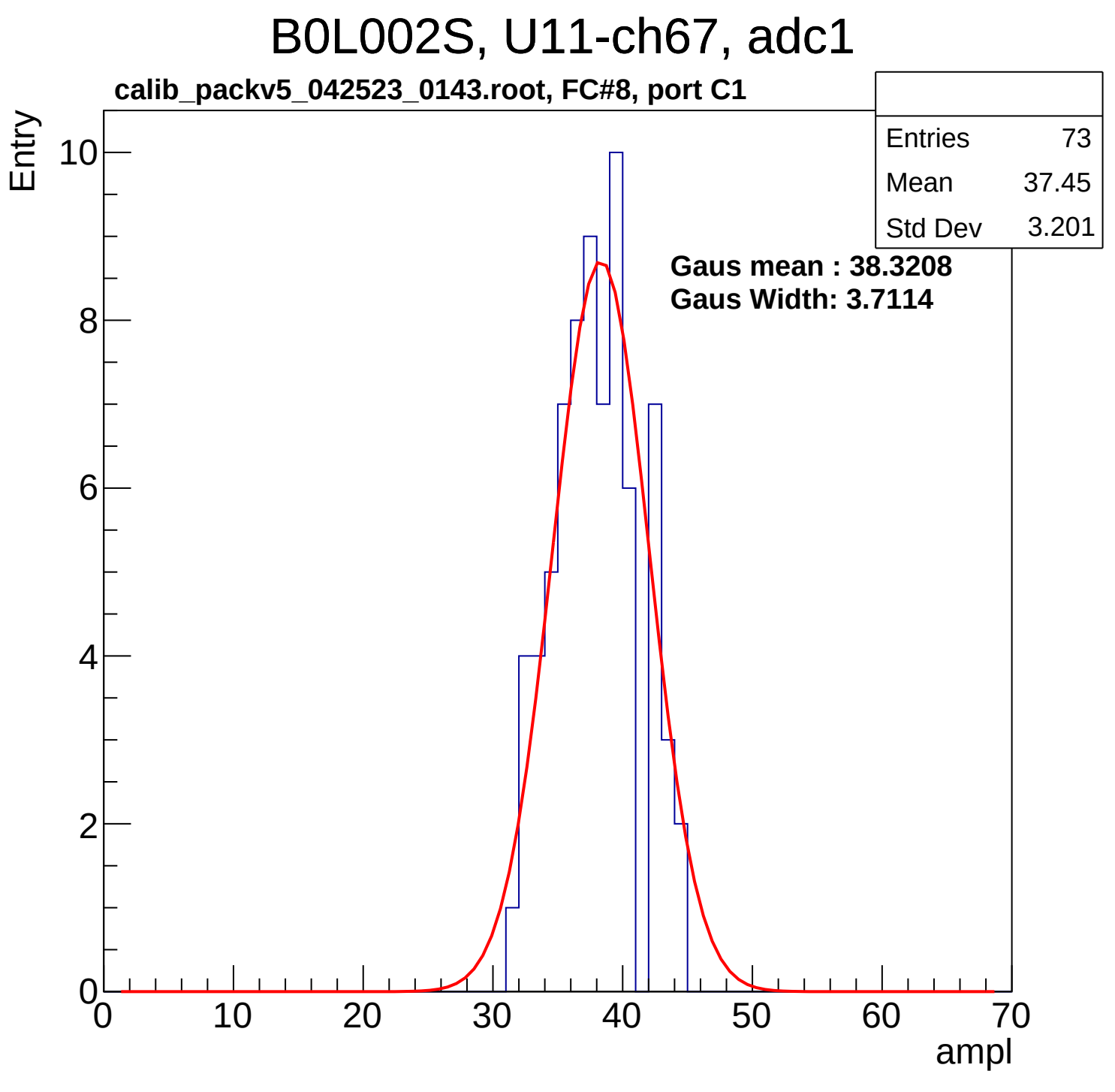
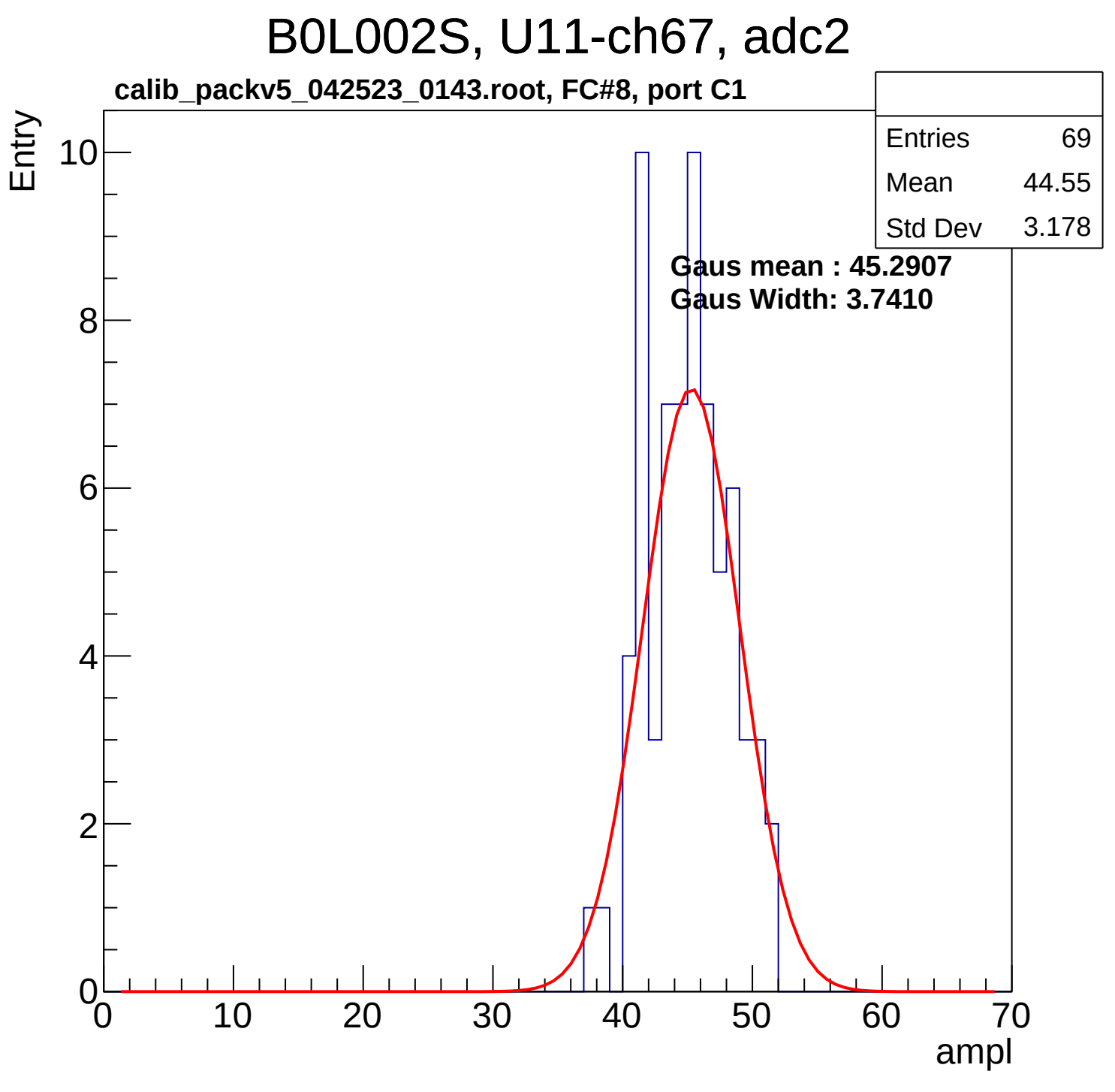

B0L002S, U11-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.7
Std Dev 3.029
Gaus mean : 31.6930
Gaus Width: 3.0653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.45
Std Dev 3.201
Gaus mean : 38.3208
Gaus Width: 3.7114
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.55
Std Dev 3.178
Gaus mean : 45.2907
Gaus Width: 3.7410
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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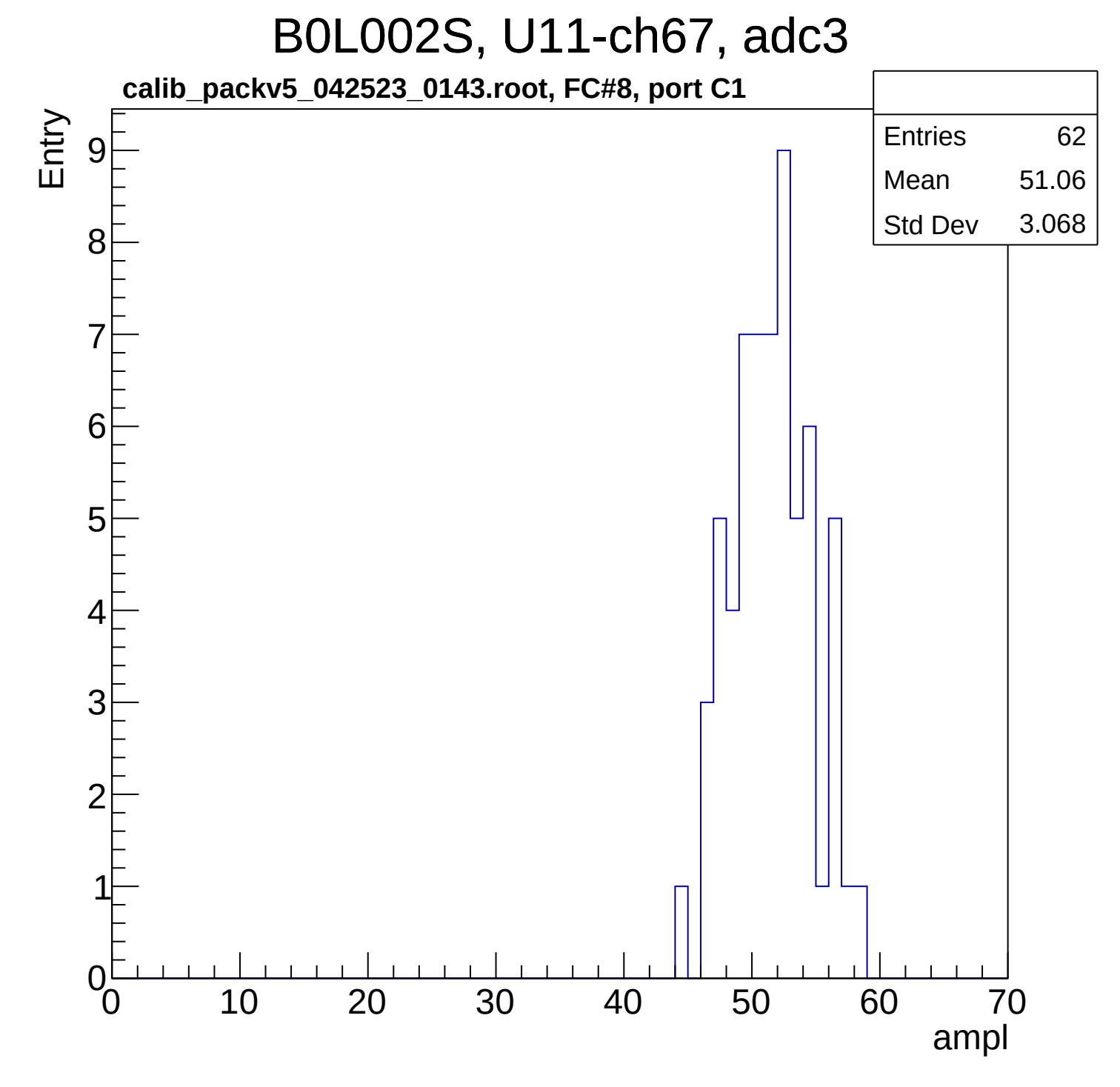
B0L002S, U11-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.06
Std Dev 3.068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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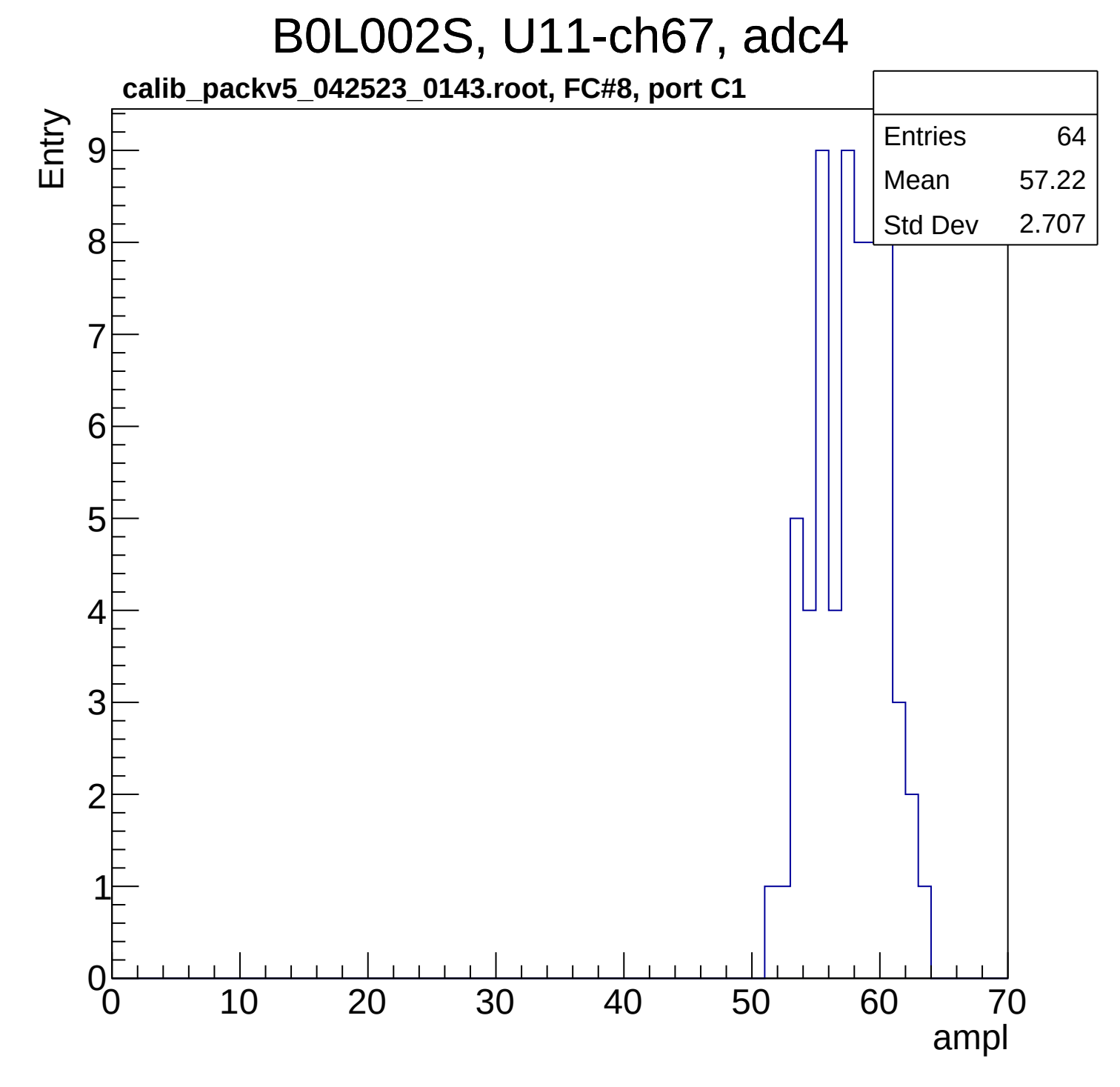
B0L002S, U11-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 57.22
Std Dev 2.707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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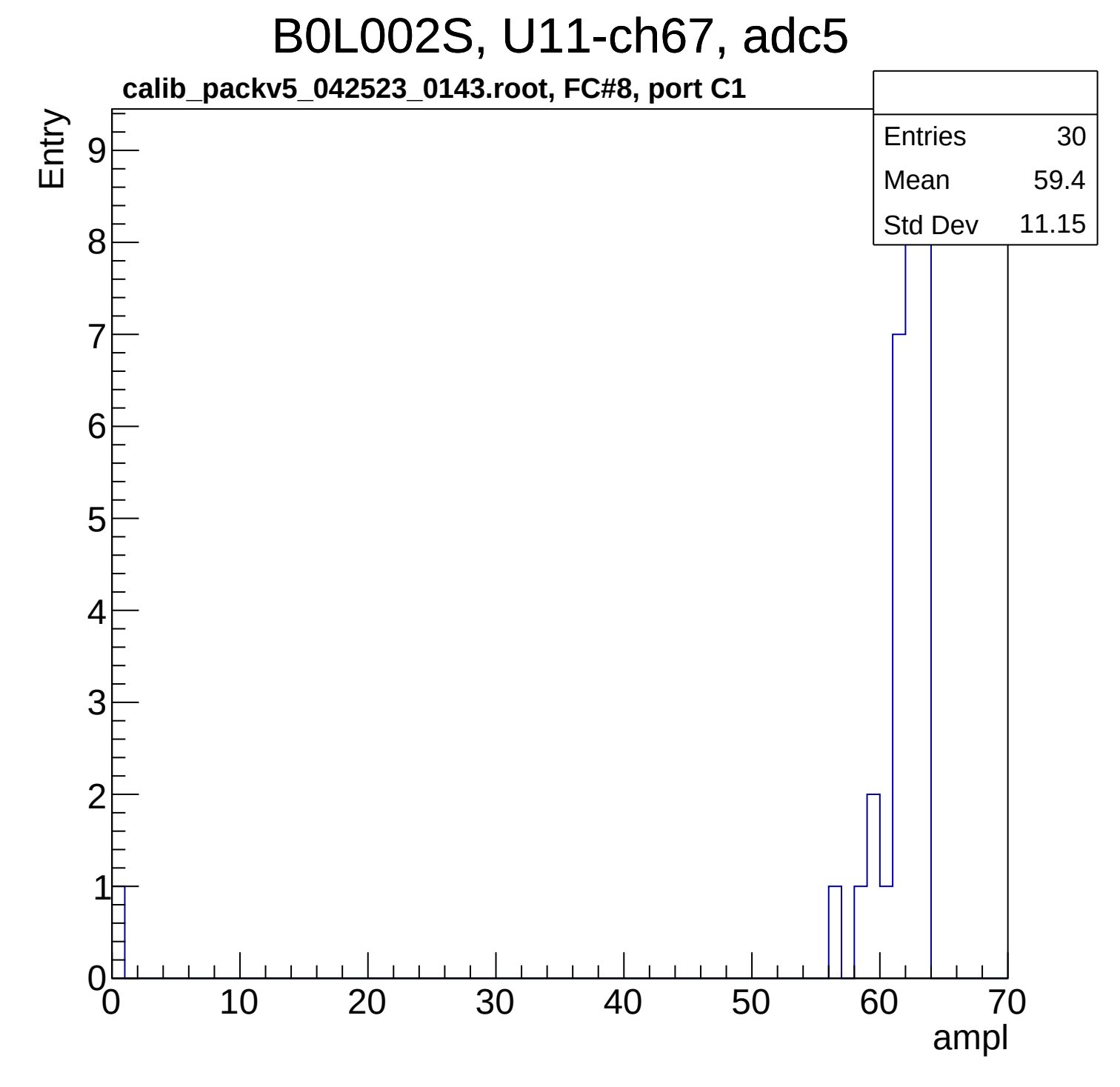
B0L002S, U11-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.4
Std Dev 11.15
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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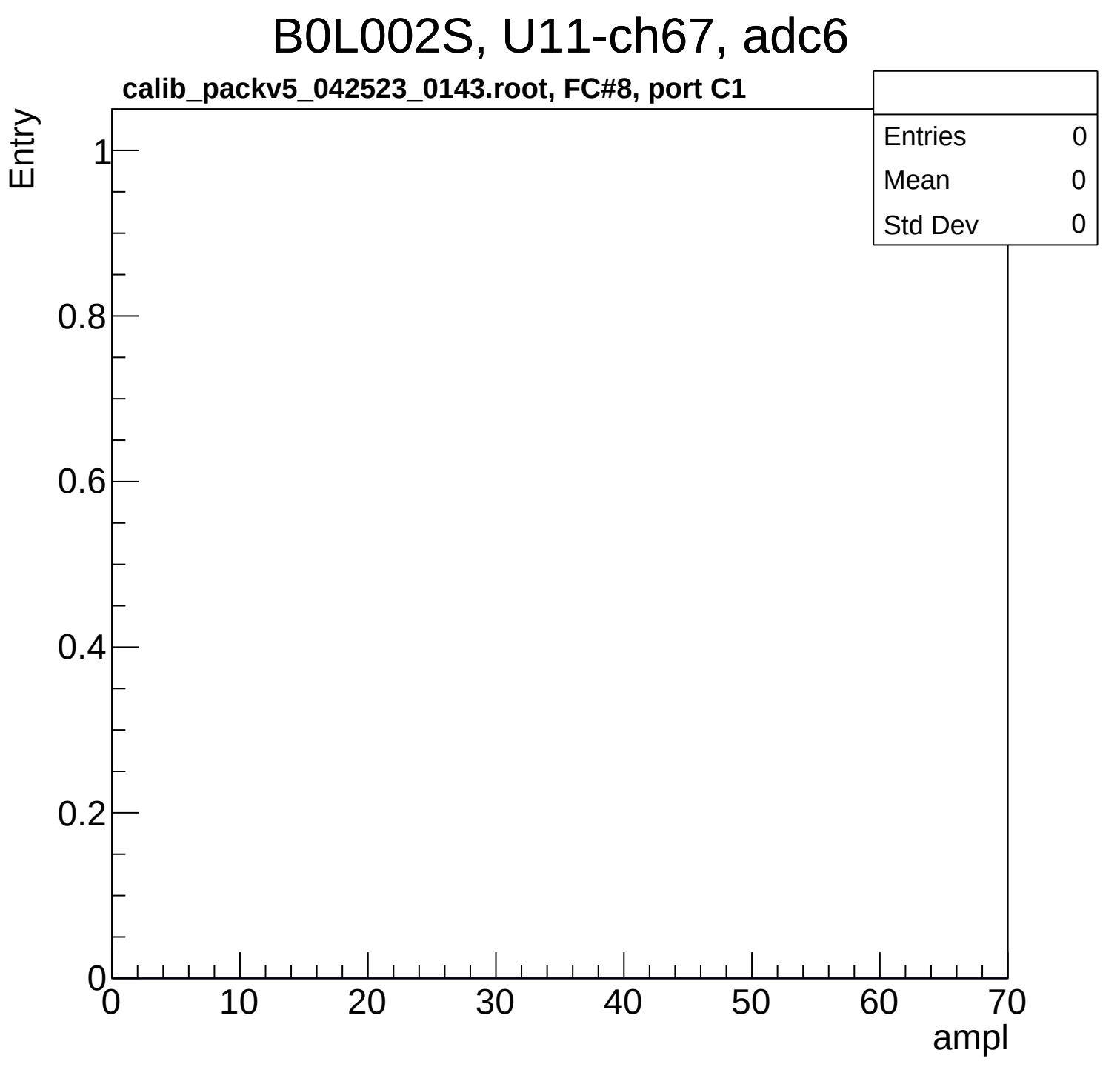
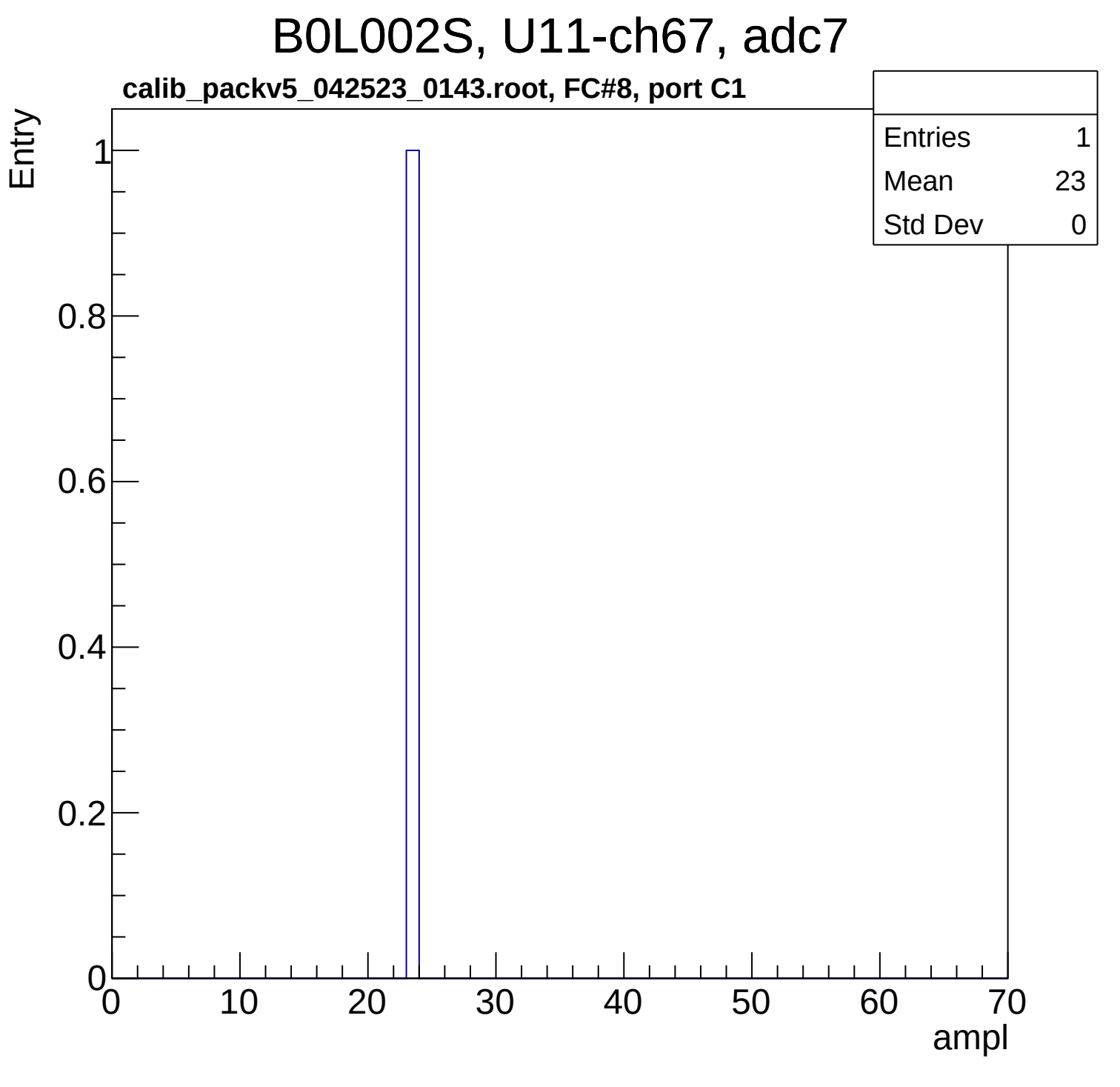
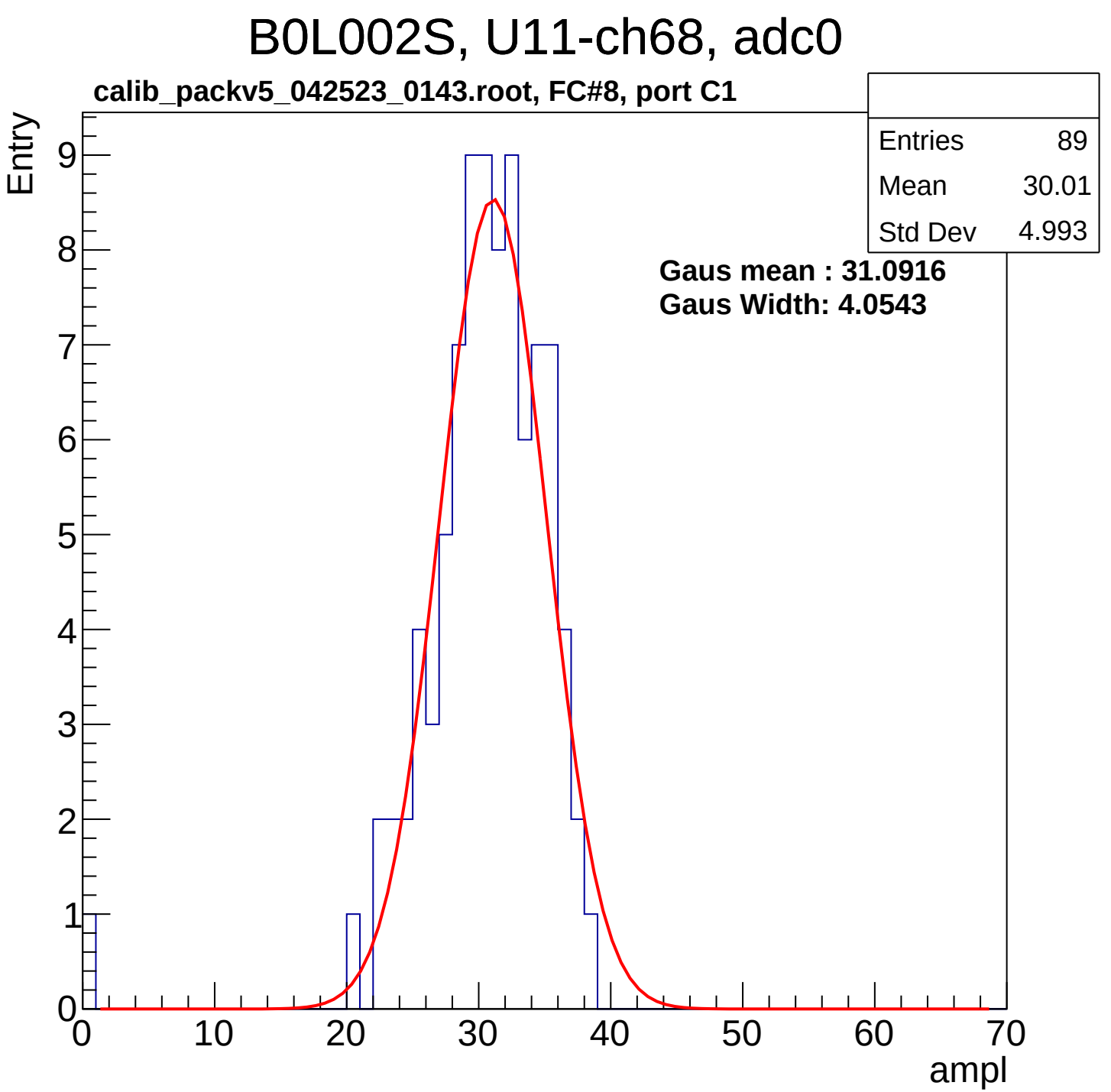

B0L002S, U11-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 30.01
Std Dev 4.993
Gaus mean : 31.0916
Gaus Width: 4.0543
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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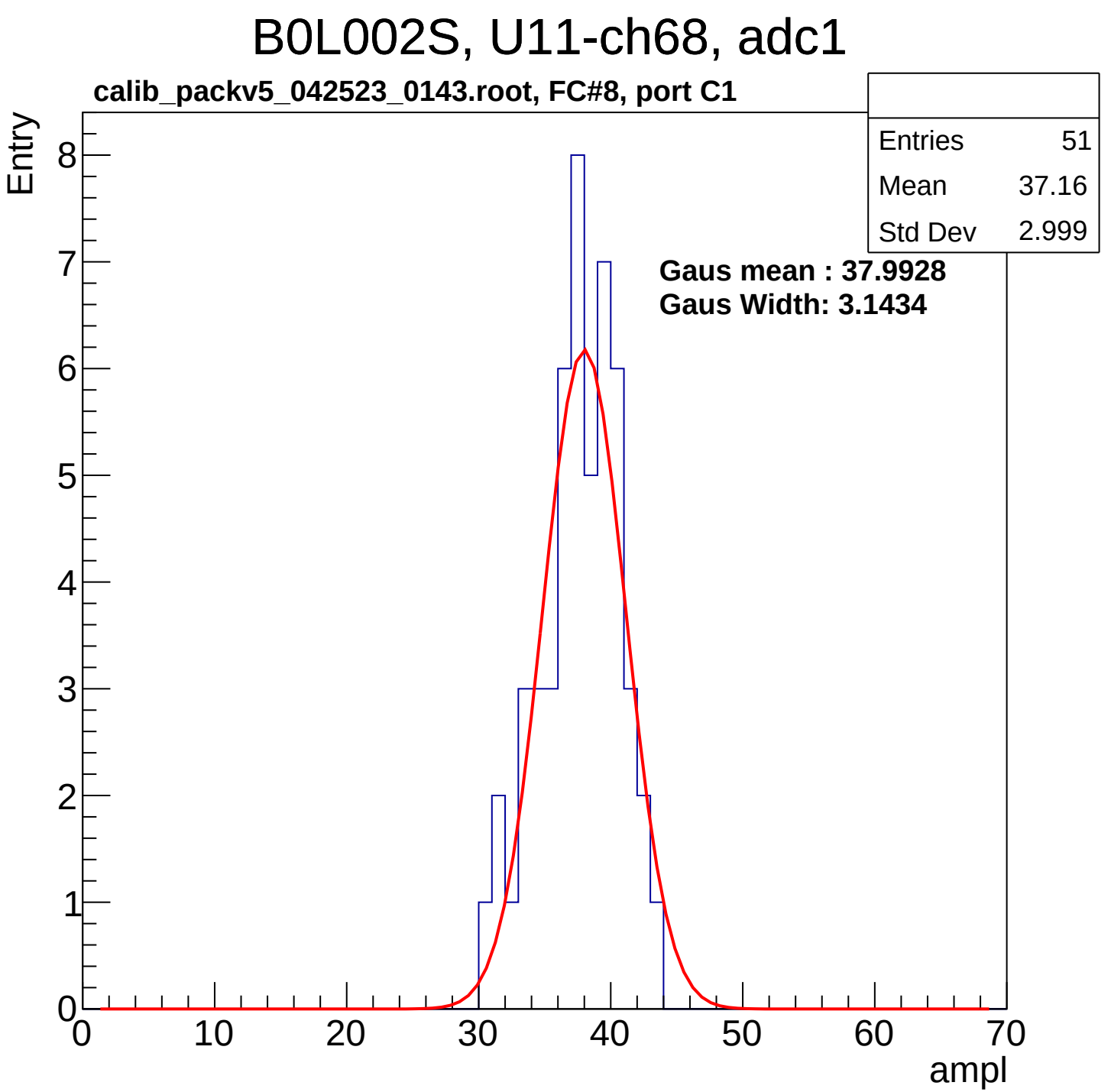
40
50
60
70


B0L002S, U11-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 37.16
Std Dev 2.999
Gaus mean : 37.9928
Gaus Width: 3.1434
Entry
ampl
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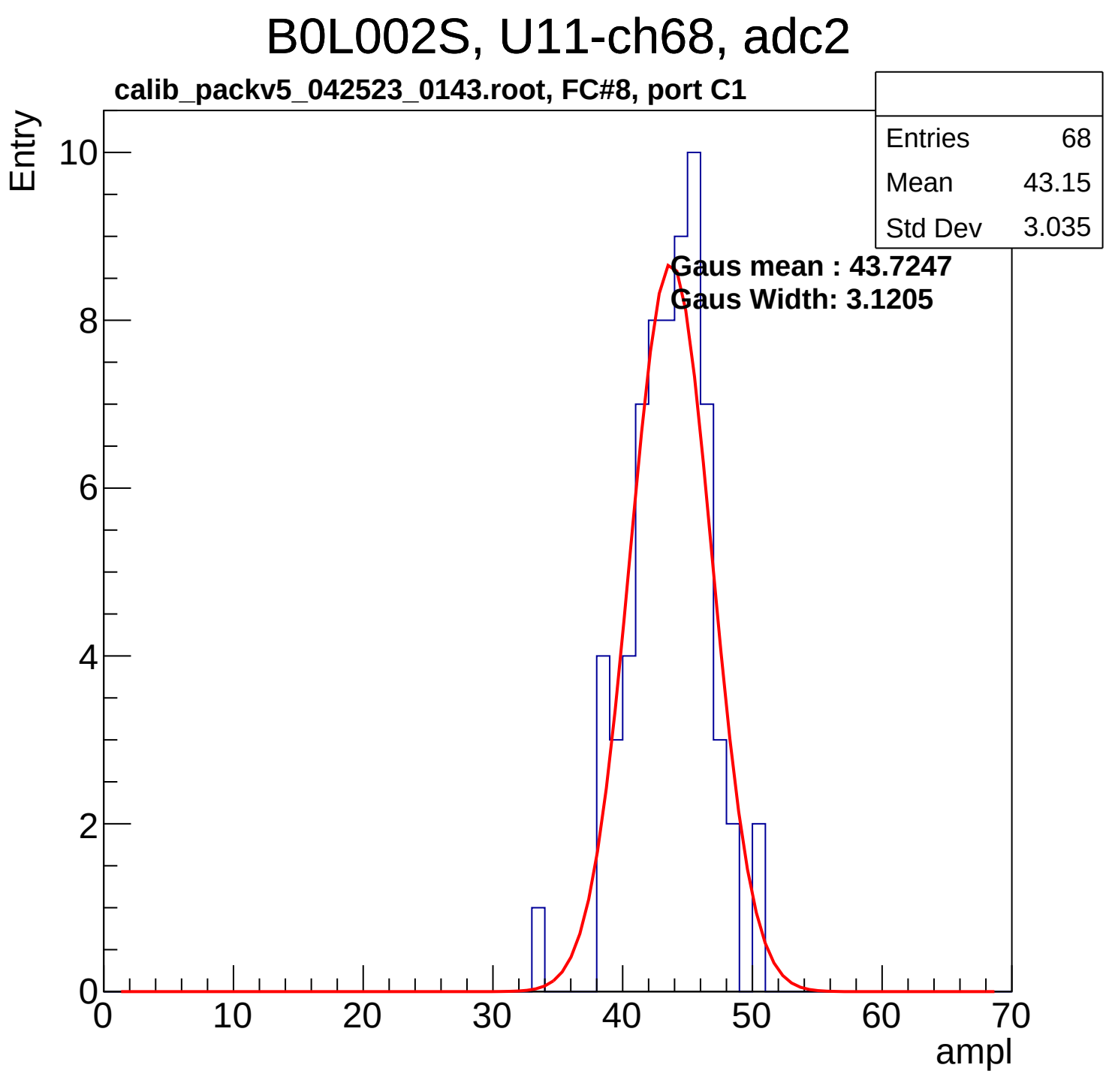
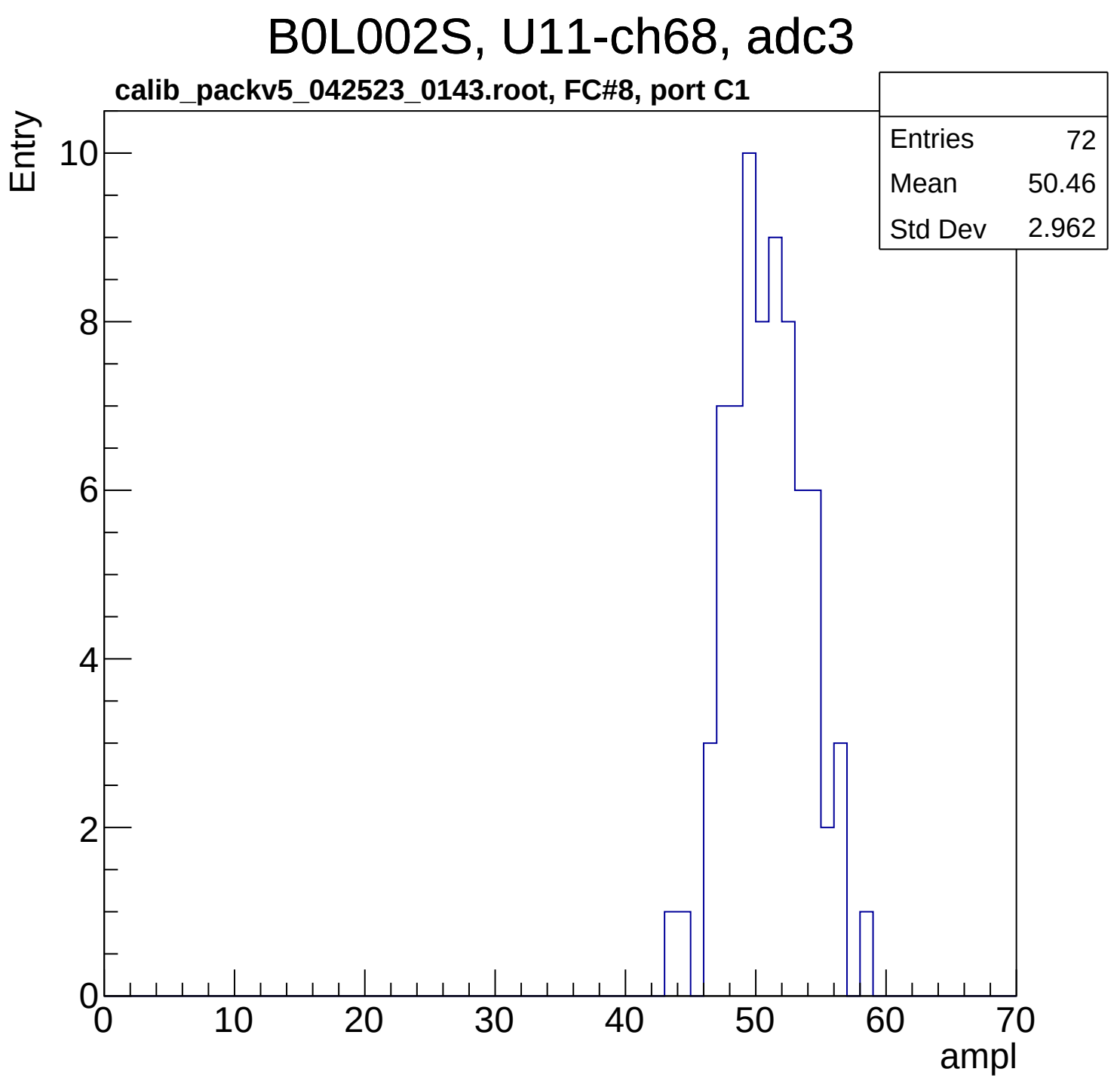
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.15
Std Dev 3.035
Gaus mean : 43.7247
Gaus Width: 3.1205
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.46
Std Dev 2.962
Entry
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2
4
6
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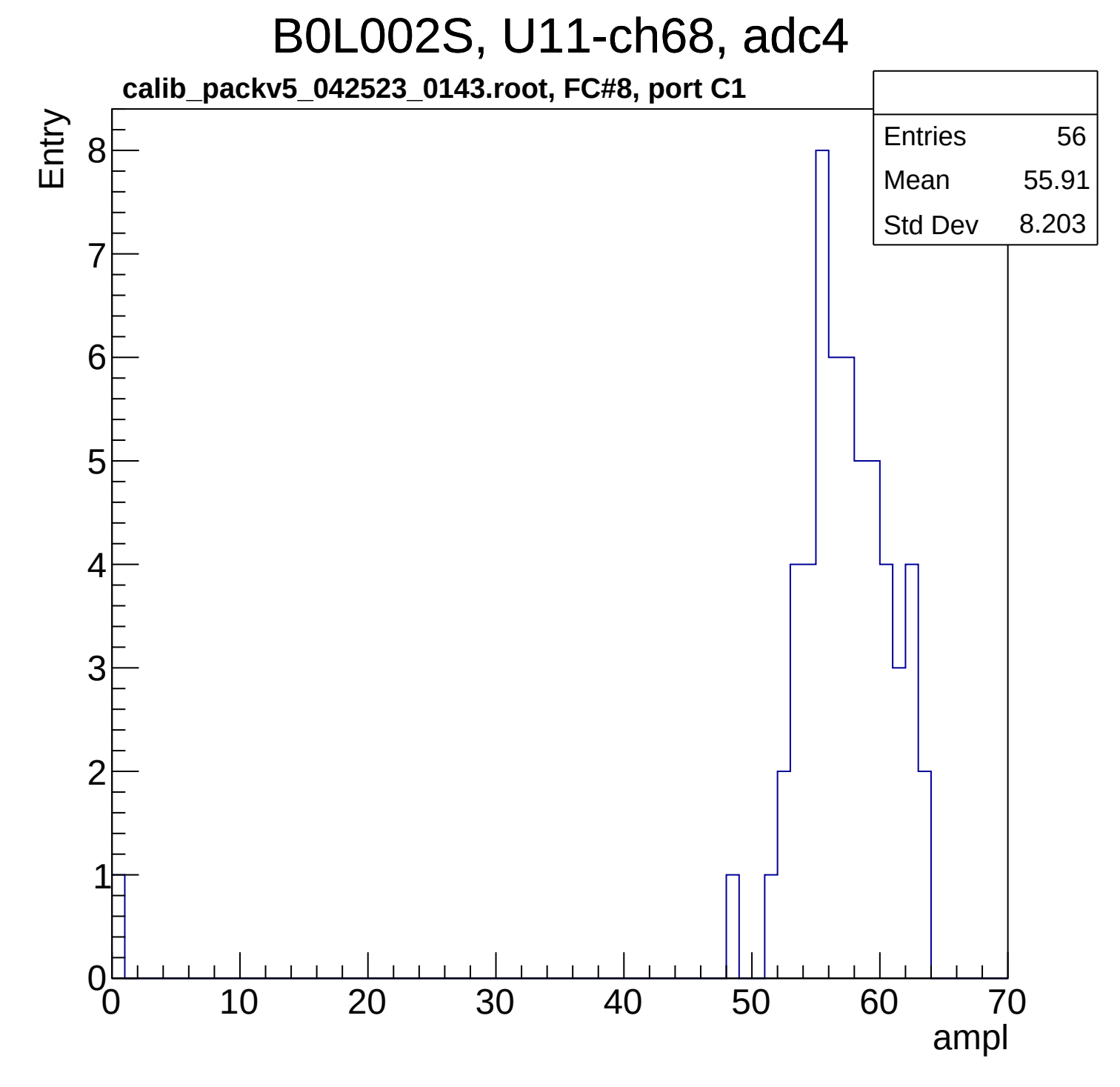
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.91
Std Dev 8.203
Entry
ampl
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2
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4
5
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7
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0
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20
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40
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60
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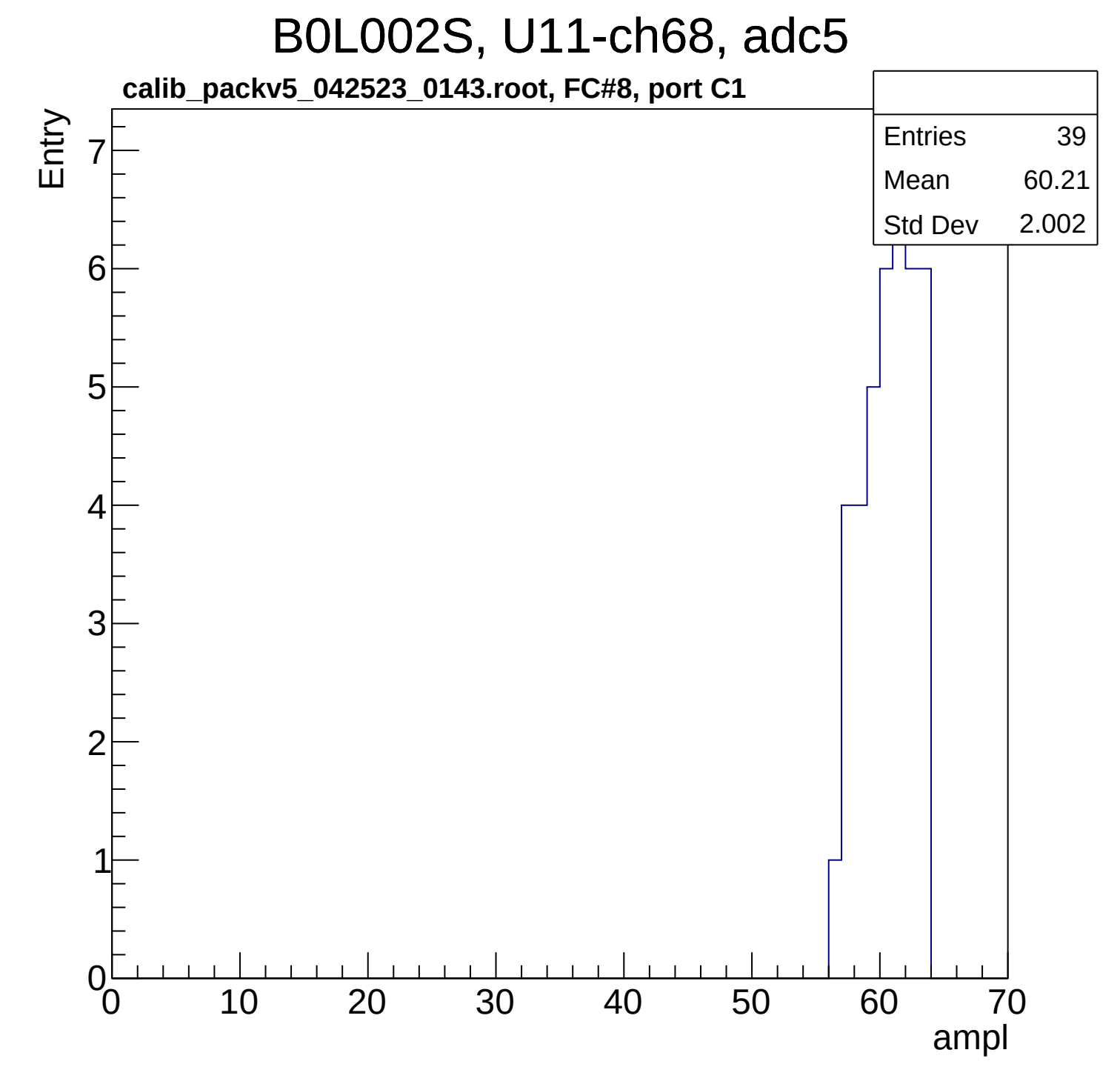
B0L002S, U11-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.21
Std Dev 2.002
Entry
ampl
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3
4
5
6
7
0
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40
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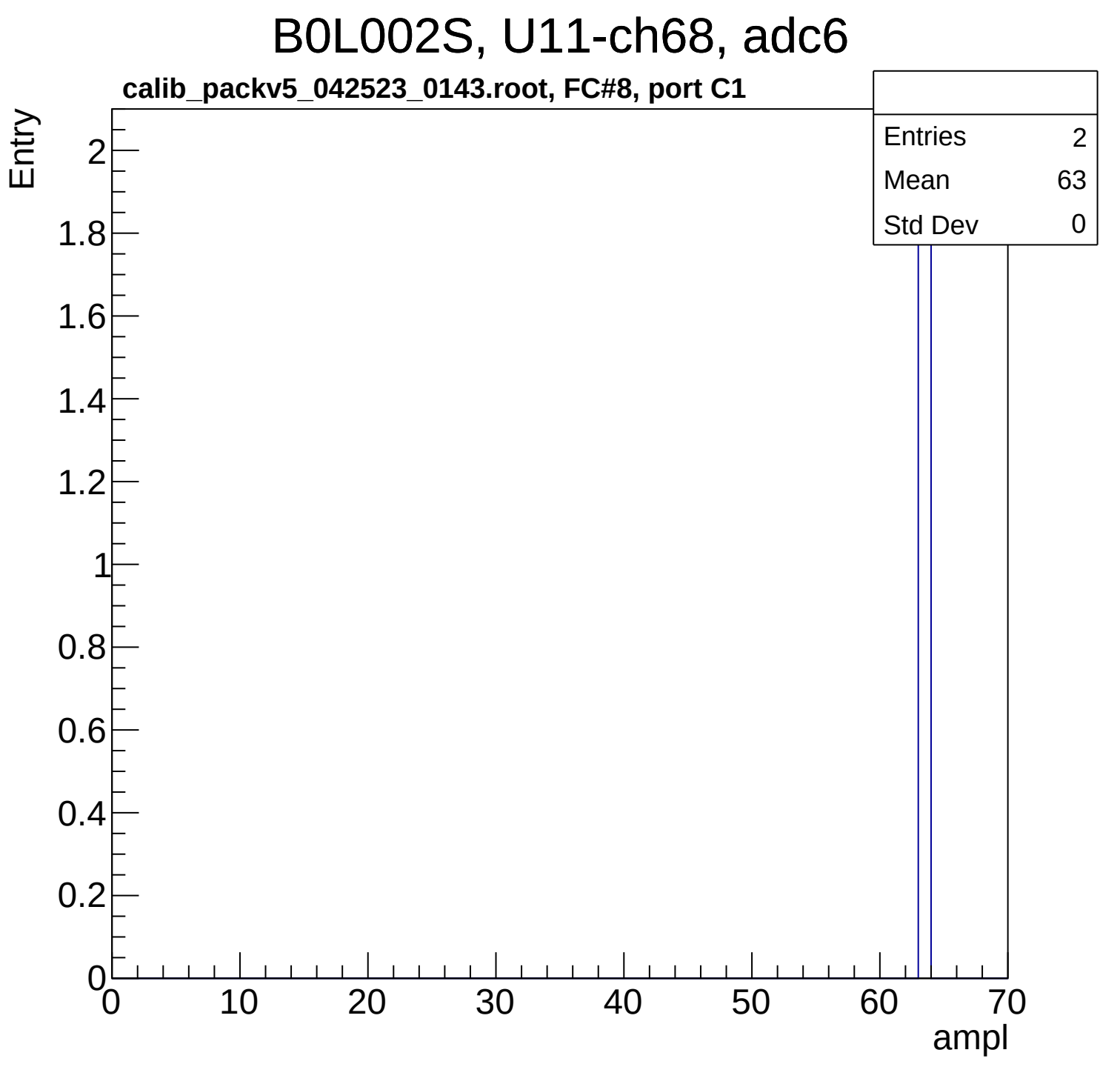
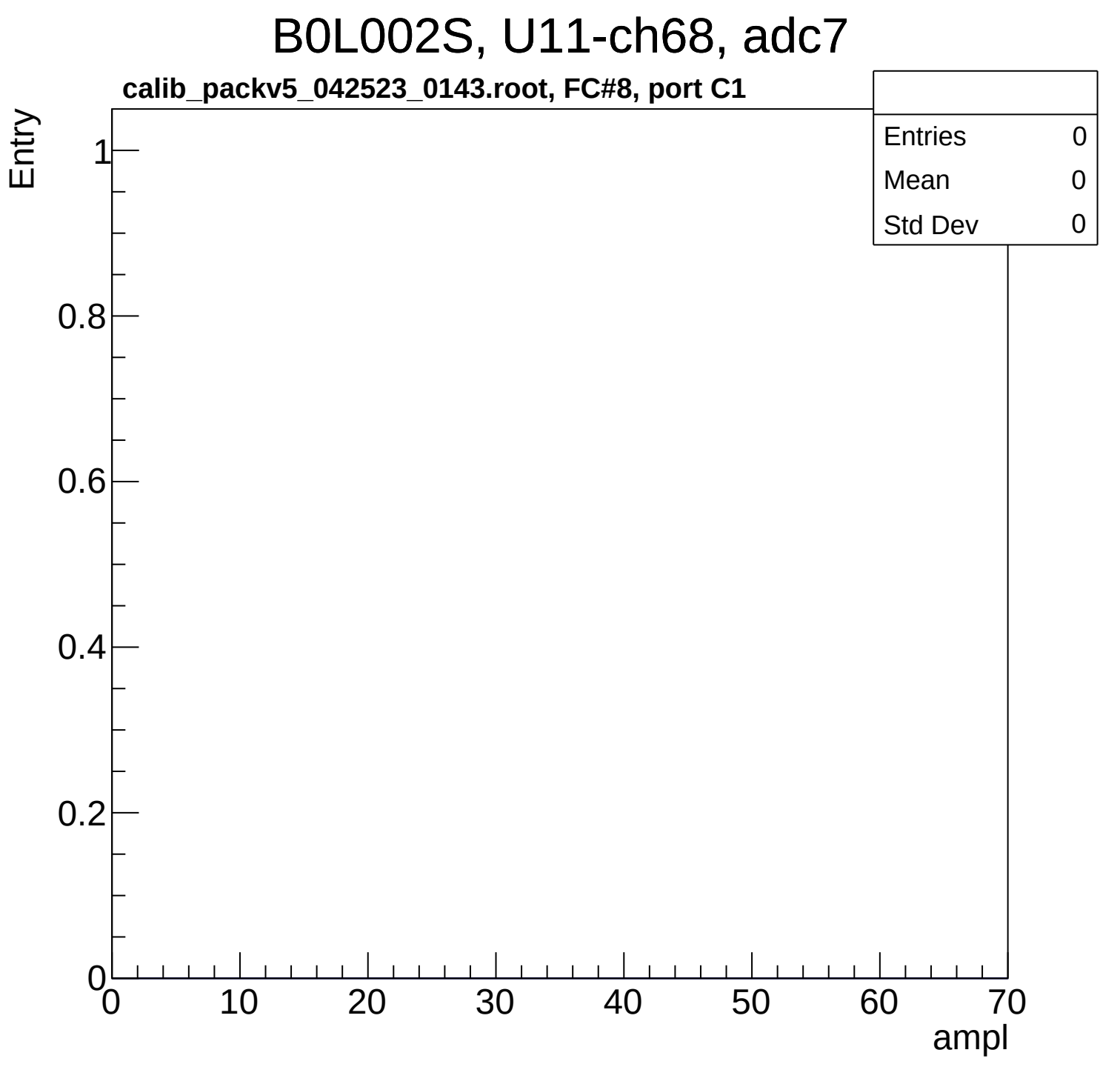
60
70


B0L002S, U11-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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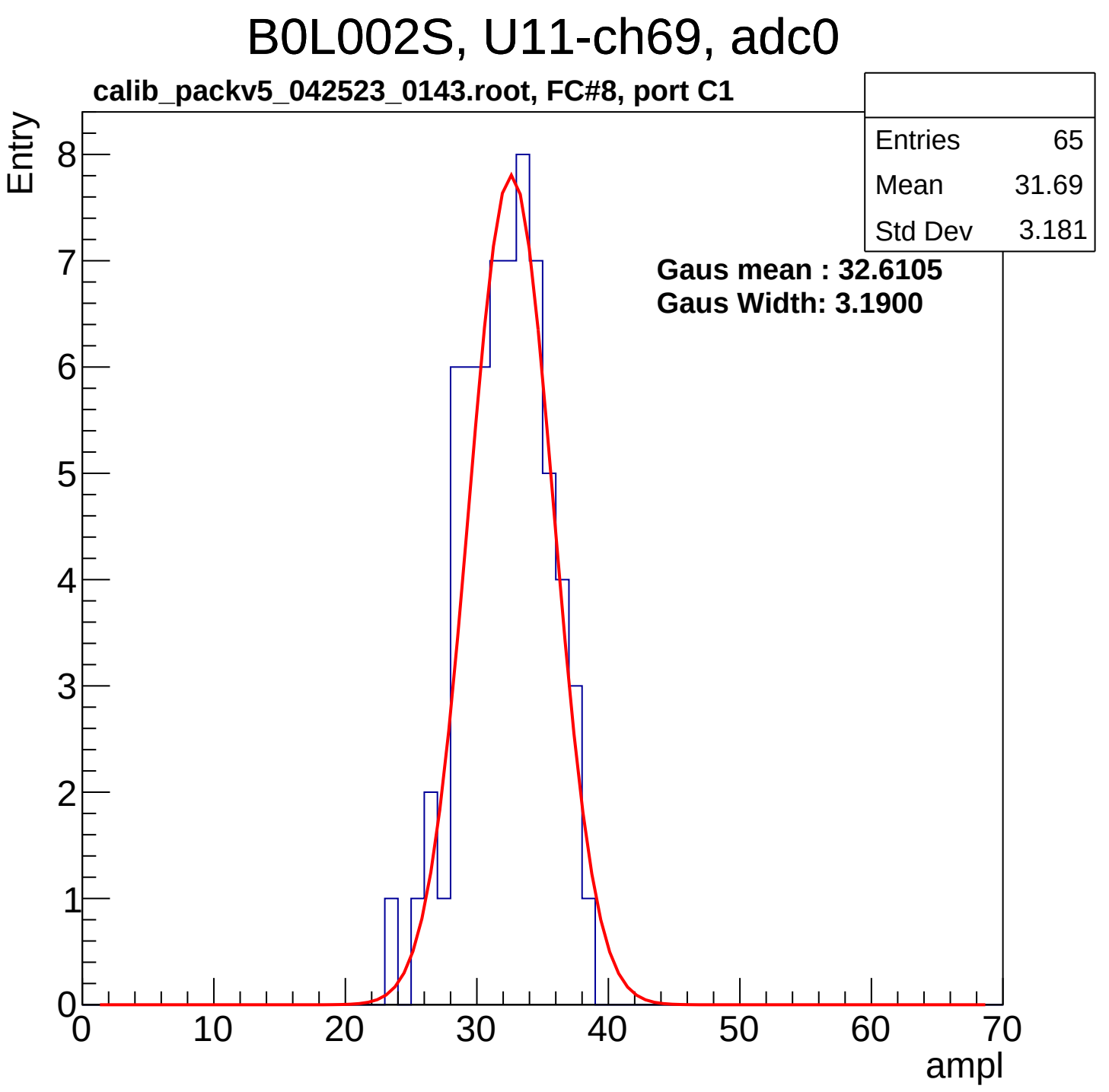
50
60
70
ampl


B0L002S, U11-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 31.69
Std Dev 3.181
Gaus mean : 32.6105
Gaus Width: 3.1900
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
50
60
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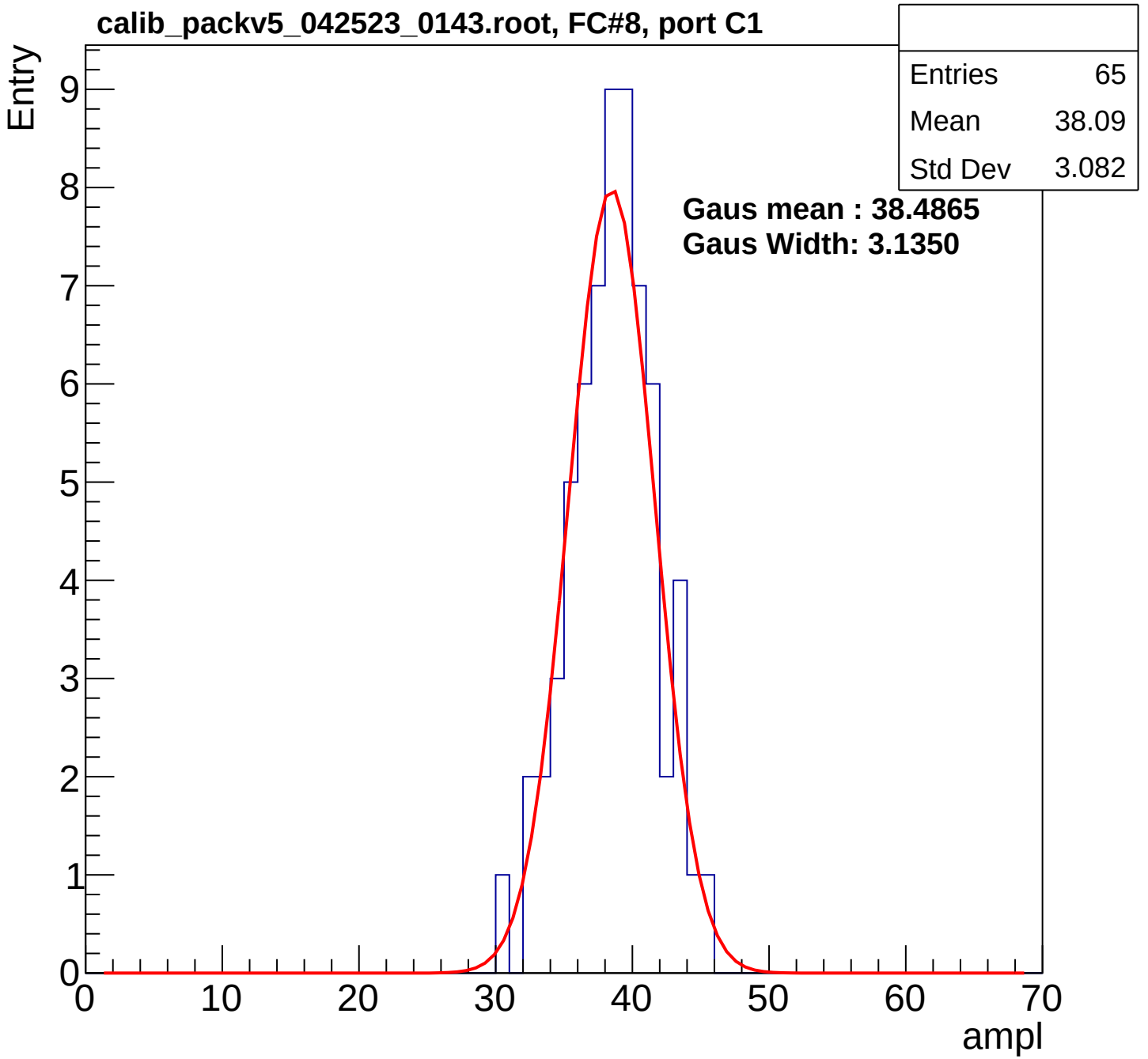
B0L002S, U11-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.09
Std Dev 3.082
Gaus mean : 38.4865
Gaus Width: 3.1350
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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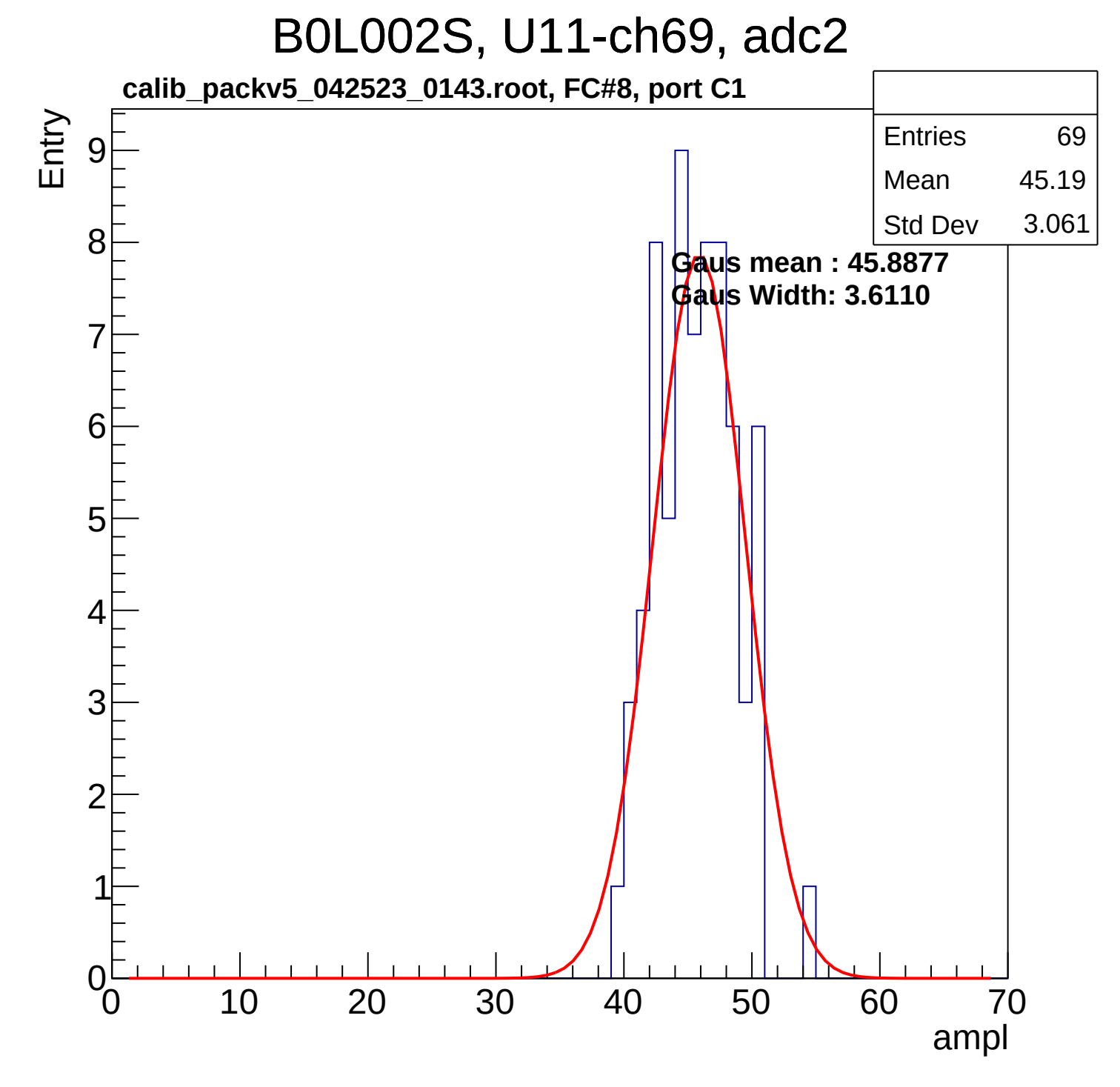
B0L002S, U11-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.19
Std Dev 3.061
Gaus mean : 45.8877
Gaus Width: 3.6110
Entry
ampl
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2
3
4
5
6
7
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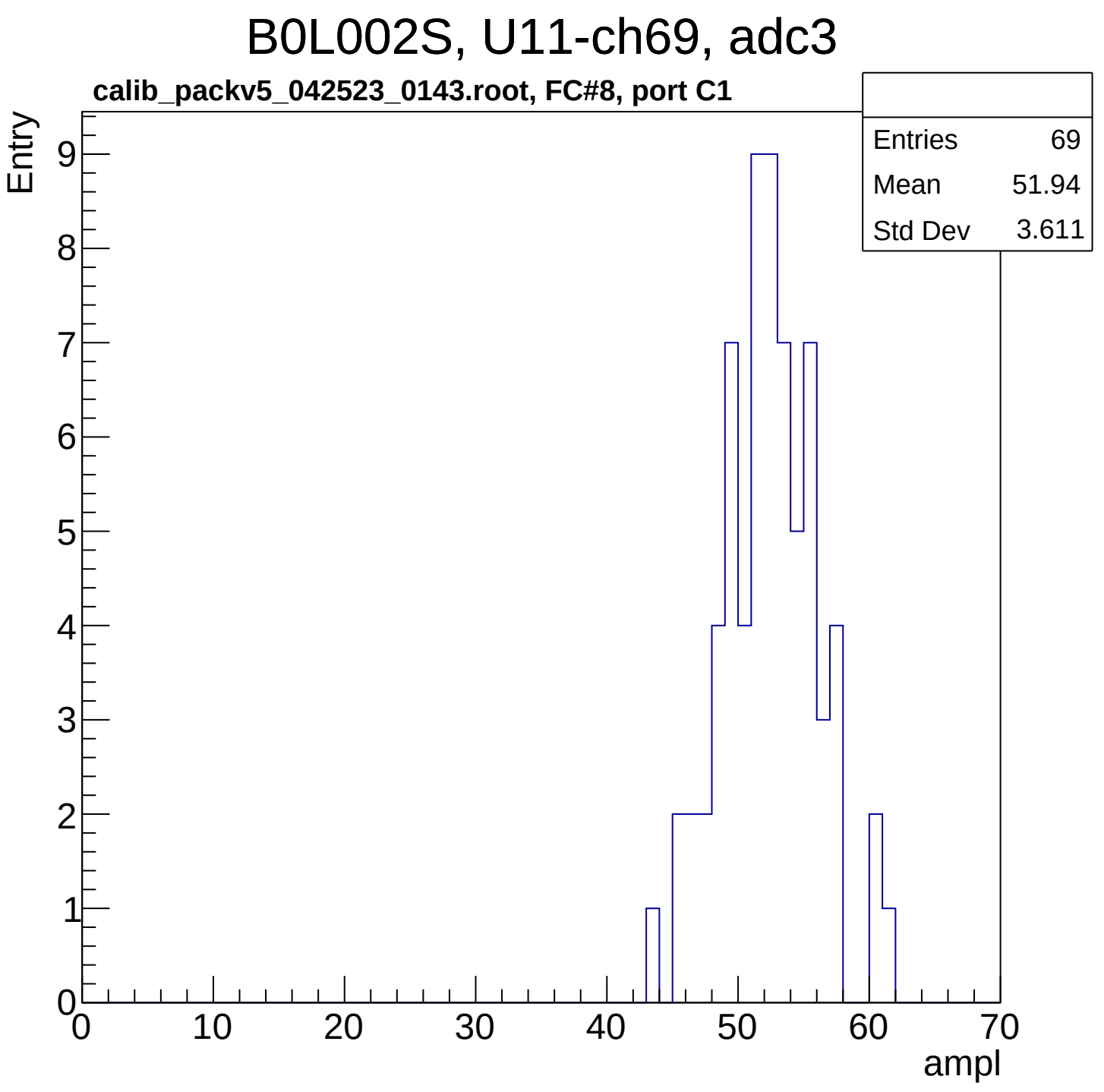
B0L002S, U11-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.94
Std Dev 3.611
Entry
ampl
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3
4
5
6
7
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0
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60
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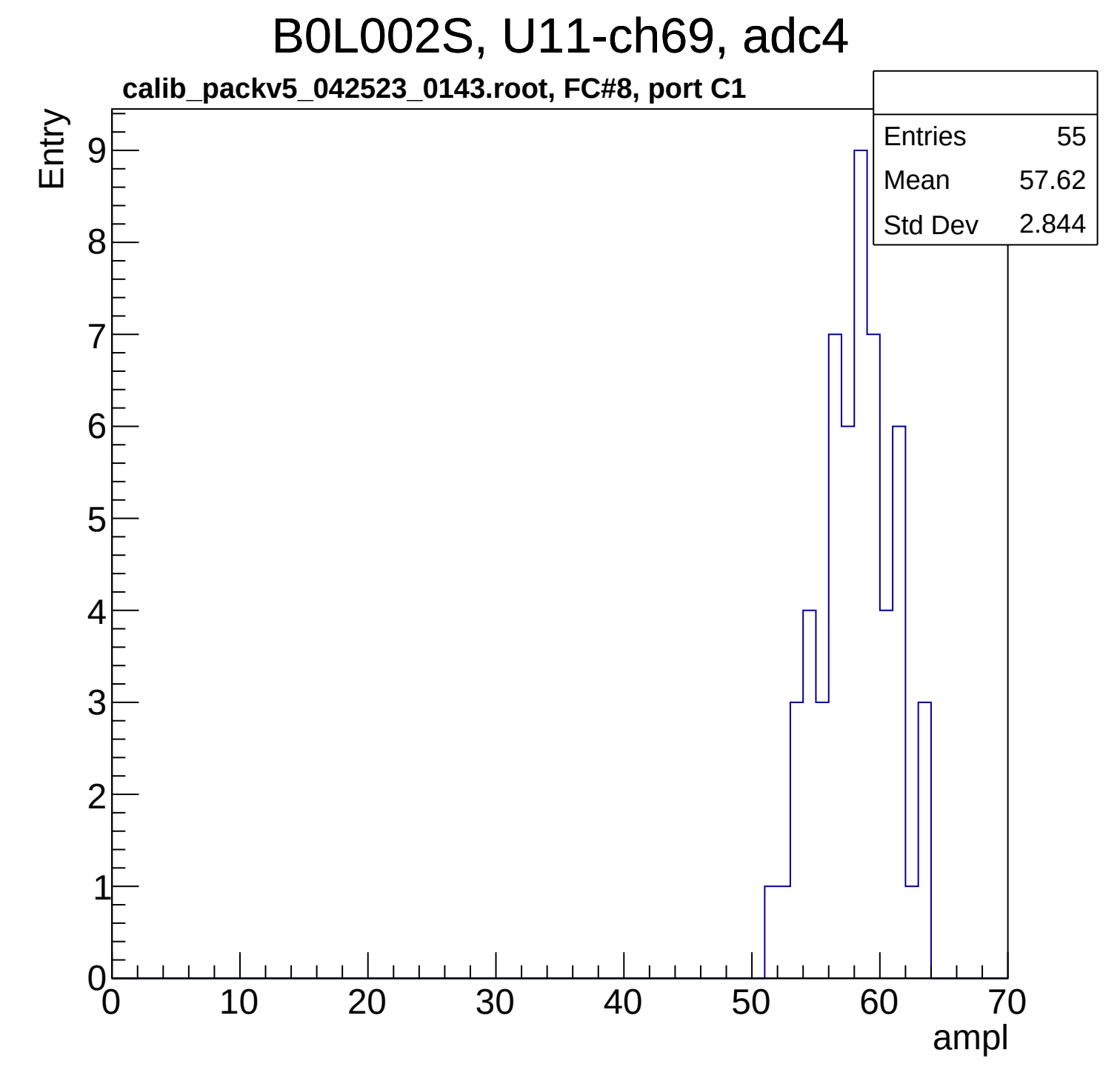
B0L002S, U11-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.62
Std Dev 2.844
Entry
ampl
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3
4
5
6
7
8
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0
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60
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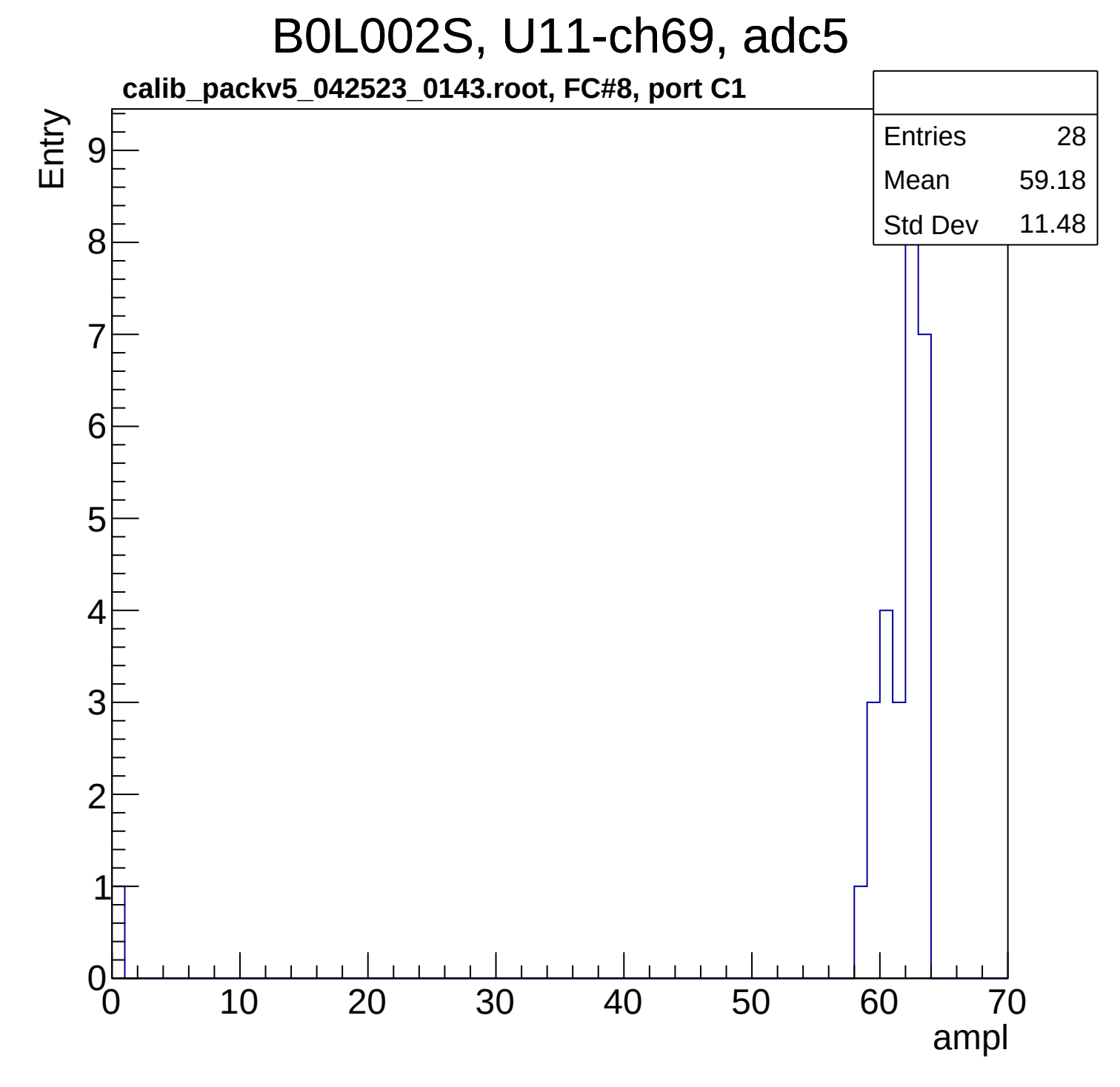
B0L002S, U11-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 59.18
Std Dev 11.48
Entry
ampl
0
1
2
3
4
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6
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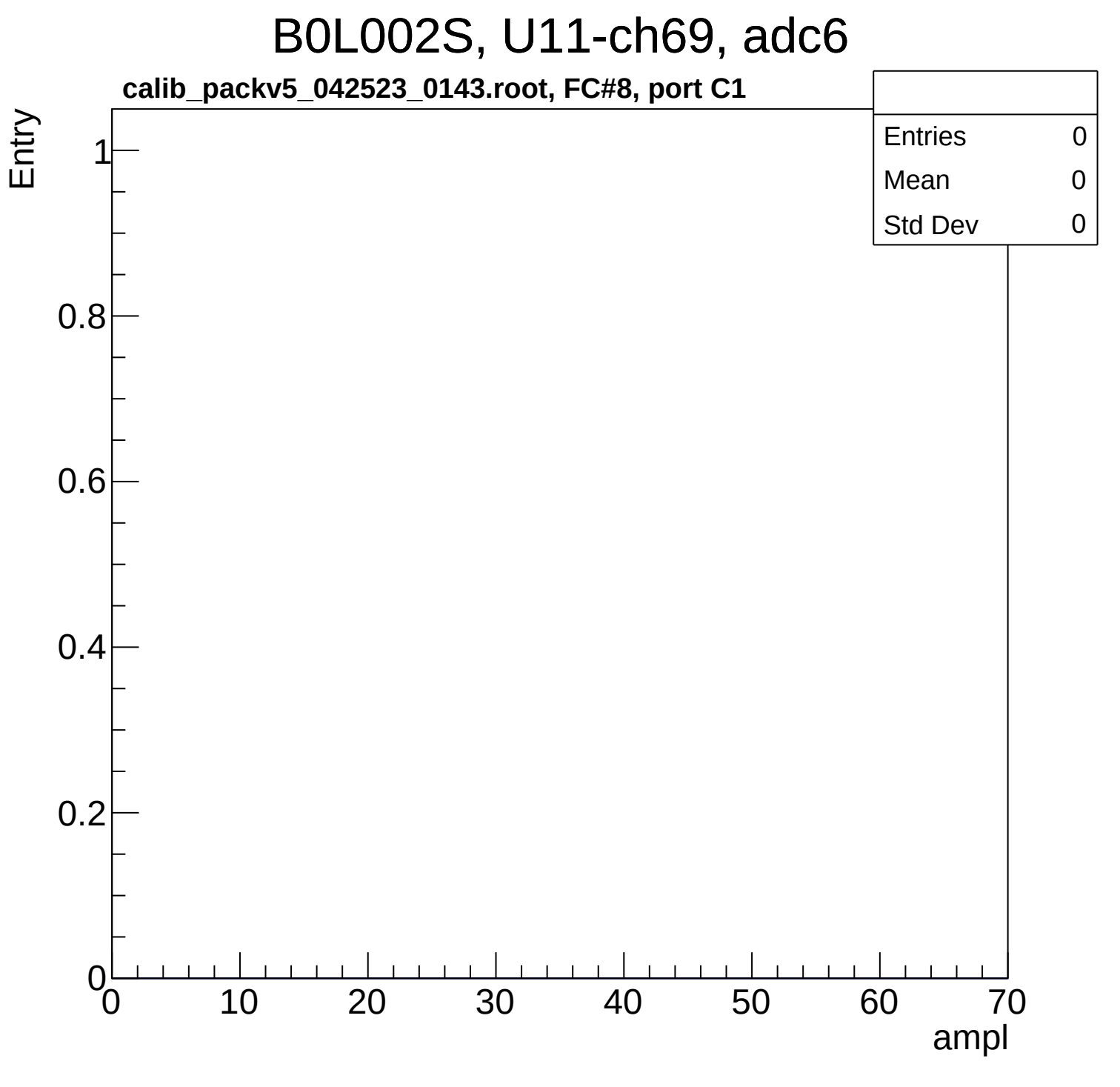
B0L002S, U11-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
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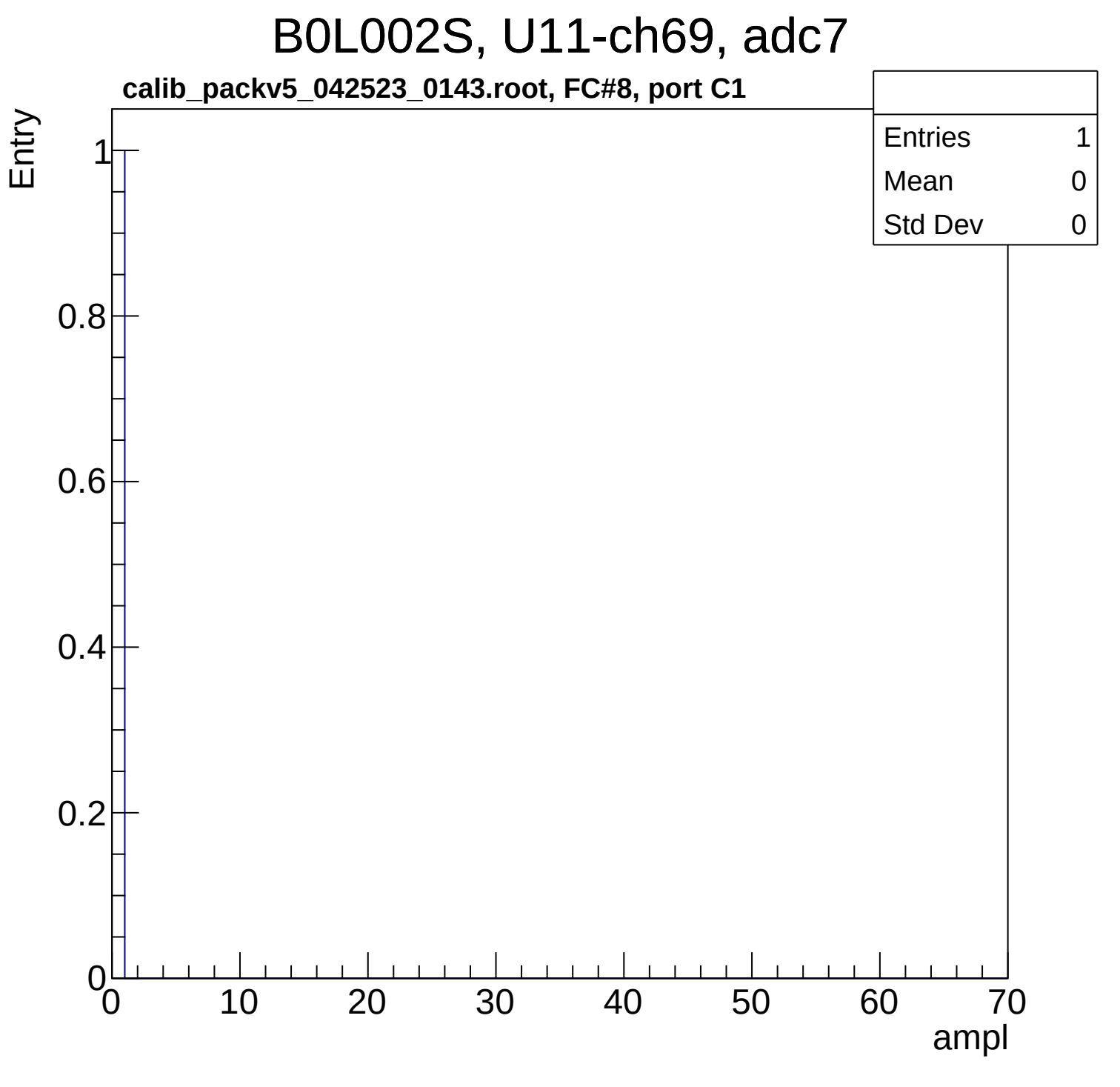
70
ampl


B0L002S, U11-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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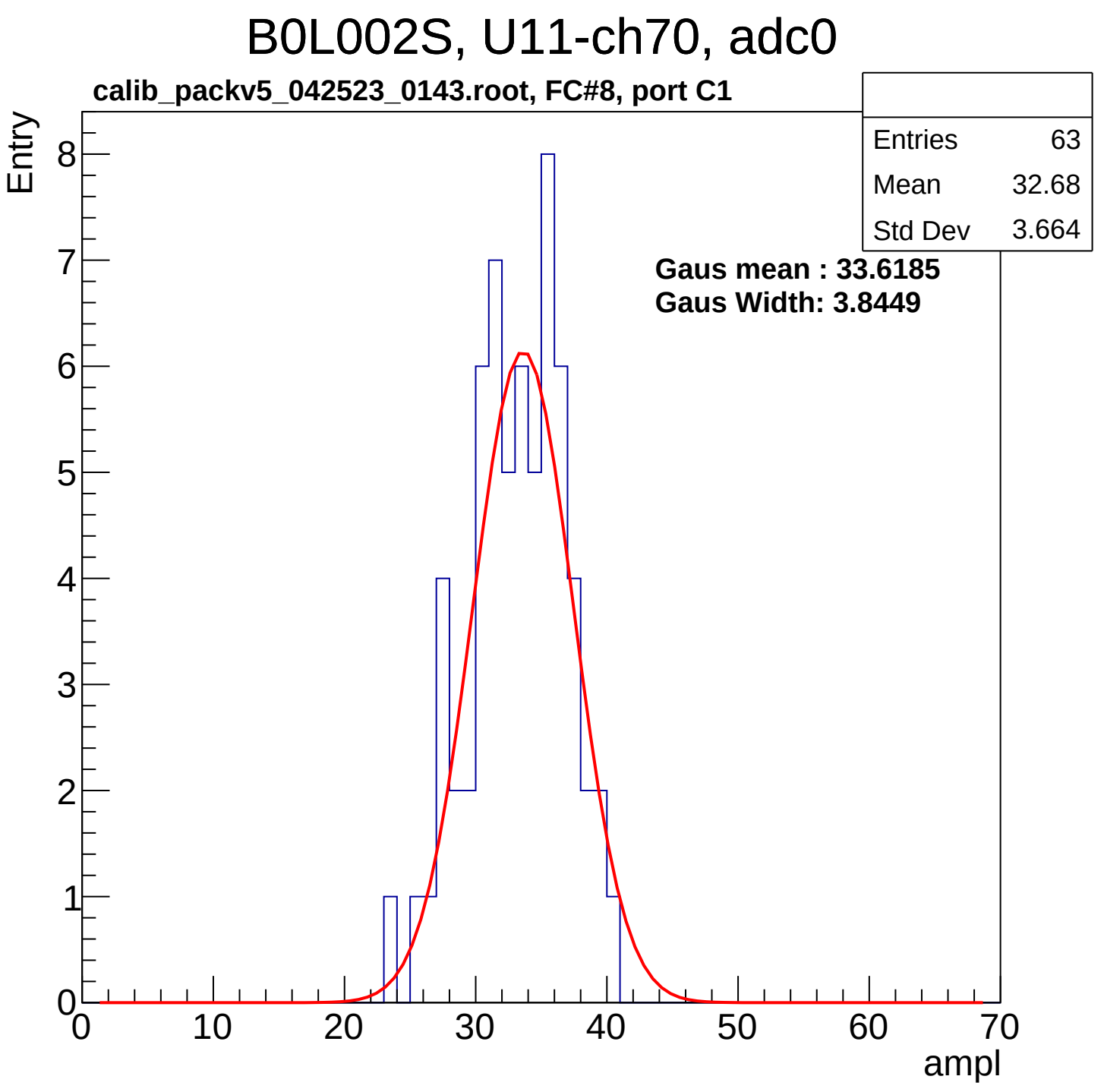
50
60
70
ampl


B0L002S, U11-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 32.68
Std Dev 3.664
Gaus mean : 33.6185
Gaus Width: 3.8449
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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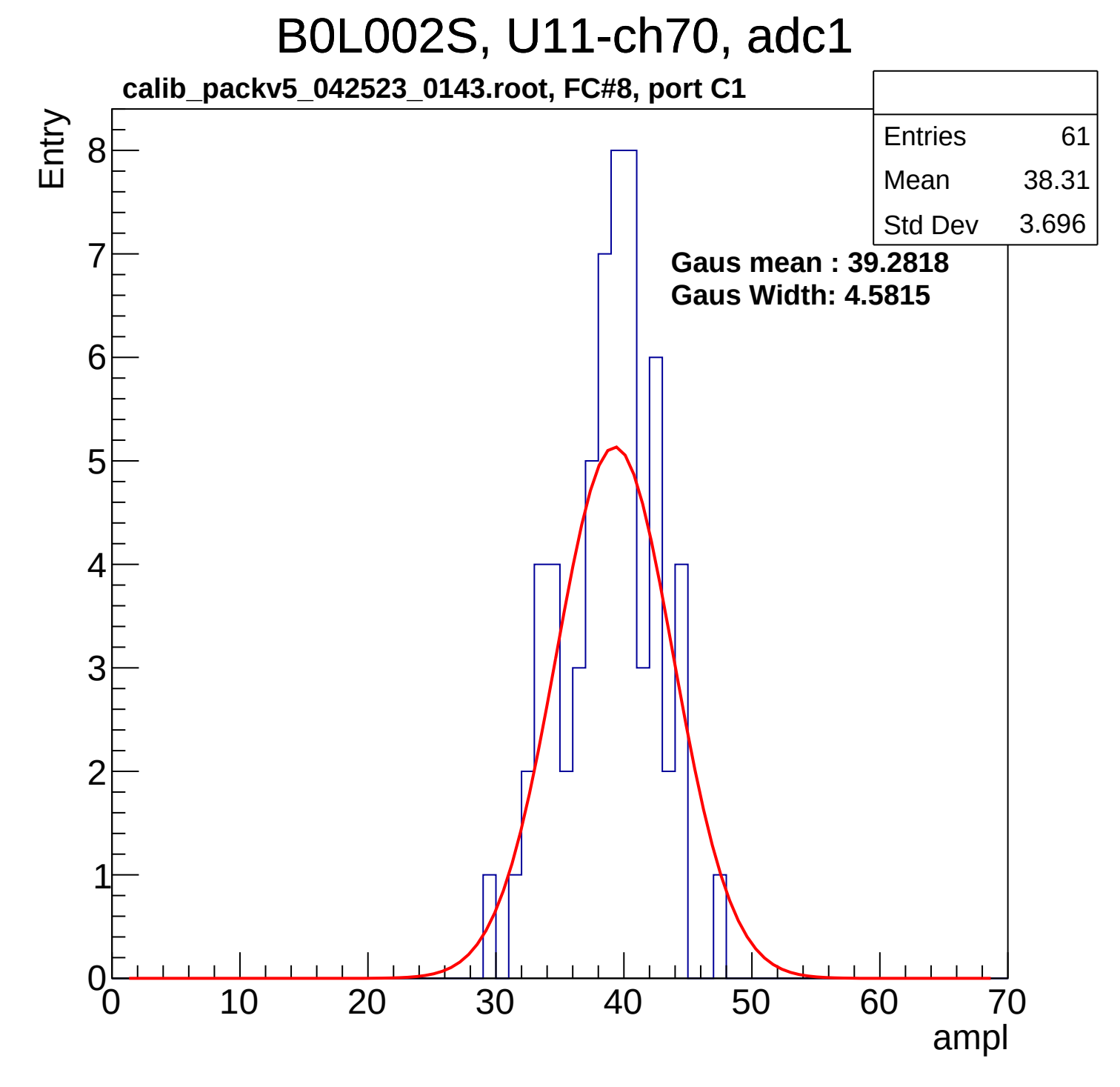
B0L002S, U11-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.31
Std Dev 3.696
Gaus mean : 39.2818
Gaus Width: 4.5815
Entry
ampl
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2
3
4
5
6
7
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0
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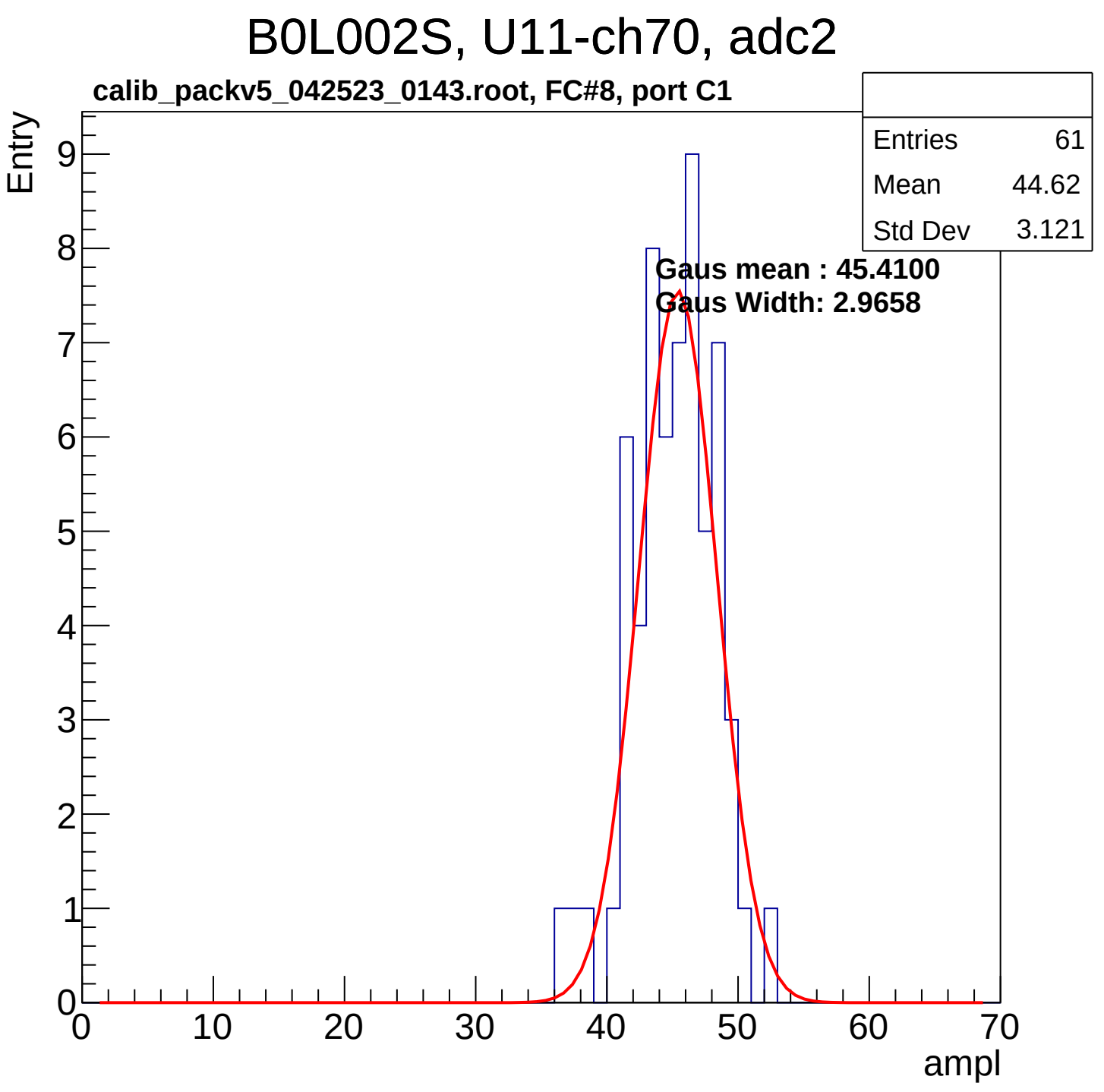
B0L002S, U11-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.62
Std Dev 3.121
Gaus mean : 45.4100
Gaus Width: 2.9658
Entry
ampl
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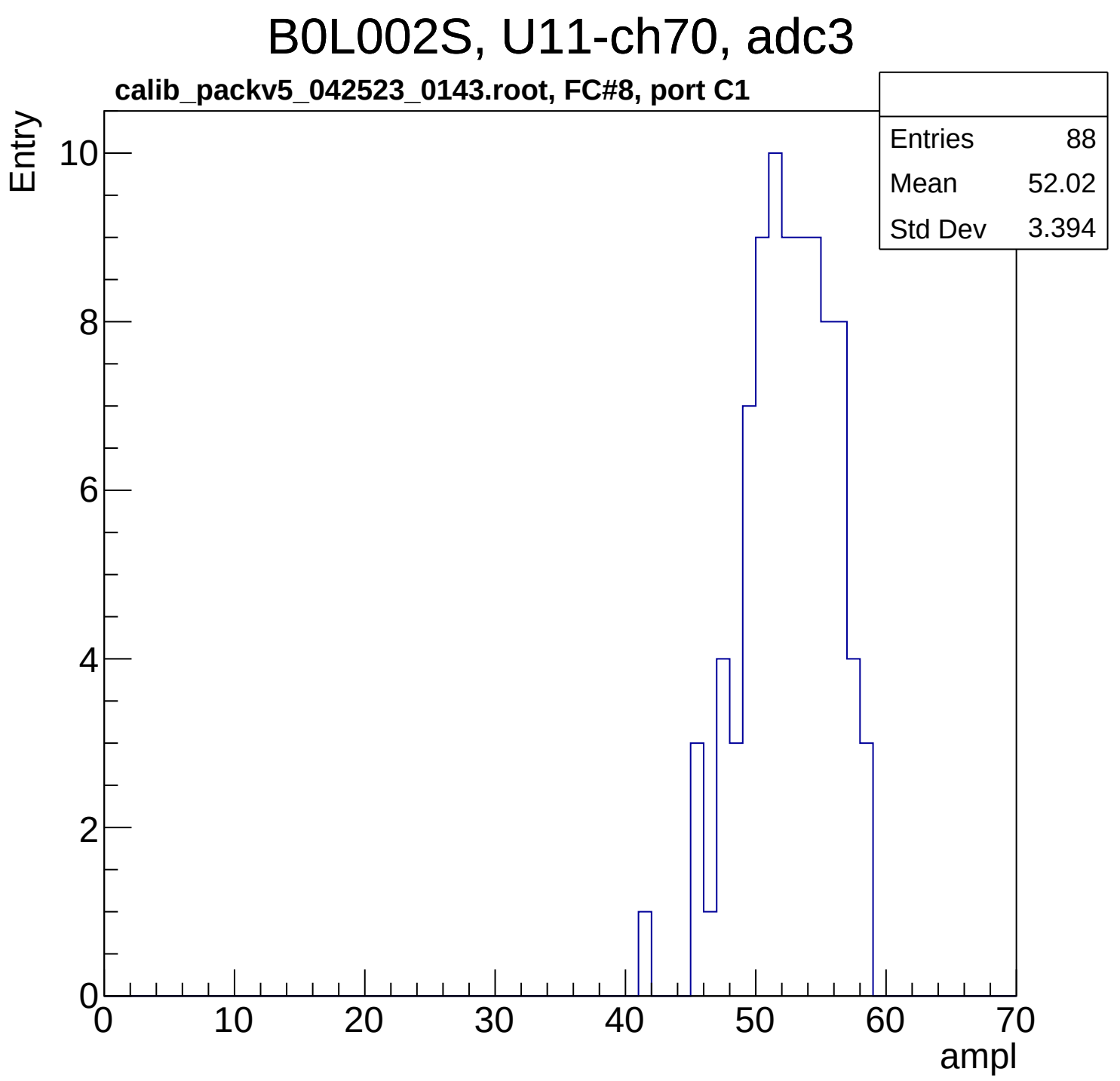
B0L002S, U11-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 52.02
Std Dev 3.394
Entry
ampl
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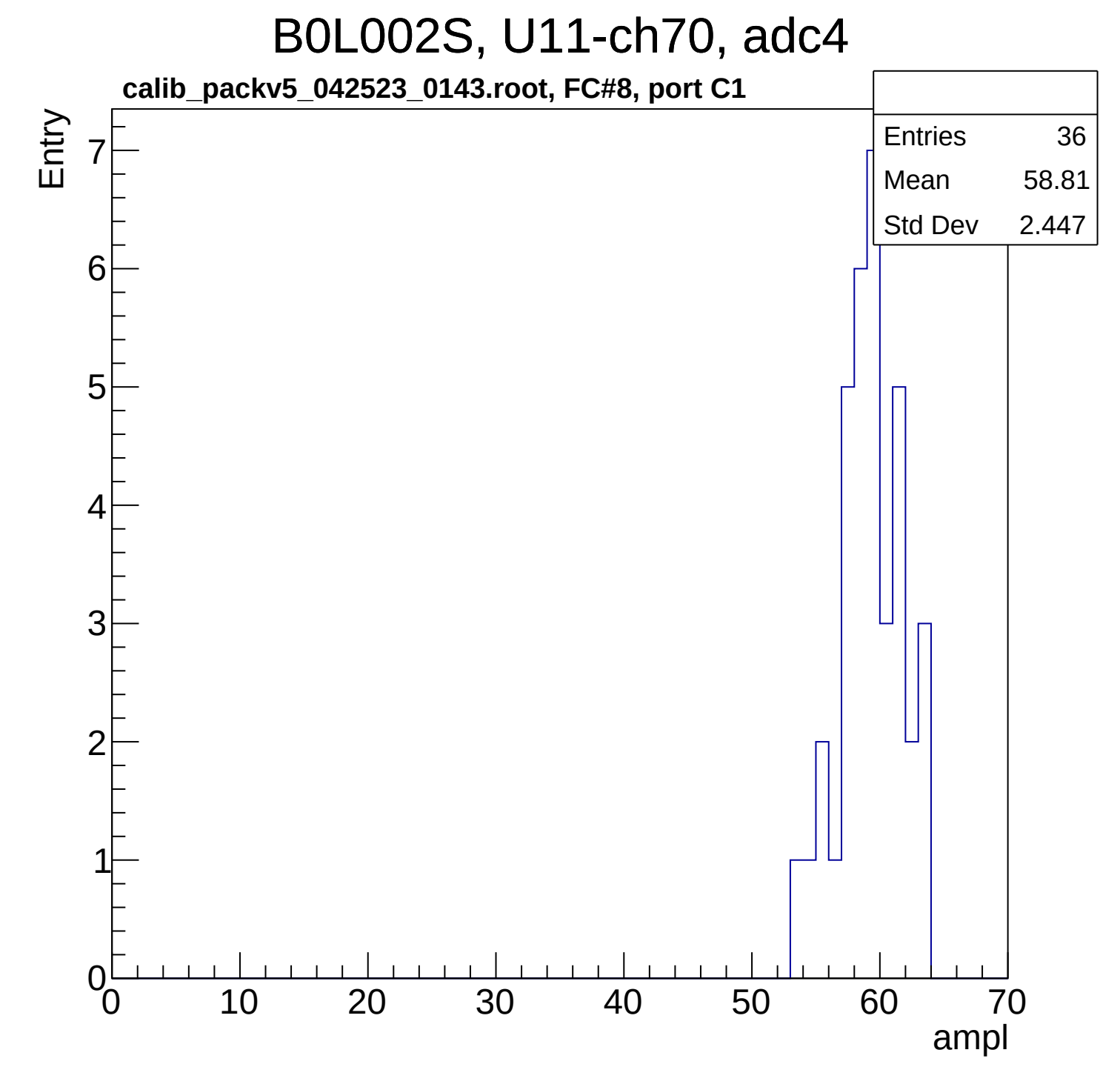
B0L002S, U11-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.81
Std Dev 2.447
Entry
ampl
0
1
2
3
4
5
6
7
0
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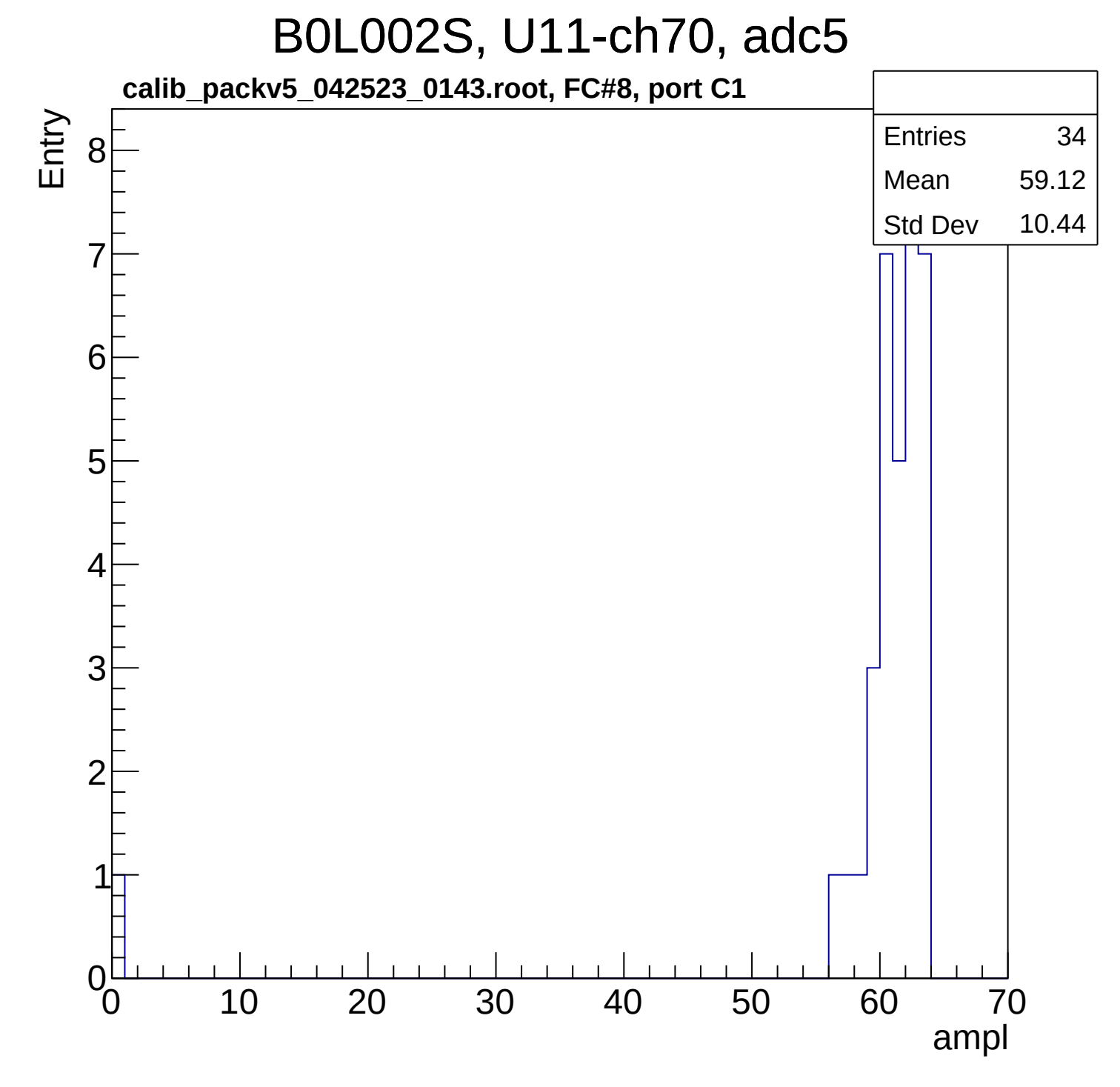
B0L002S, U11-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.12
Std Dev 10.44
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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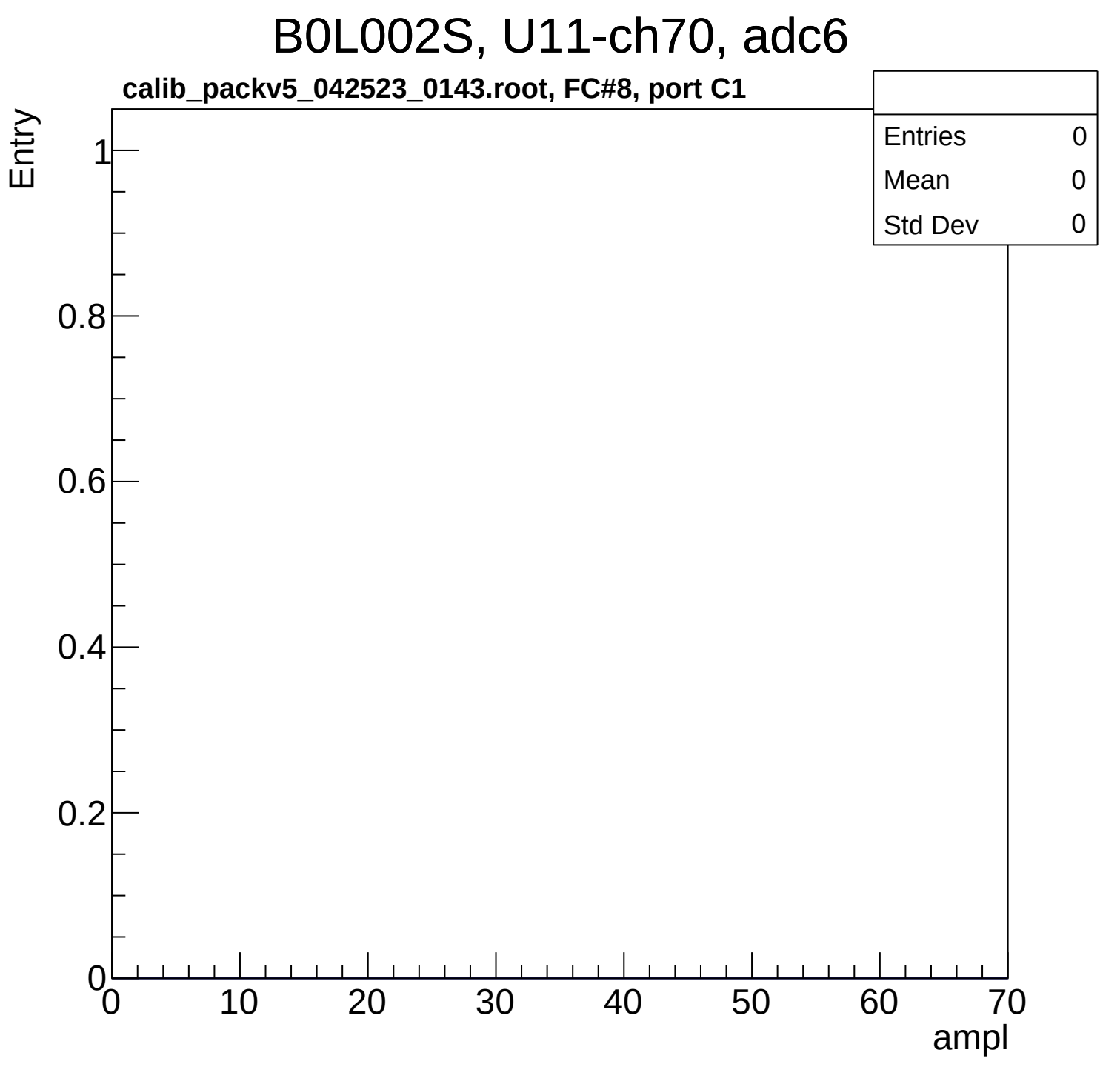
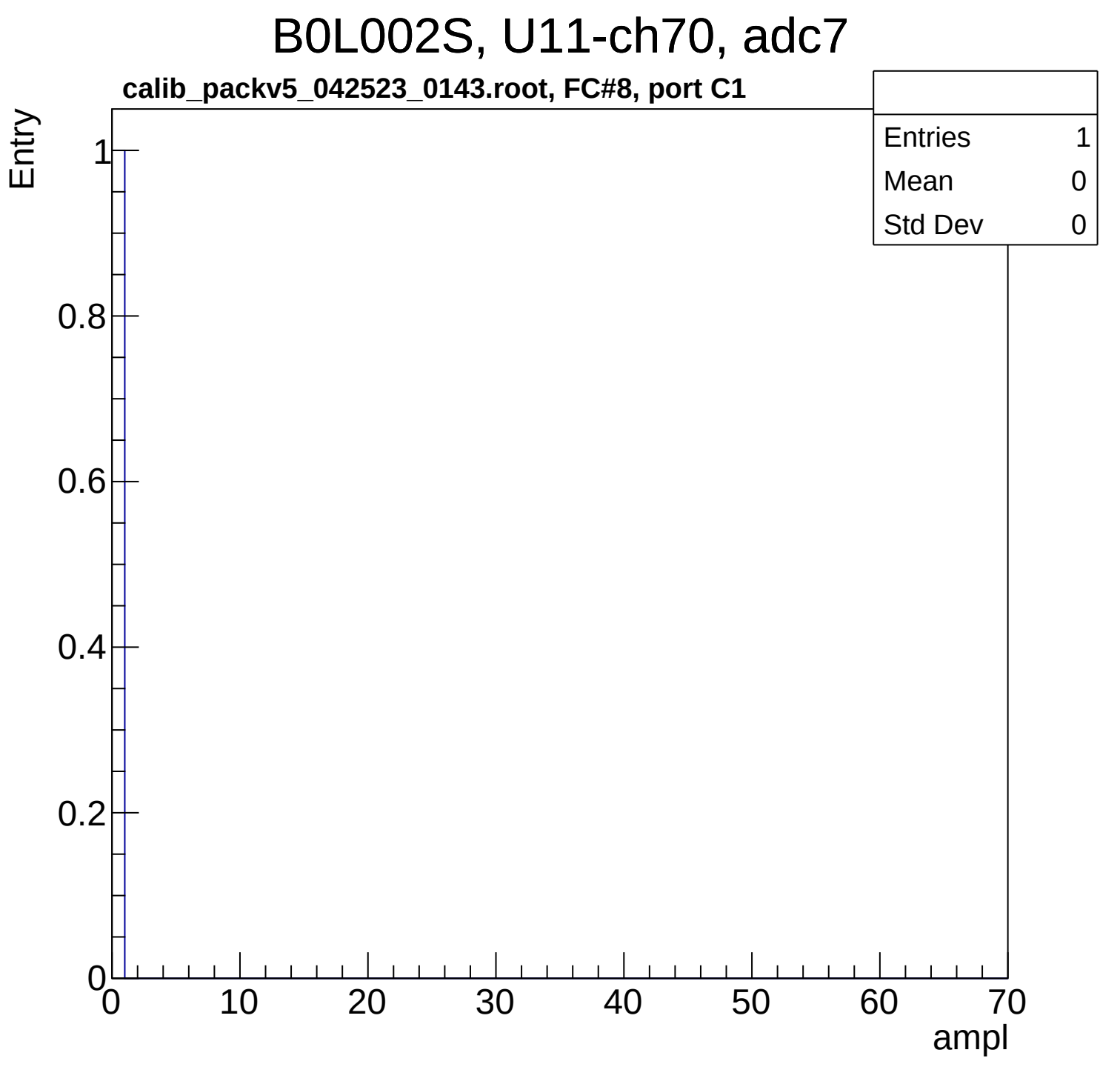
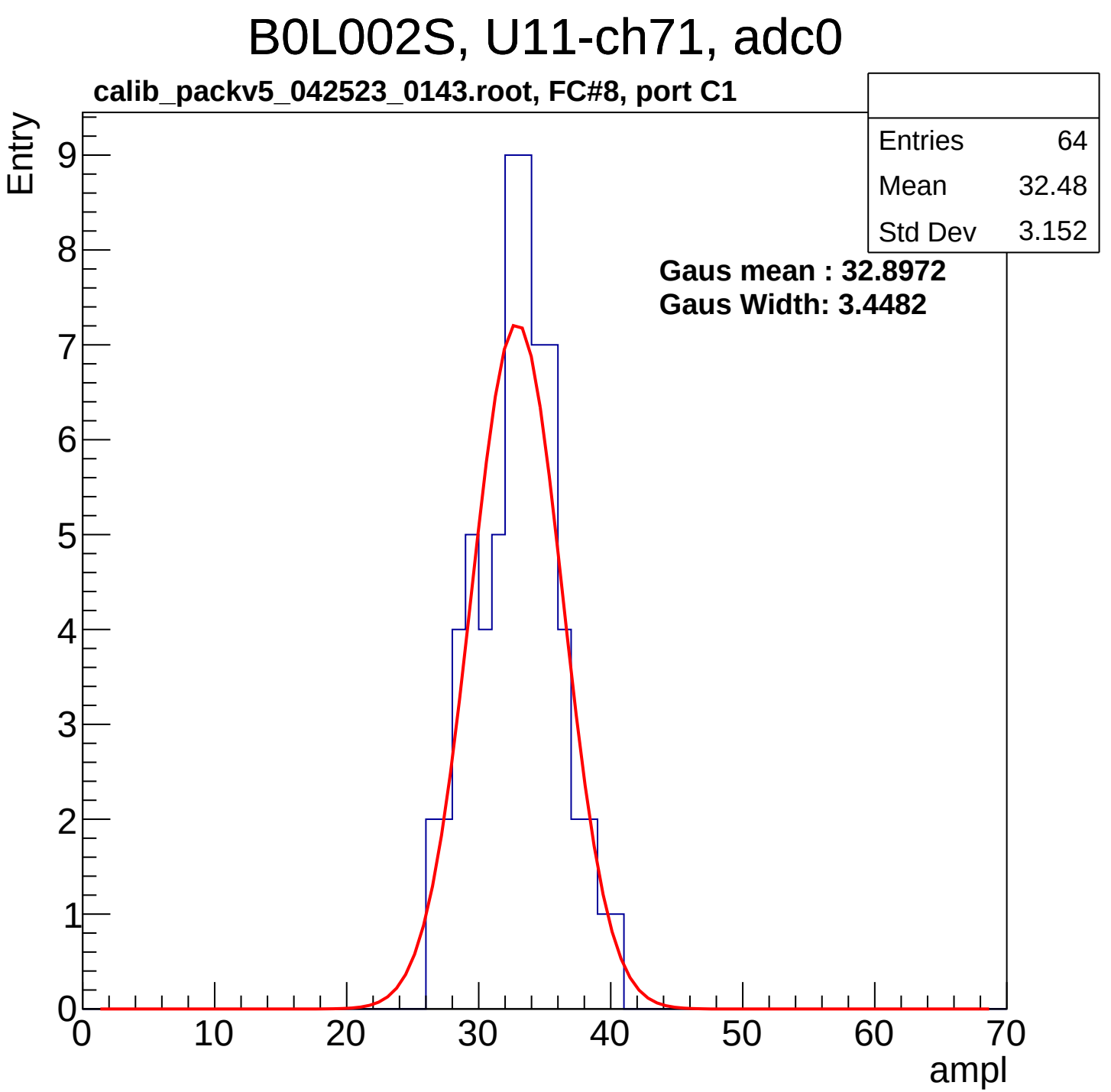

B0L002S, U11-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 32.48
Std Dev 3.152
Gaus mean : 32.8972
Gaus Width: 3.4482
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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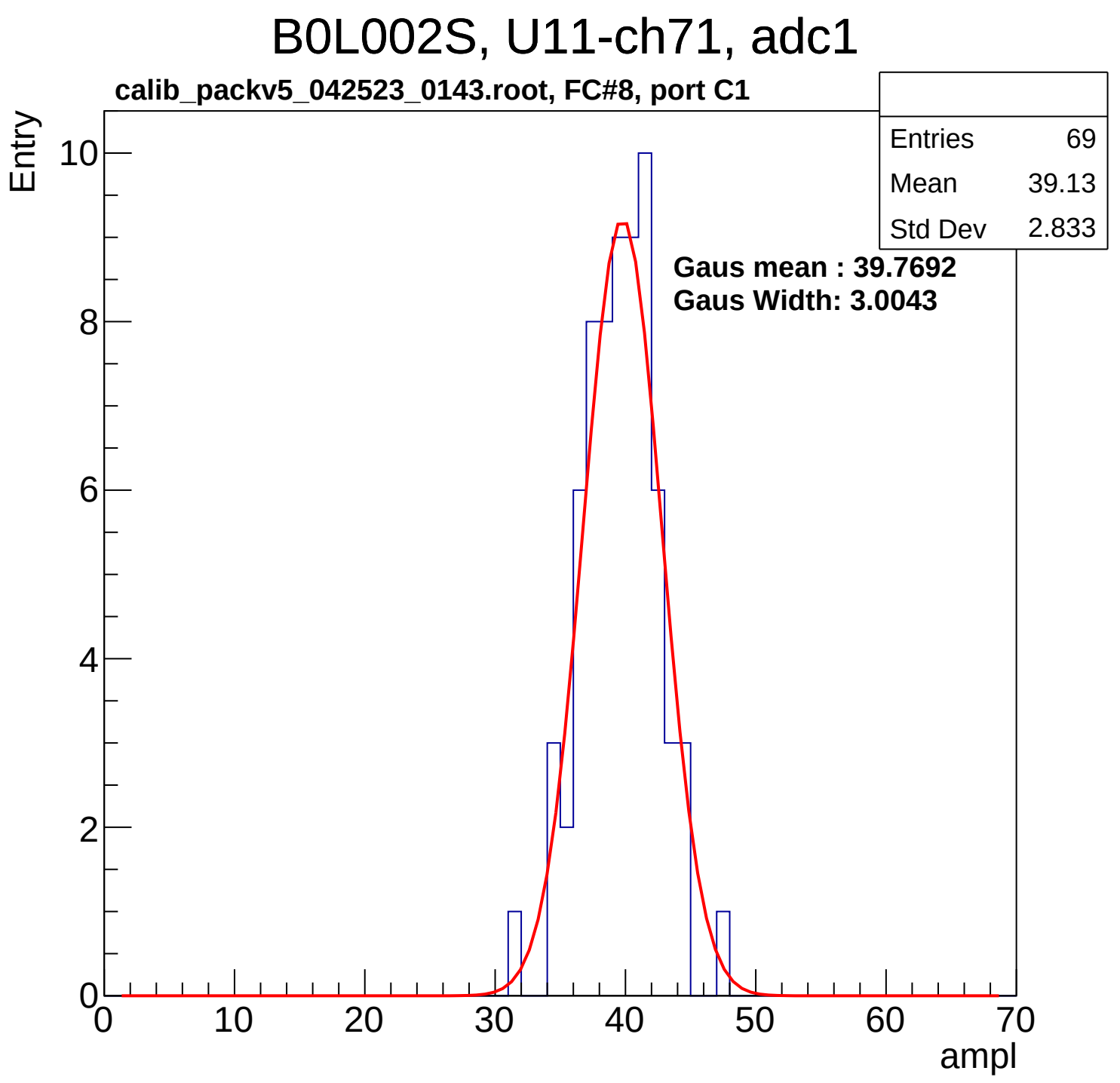
40
50
60
70


B0L002S, U11-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 39.13
Std Dev 2.833
Gaus mean : 39.7692
Gaus Width: 3.0043
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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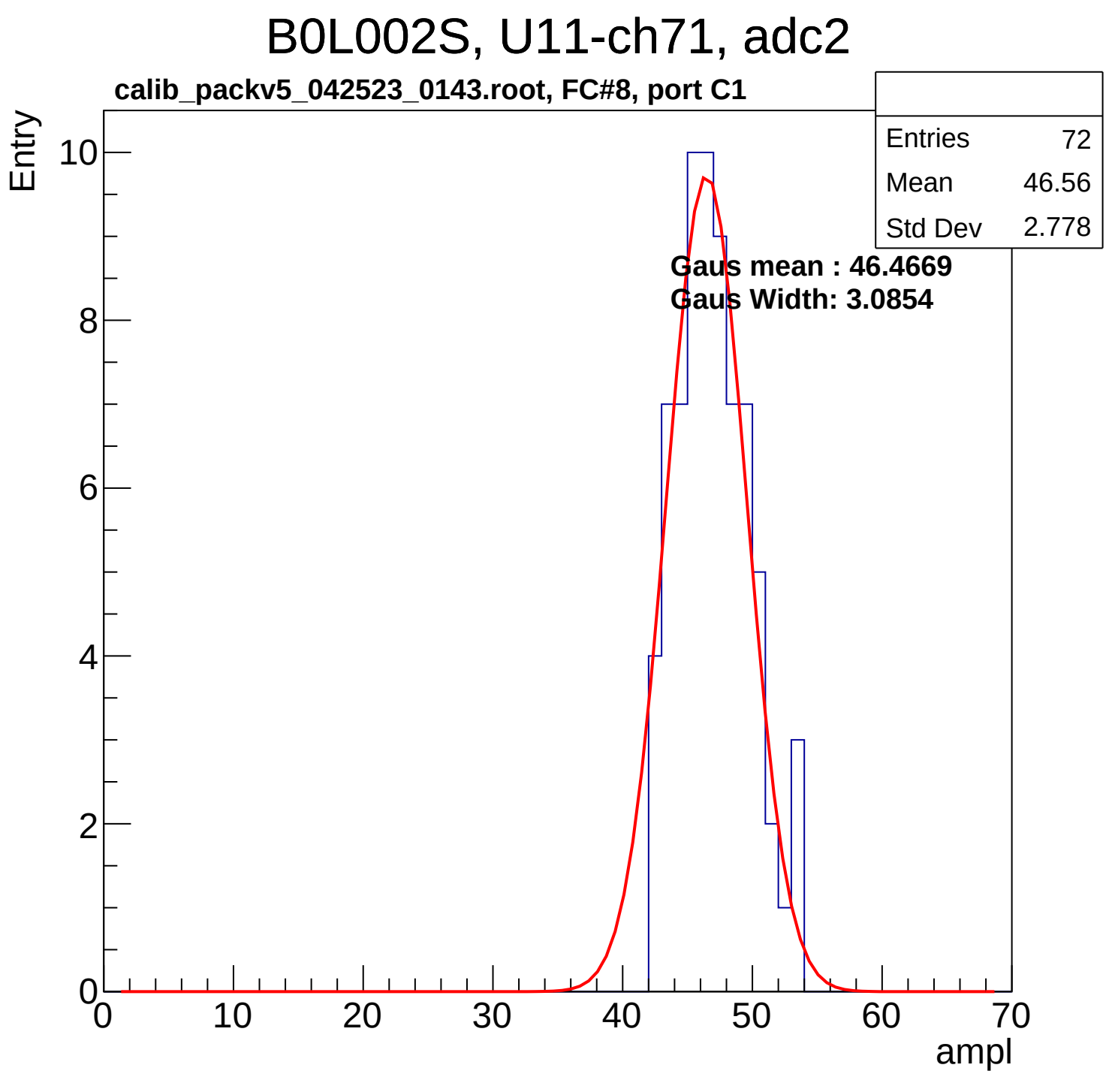
B0L002S, U11-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 46.56
Std Dev 2.778
Gaus mean : 46.4669
Gaus Width: 3.0854
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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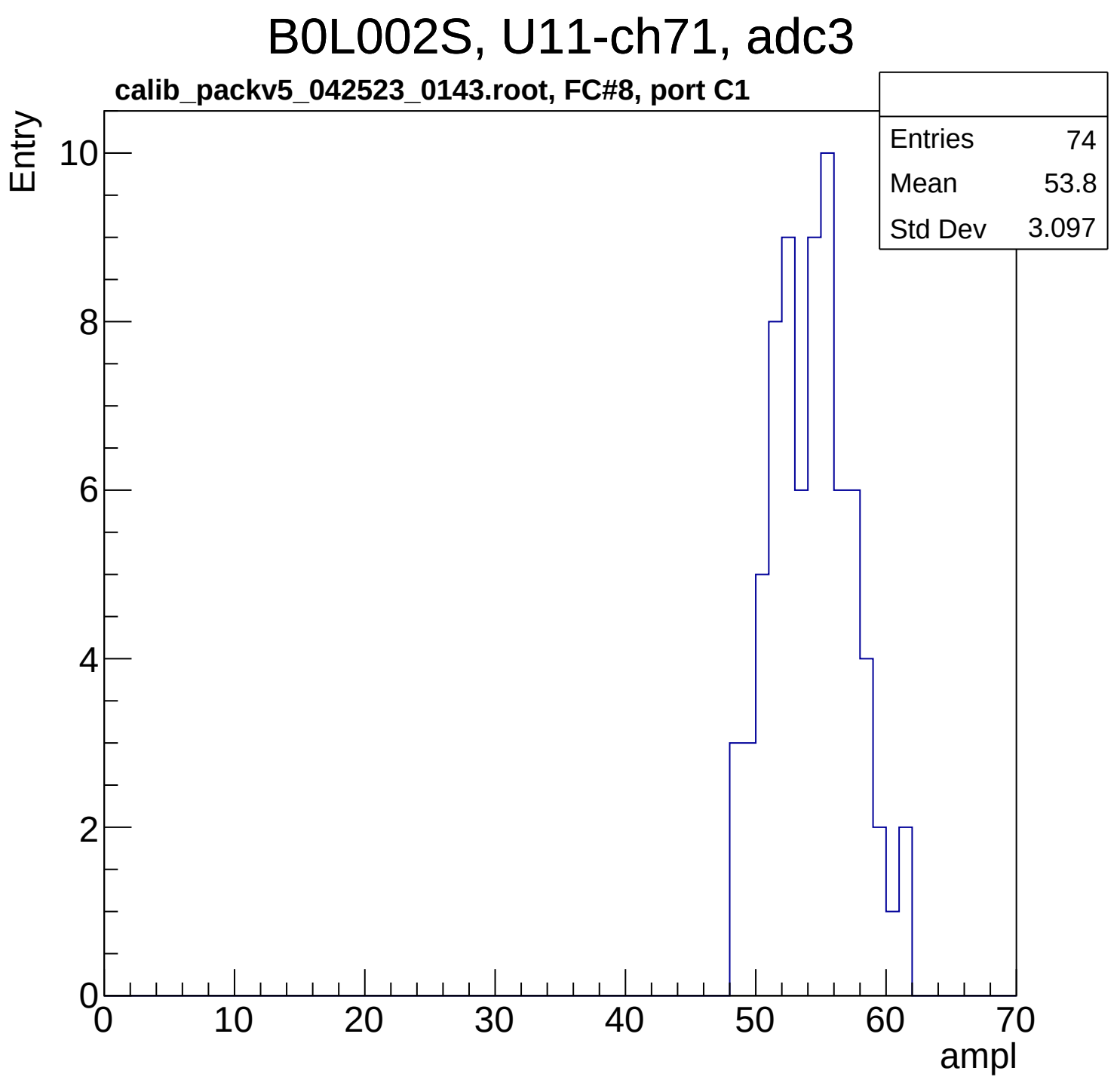
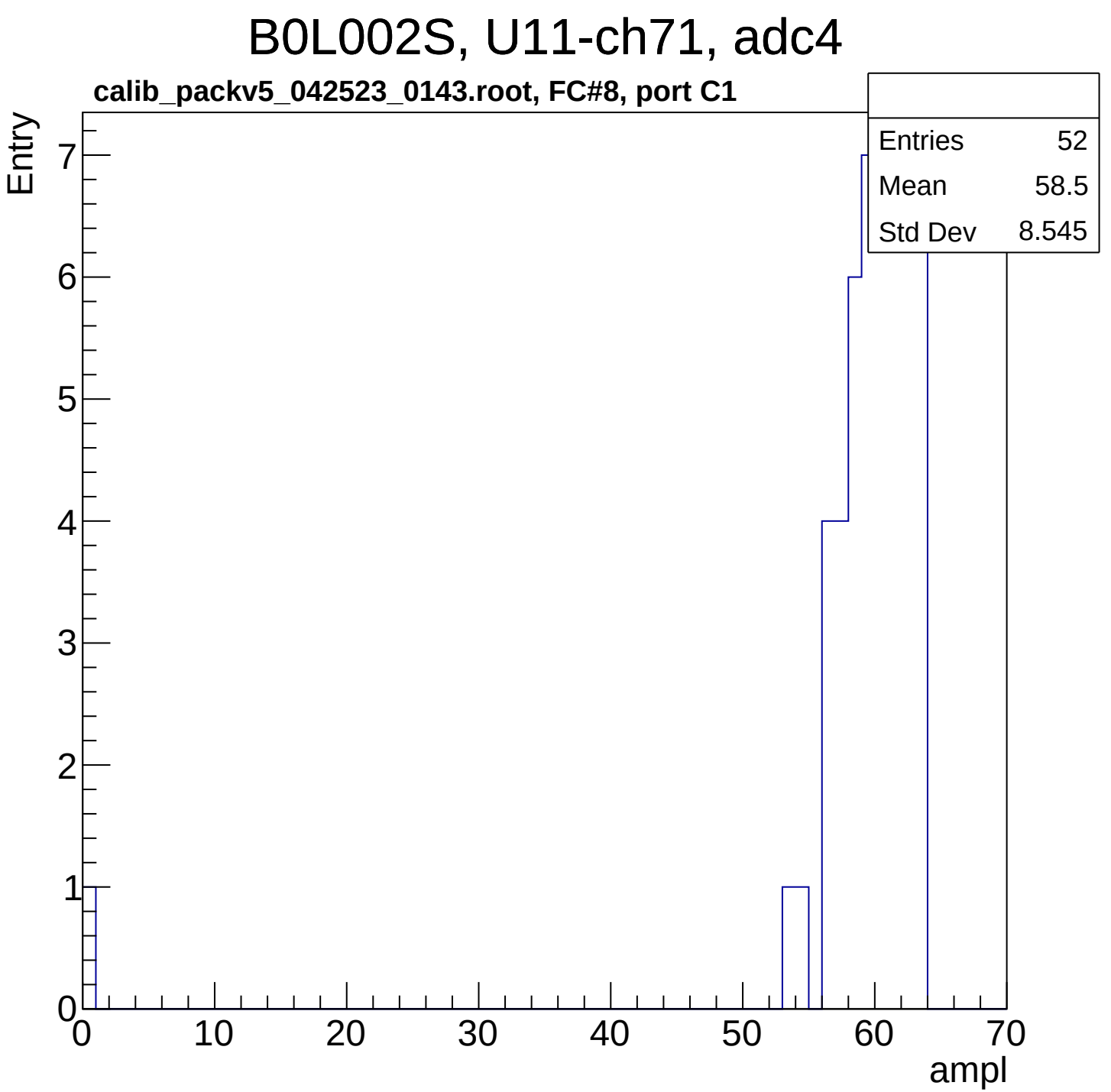

B0L002S, U11-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 53.8
Std Dev 3.097
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.5
Std Dev 8.545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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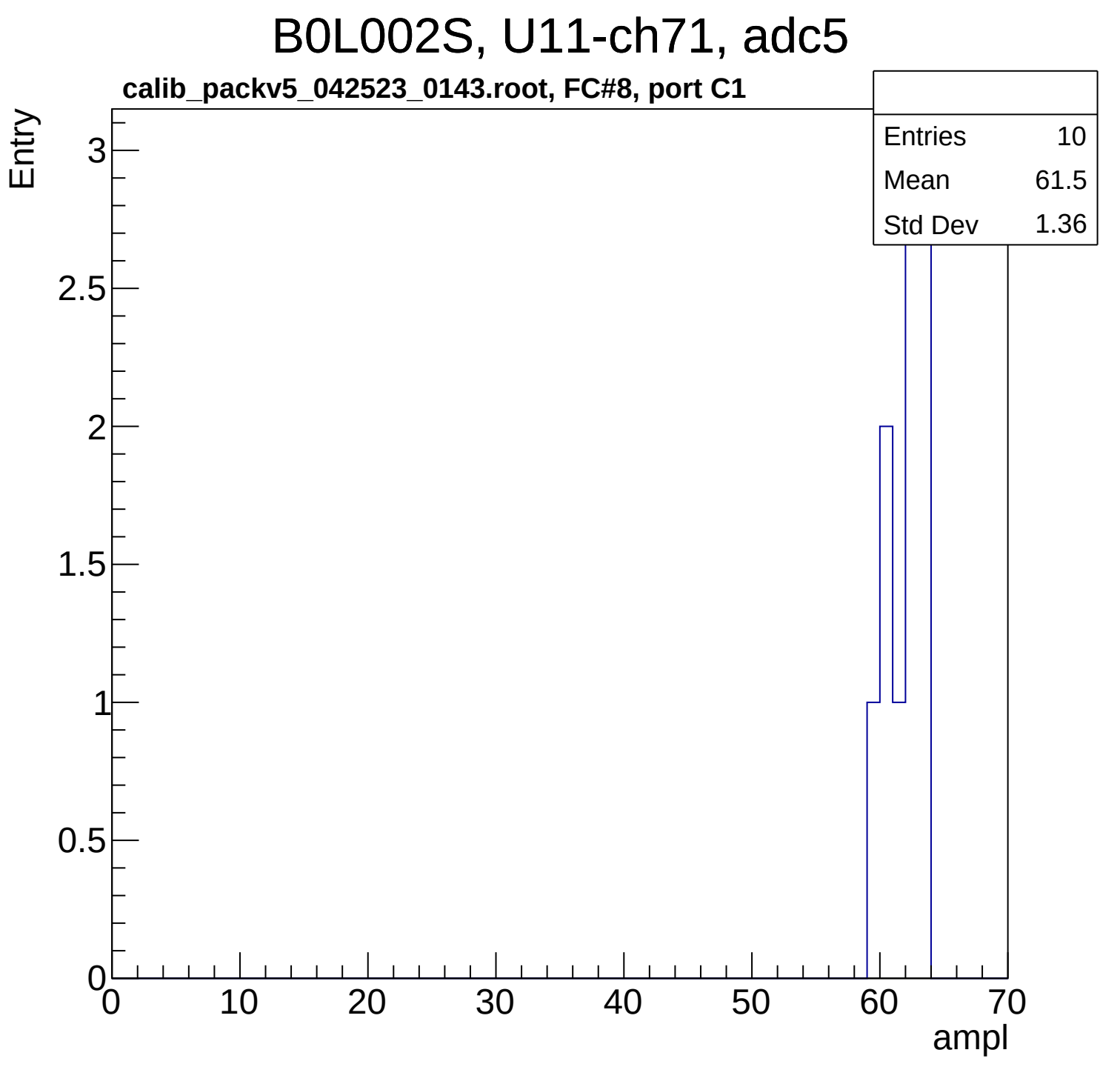
60
70


B0L002S, U11-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.5
Std Dev 1.36
Entry
ampl
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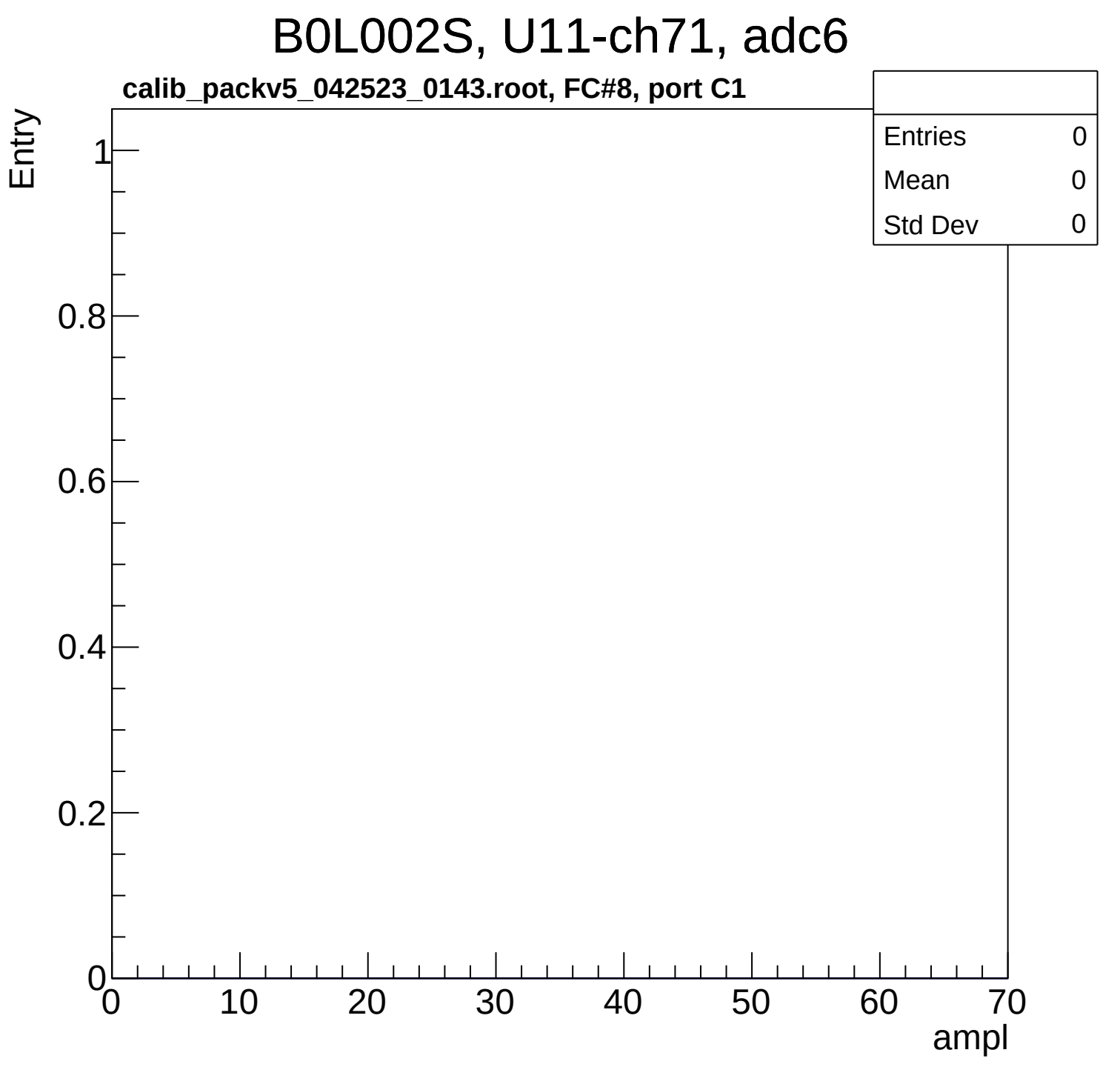
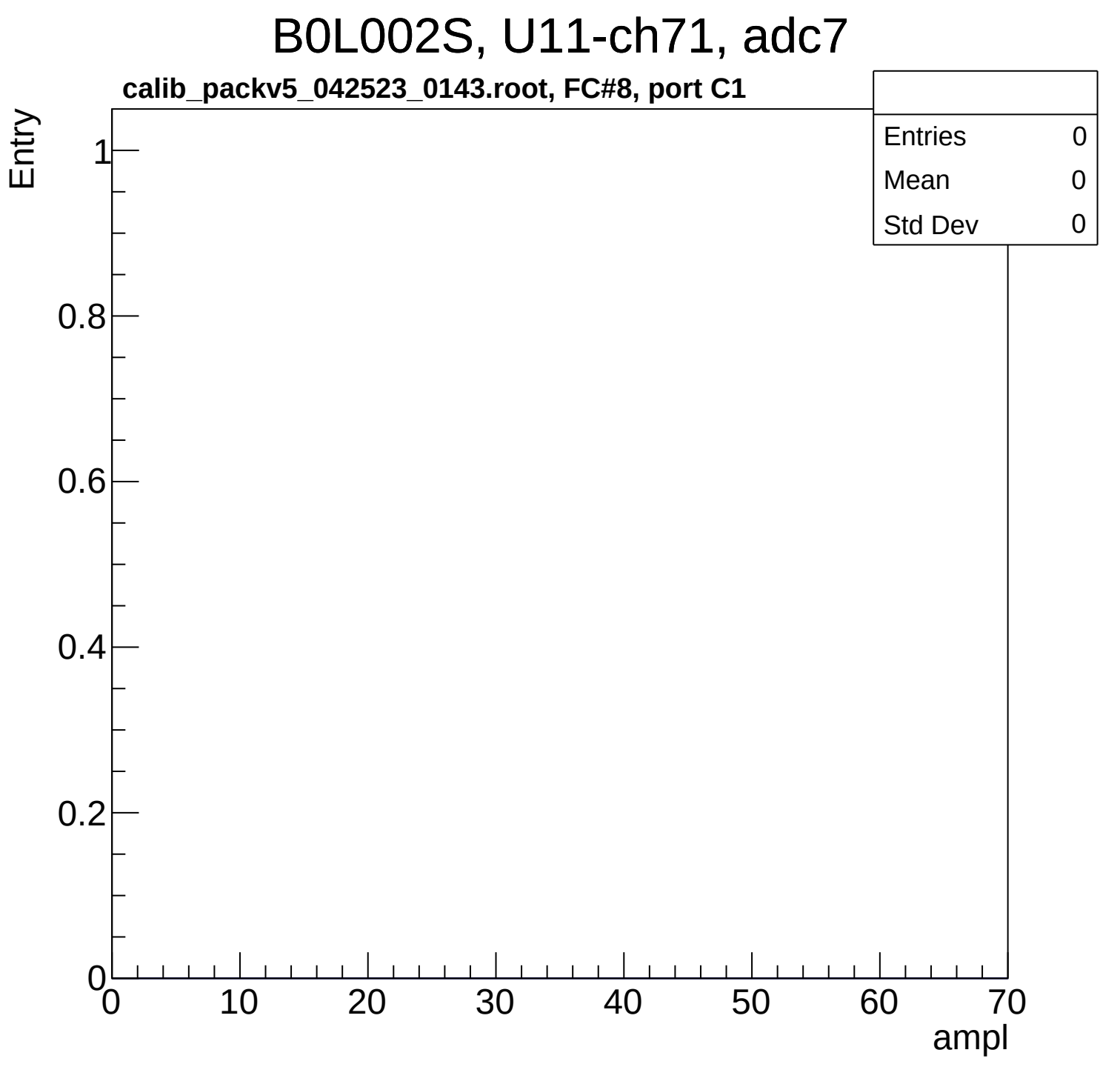
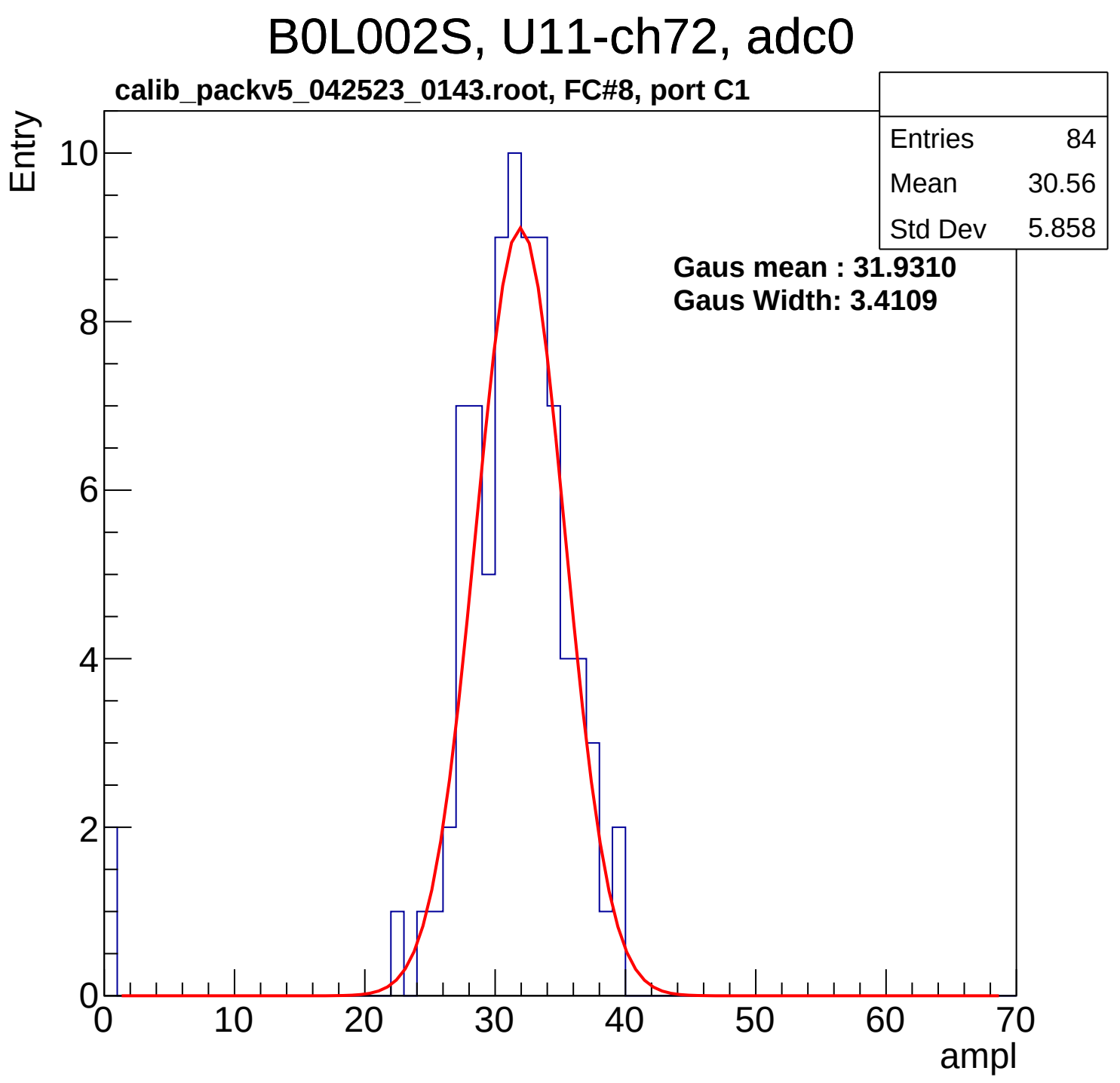
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U11-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.56
Std Dev 5.858
Gaus mean : 31.9310
Gaus Width: 3.4109
Entry
ampl
0
2
4
6
8
10
0
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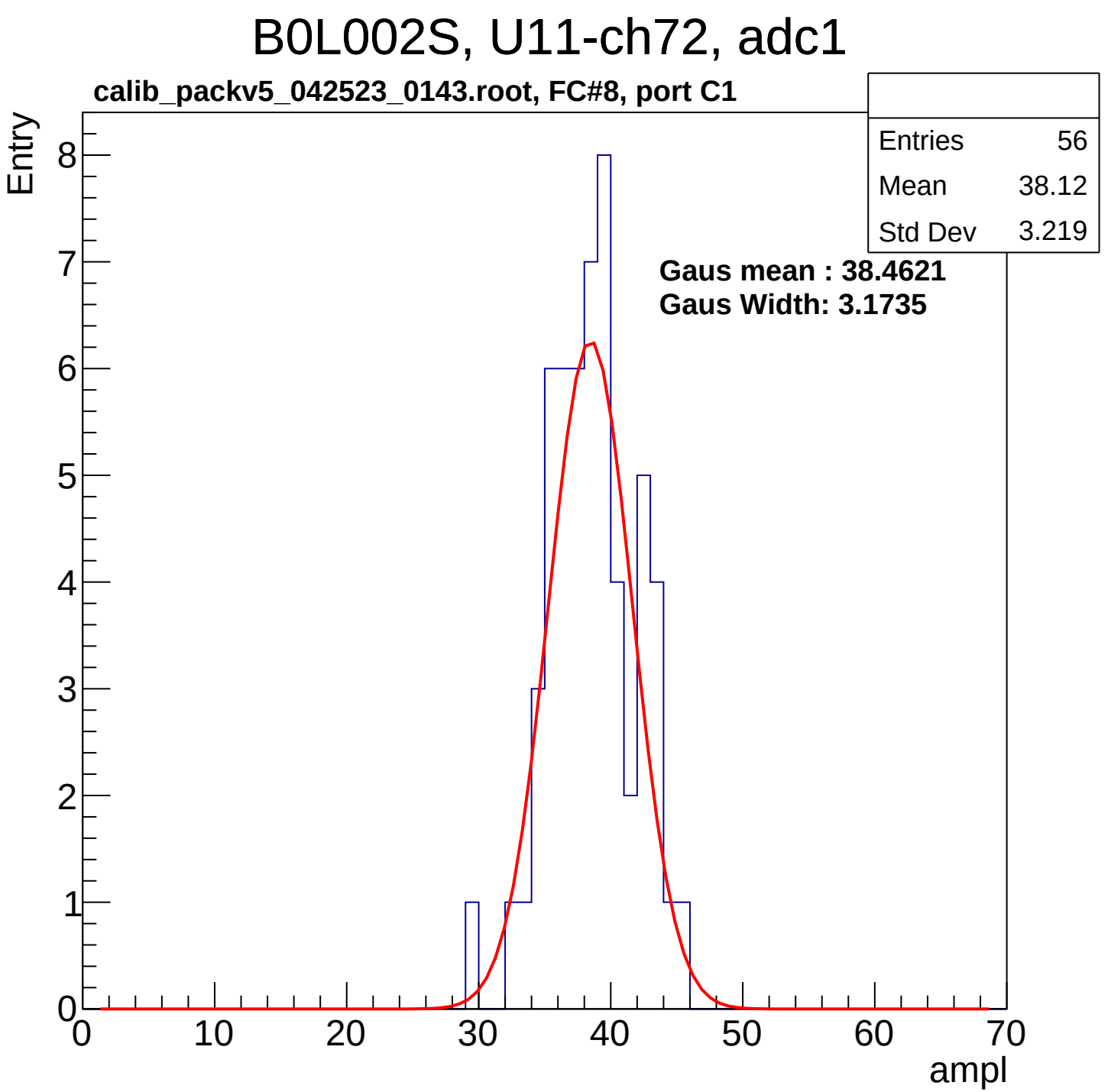
20
30
40
50
60
70


B0L002S, U11-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 38.12
Std Dev 3.219
Gaus mean : 38.4621
Gaus Width: 3.1735
Entry
ampl
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2
3
4
5
6
7
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0
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20
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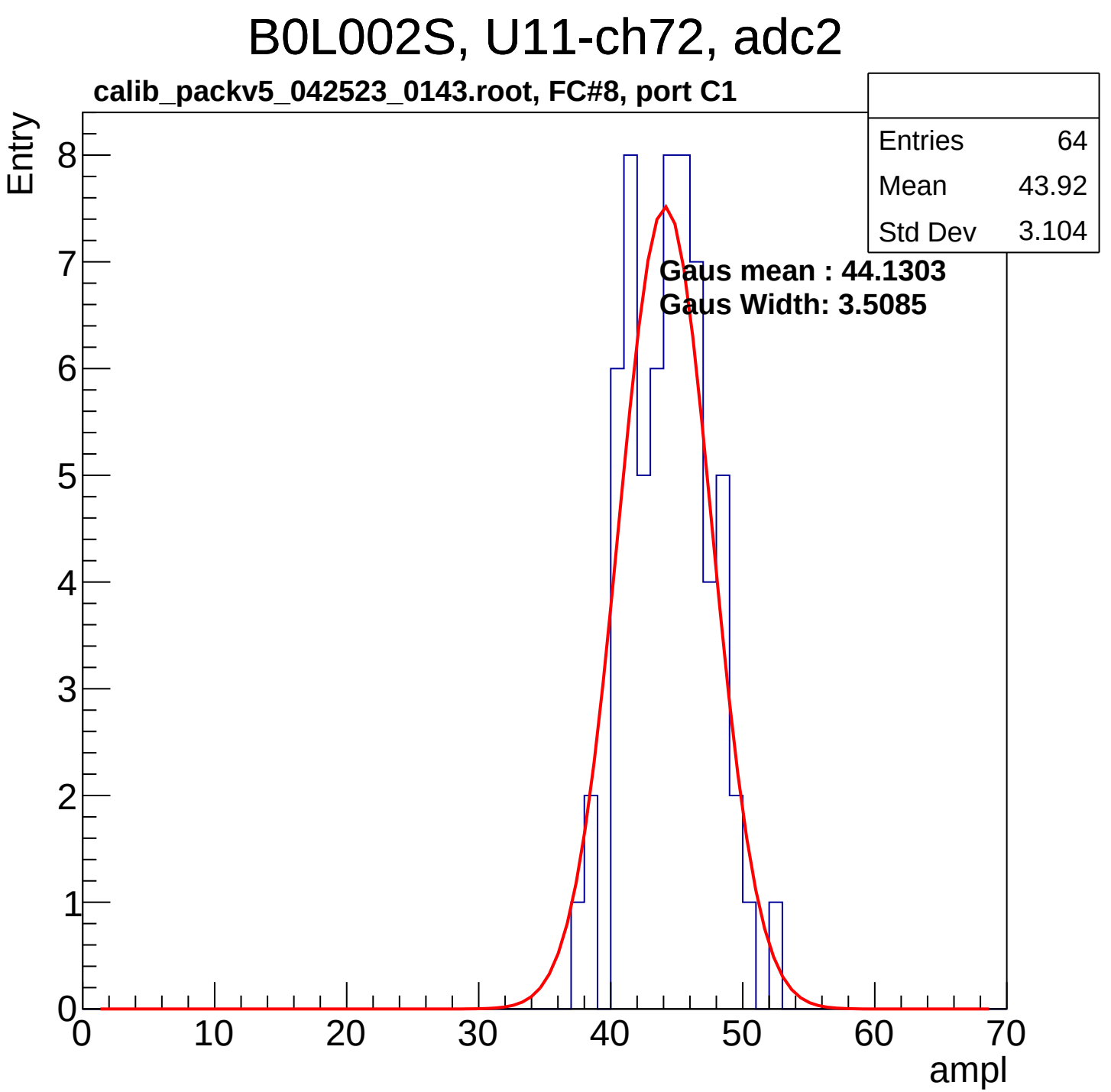
B0L002S, U11-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.92
Std Dev 3.104
Gaus mean : 44.1303
Gaus Width: 3.5085
Entry
ampl
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5
6
7
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0
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20
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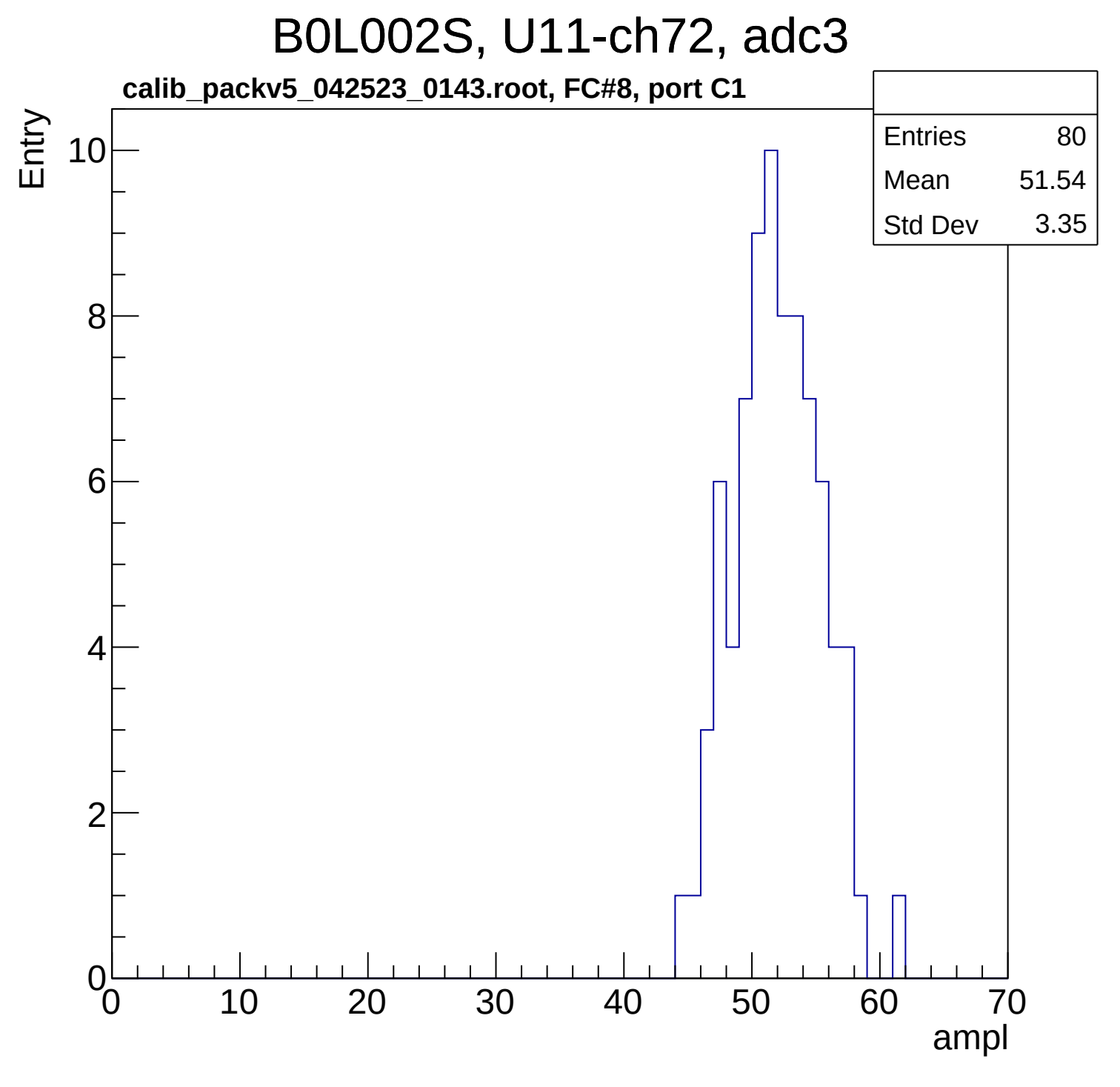
40
50
60
70


B0L002S, U11-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 51.54
Std Dev 3.35
Entry
0
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4
6
8
10
0
10
20
30
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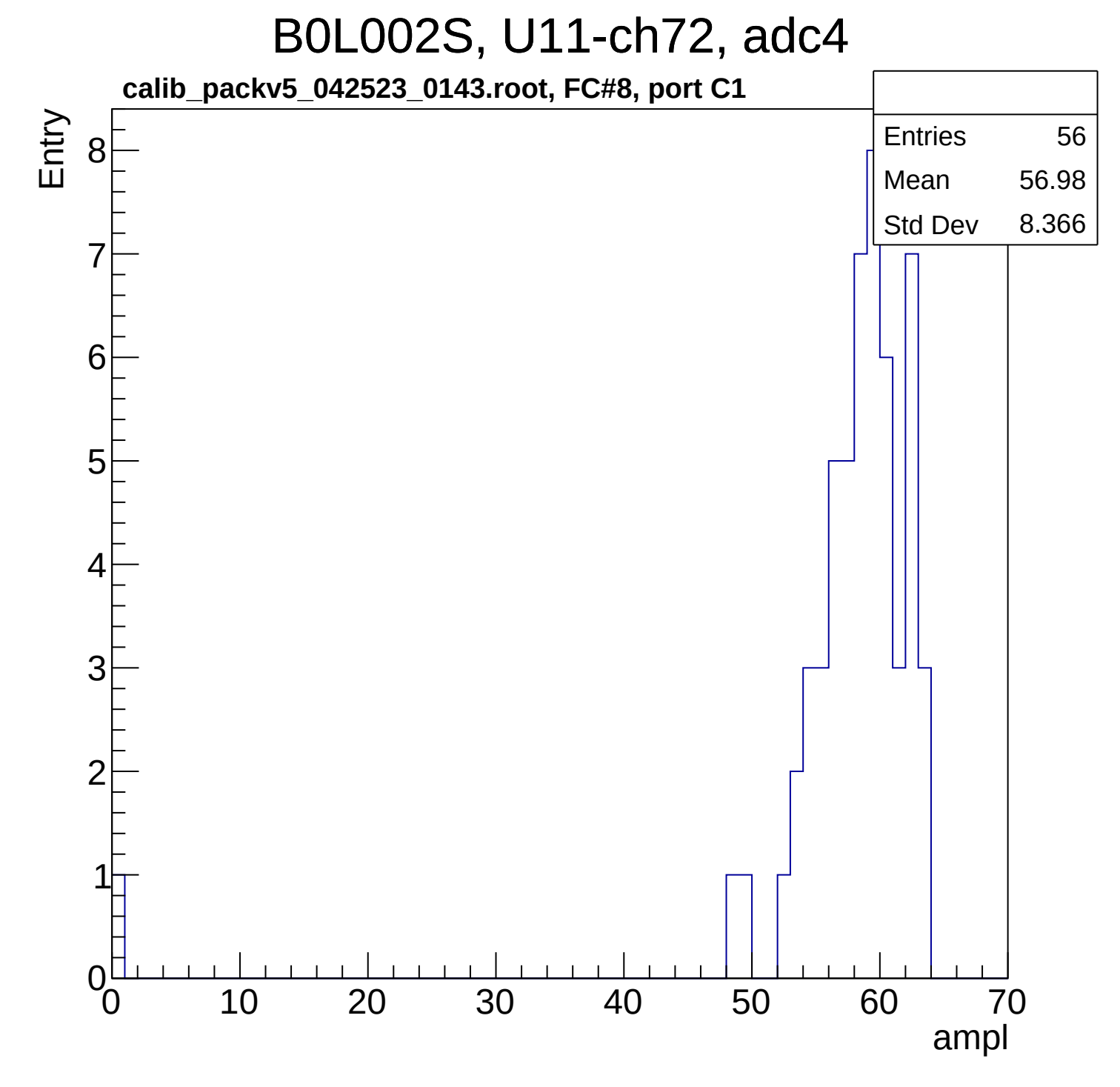
50
60
70
ampl


B0L002S, U11-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.98
Std Dev 8.366
Entry
ampl
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5
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7
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0
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20
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60
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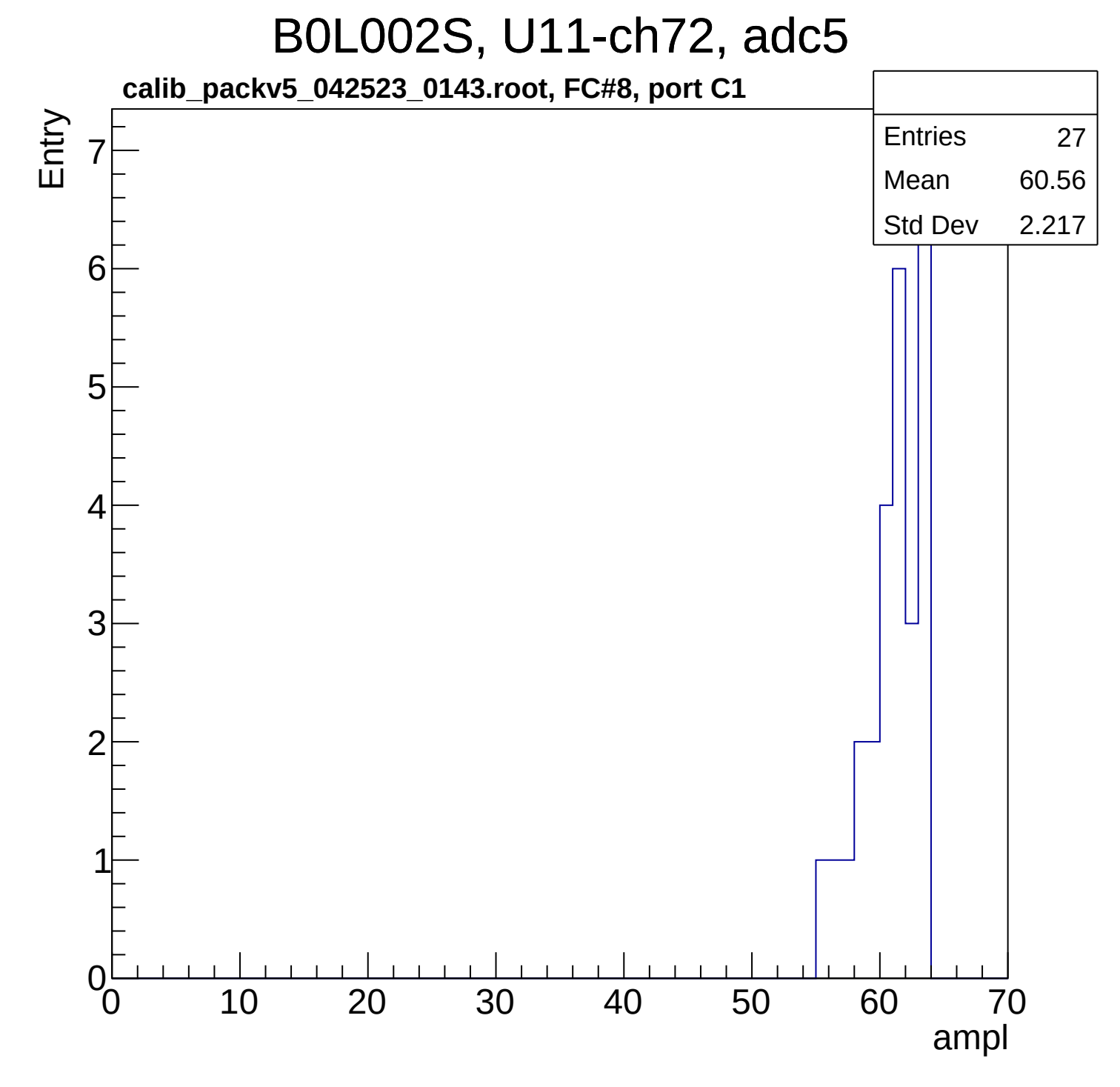
B0L002S, U11-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 60.56
Std Dev 2.217
Entry
ampl
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4
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6
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0
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20
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40
50
60
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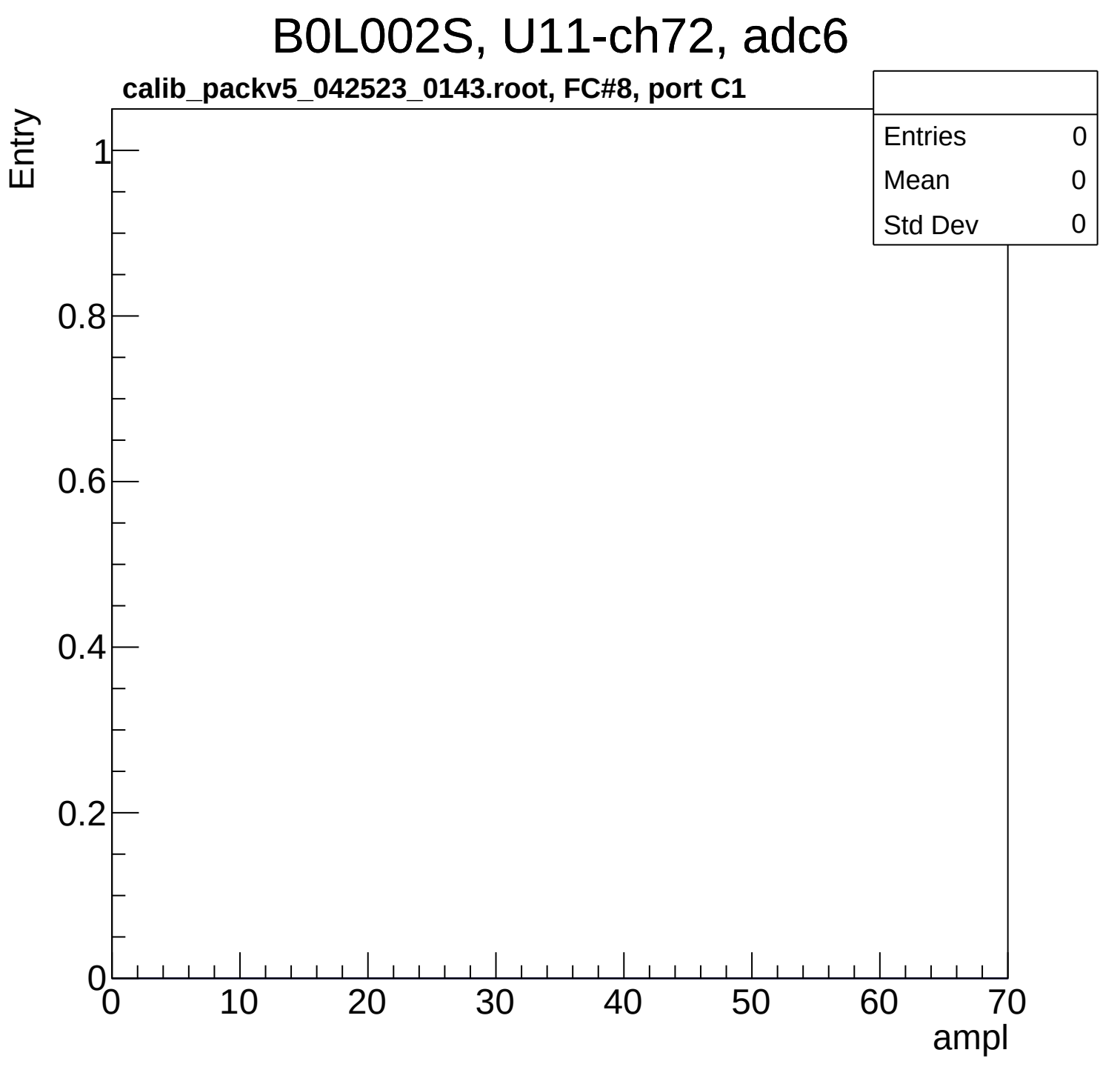
B0L002S, U11-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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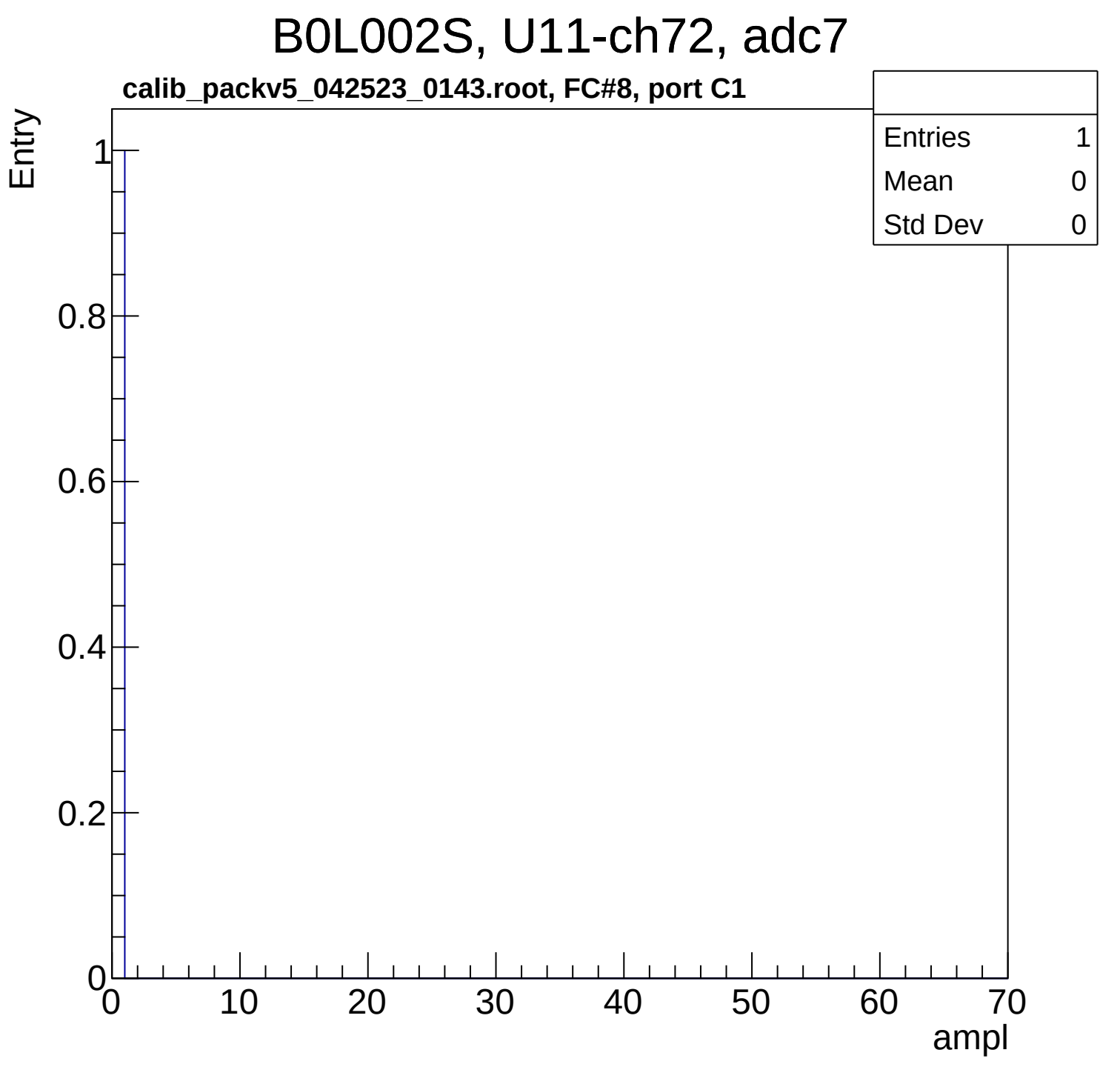
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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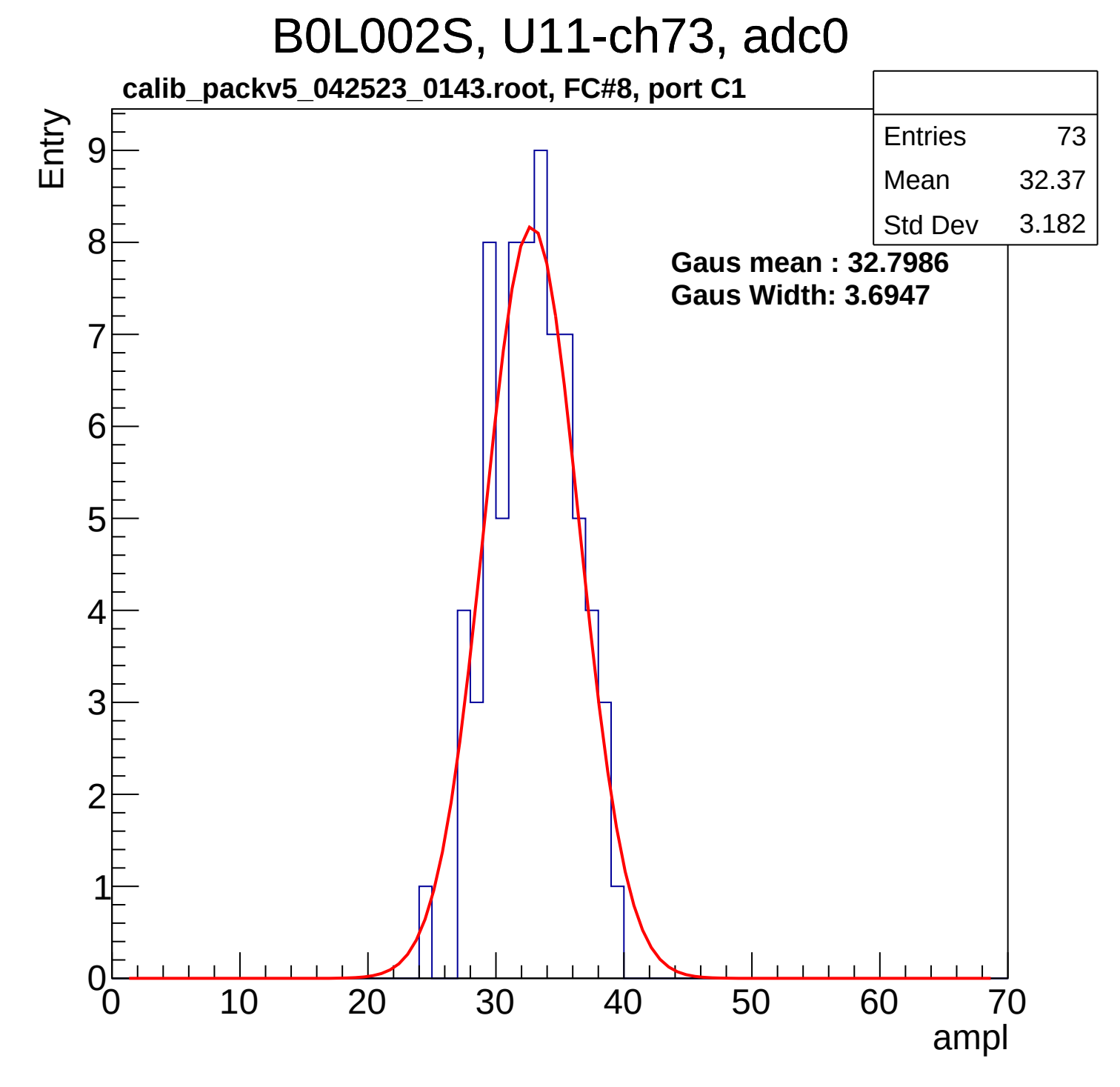
50
60
70
ampl


B0L002S, U11-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 32.37
Std Dev 3.182
Gaus mean : 32.7986
Gaus Width: 3.6947
Entry
ampl
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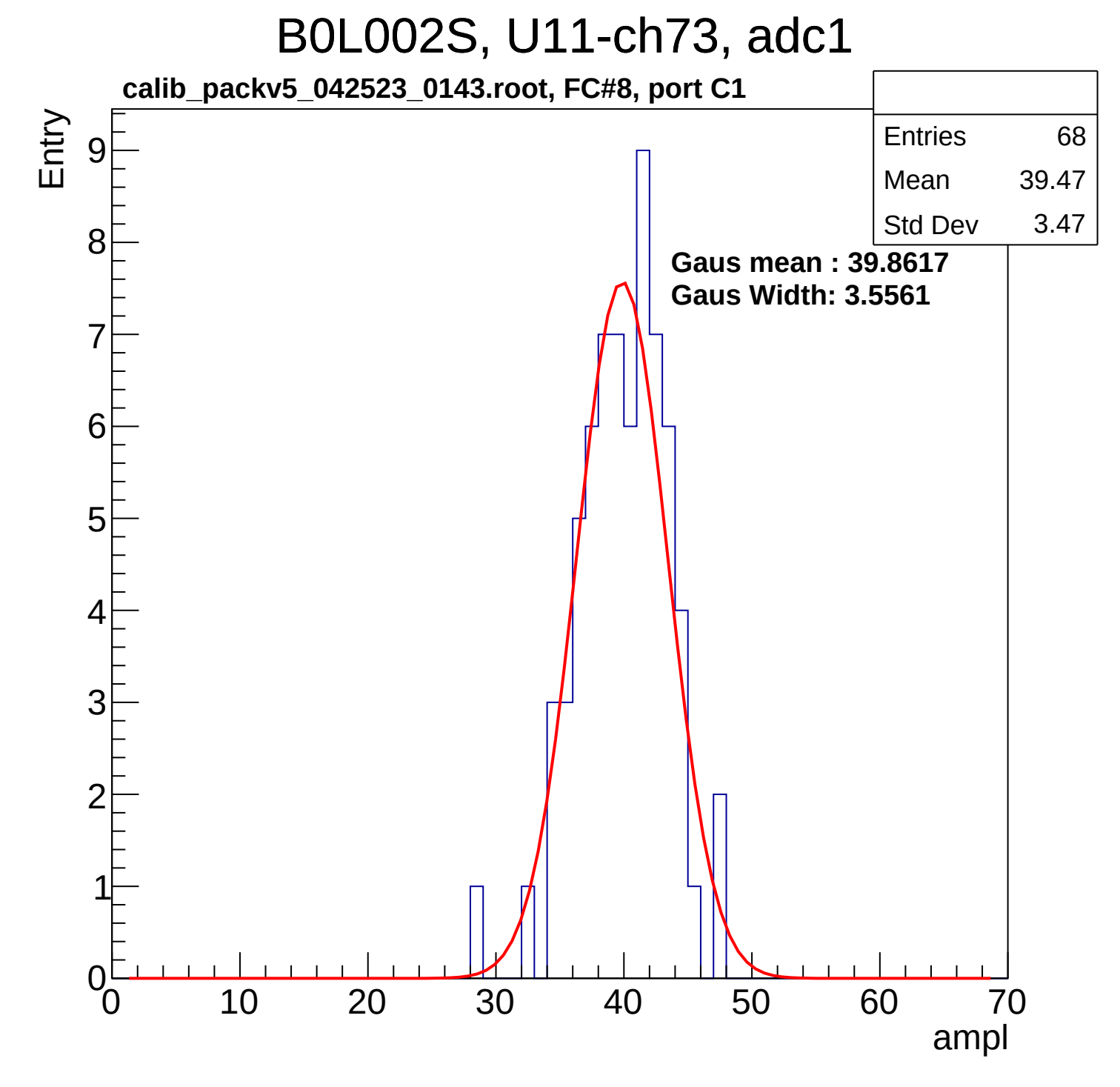
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 39.47
Std Dev 3.47
Gaus mean : 39.8617
Gaus Width: 3.5561
Entry
ampl
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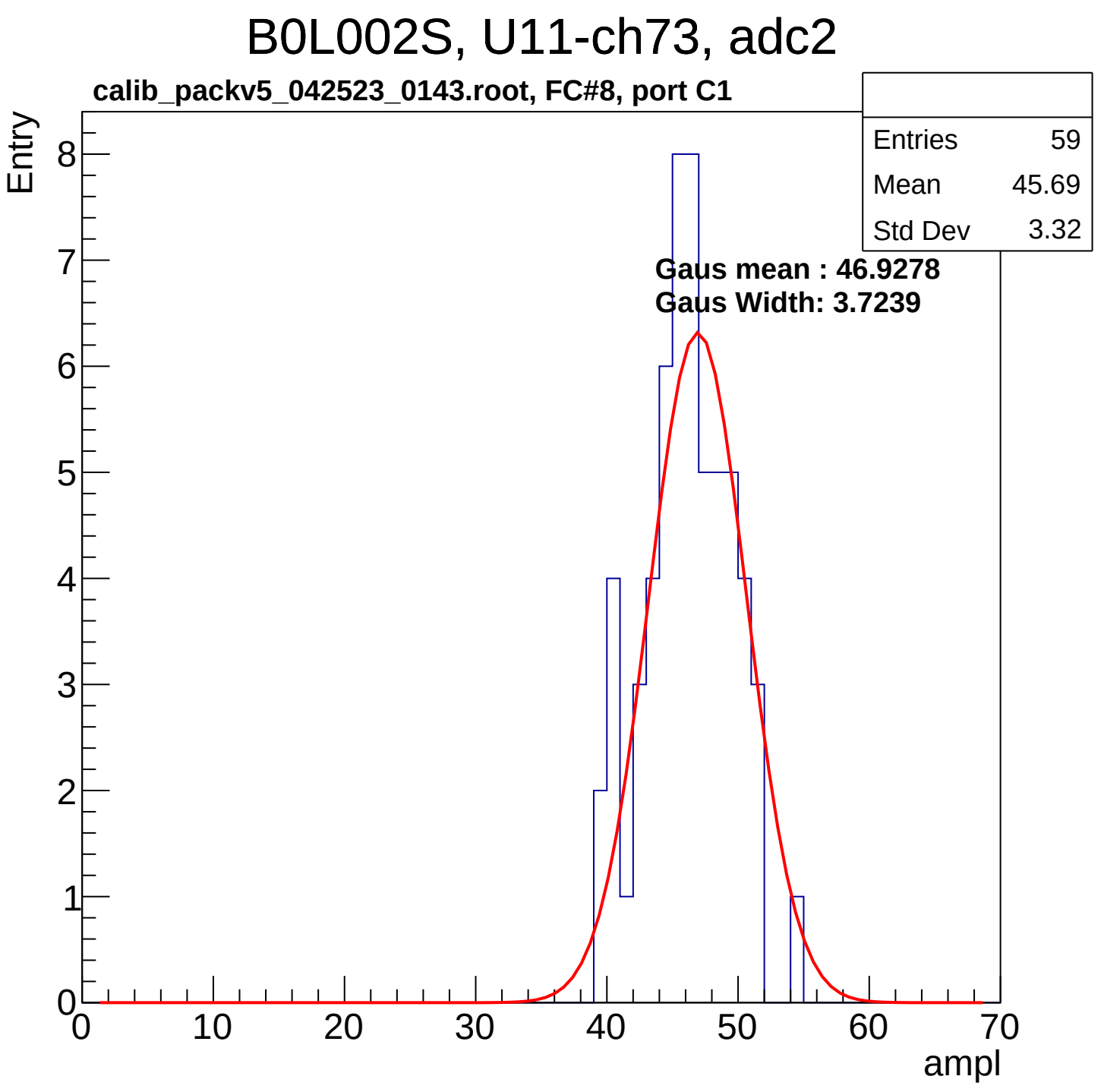
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 45.69
Std Dev 3.32
Gaus mean : 46.9278
Gaus Width: 3.7239
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
50
60
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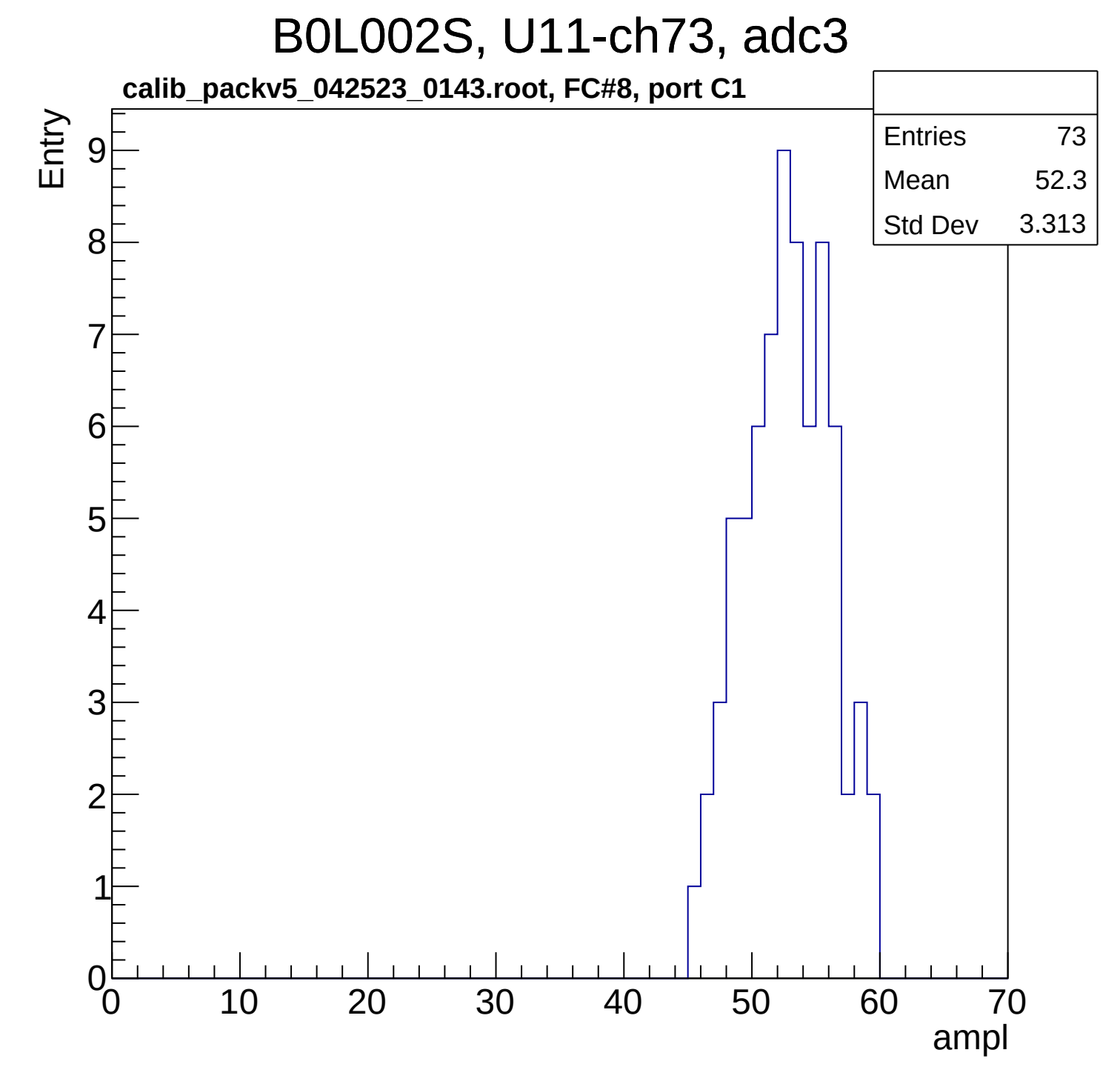
B0L002S, U11-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 52.3
Std Dev 3.313
Entry
ampl
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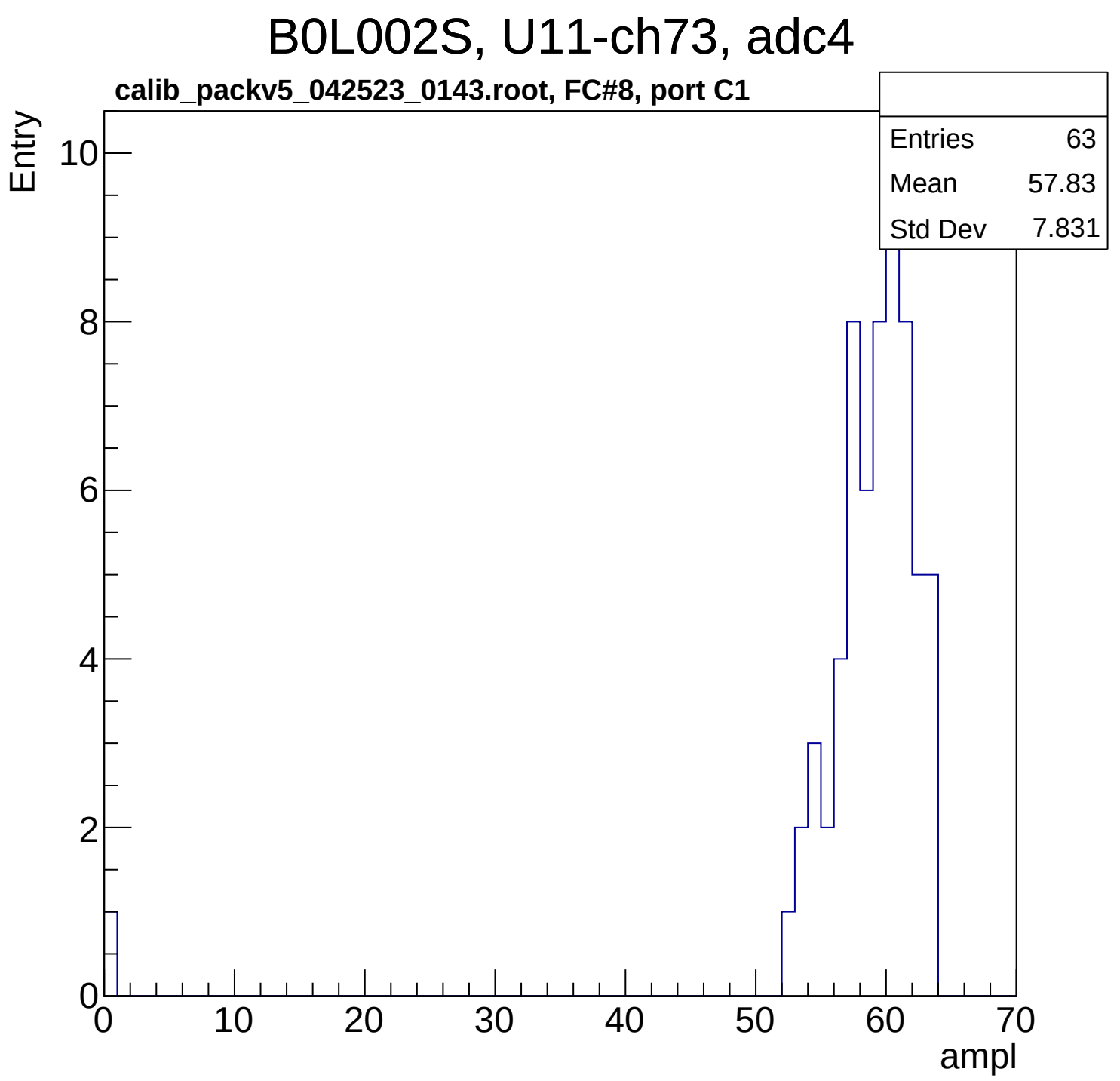
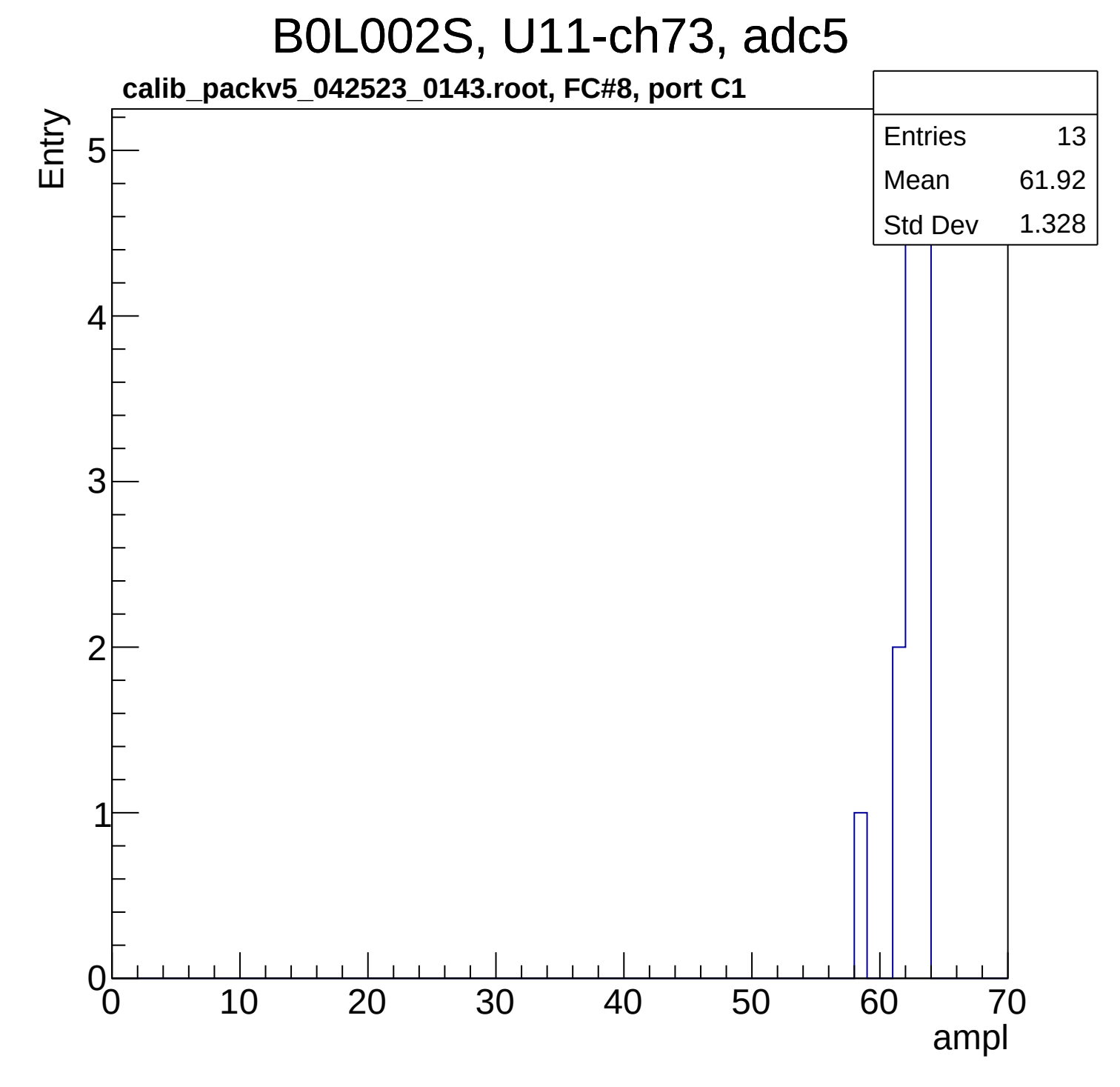
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.83
Std Dev 7.831
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 61.92
Std Dev 1.328
Entry
ampl
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2
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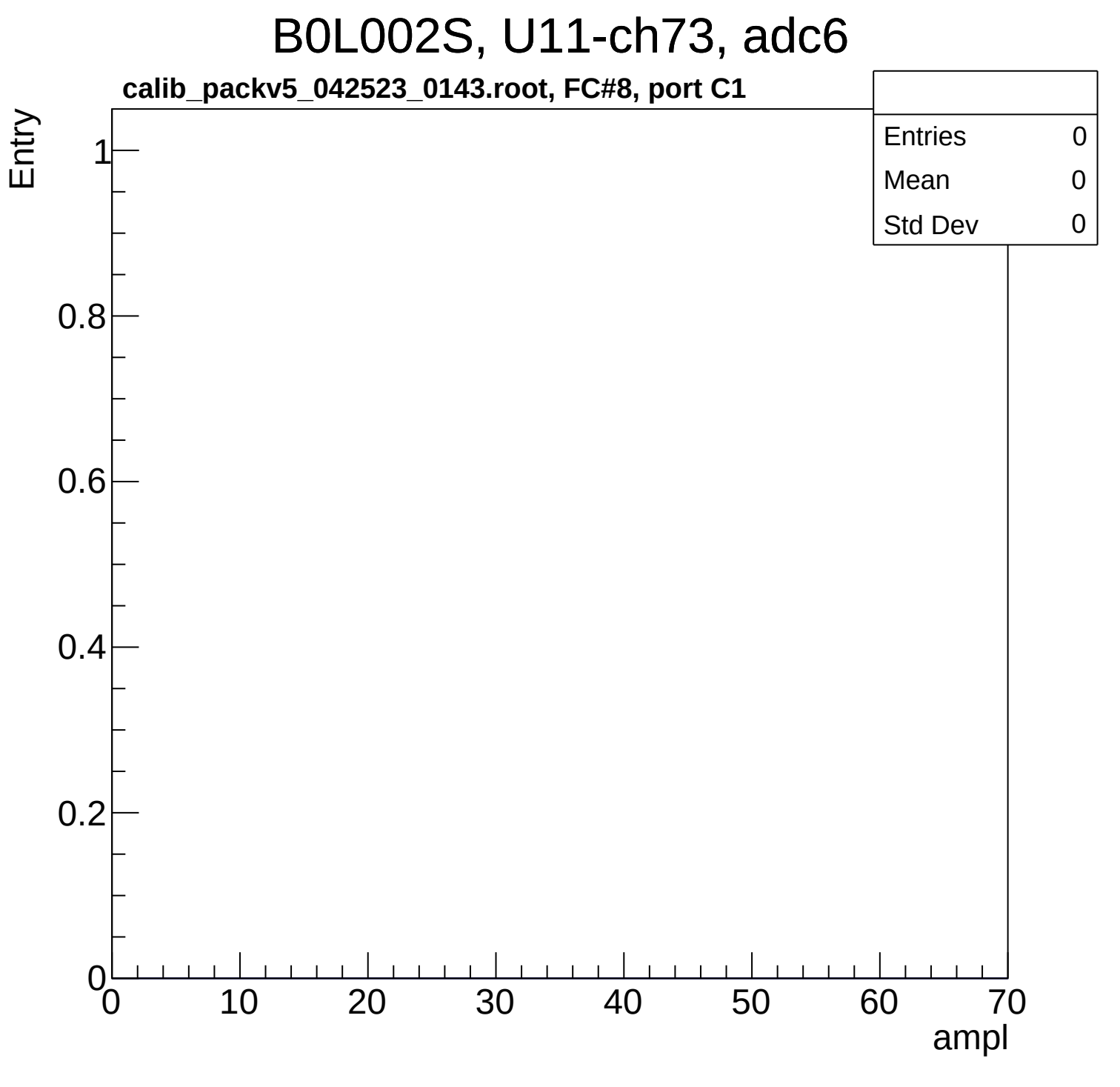
B0L002S, U11-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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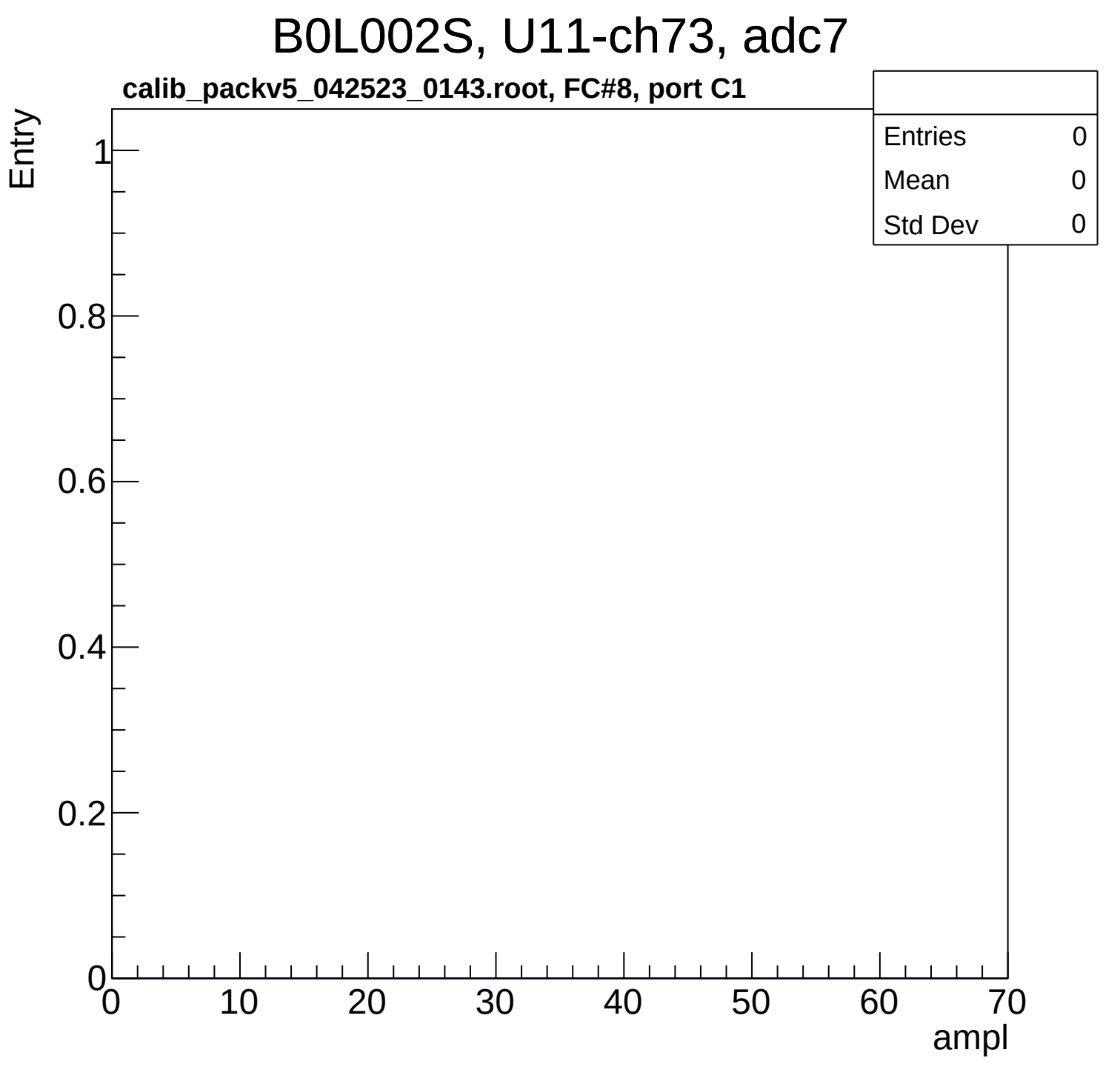
70
ampl


B0L002S, U11-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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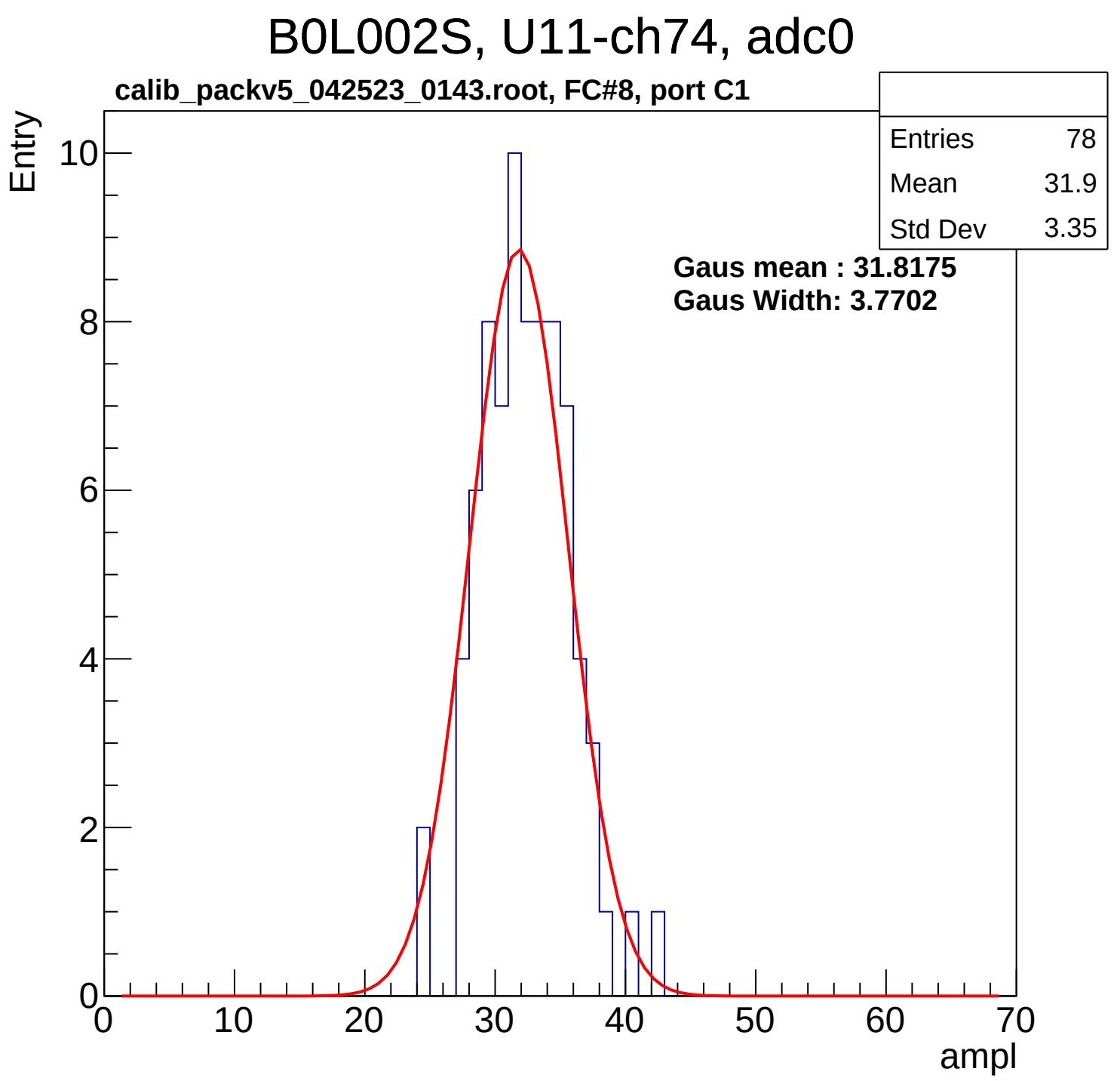
40
50
60
70
ampl


B0L002S, U11-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 31.9
Std Dev 3.35
Gaus mean : 31.8175
Gaus Width: 3.7702
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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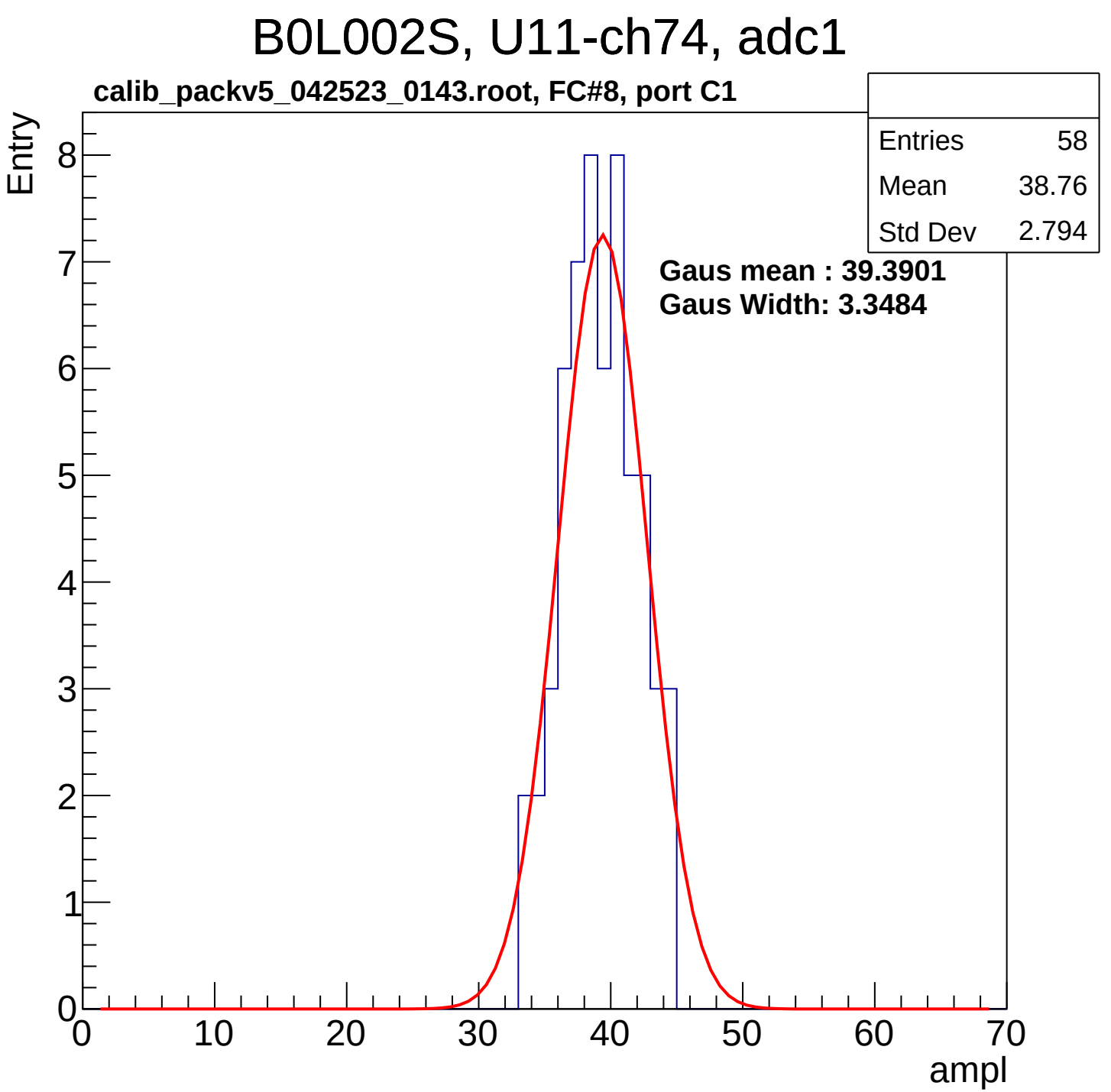
B0L002S, U11-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 38.76
Std Dev 2.794
Gaus mean : 39.3901
Gaus Width: 3.3484
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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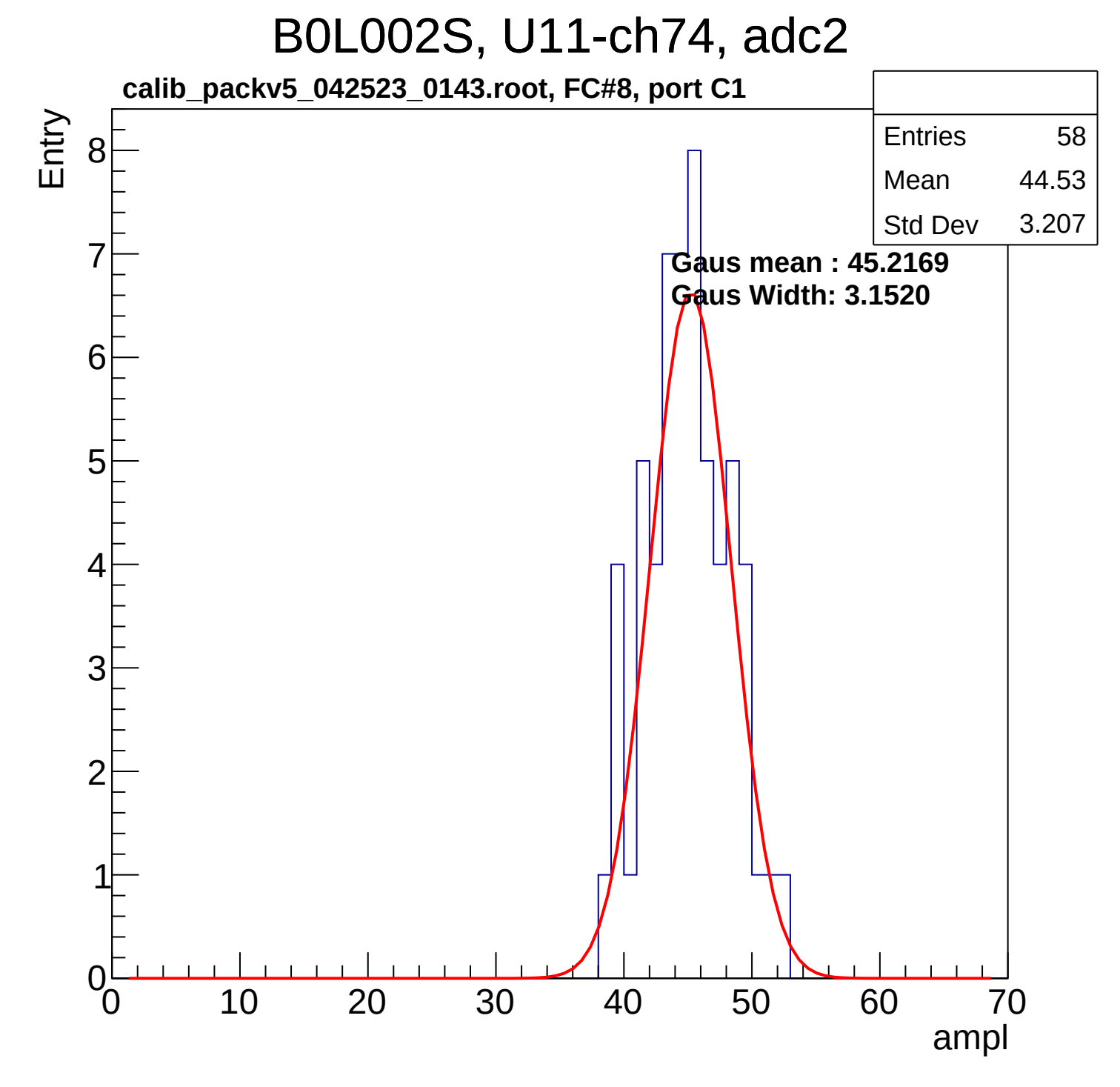
B0L002S, U11-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 44.53
Std Dev 3.207
Gaus mean : 45.2169
Gaus Width: 3.1520
Entry
ampl
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1
2
3
4
5
6
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0
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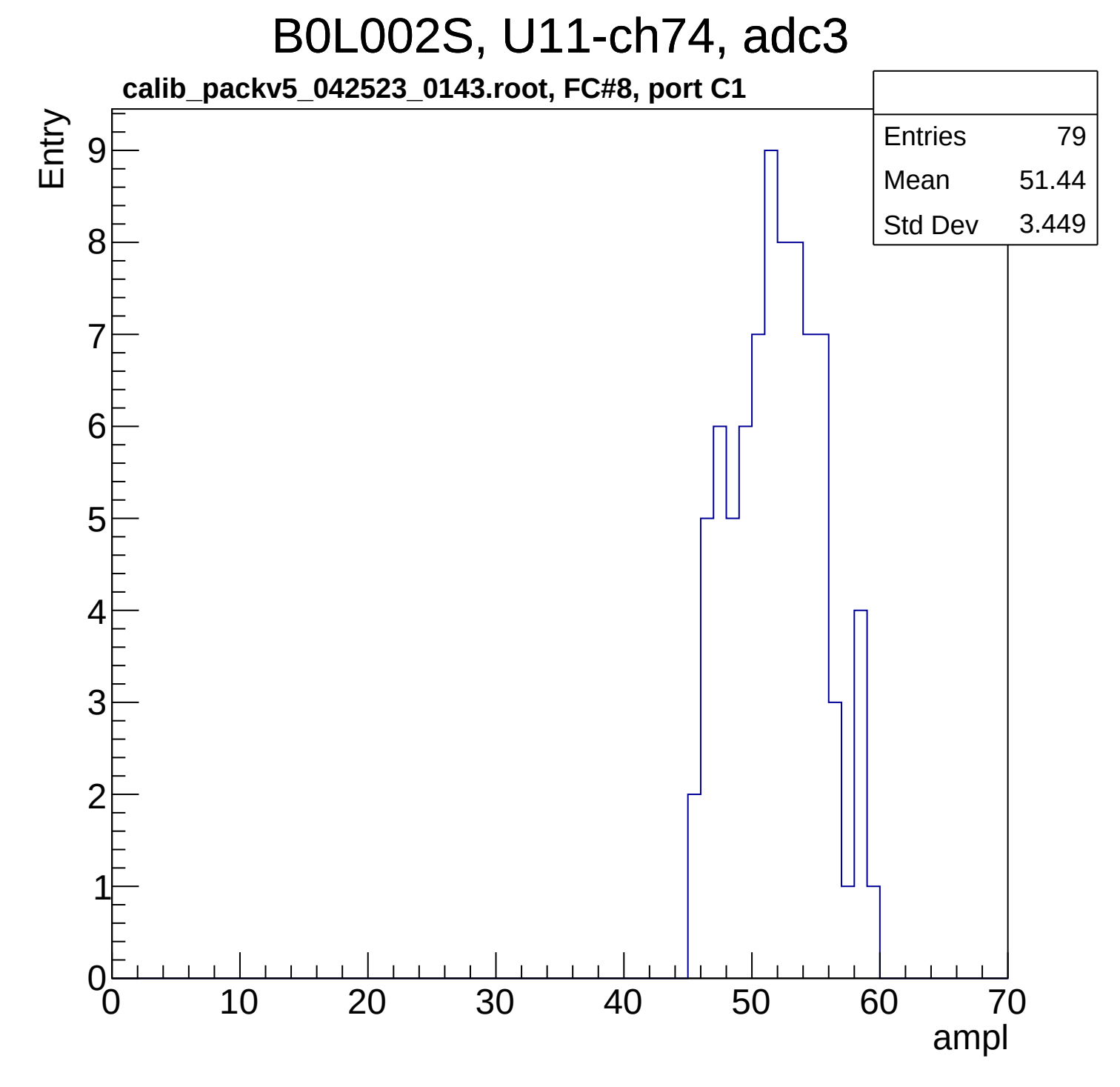
B0L002S, U11-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 51.44
Std Dev 3.449
Entry
ampl
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2
3
4
5
6
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0
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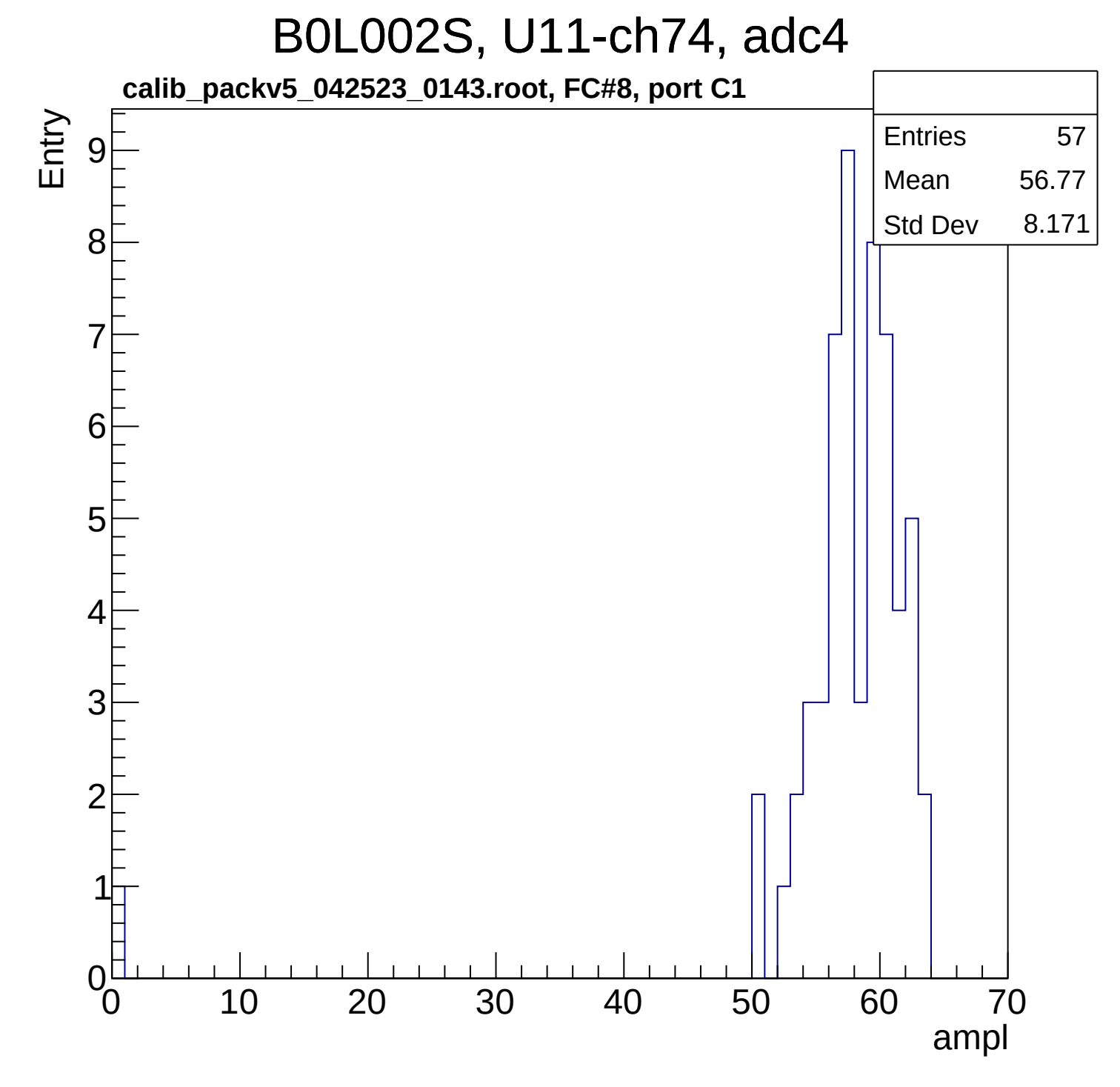
B0L002S, U11-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.77
Std Dev 8.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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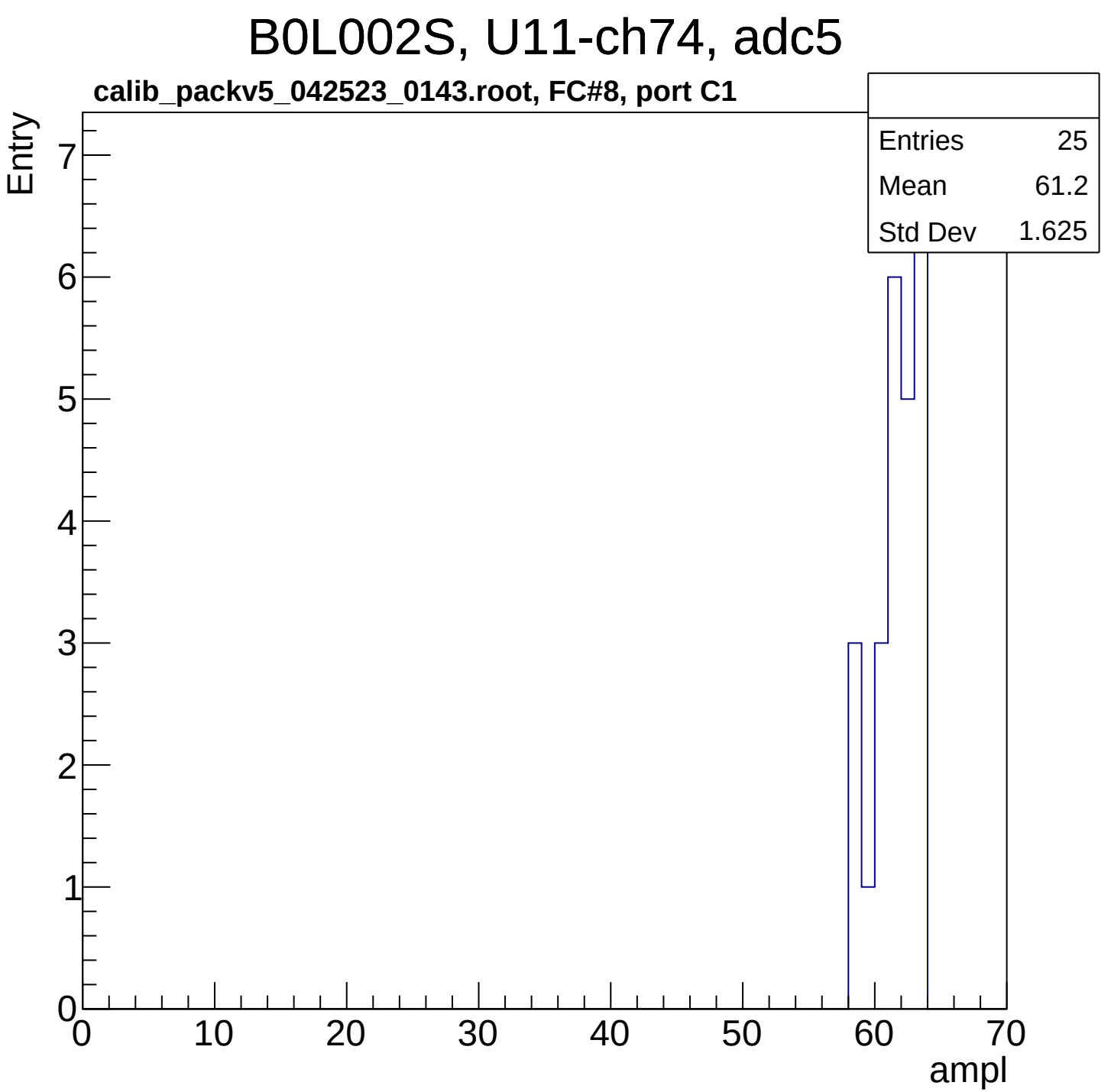
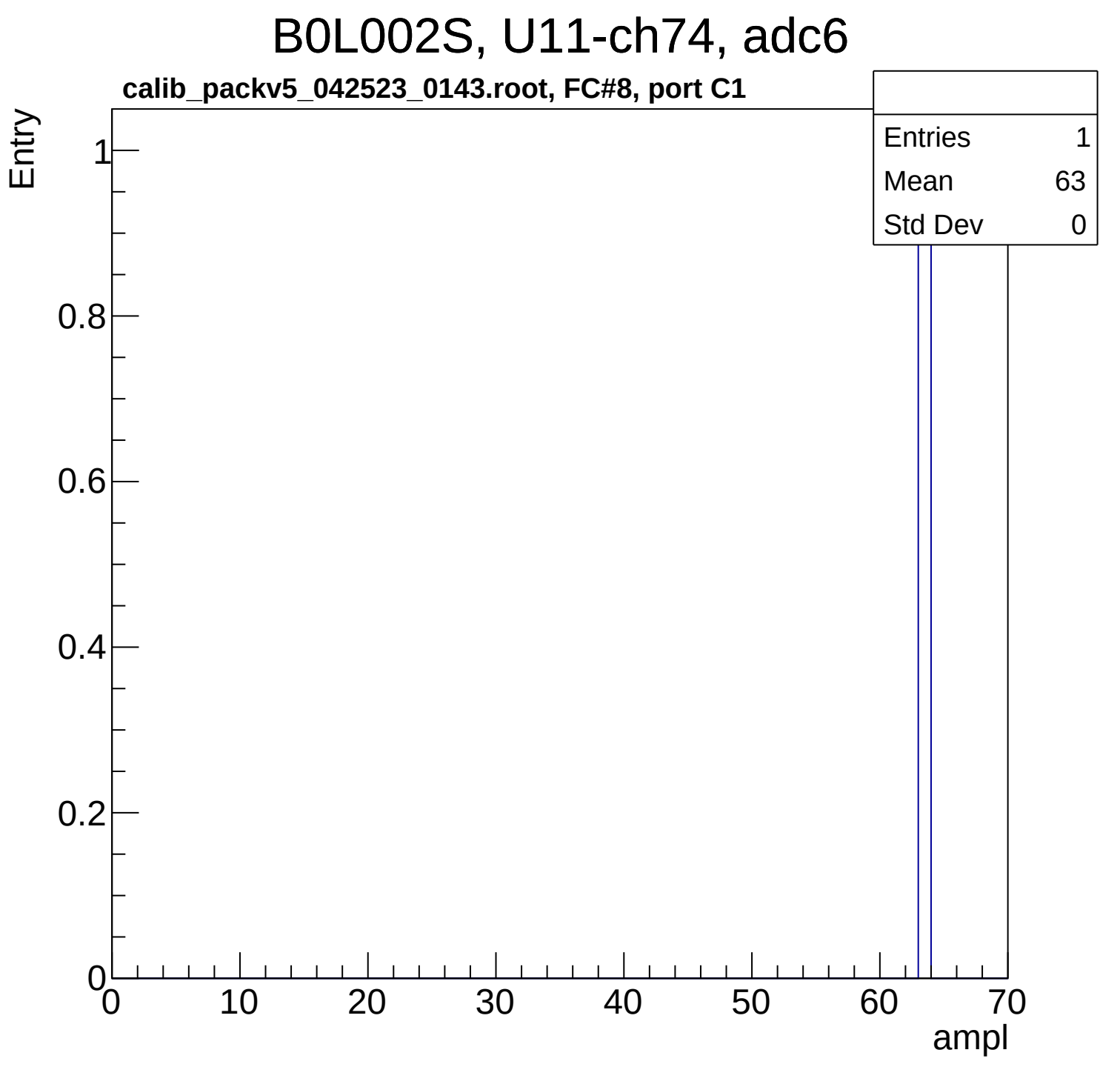
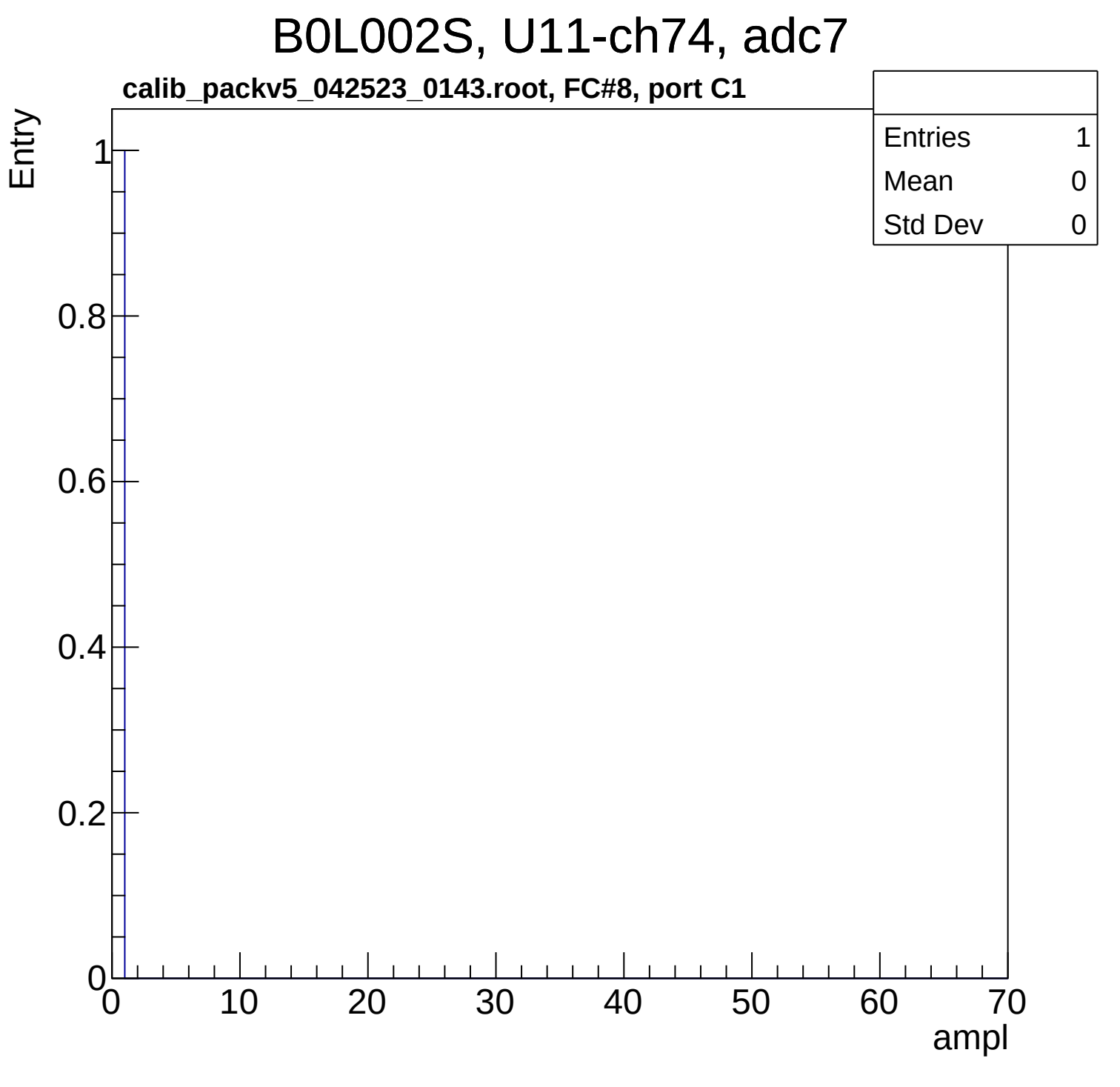
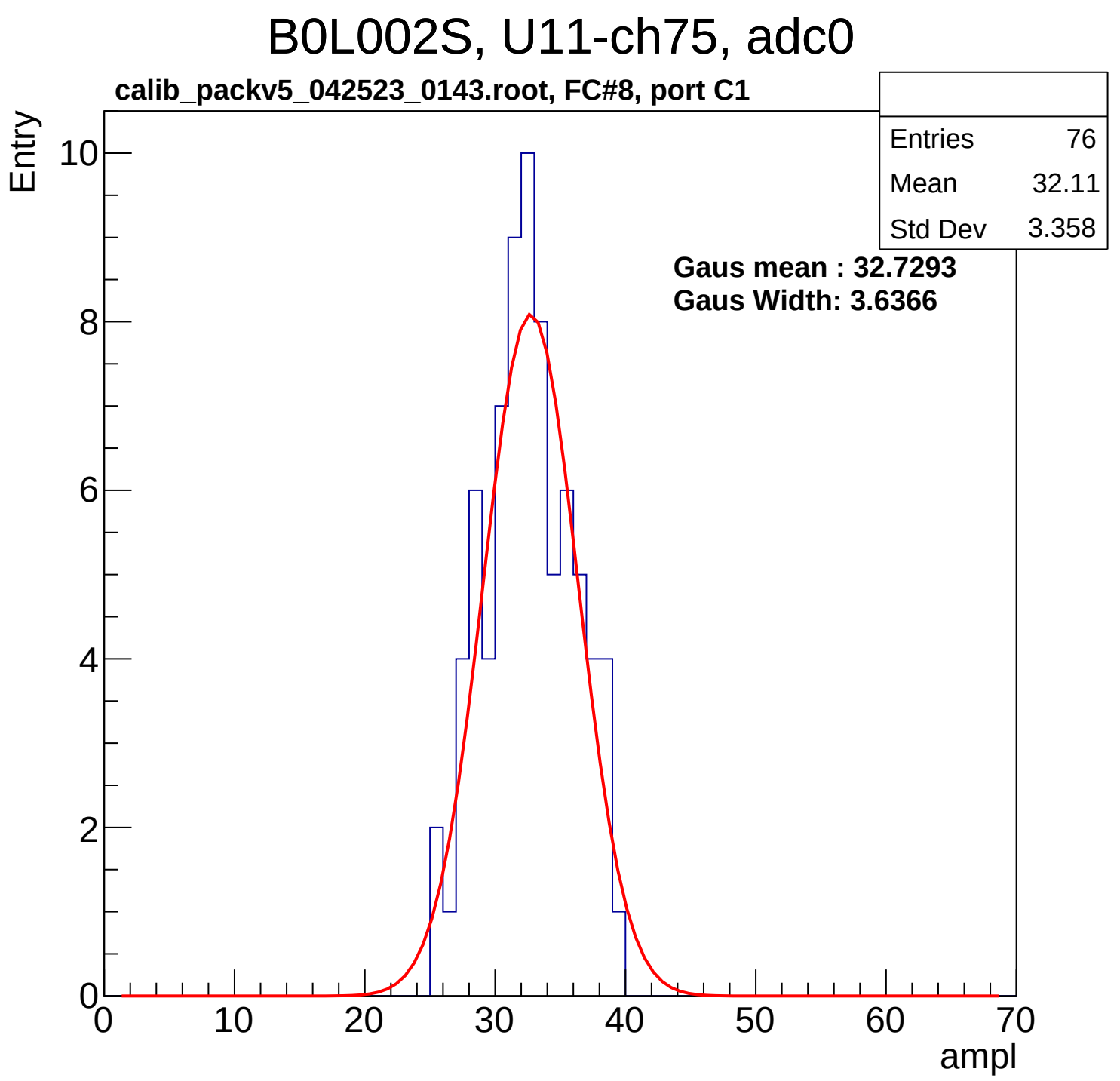

B0L002S, U11-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61.2
Std Dev 1.625
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 32.11
Std Dev 3.358
Gaus mean : 32.7293
Gaus Width: 3.6366
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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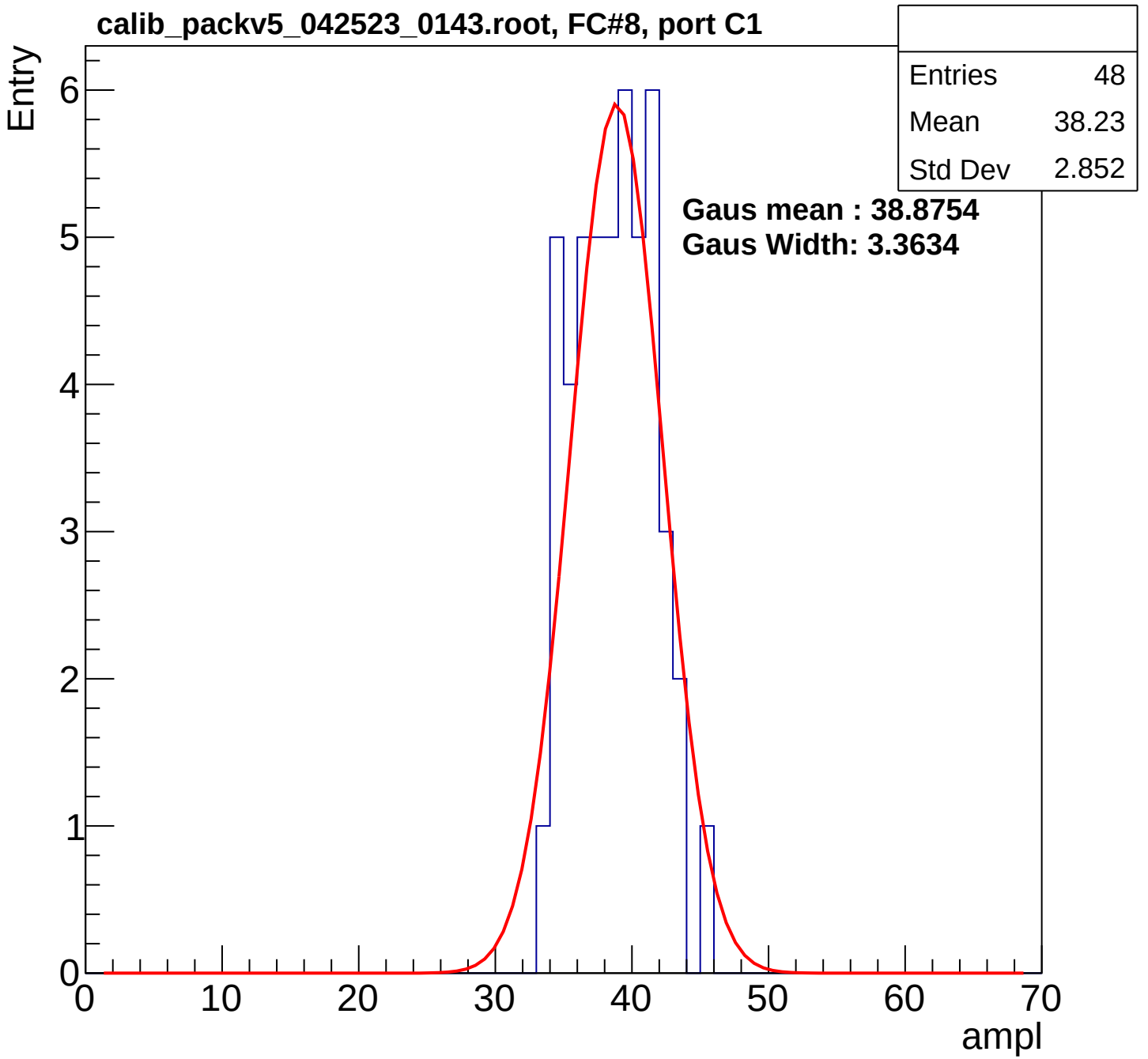
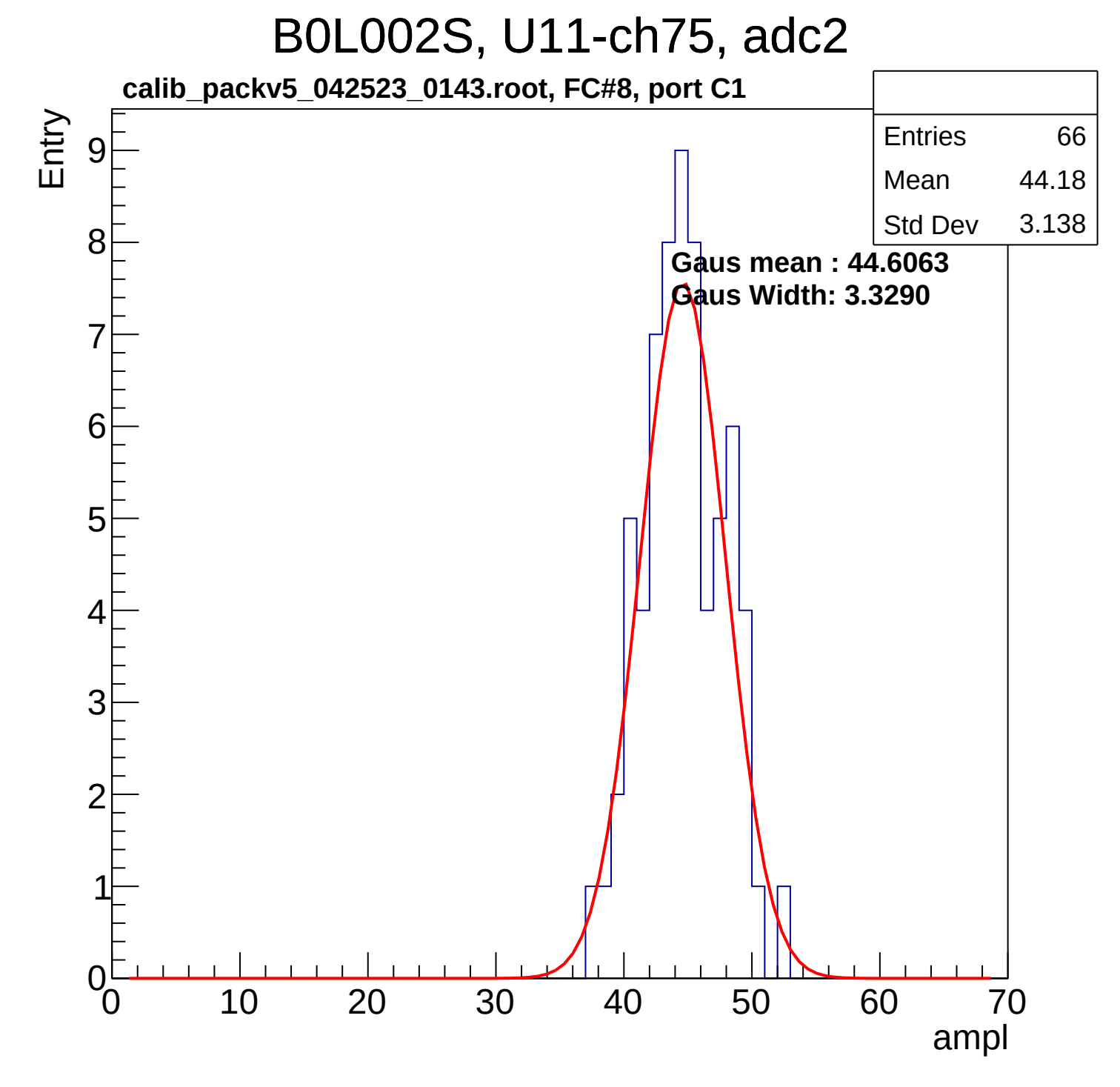
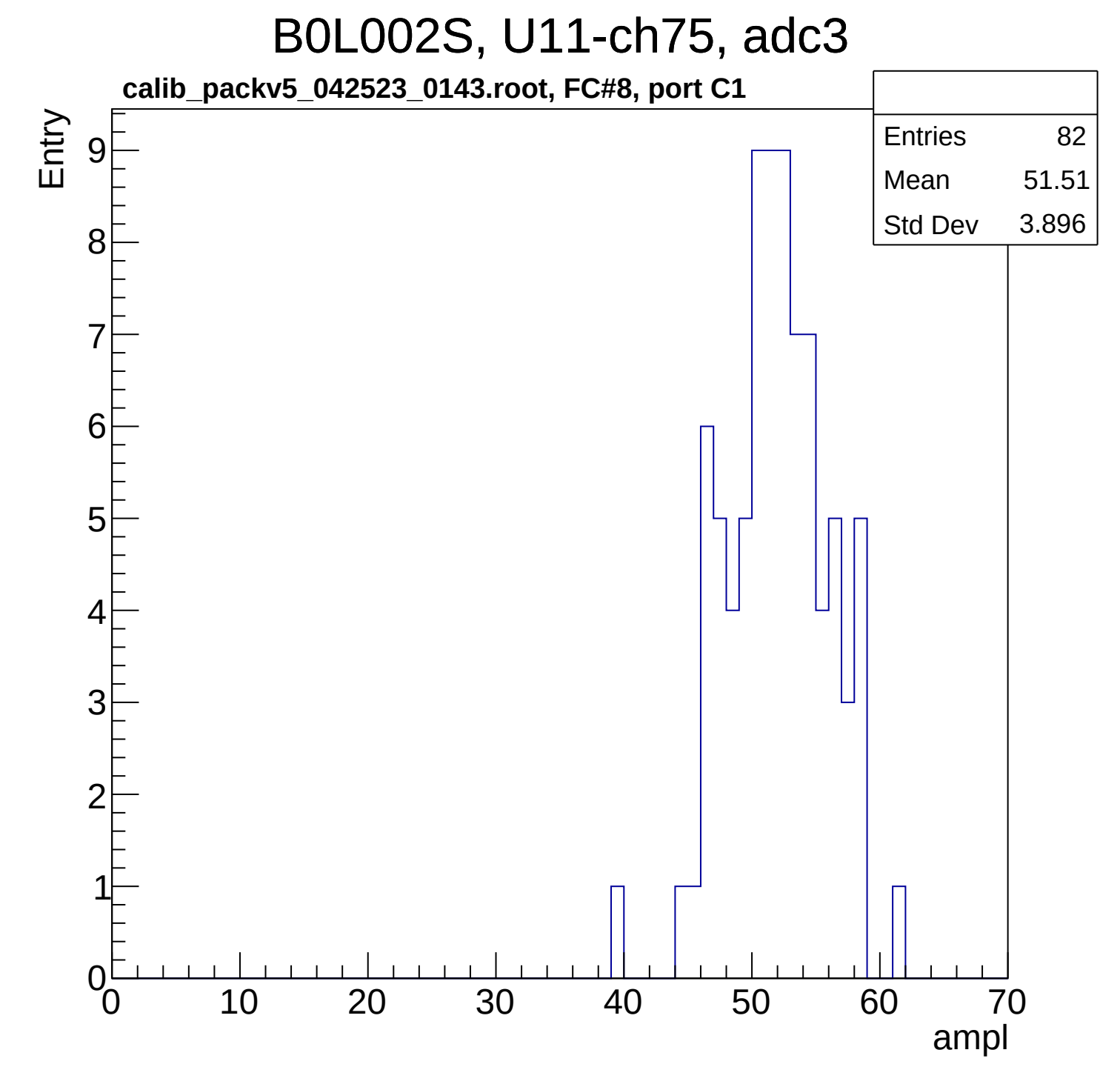

B0L002S, U11-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 38.23
Std Dev 2.852
Gaus mean : 38.8754
Gaus Width: 3.3634
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U11-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.18
Std Dev 3.138
Gaus mean : 44.6063
Gaus Width: 3.3290
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 51.51
Std Dev 3.896
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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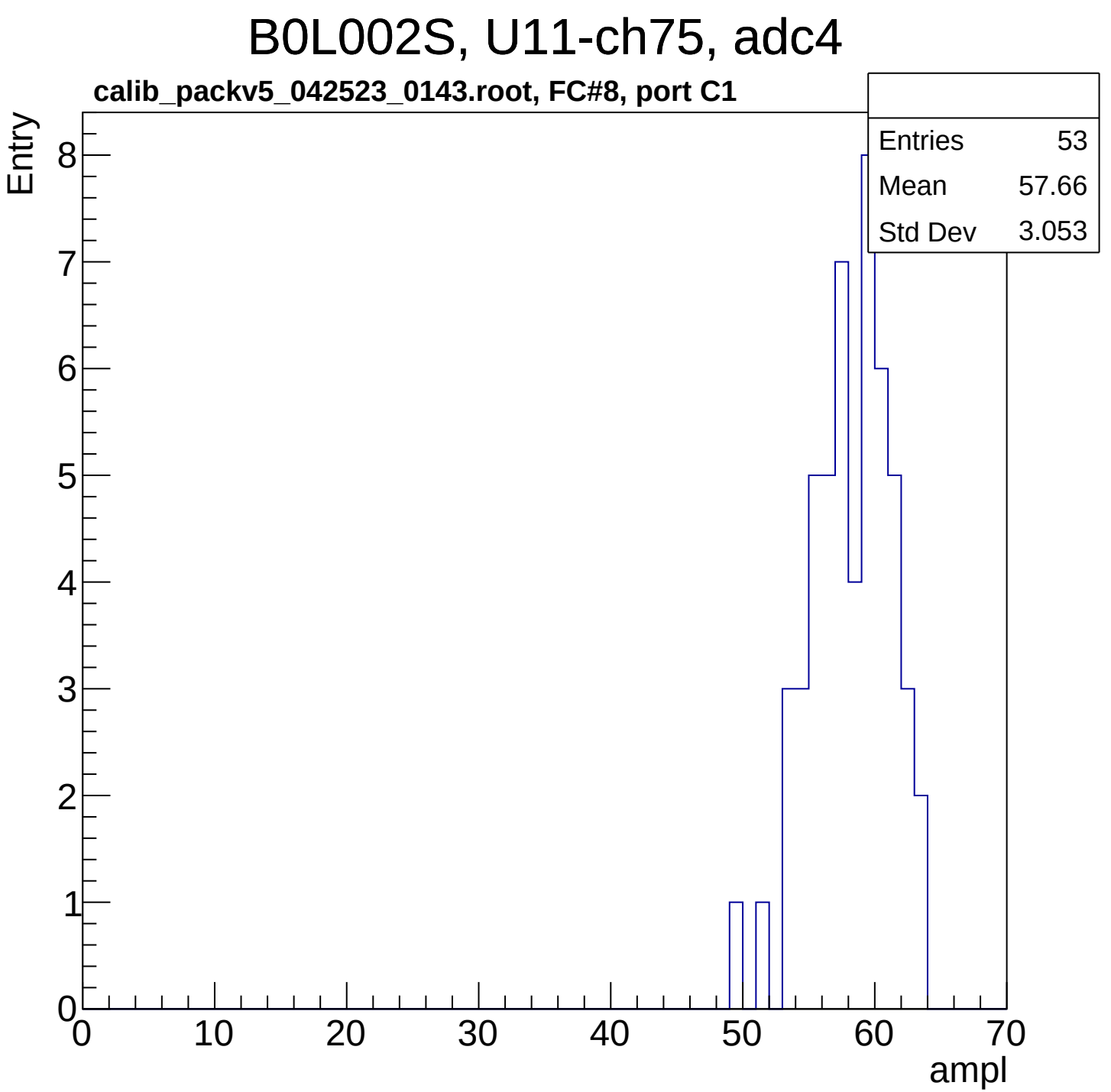
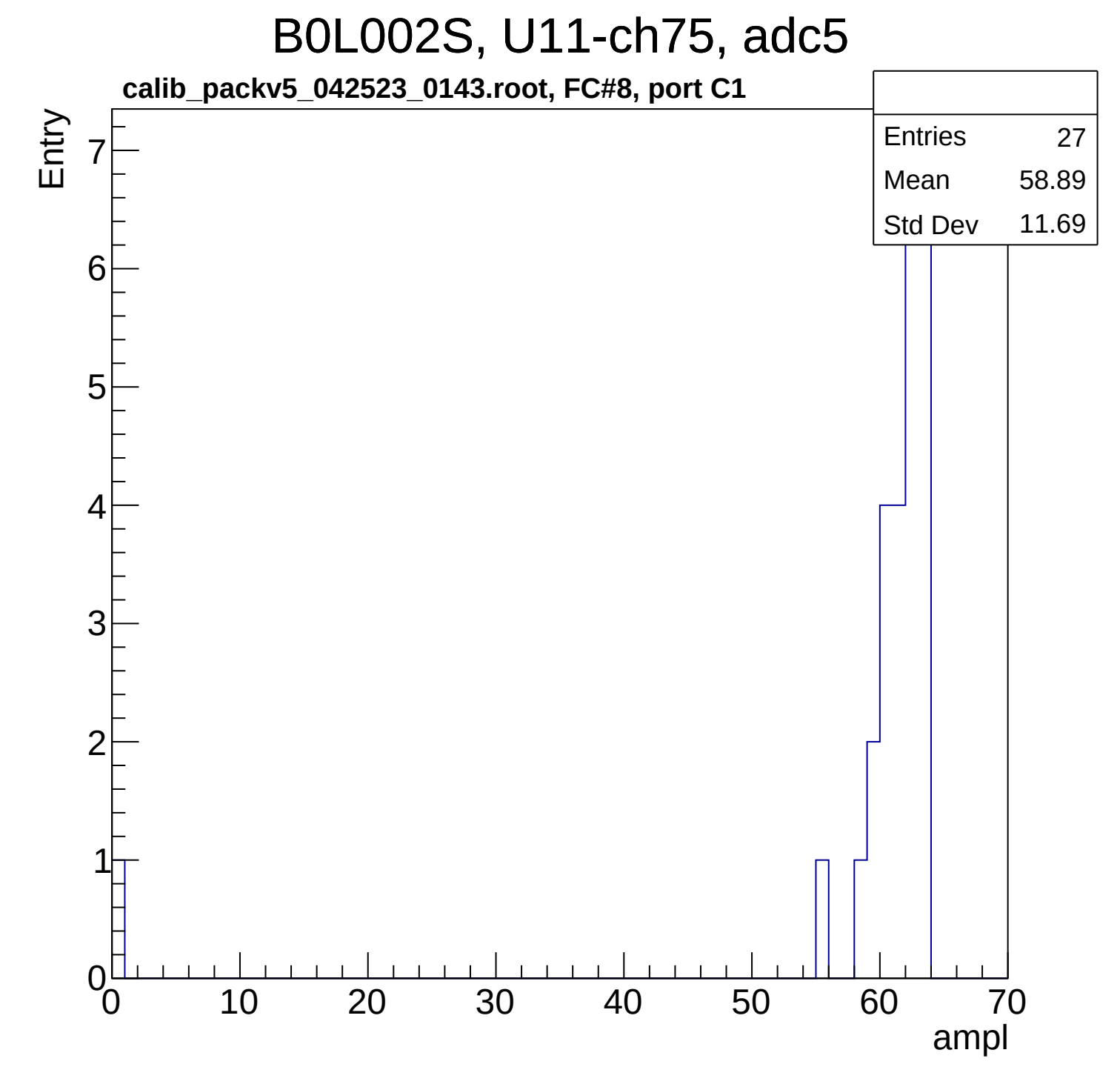
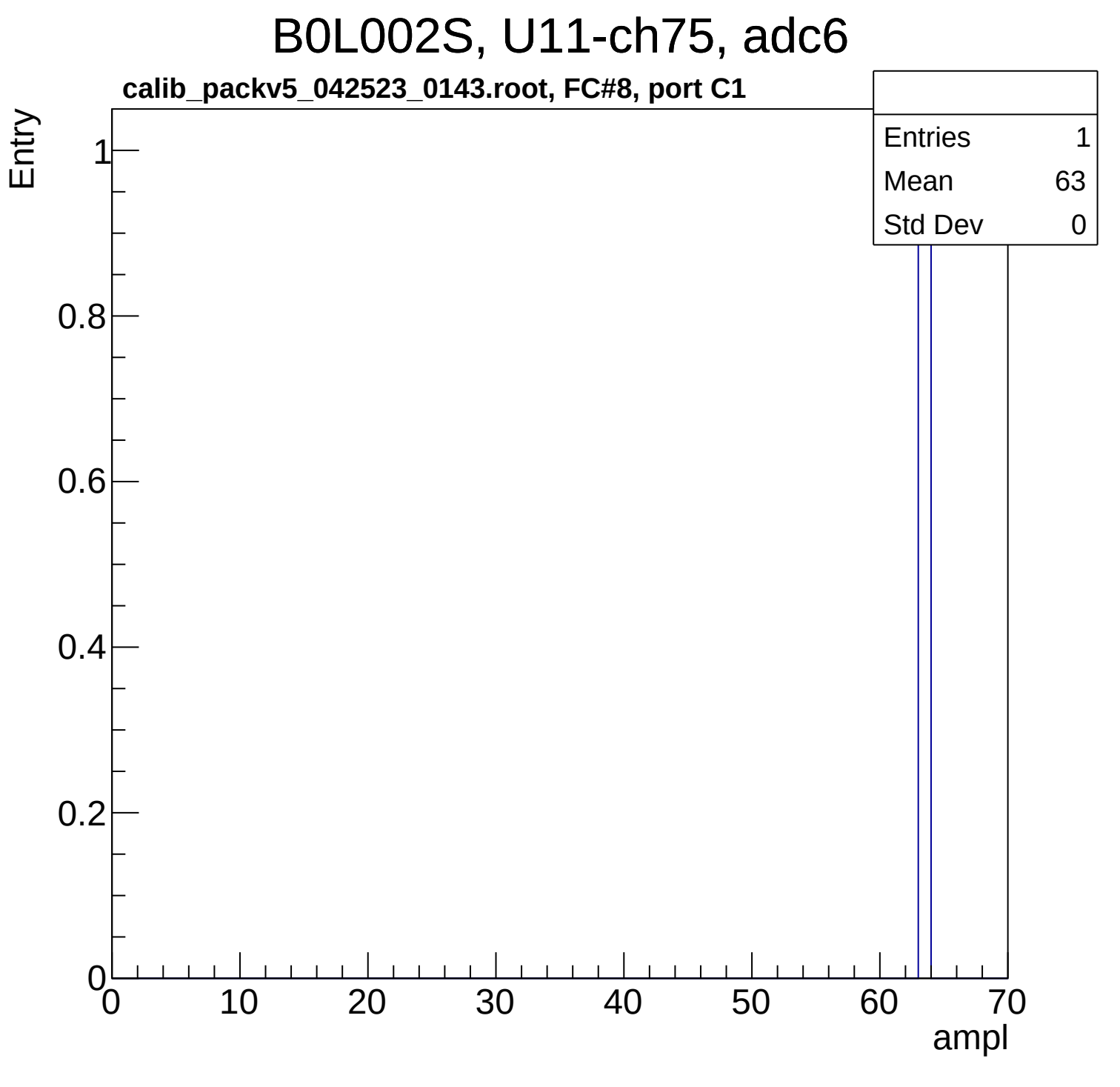
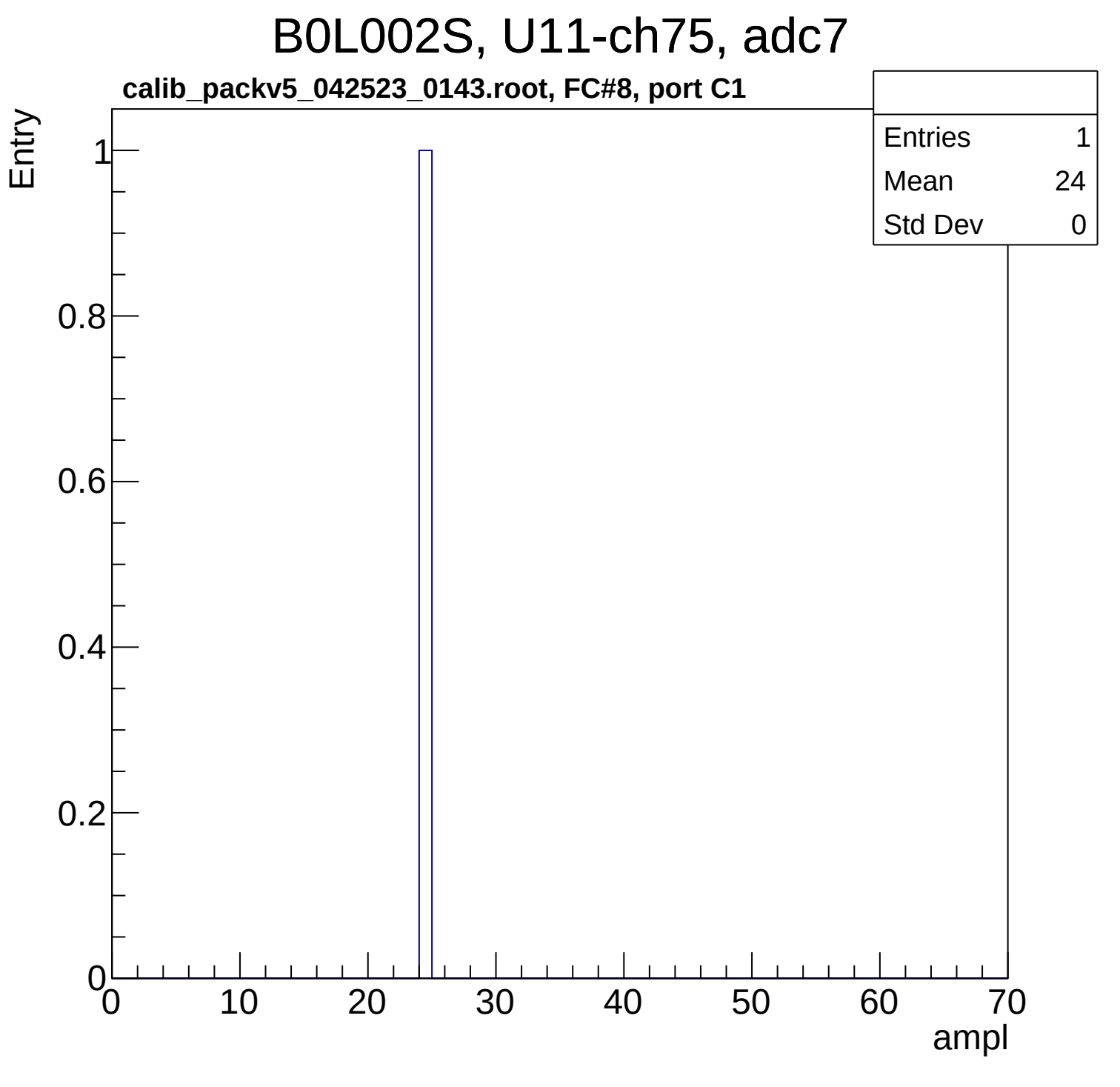

B0L002S, U11-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.66
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.89
Std Dev 11.69
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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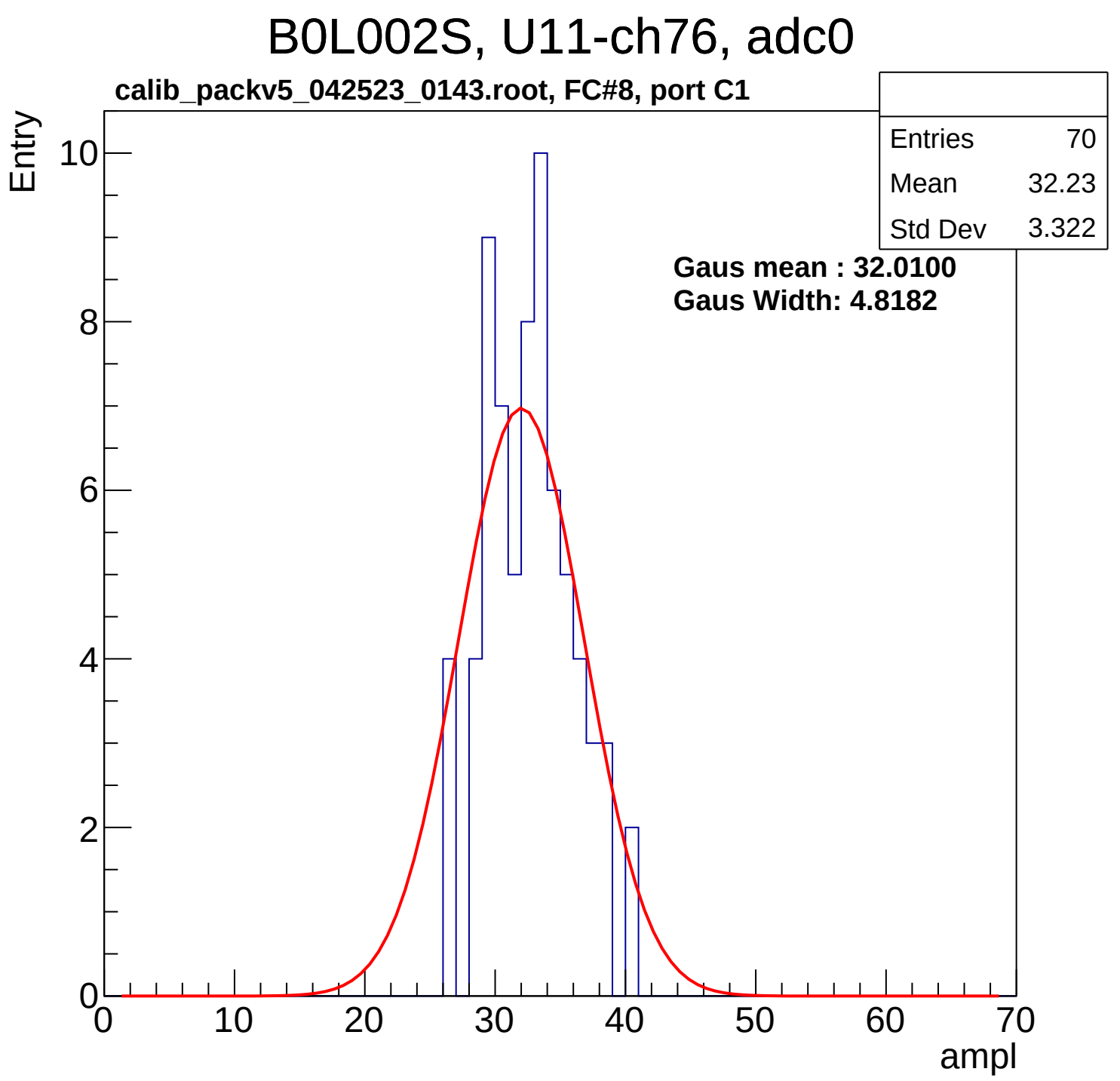
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 32.23
Std Dev 3.322
Gaus mean : 32.0100
Gaus Width: 4.8182
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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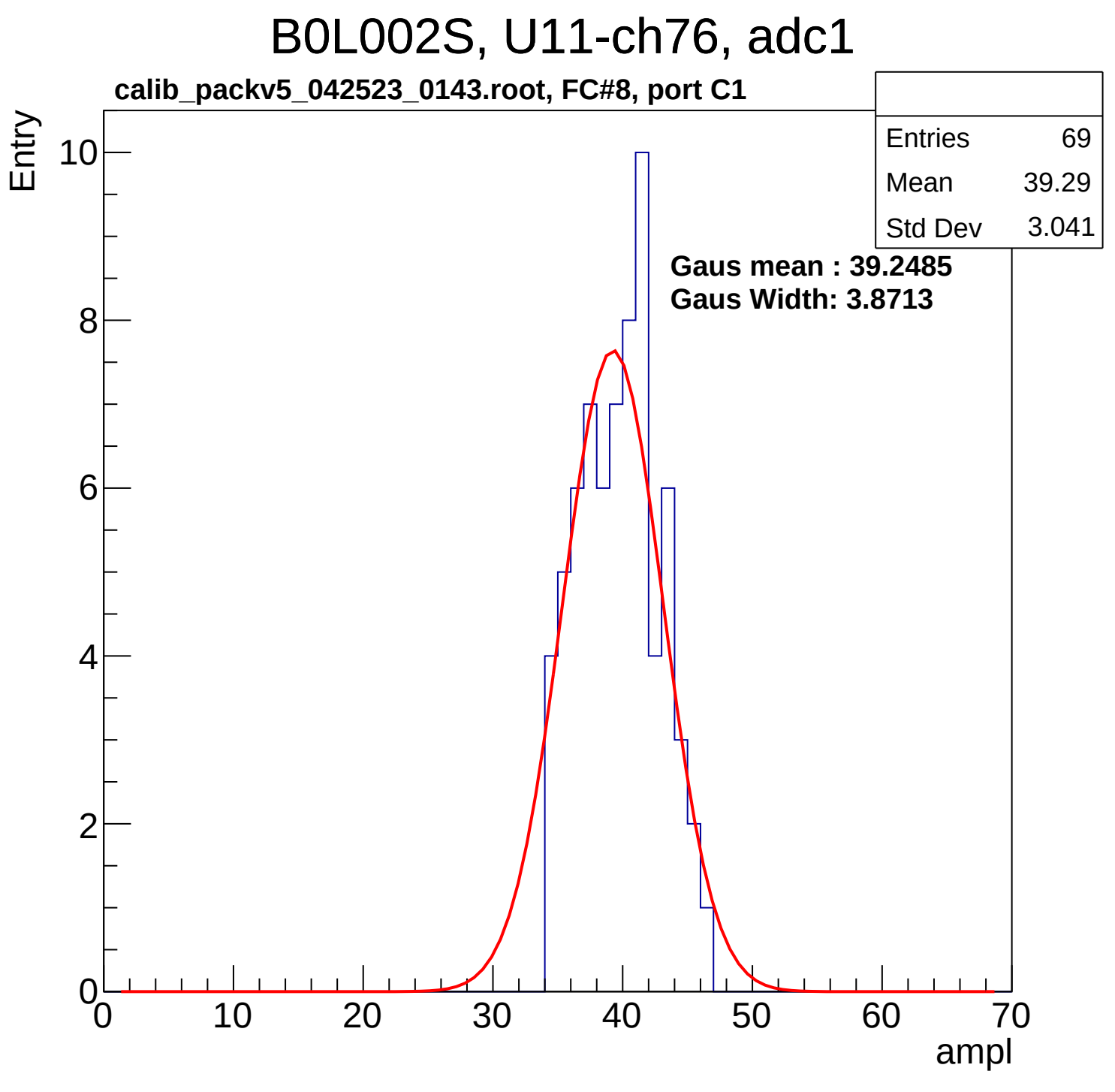
B0L002S, U11-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 39.29
Std Dev 3.041
Gaus mean : 39.2485
Gaus Width: 3.8713
Entry
ampl
0
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4
6
8
10
0
10
20
30
40
50
60
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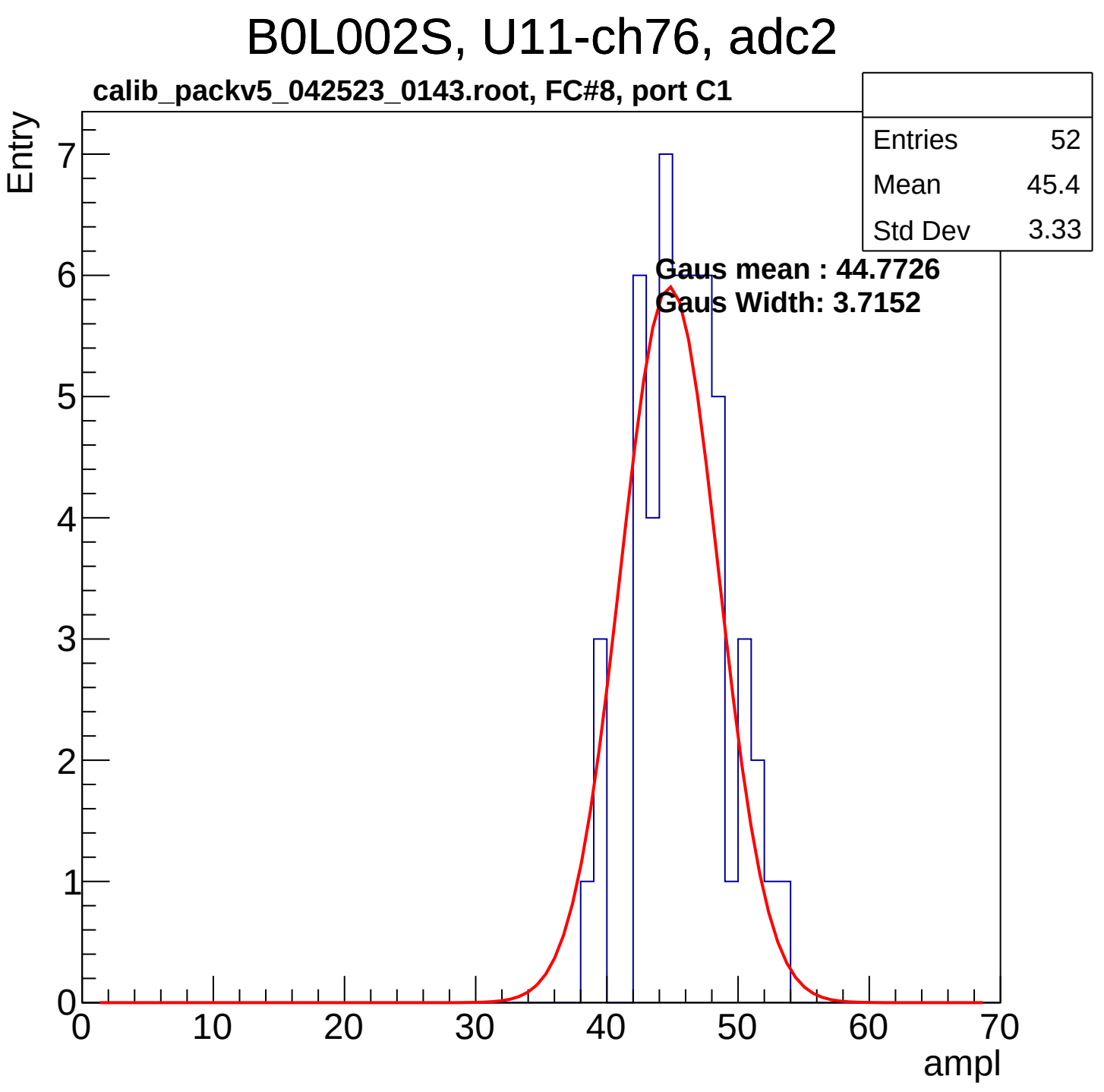
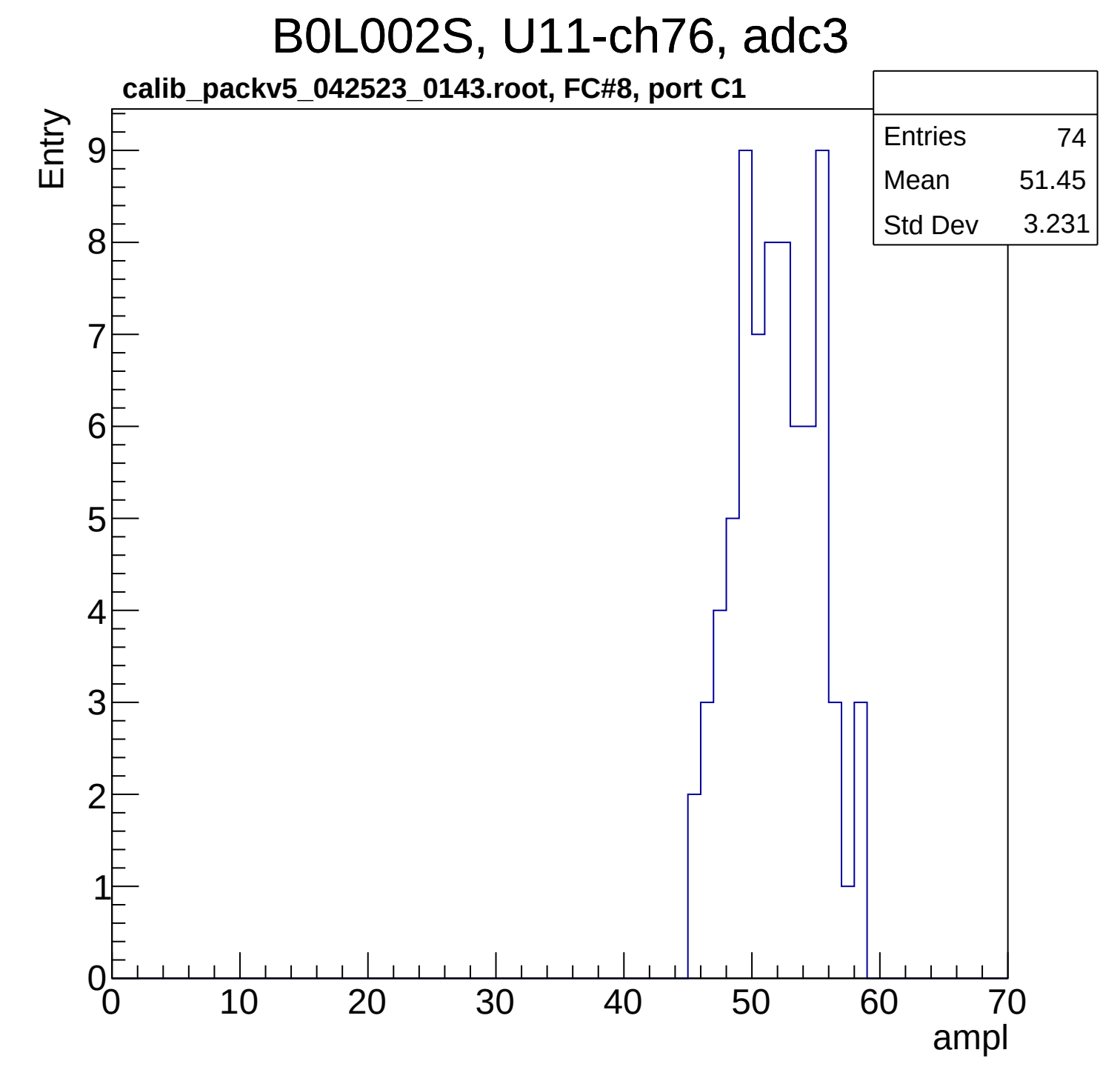

B0L002S, U11-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 45.4
Std Dev 3.33
Gaus mean : 44.7726
Gaus Width: 3.7152
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 51.45
Std Dev 3.231
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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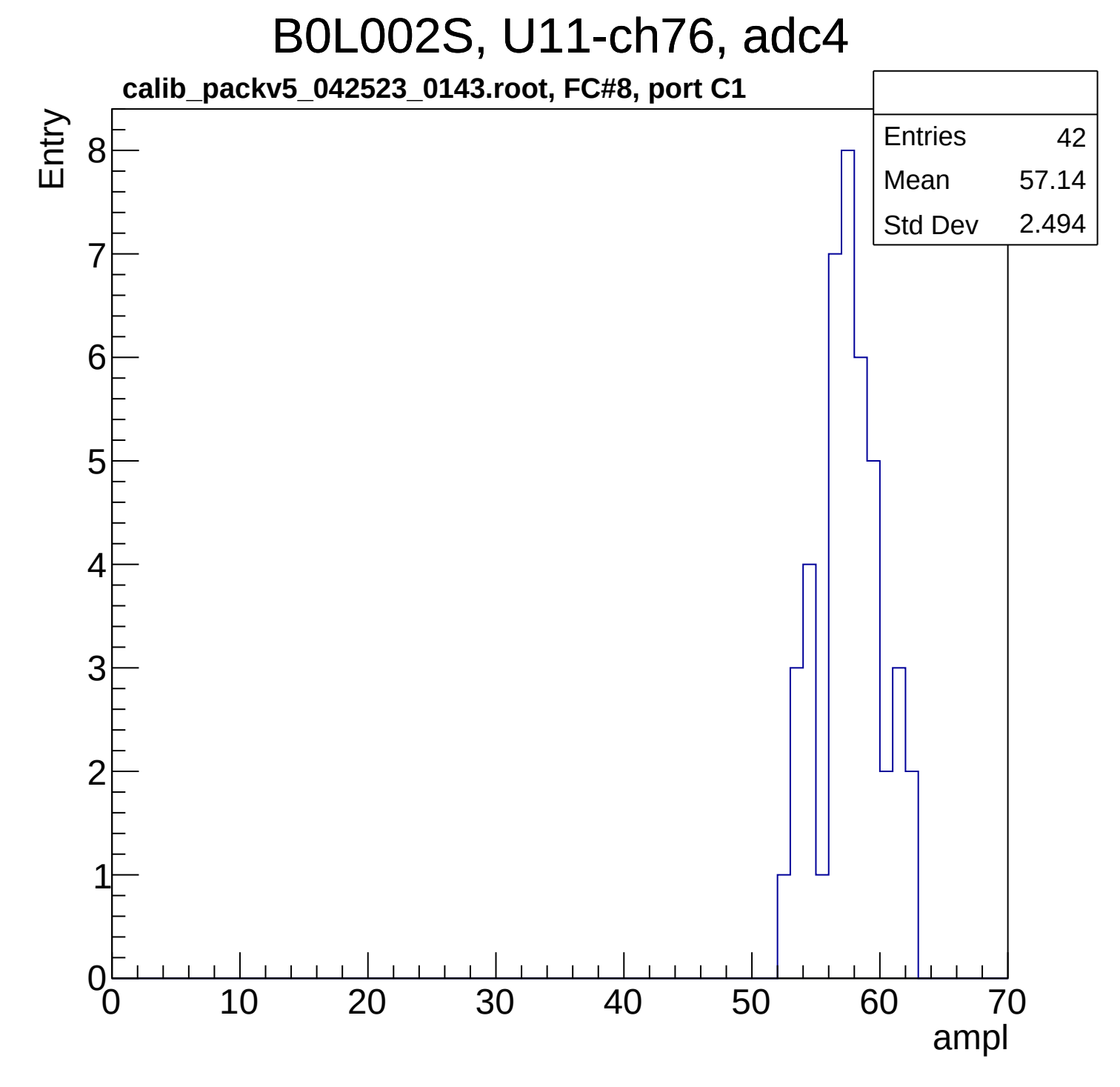
B0L002S, U11-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 57.14
Std Dev 2.494
Entry
ampl
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7
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0
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60
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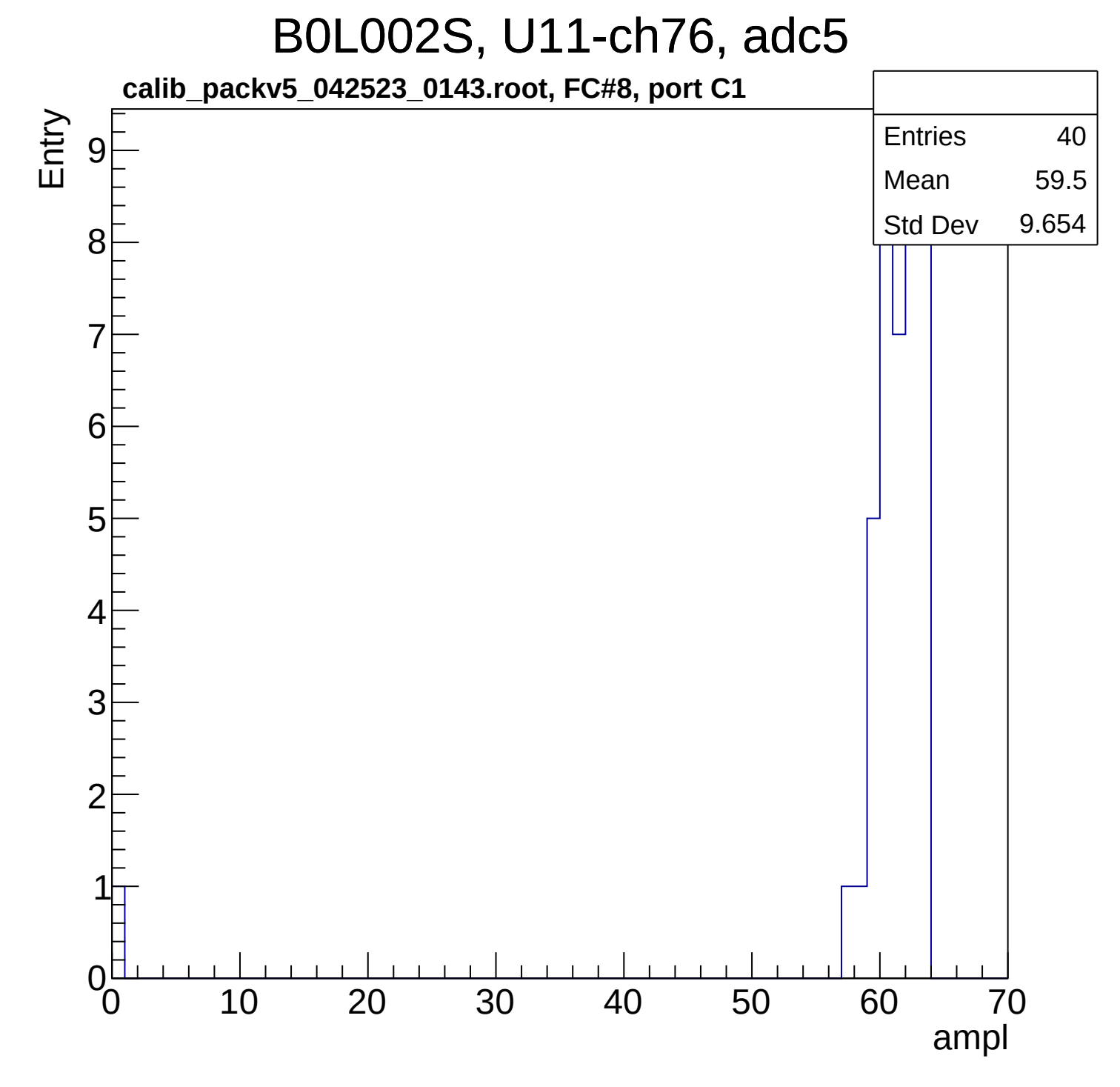
B0L002S, U11-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.5
Std Dev 9.654
Entry
ampl
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4
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6
7
8
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0
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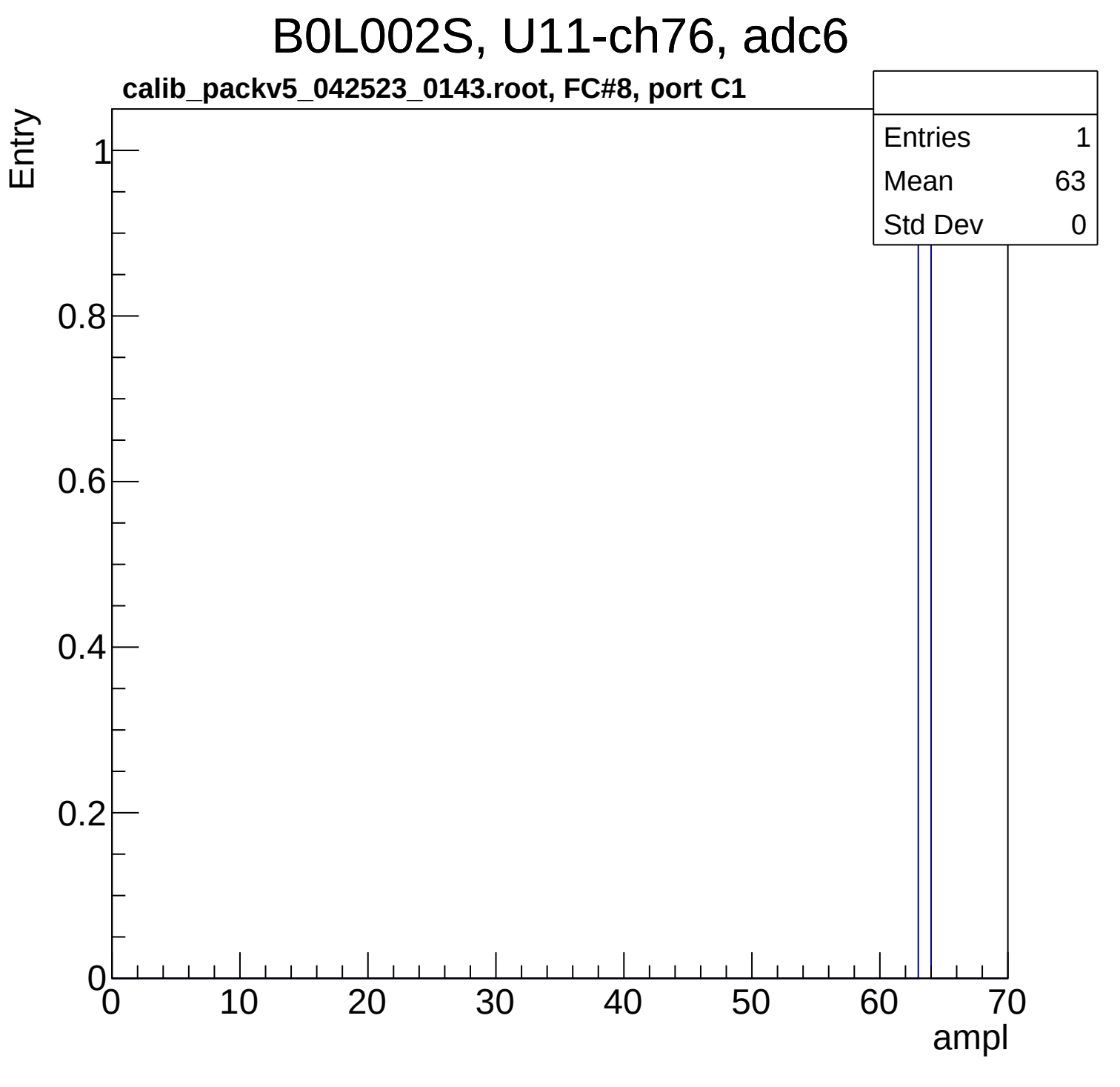
B0L002S, U11-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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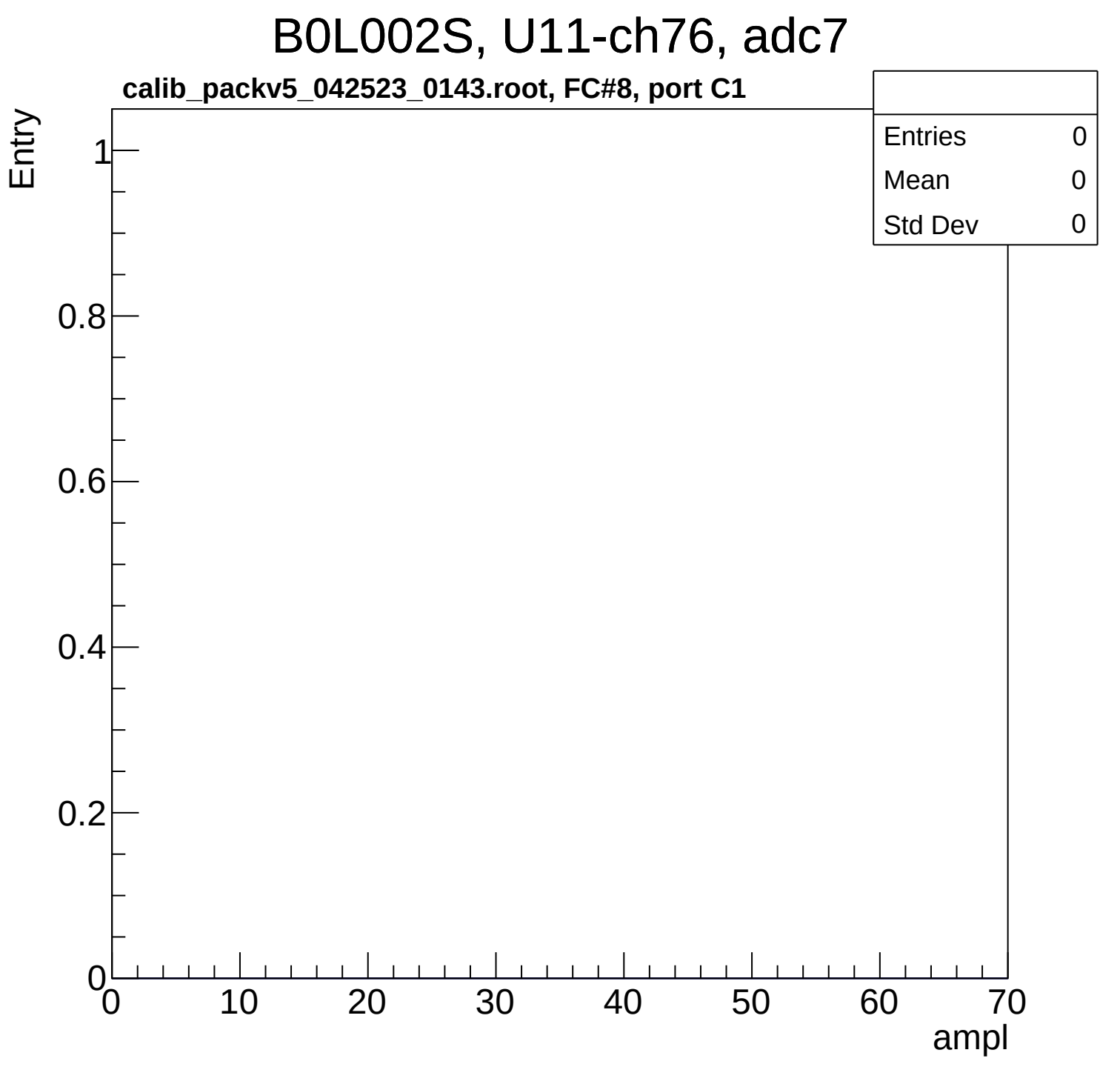
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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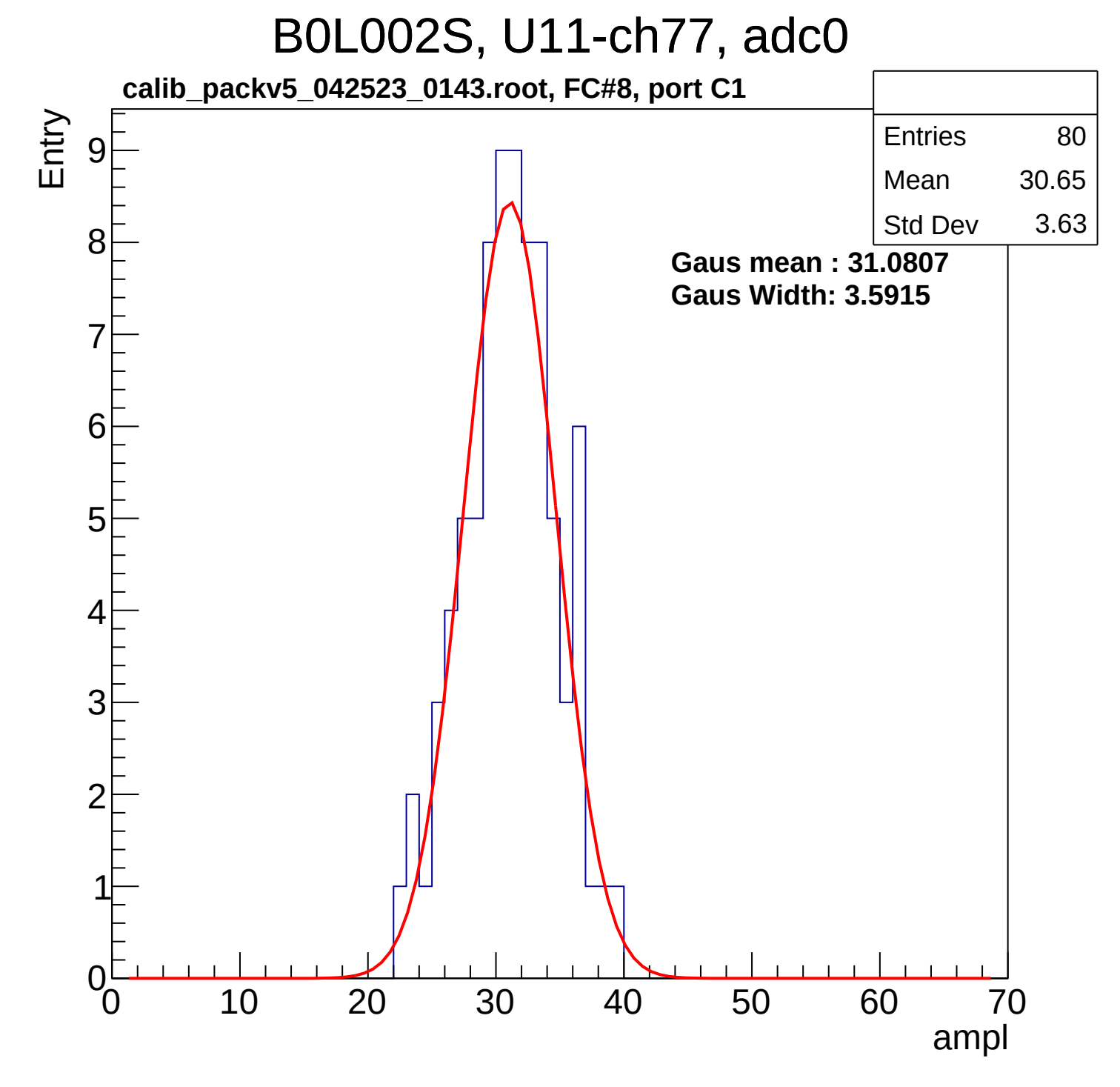
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.65
Std Dev 3.63
Gaus mean : 31.0807
Gaus Width: 3.5915
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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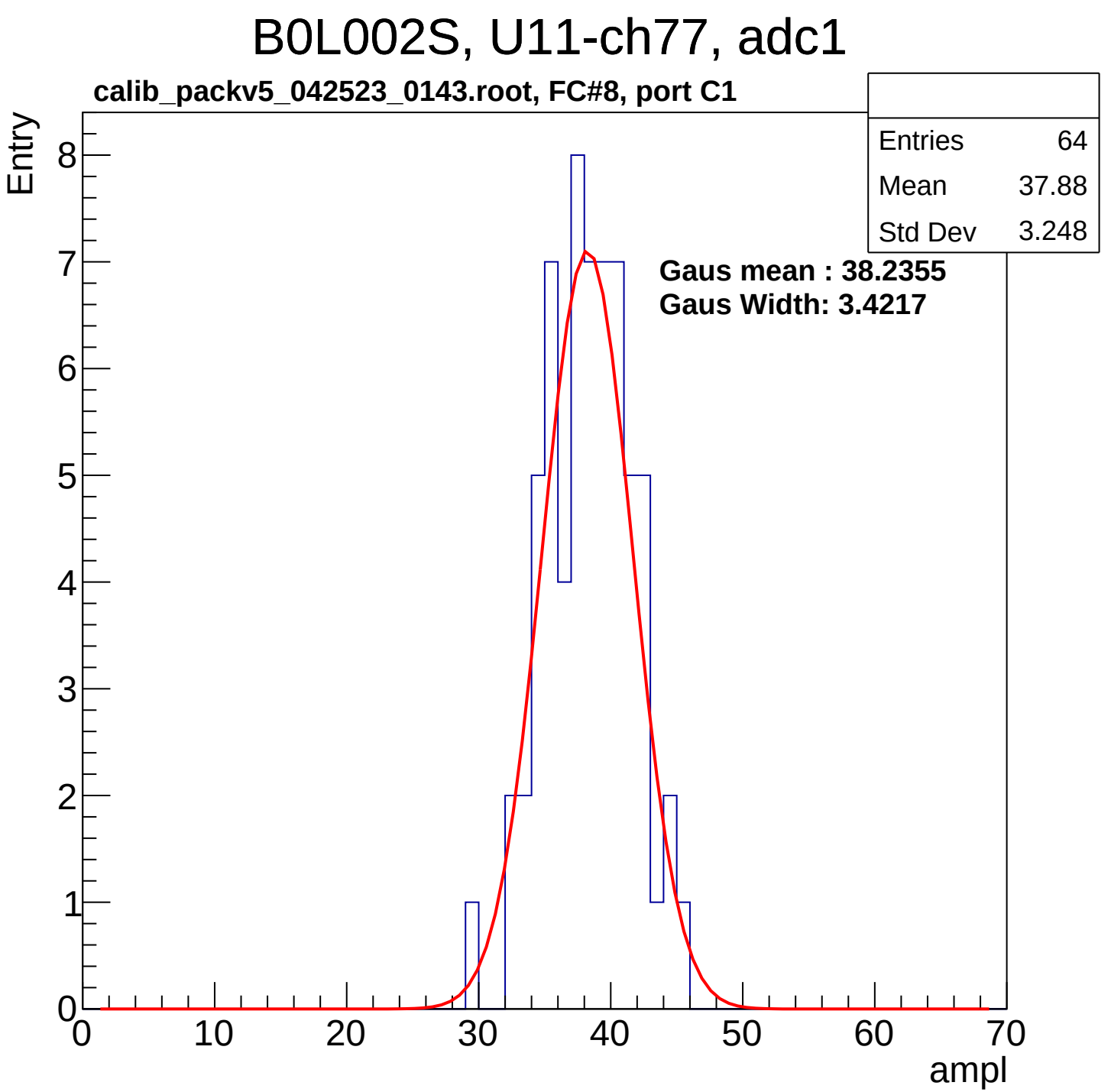
40
50
60
70


B0L002S, U11-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.88
Std Dev 3.248
Gaus mean : 38.2355
Gaus Width: 3.4217
Entry
ampl
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2
3
4
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6
7
8
0
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60
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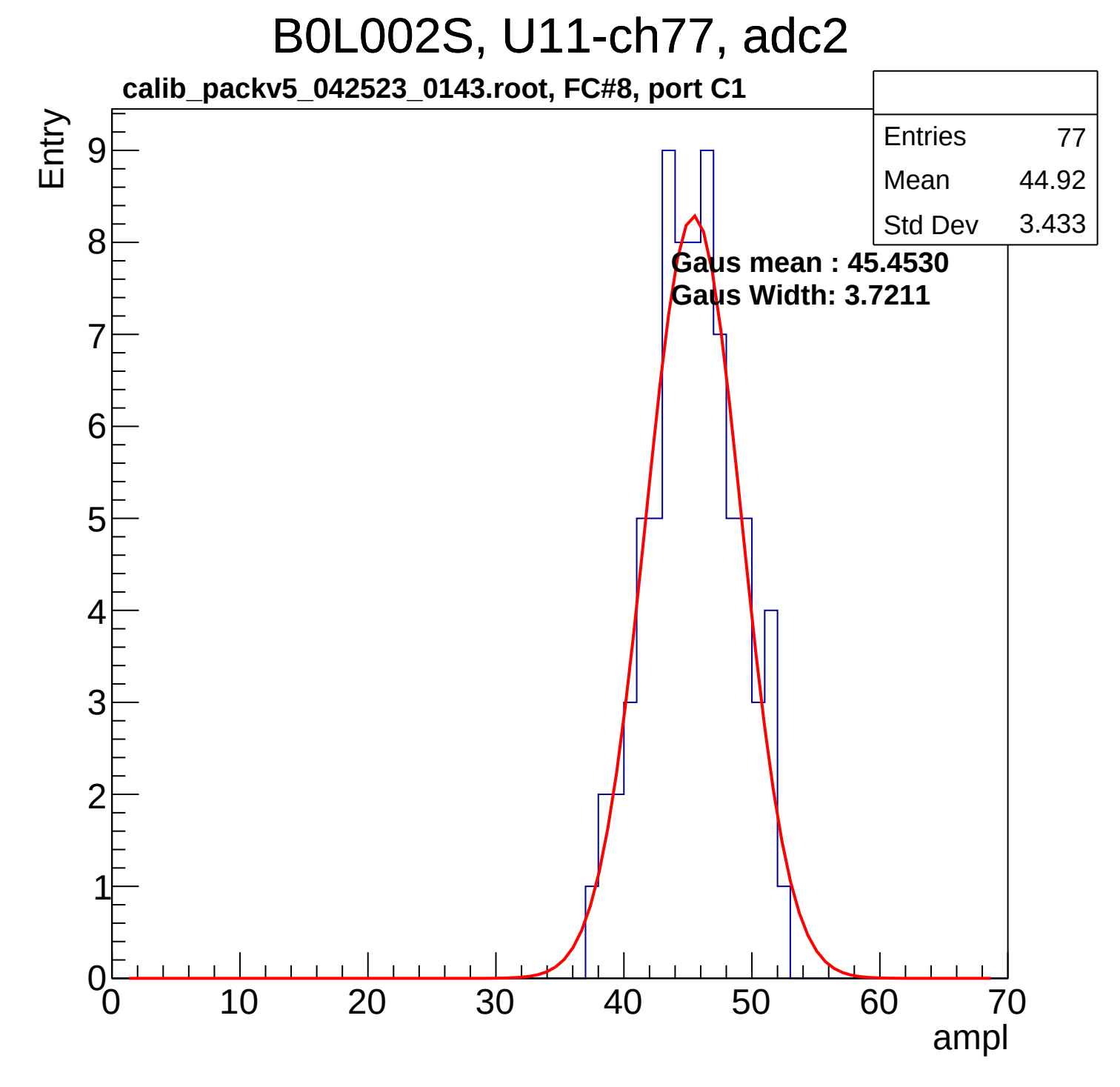
B0L002S, U11-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 44.92
Std Dev 3.433
Gaus mean : 45.4530
Gaus Width: 3.7211
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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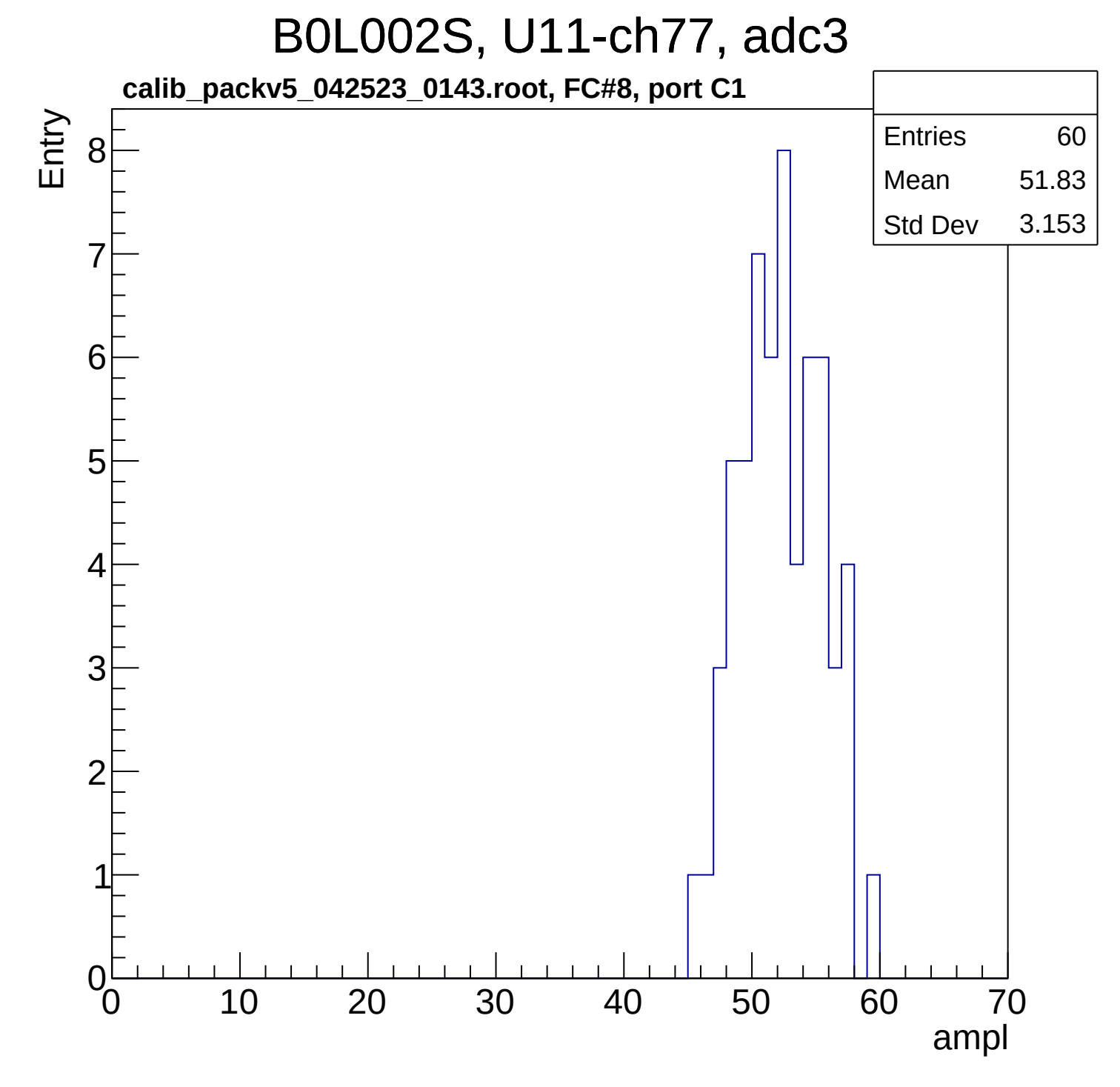
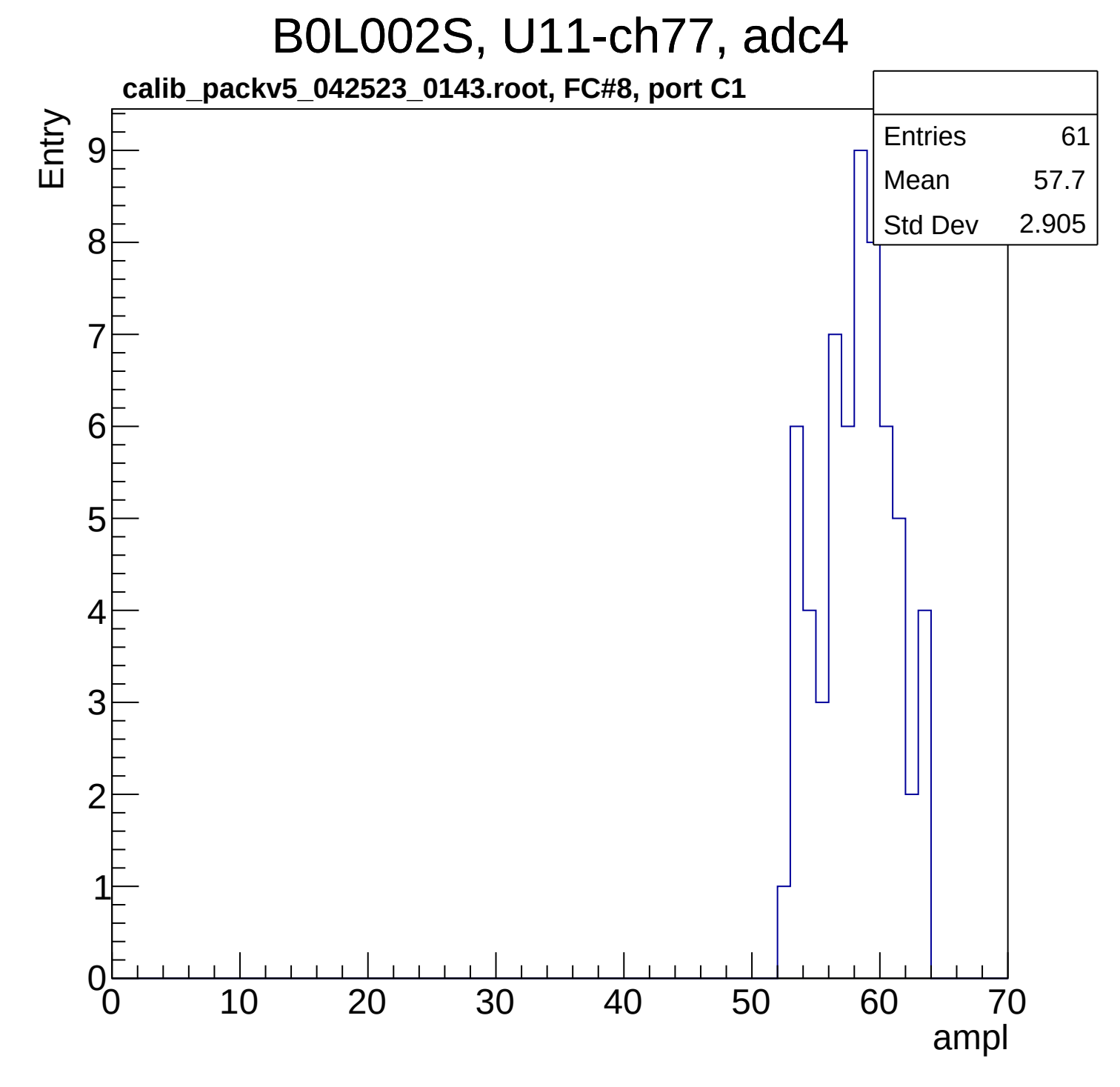

B0L002S, U11-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.83
Std Dev 3.153
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.7
Std Dev 2.905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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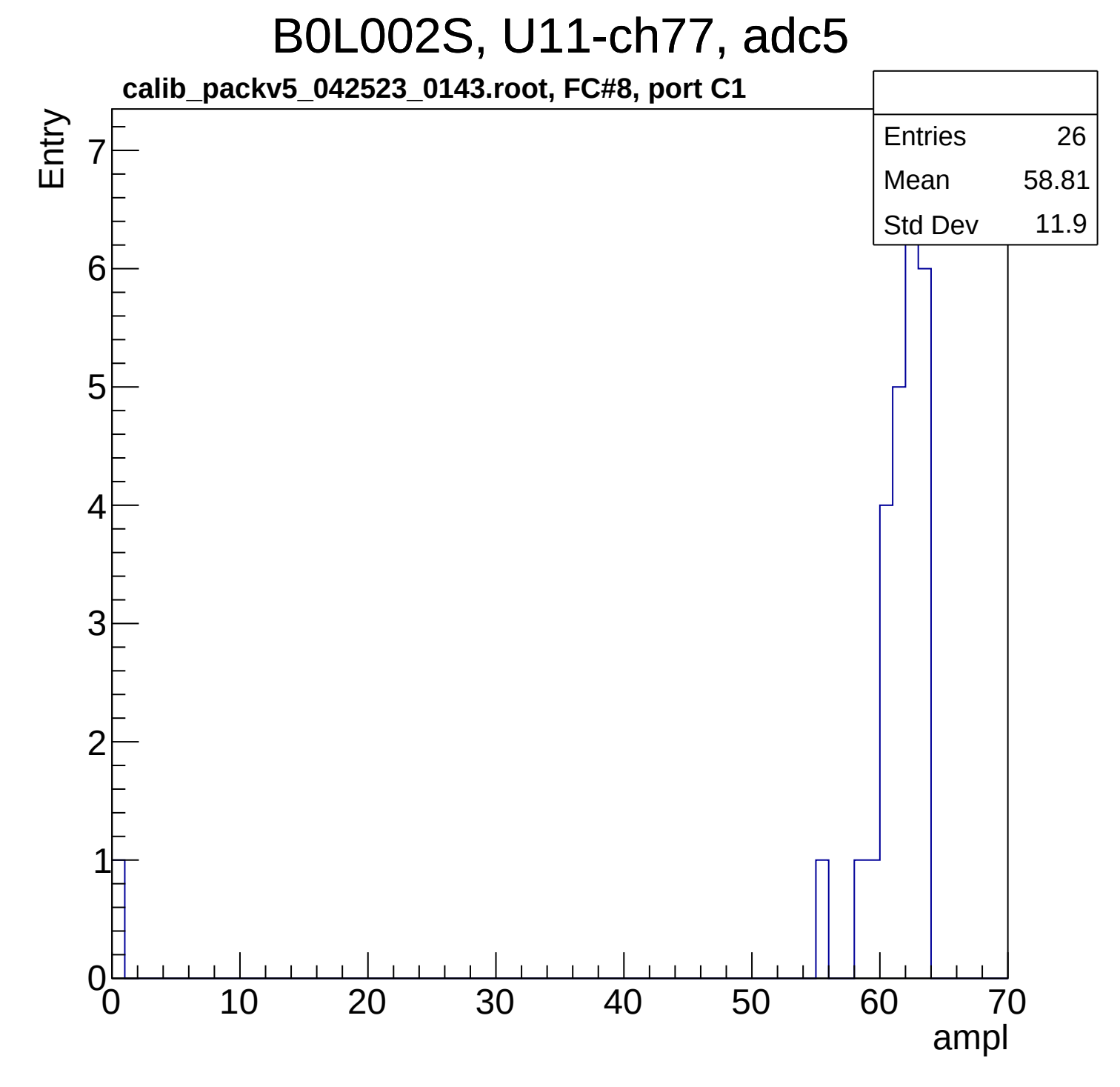
60
70


B0L002S, U11-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.81
Std Dev 11.9
Entry
ampl
0
1
2
3
4
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6
7
0
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40
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60
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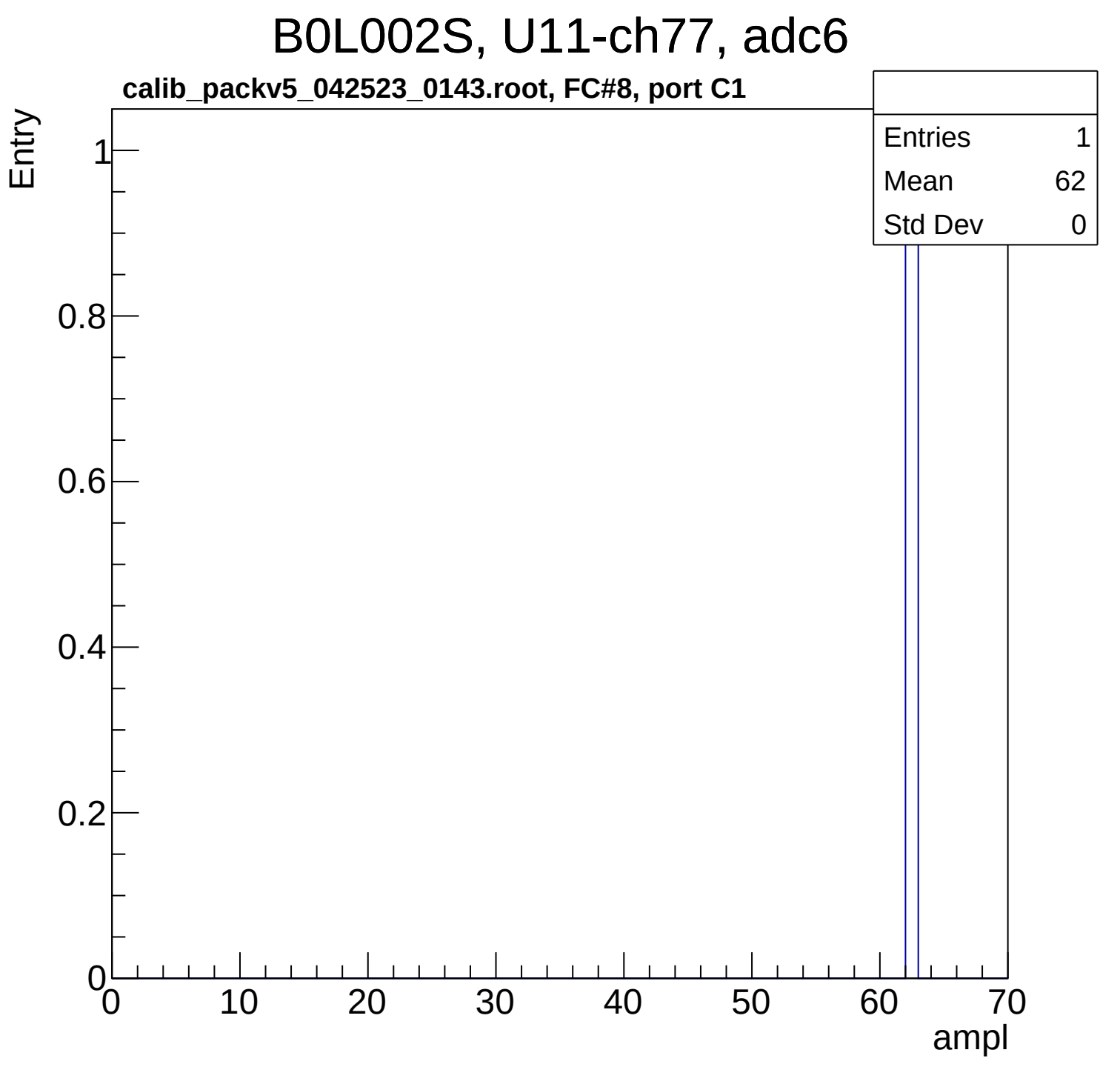
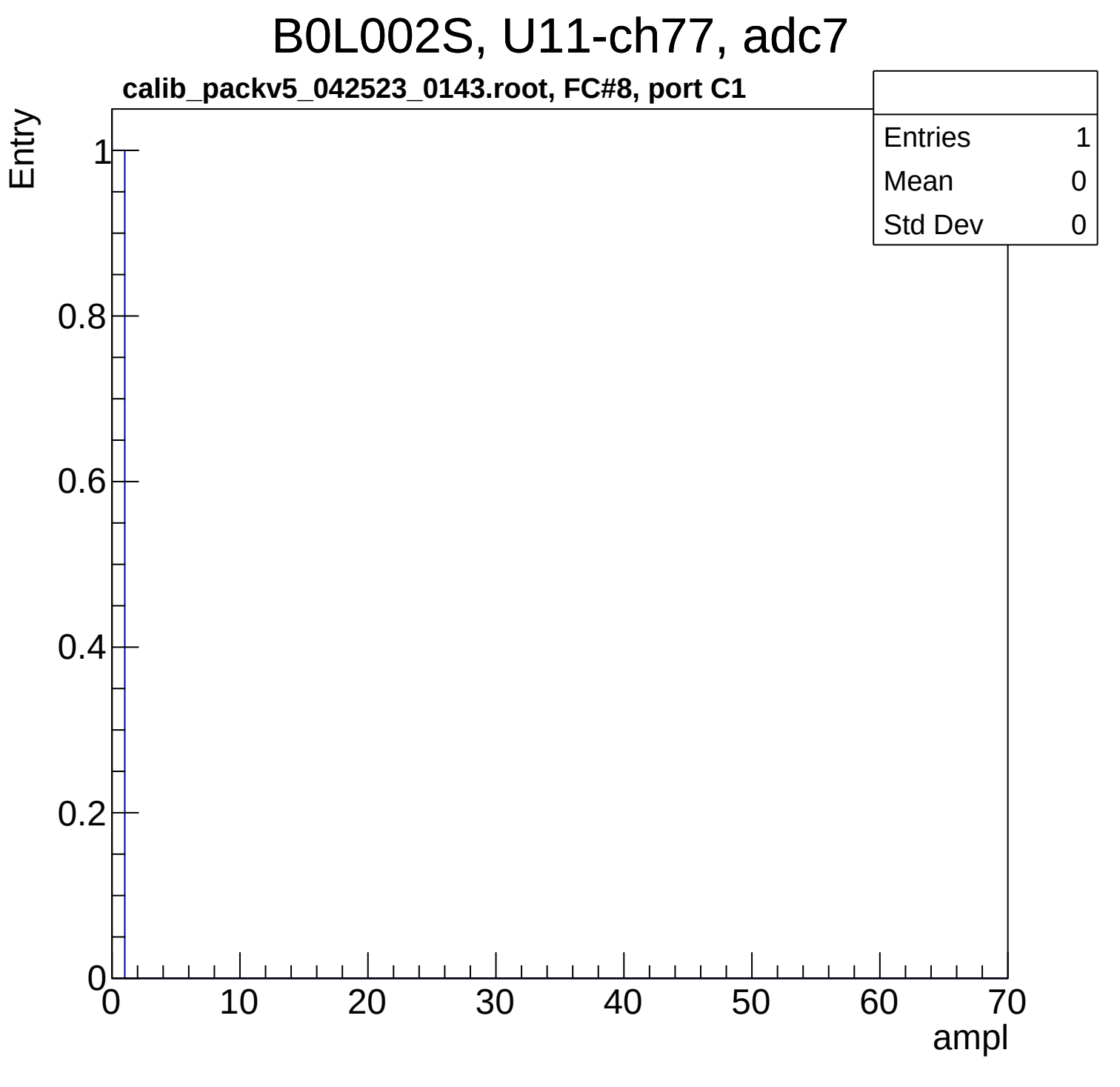

B0L002S, U11-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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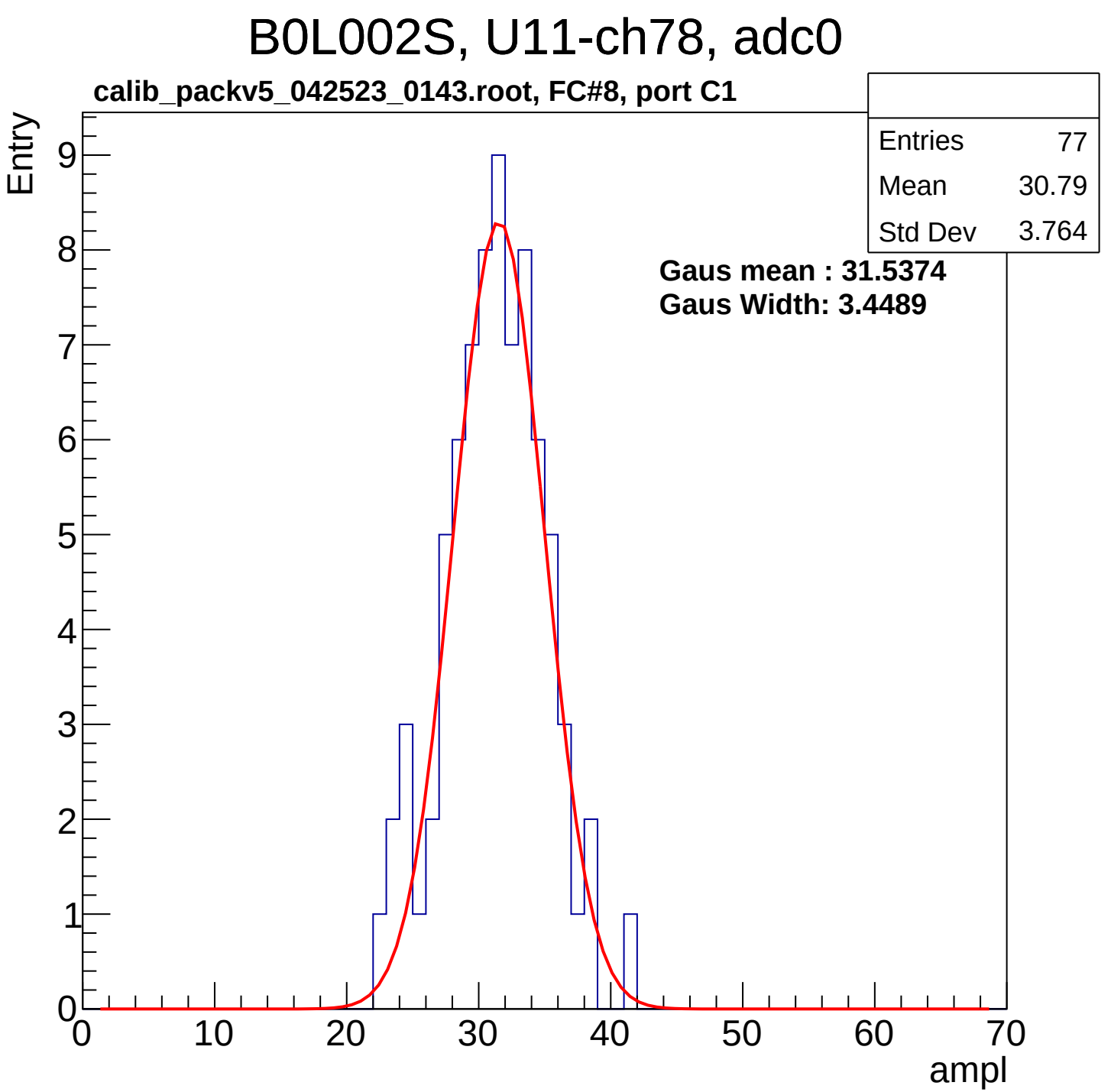
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.79
Std Dev 3.764
Gaus mean : 31.5374
Gaus Width: 3.4489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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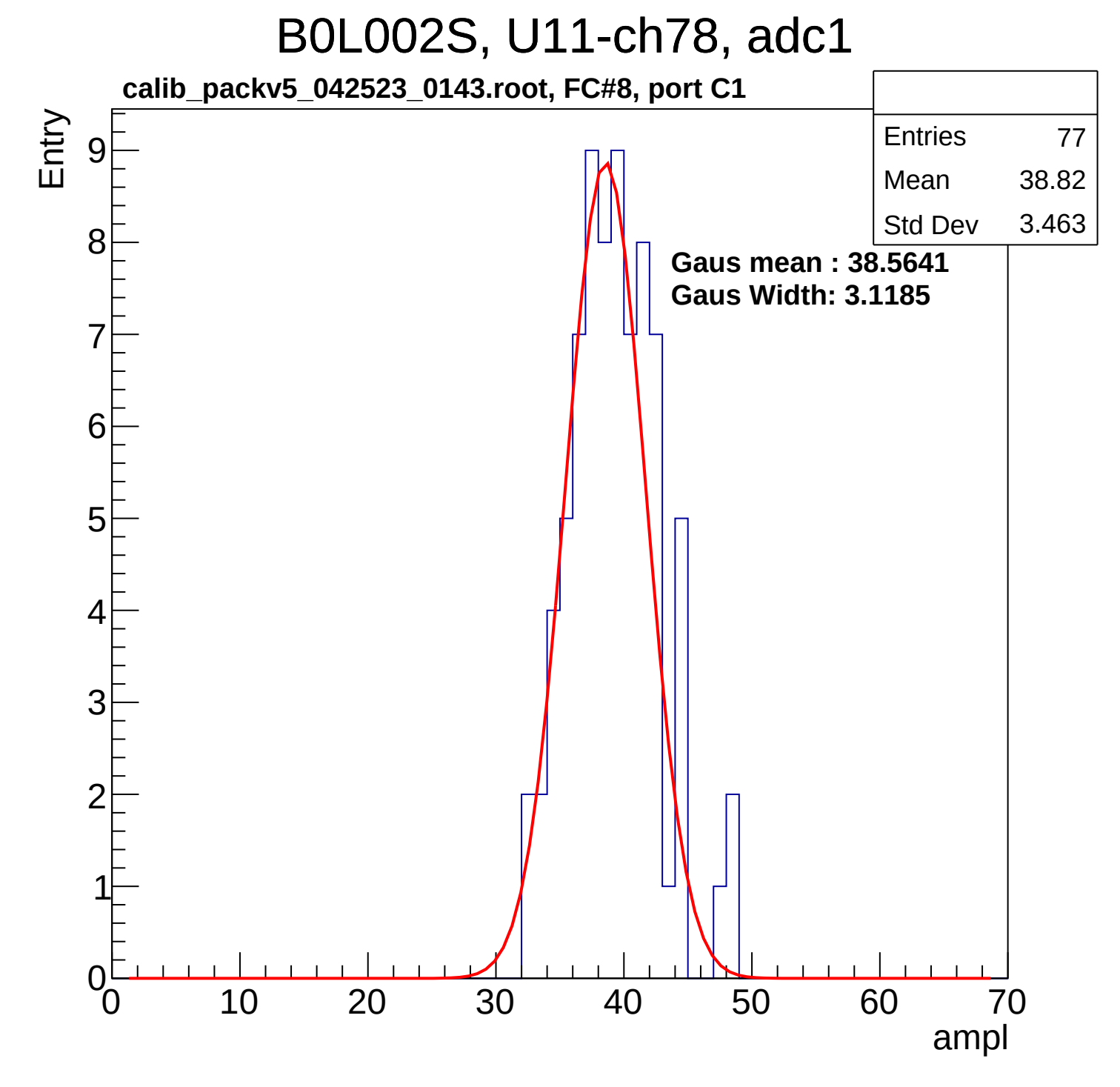
B0L002S, U11-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.82
Std Dev 3.463
Gaus mean : 38.5641
Gaus Width: 3.1185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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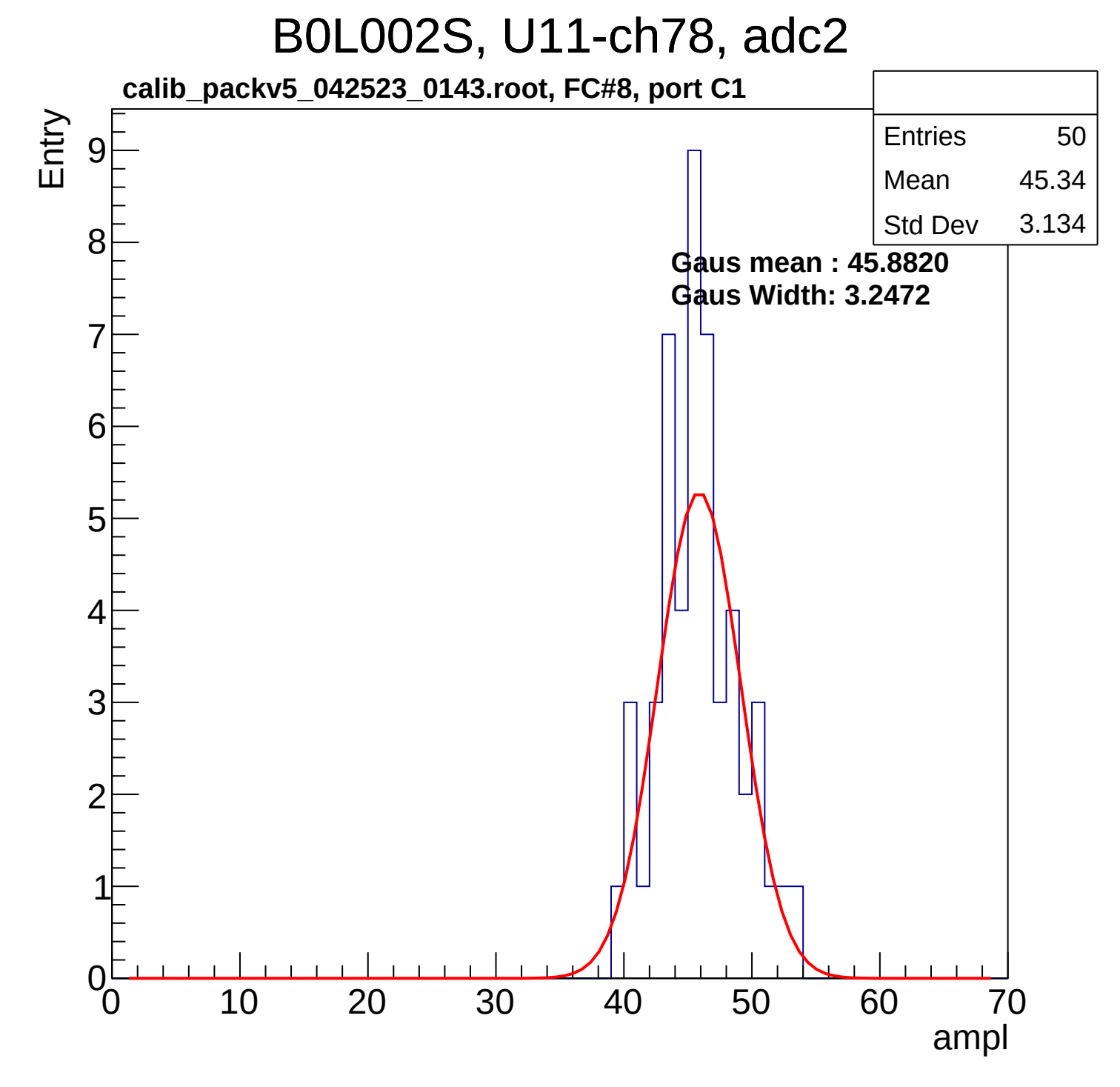
B0L002S, U11-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 45.34
Std Dev 3.134
Gaus mean : 45.8820
Gaus Width: 3.2472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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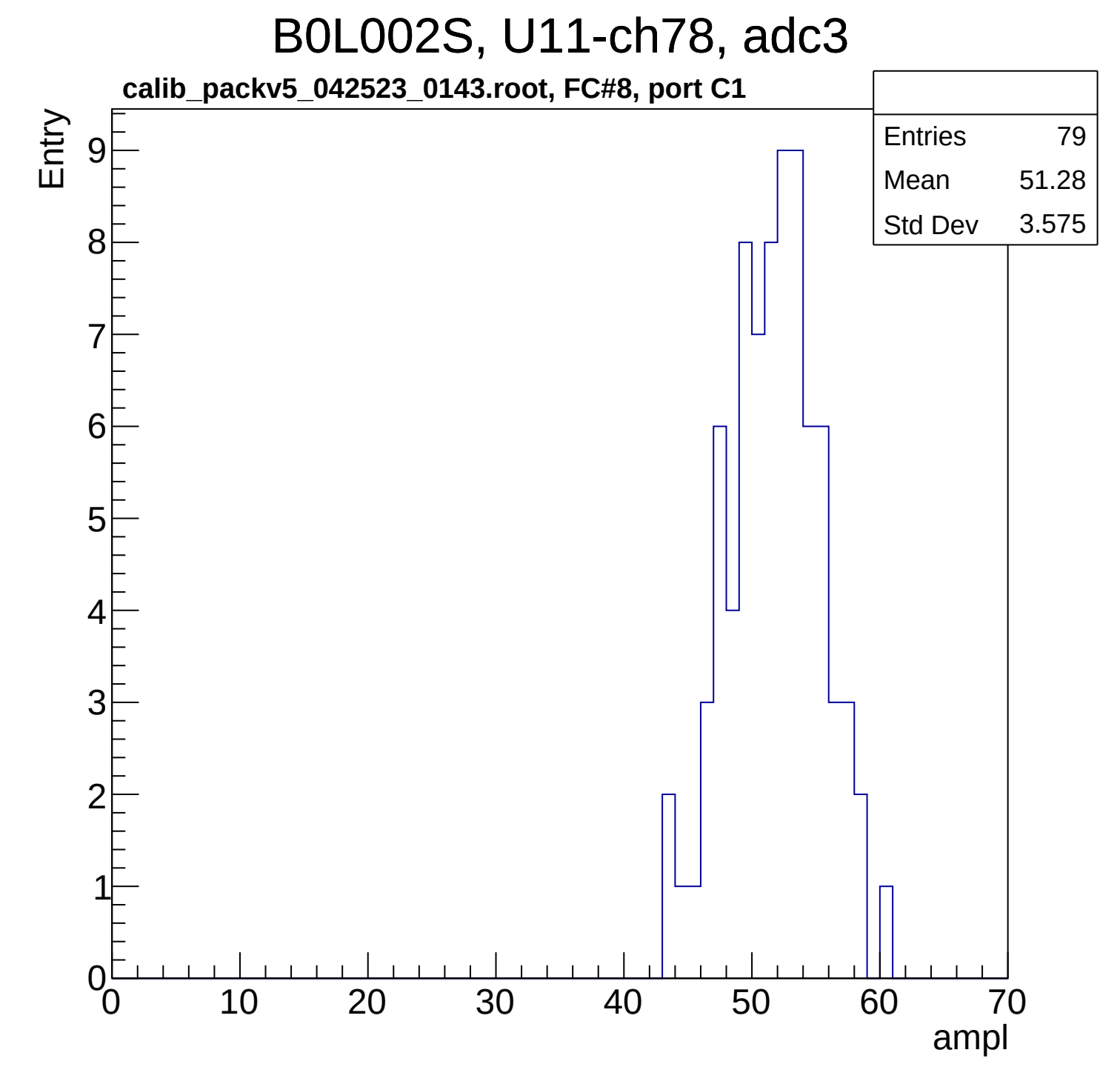
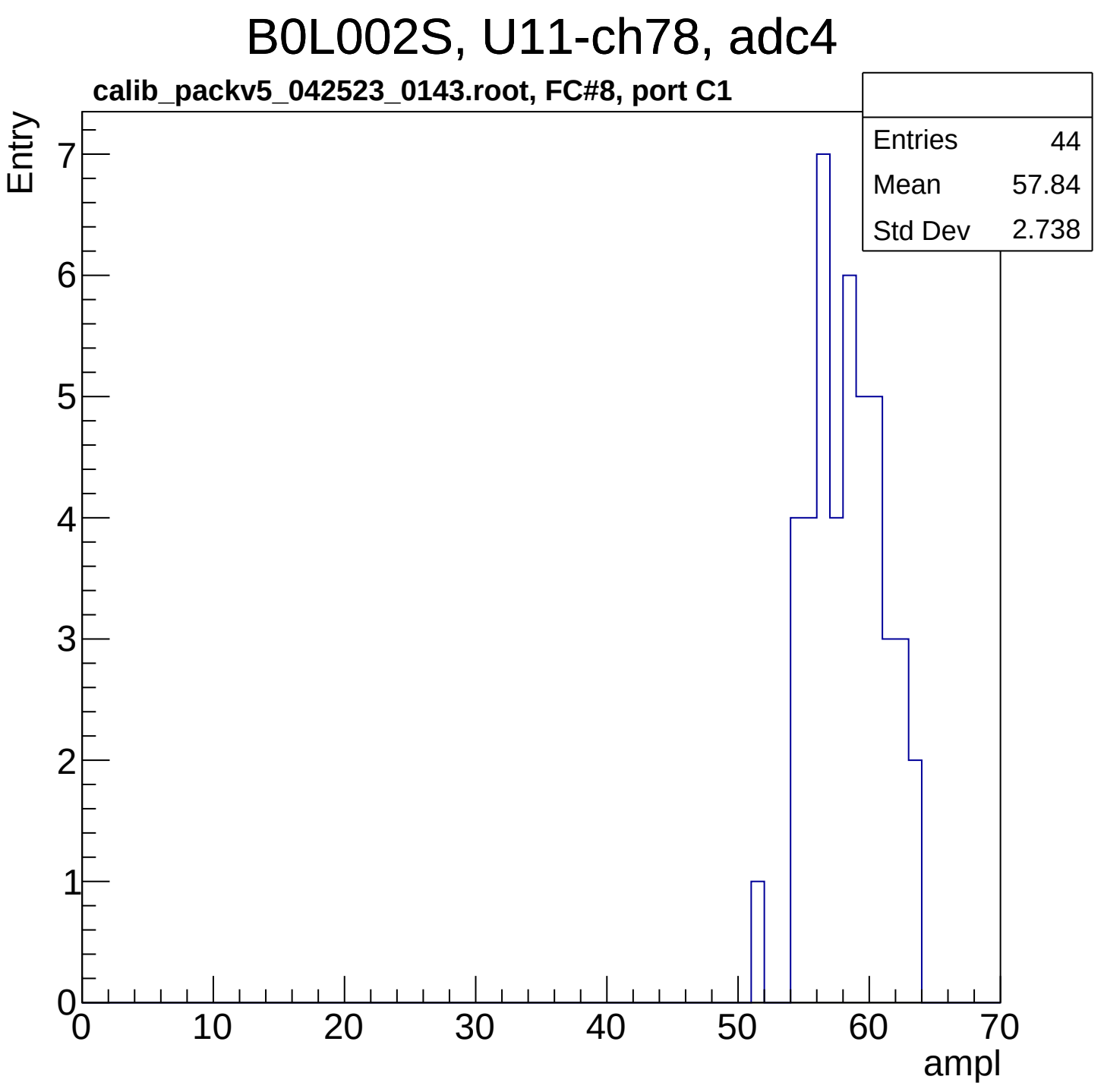
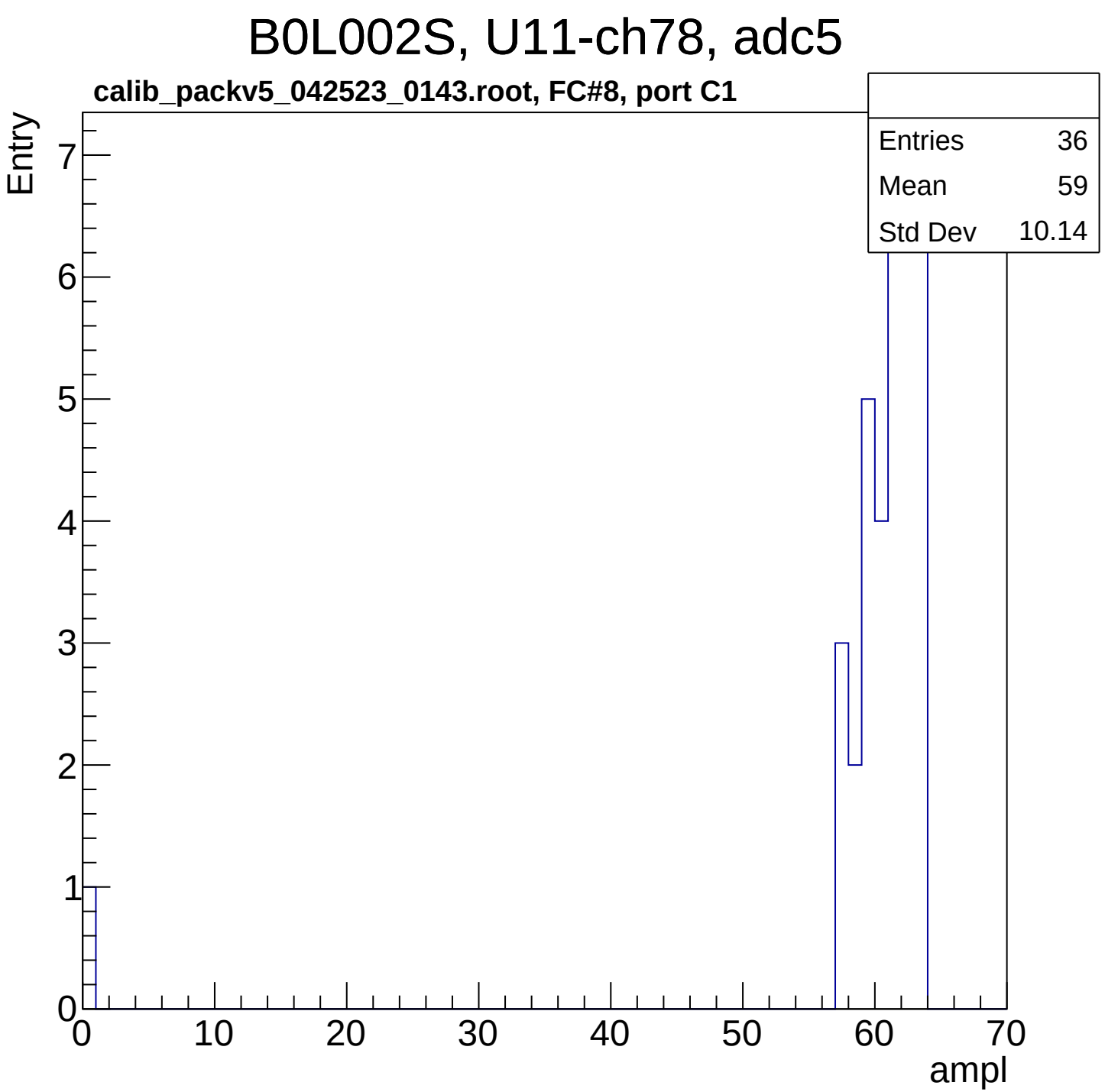
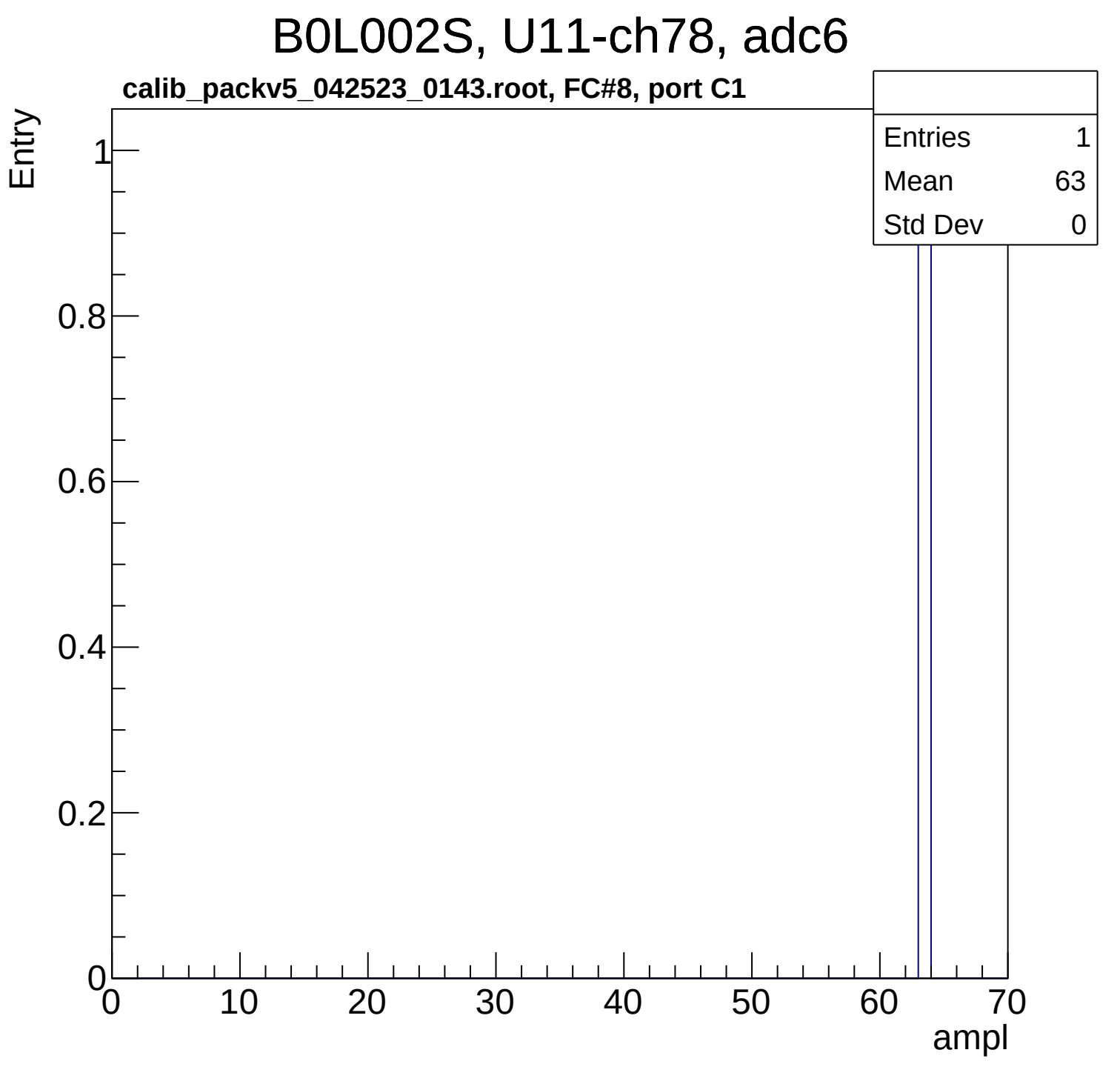
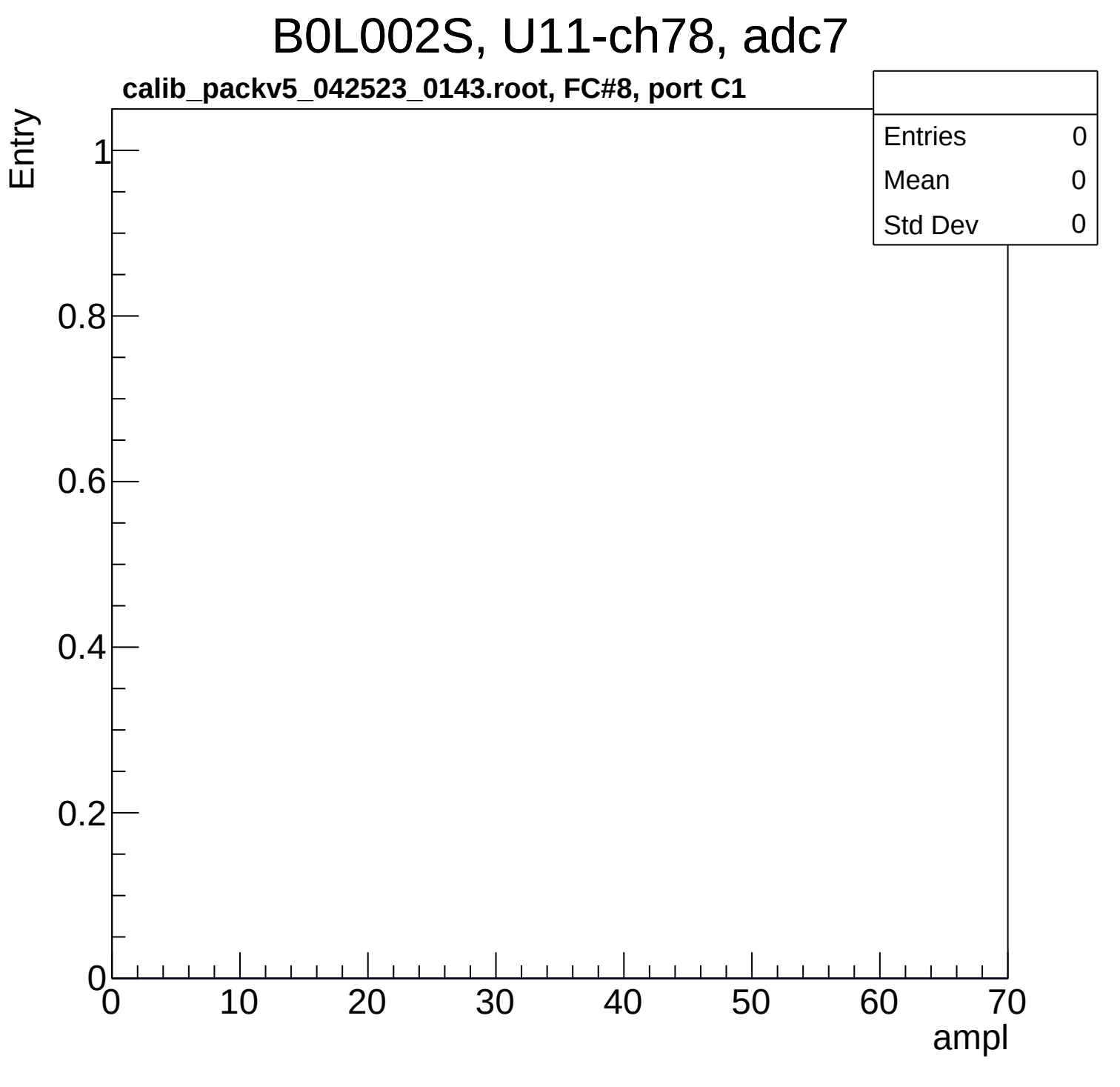
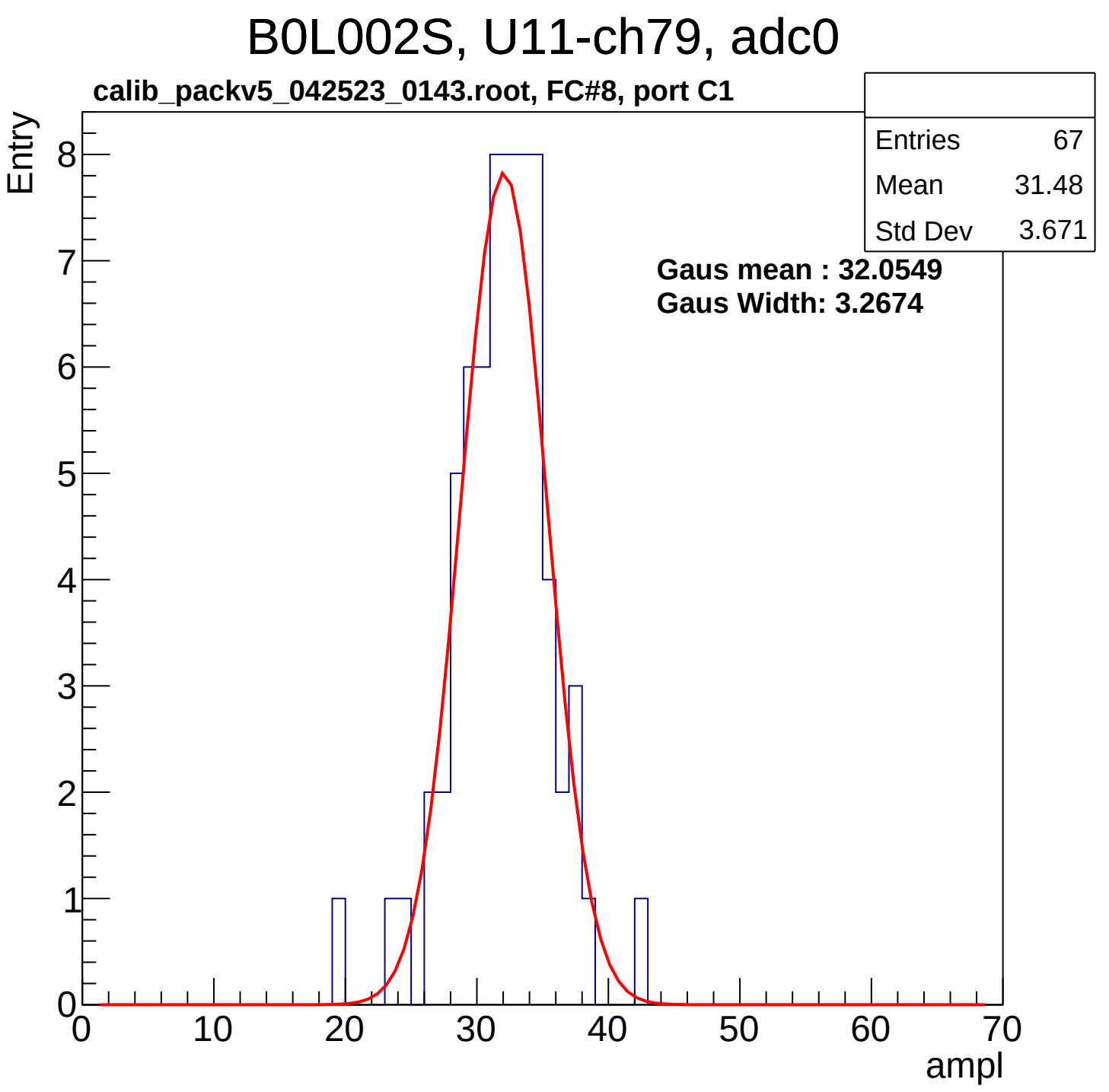

B0L002S, U11-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 51.28
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

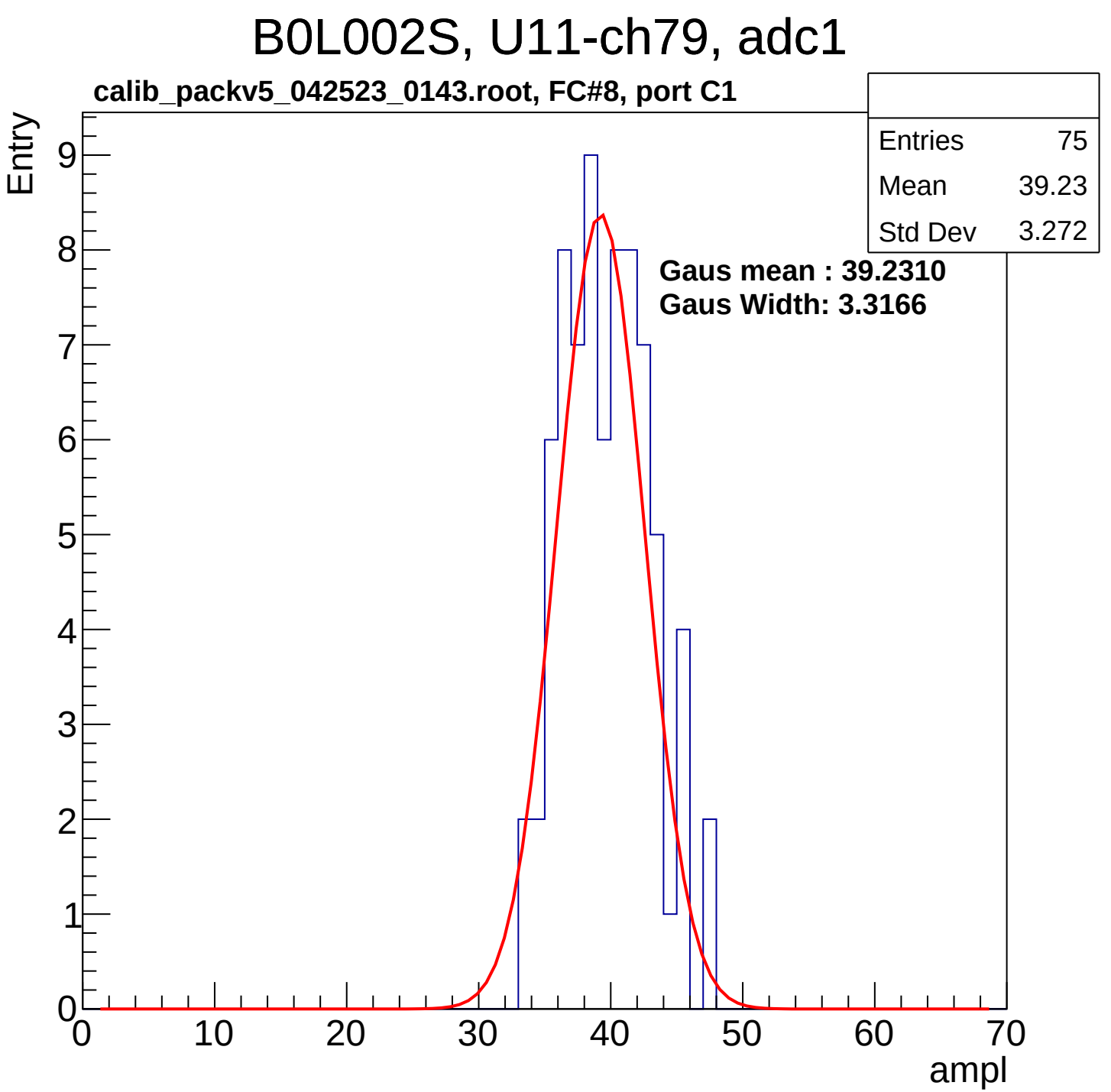

B0L002S, U11-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 57.84
Std Dev 2.738
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

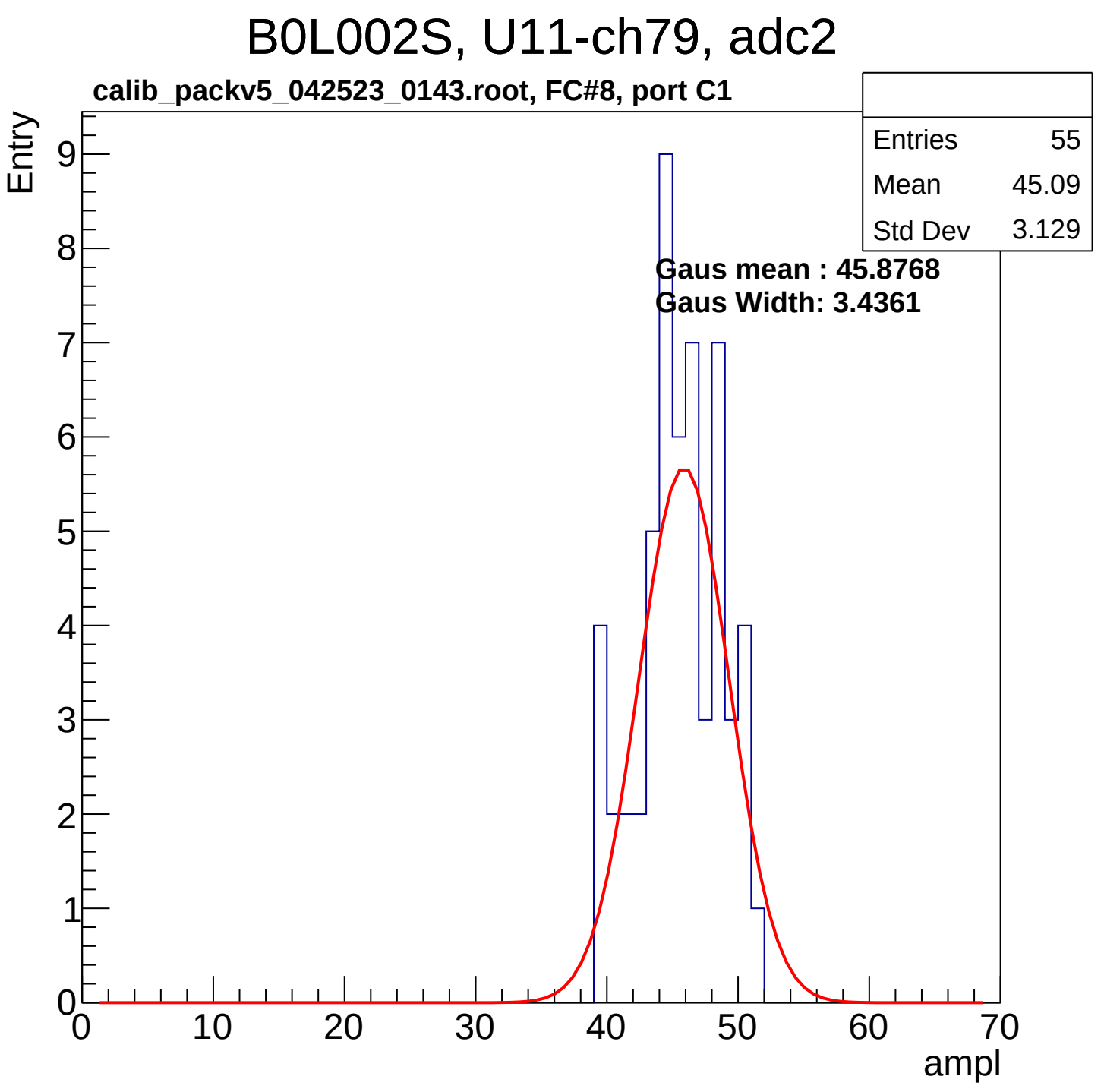

B0L002S, U11-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

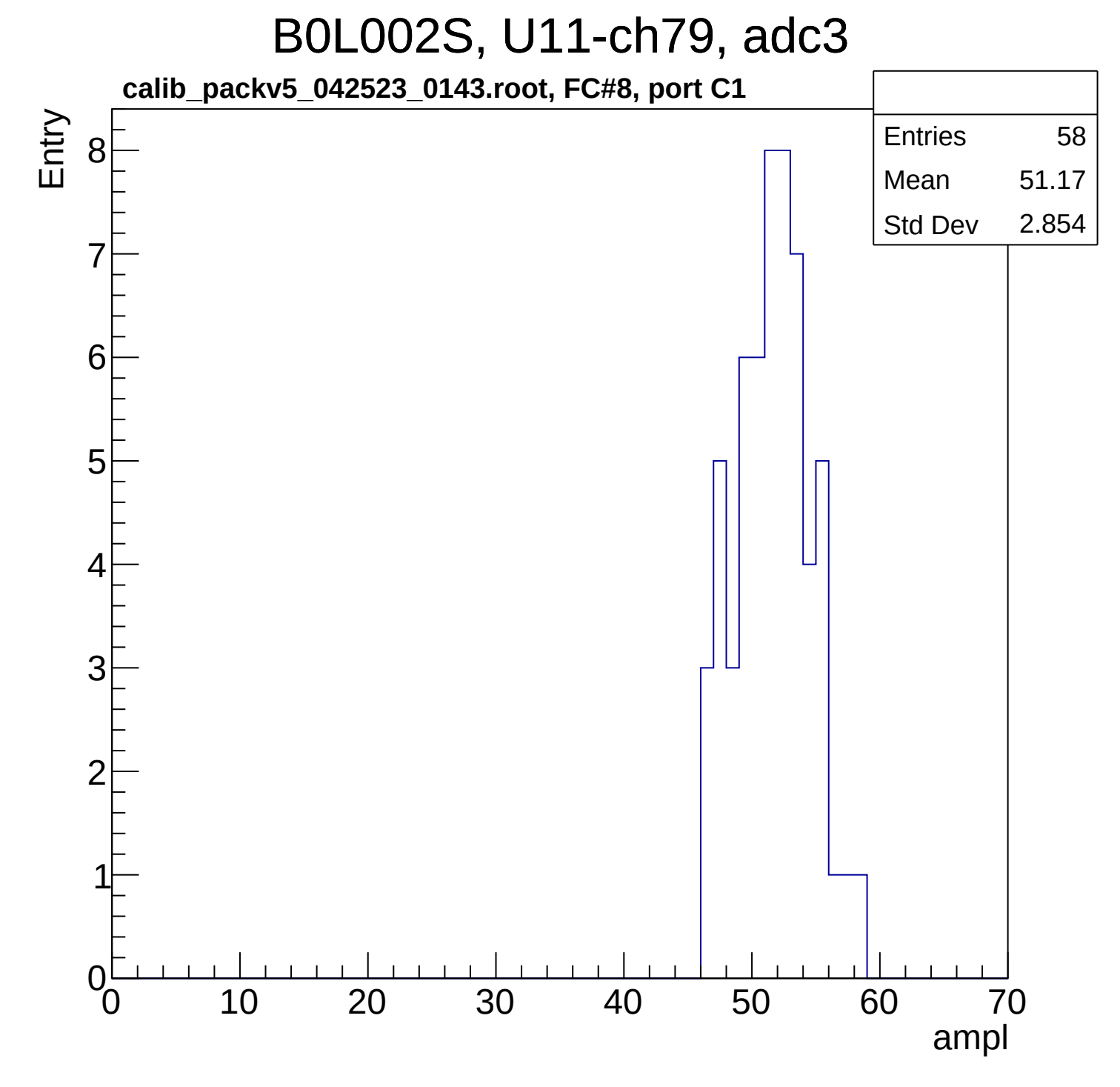

B0L002S, U11-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.48
Std Dev 3.671
Gaus mean : 32.0549
Gaus Width: 3.2674
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 39.23
Std Dev 3.272
Gaus mean : 39.2310
Gaus Width: 3.3166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.09
Std Dev 3.129
Gaus mean : 45.8768
Gaus Width: 3.4361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.17
Std Dev 2.854
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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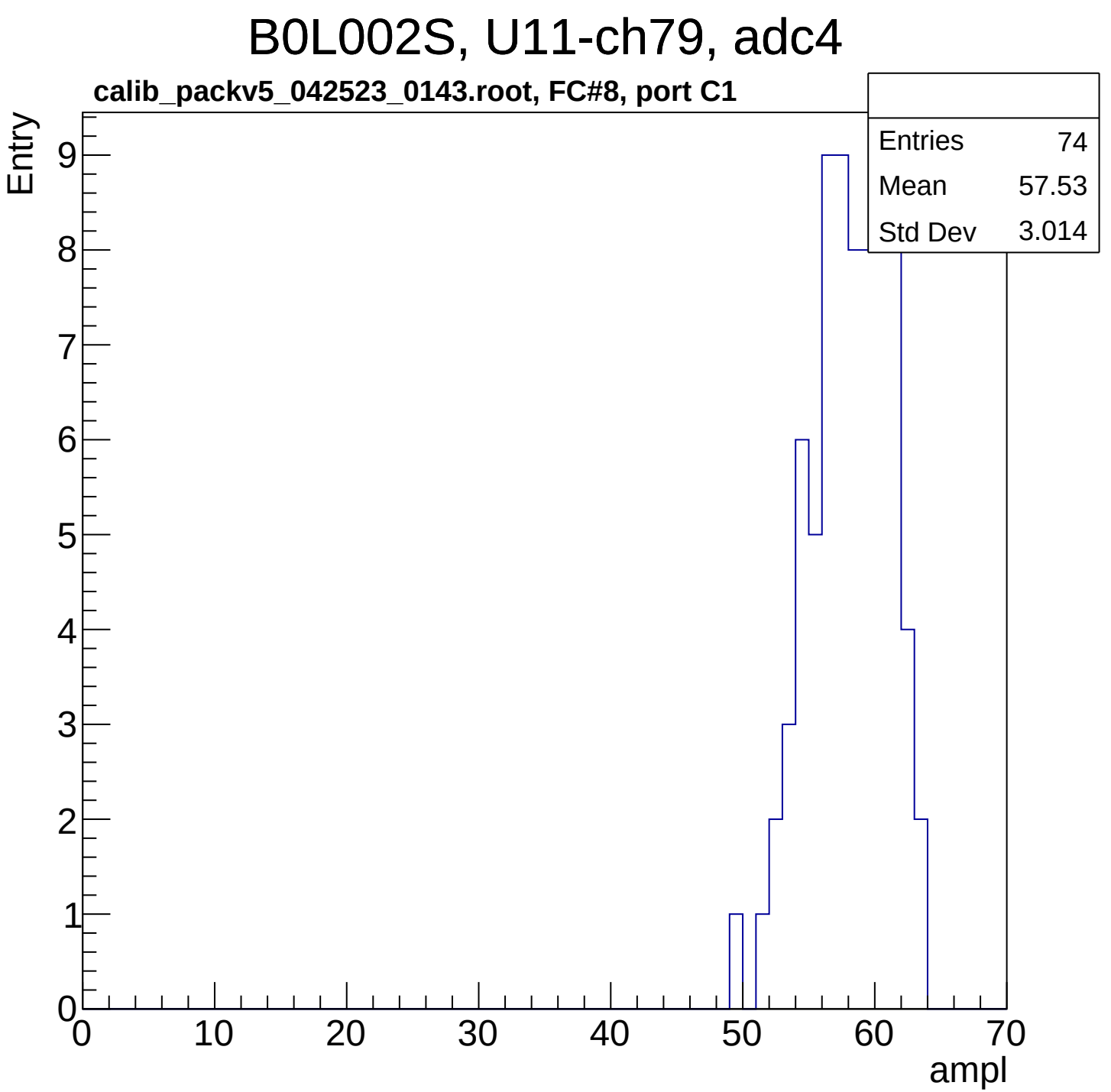
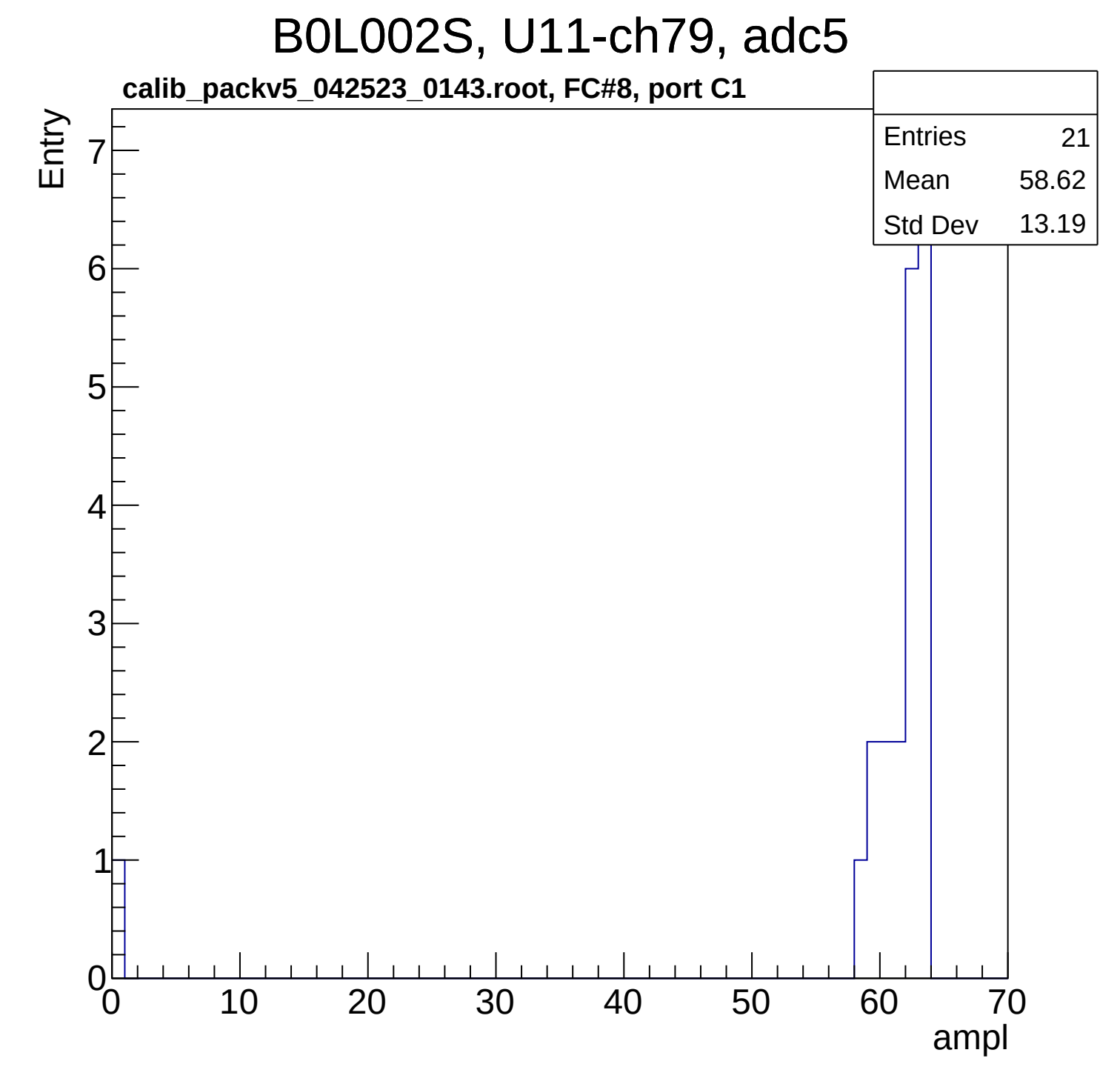

B0L002S, U11-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 57.53
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.62
Std Dev 13.19
Entry
ampl
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1
2
3
4
5
6
7
0
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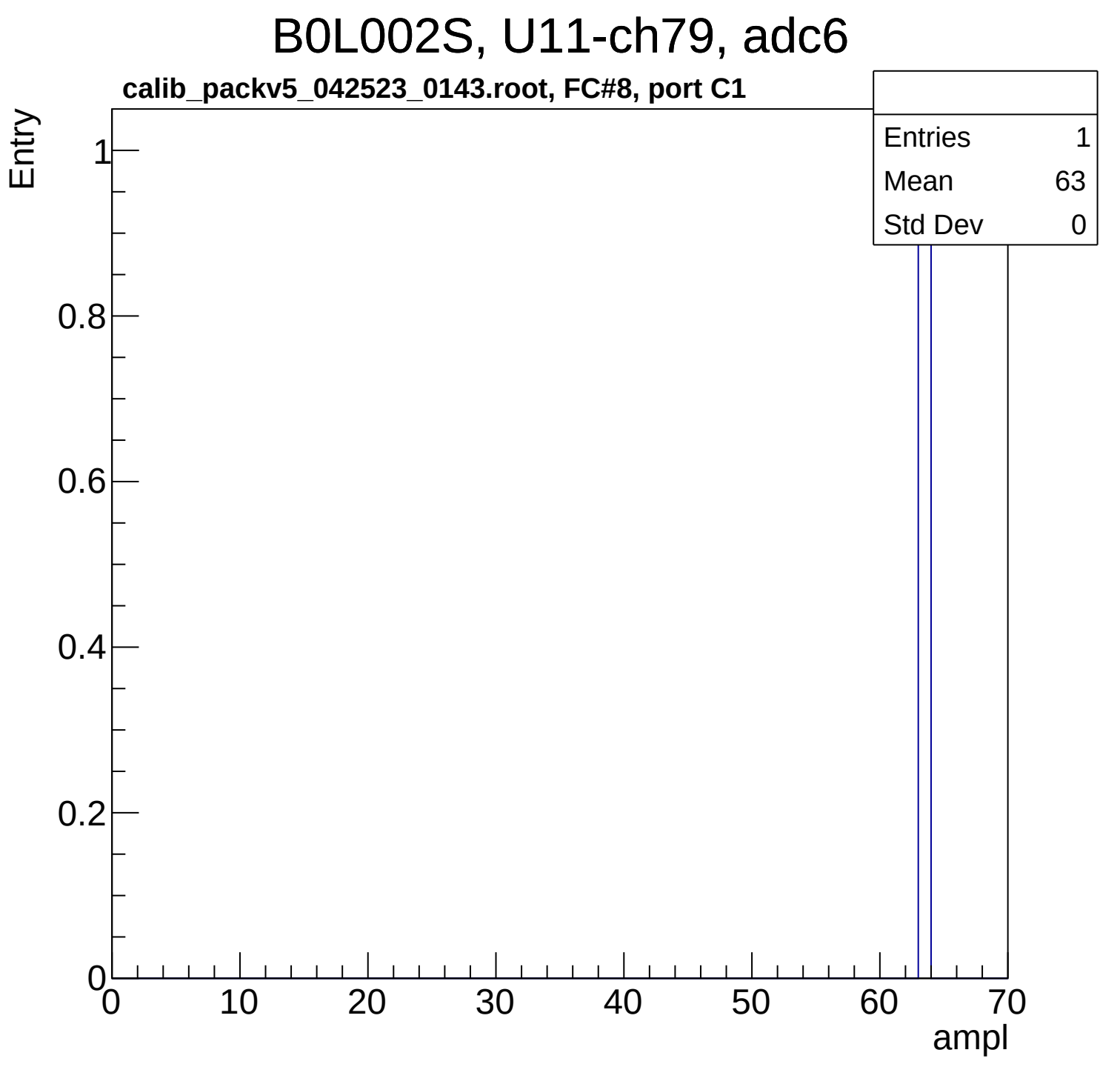
20
30
40
50
60
70


B0L002S, U11-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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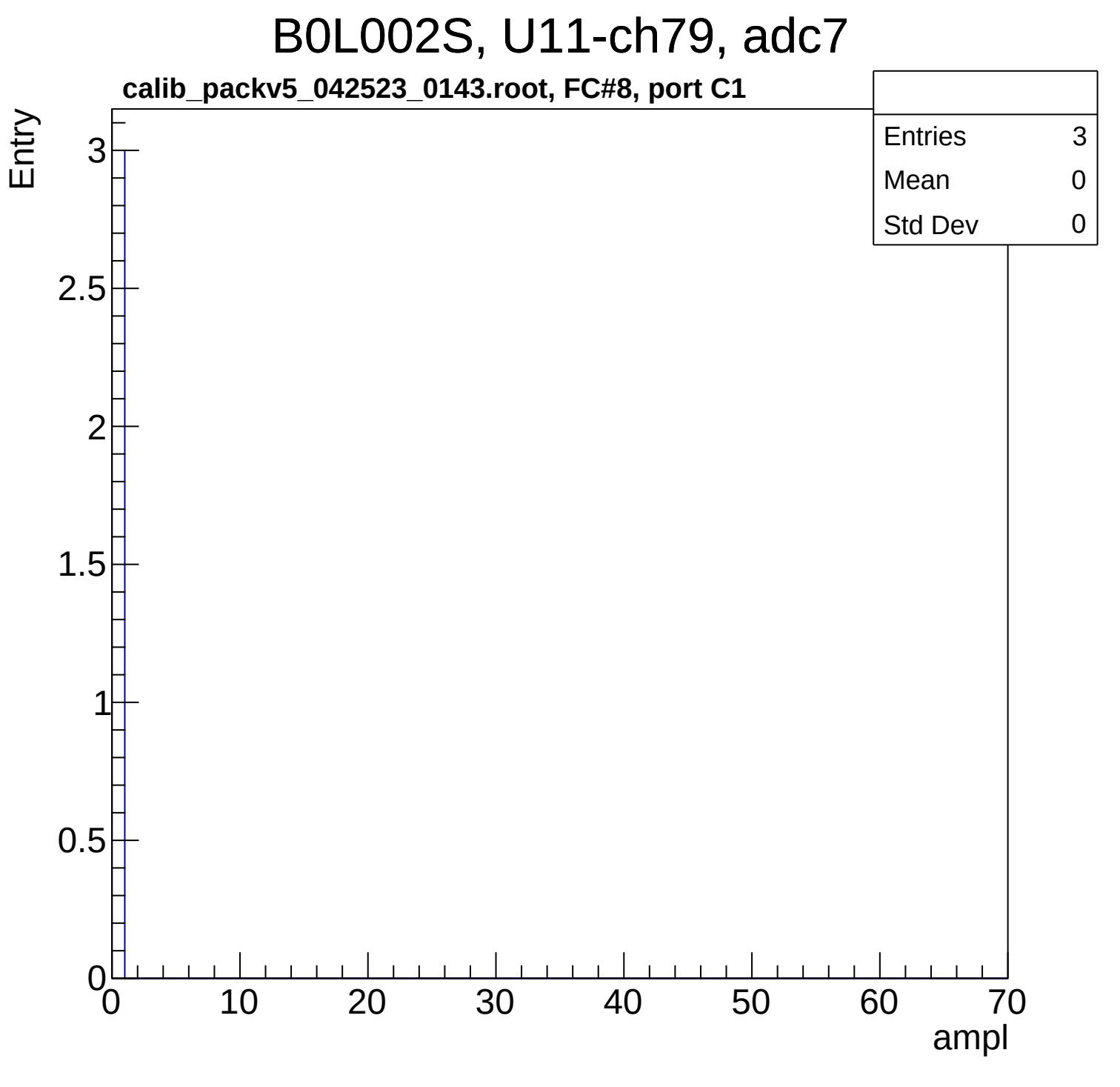
50
60
70
ampl


B0L002S, U11-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
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1
1.5
2
2.5
3
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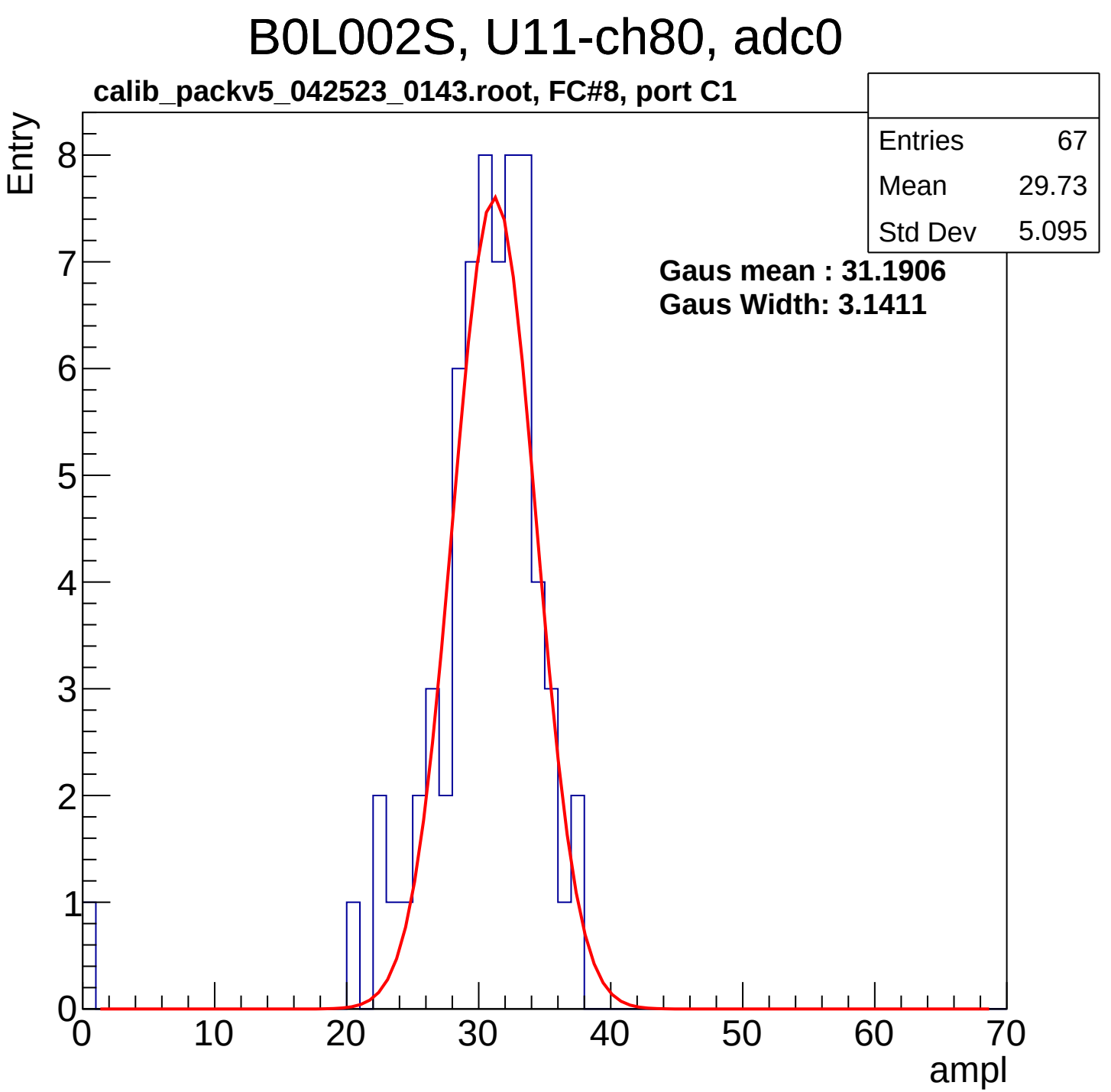
50
60
70
ampl


B0L002S, U11-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.73
Std Dev 5.095
Gaus mean : 31.1906
Gaus Width: 3.1411
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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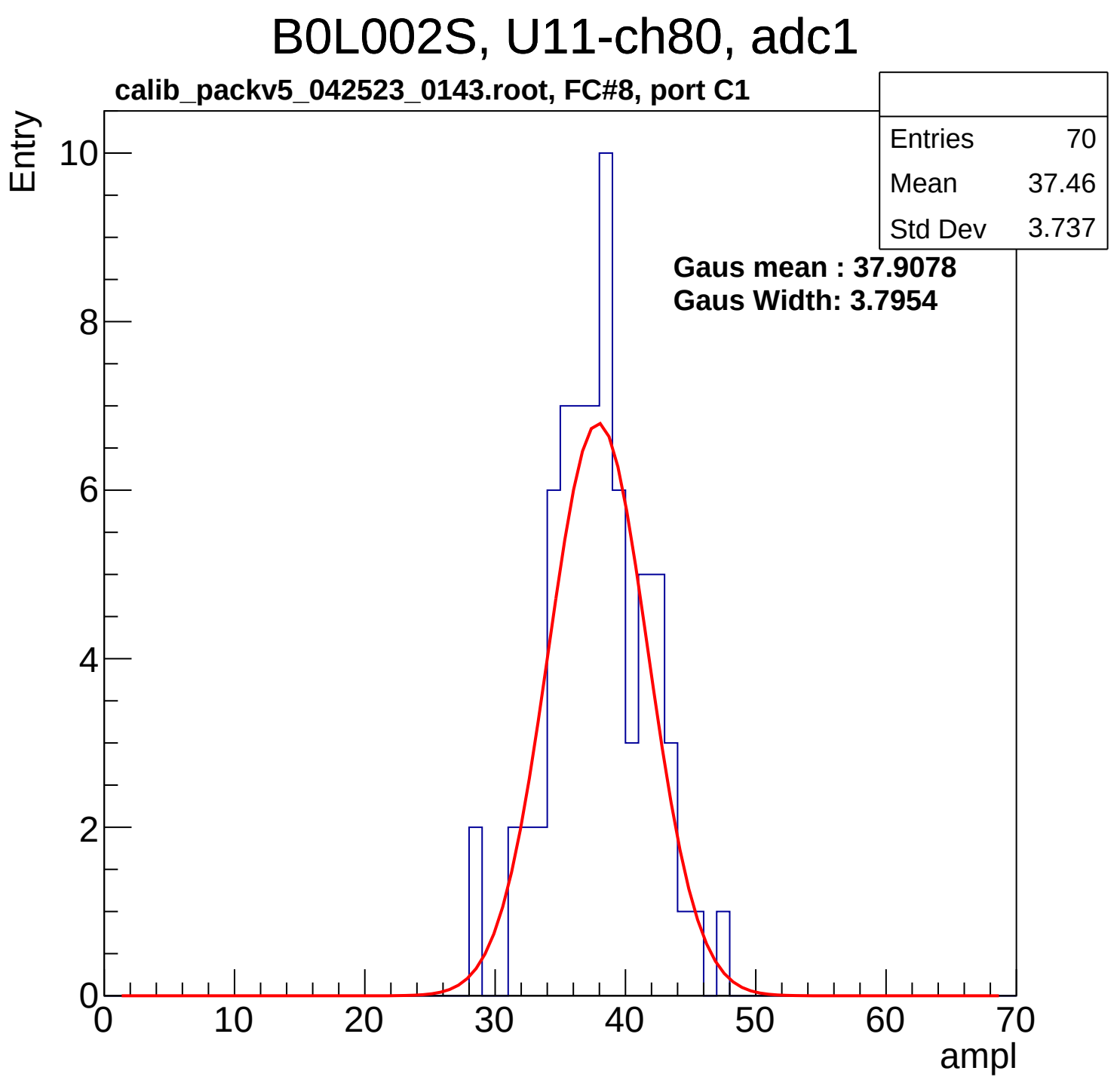
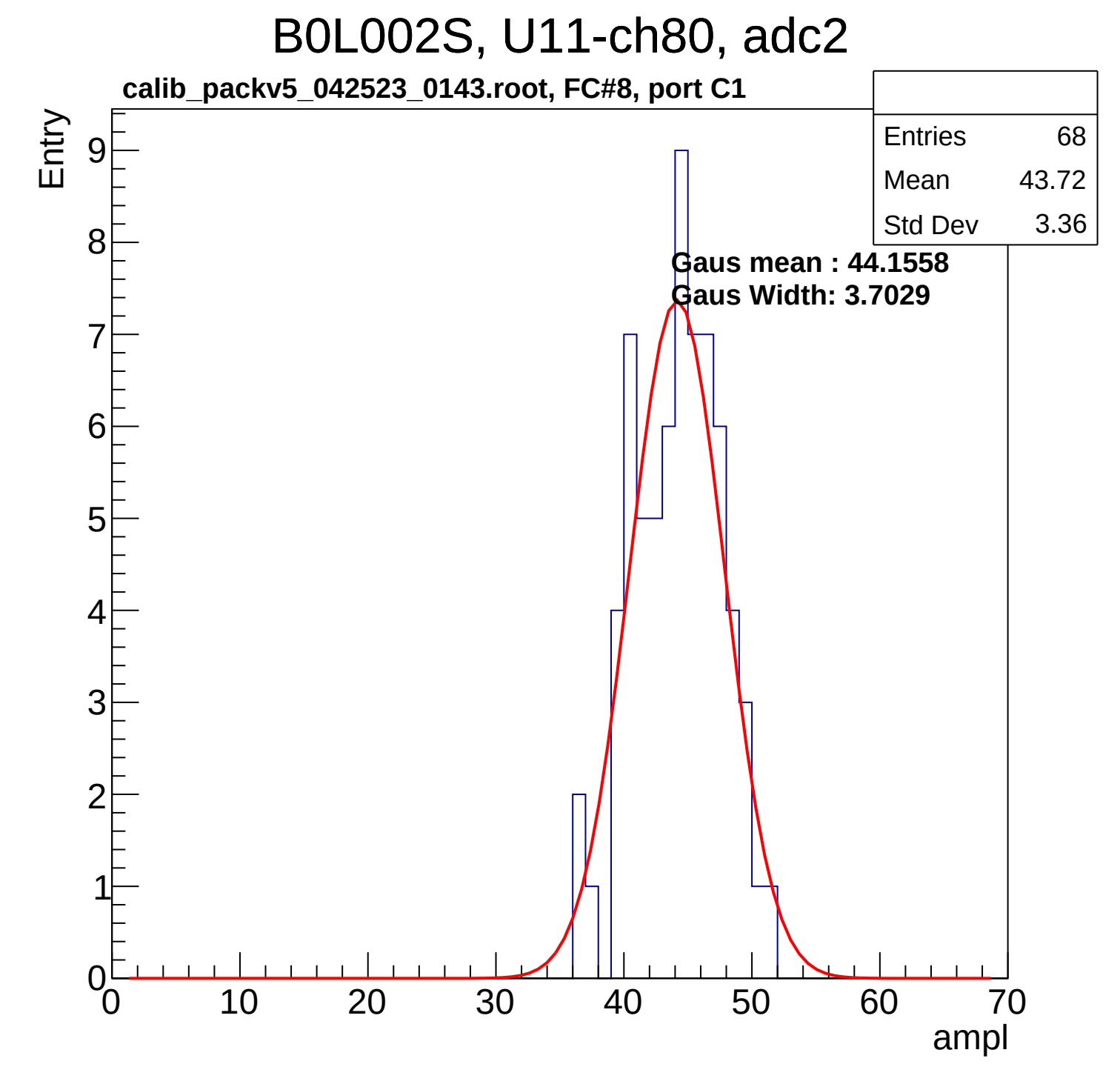
40
50
60
70


B0L002S, U11-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.46
Std Dev 3.737
Gaus mean : 37.9078
Gaus Width: 3.7954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.72
Std Dev 3.36
Gaus mean : 44.1558
Gaus Width: 3.7029
Entry
ampl
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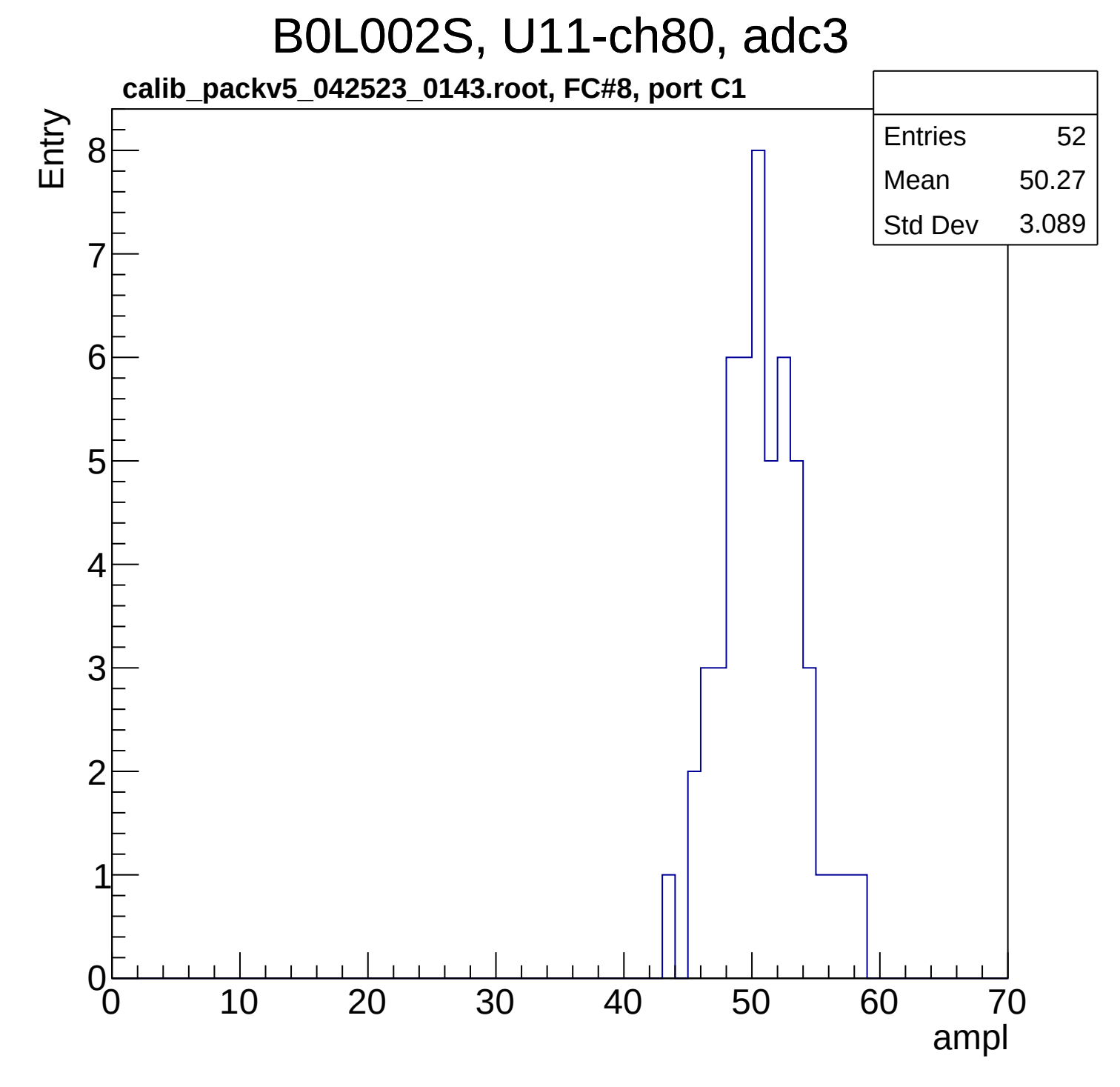
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.27
Std Dev 3.089
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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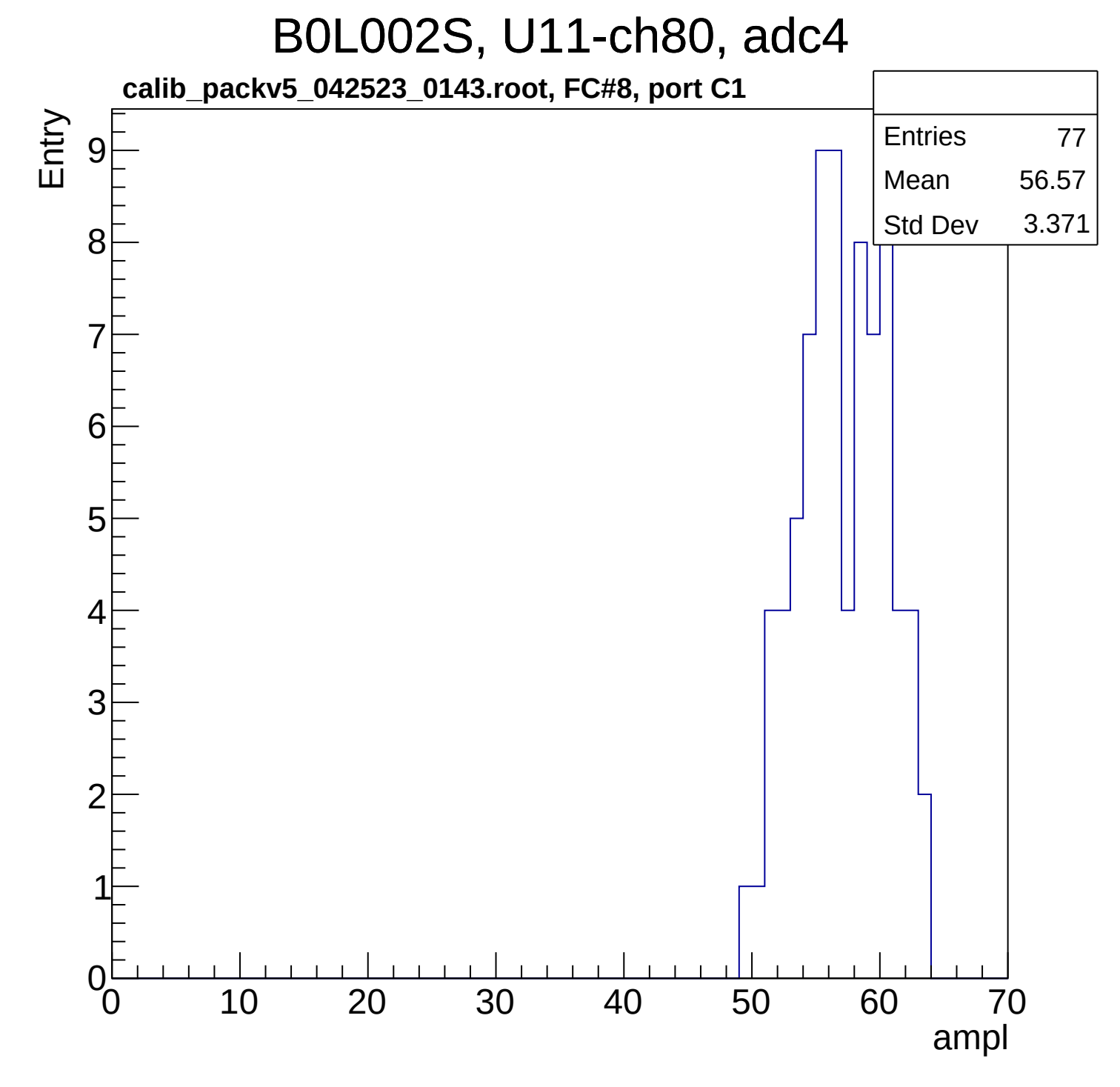
B0L002S, U11-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 56.57
Std Dev 3.371
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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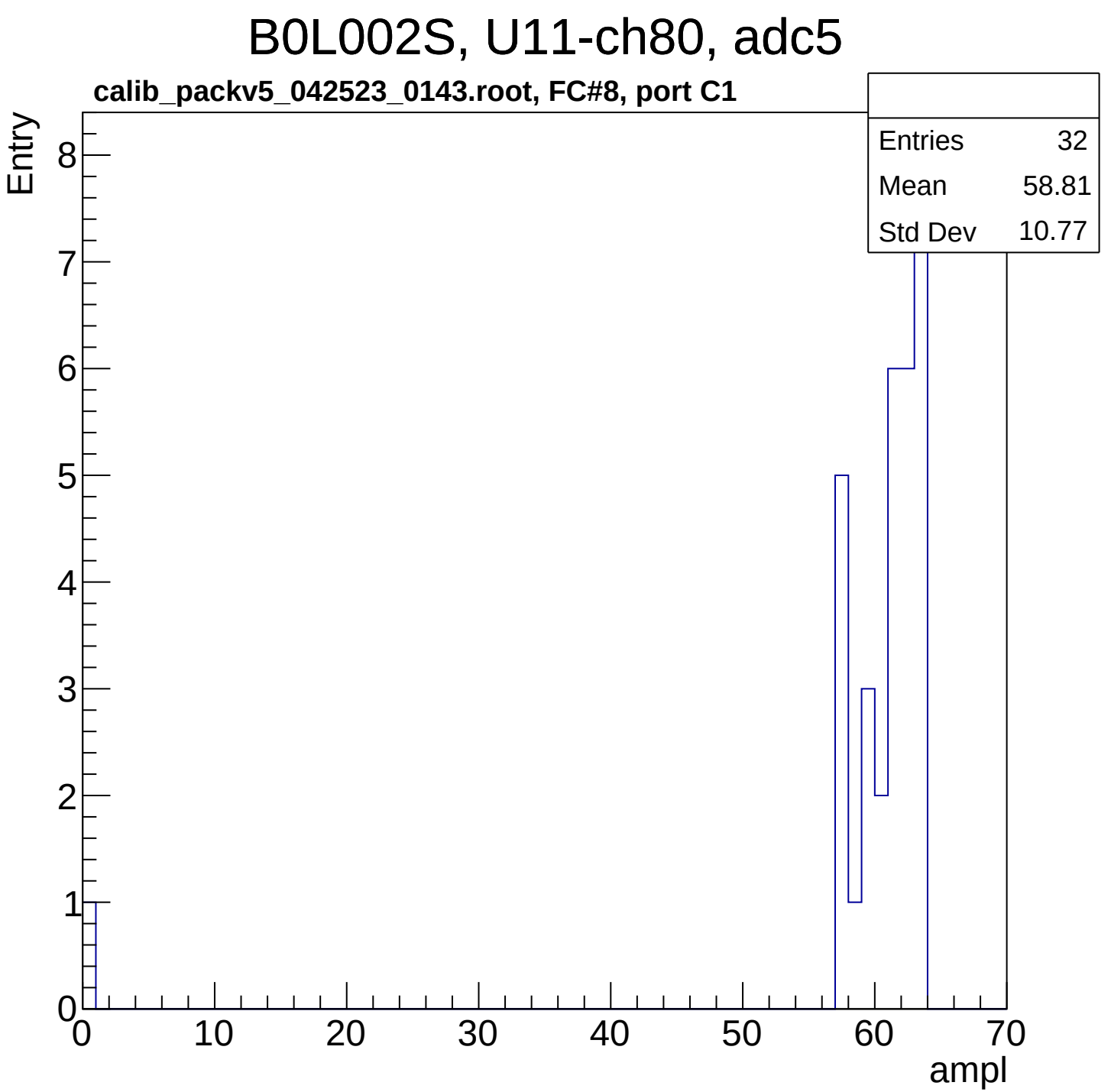
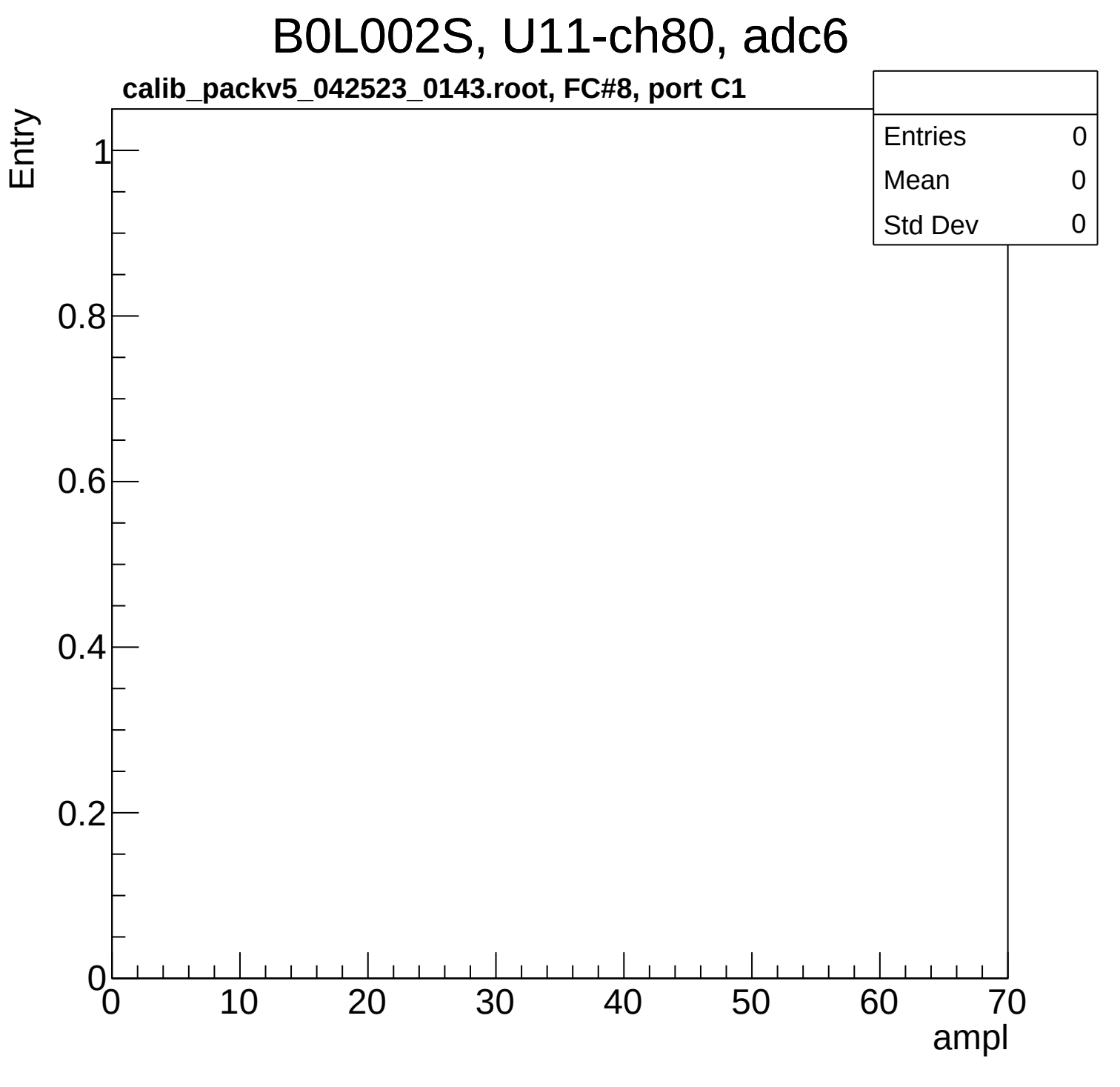
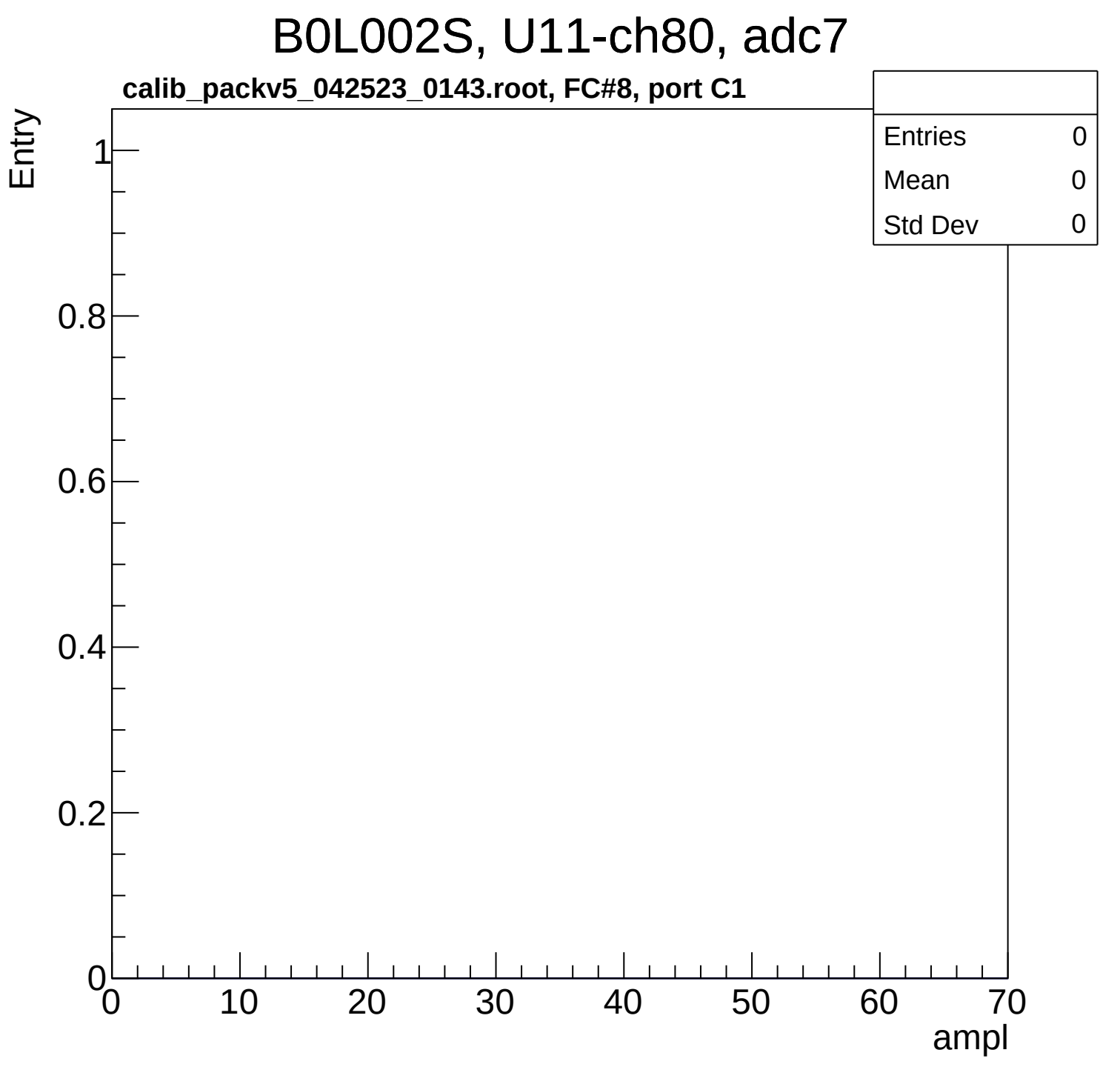

B0L002S, U11-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.81
Std Dev 10.77
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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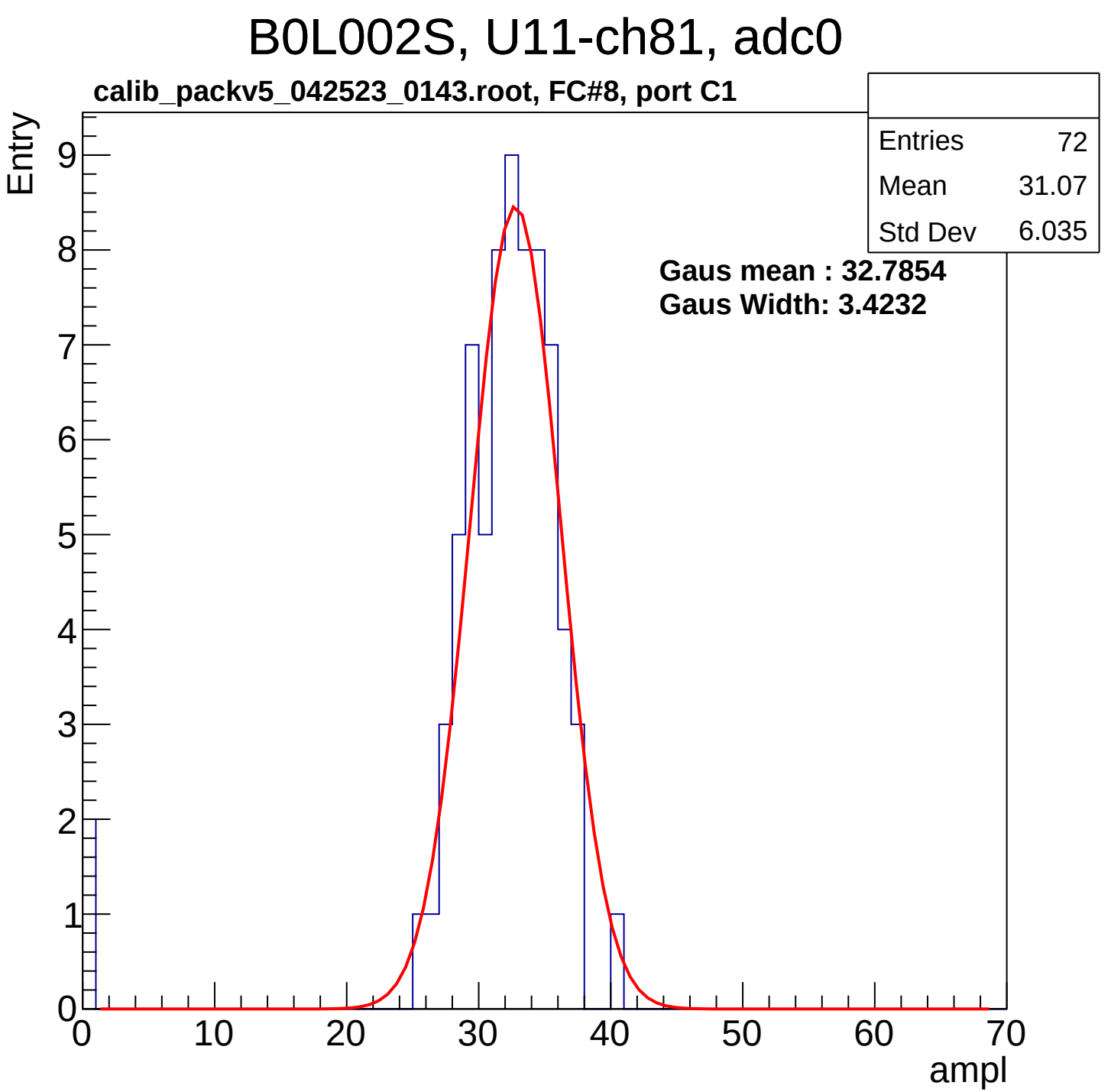
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.07
Std Dev 6.035
Gaus mean : 32.7854
Gaus Width: 3.4232
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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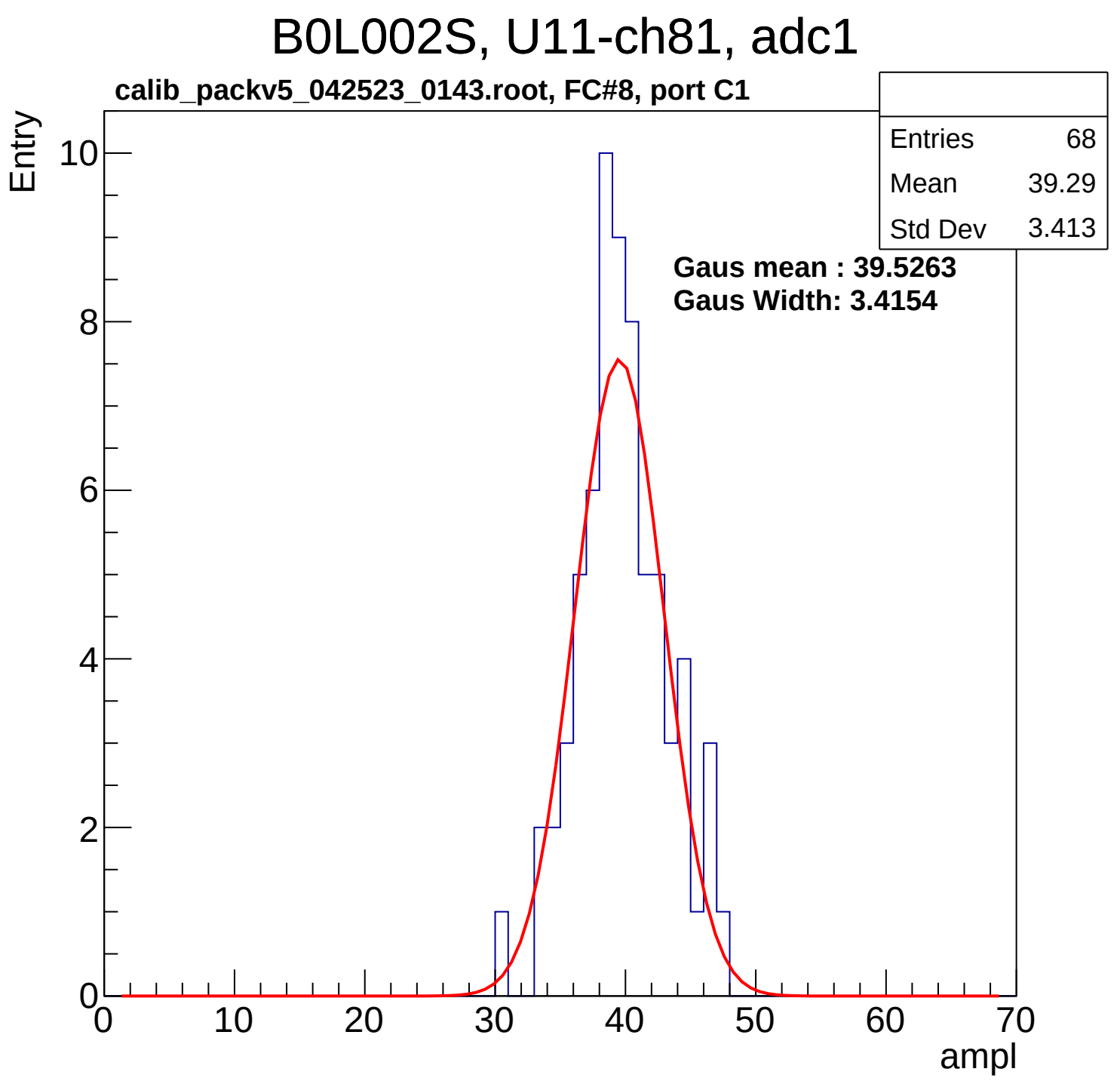
B0L002S, U11-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 39.29
Std Dev 3.413
Gaus mean : 39.5263
Gaus Width: 3.4154
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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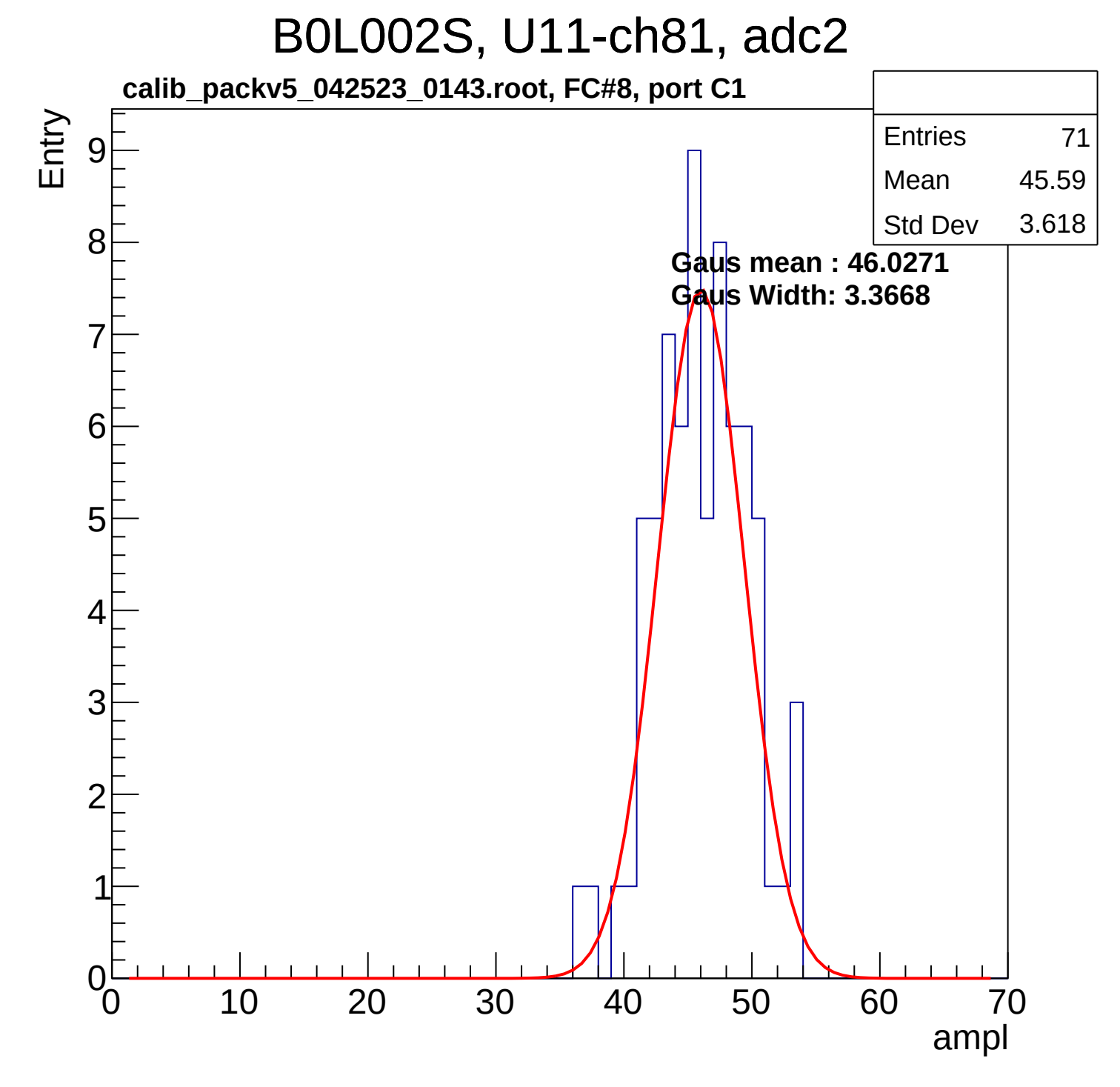
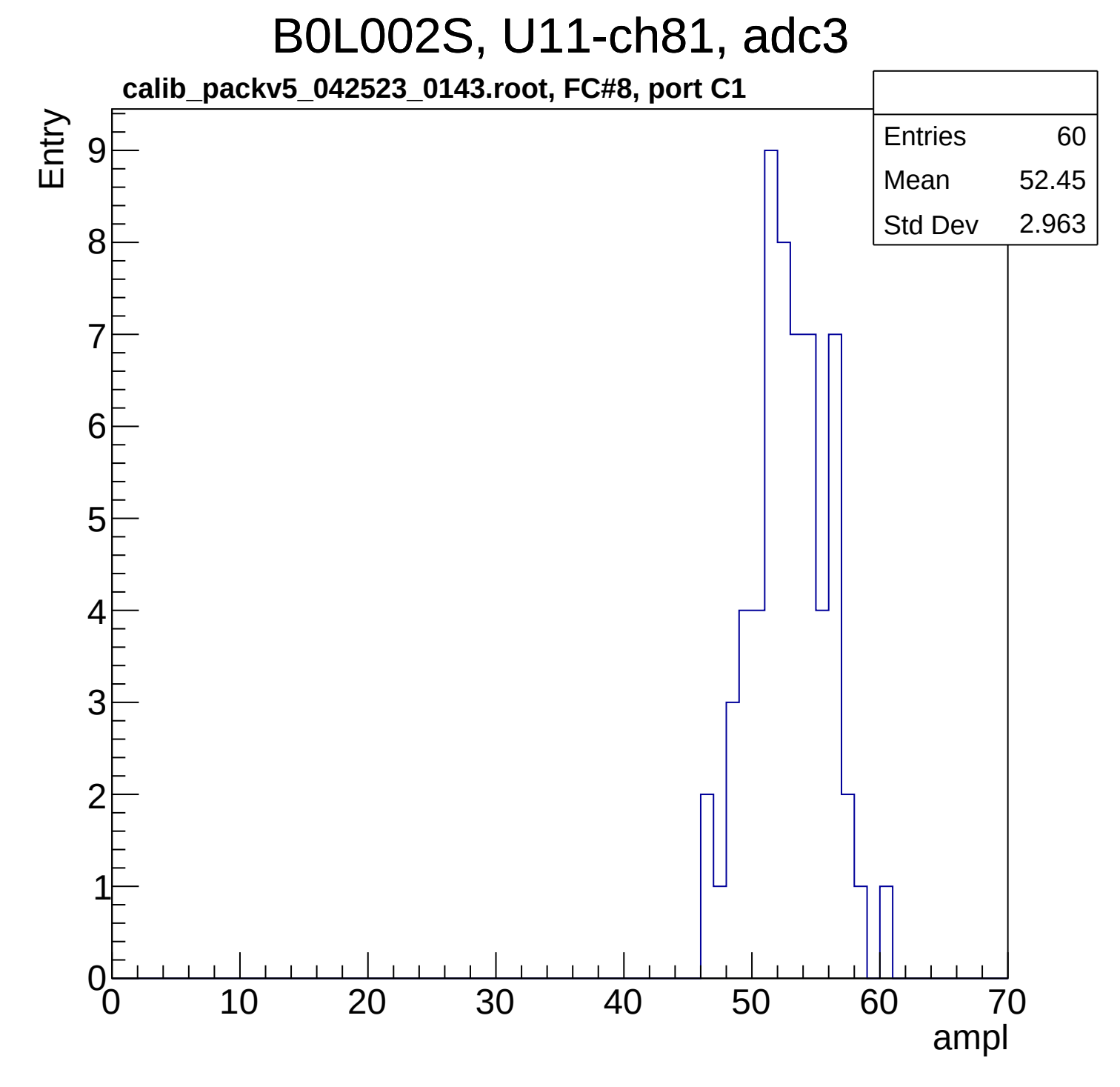

B0L002S, U11-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 45.59
Std Dev 3.618
Gaus mean : 46.0271
Gaus Width: 3.3668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.45
Std Dev 2.963
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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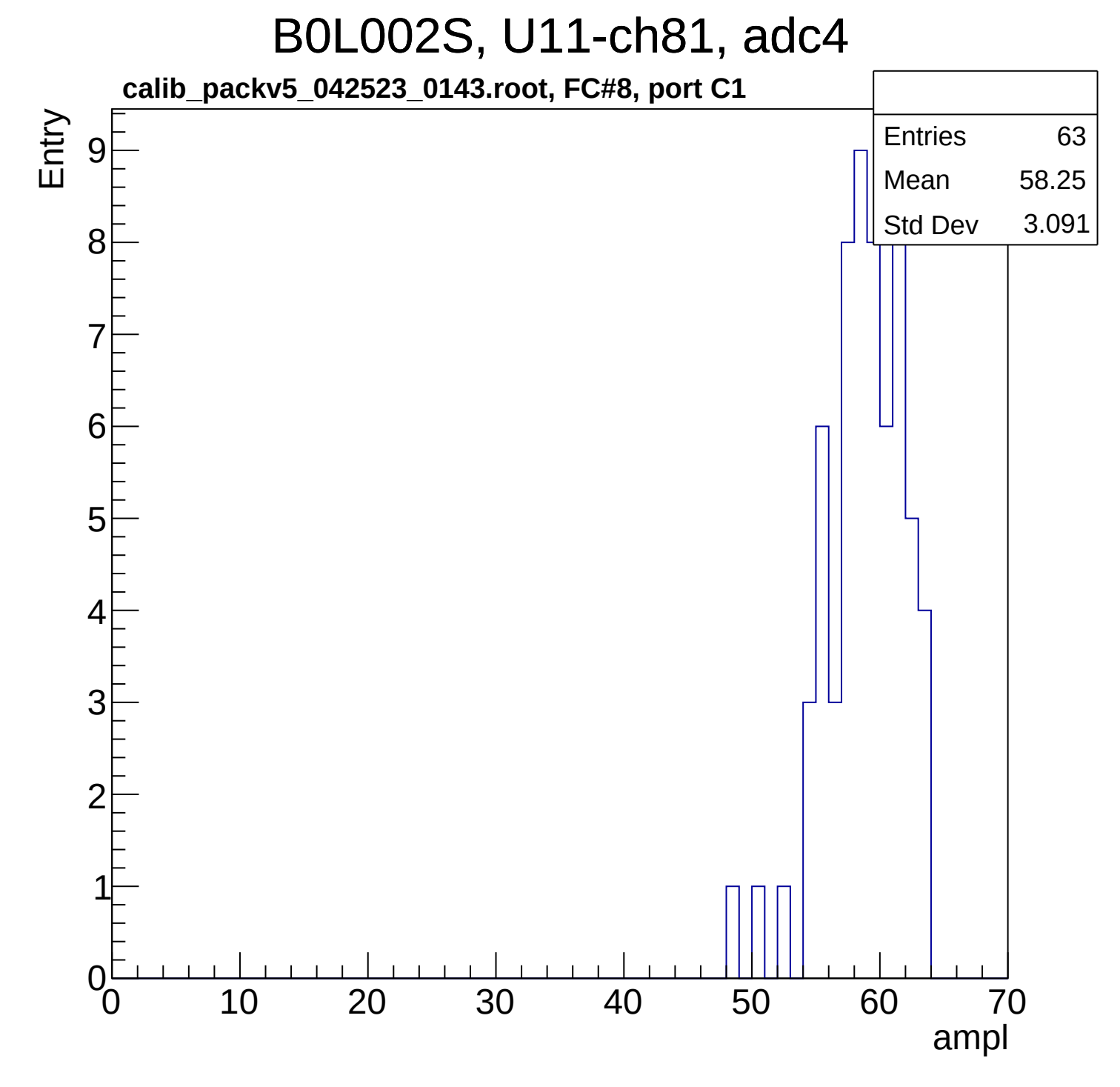
B0L002S, U11-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 58.25
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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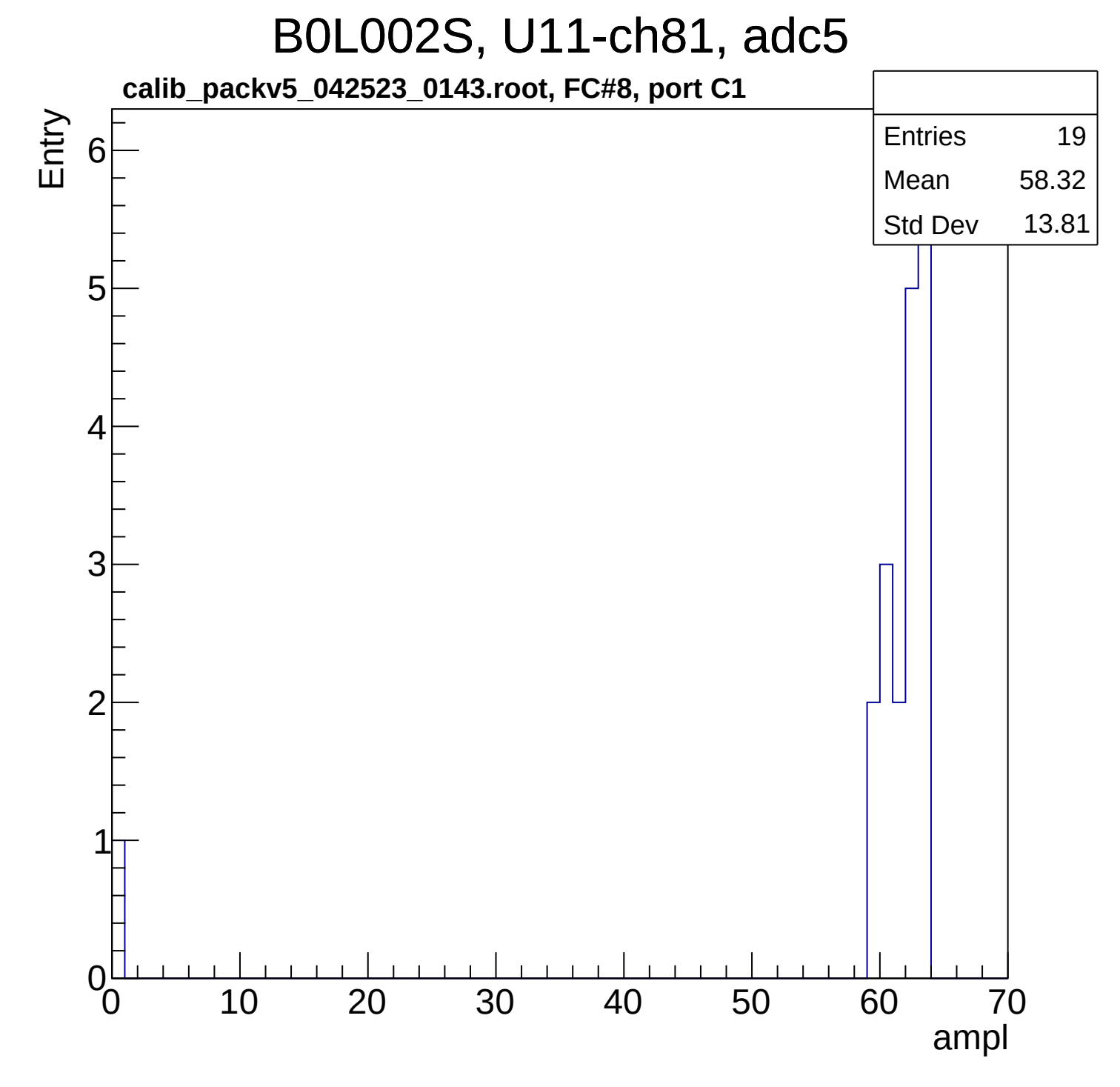
B0L002S, U11-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 58.32
Std Dev 13.81
Entry
0
1
2
3
4
5
6
0
10
20
30
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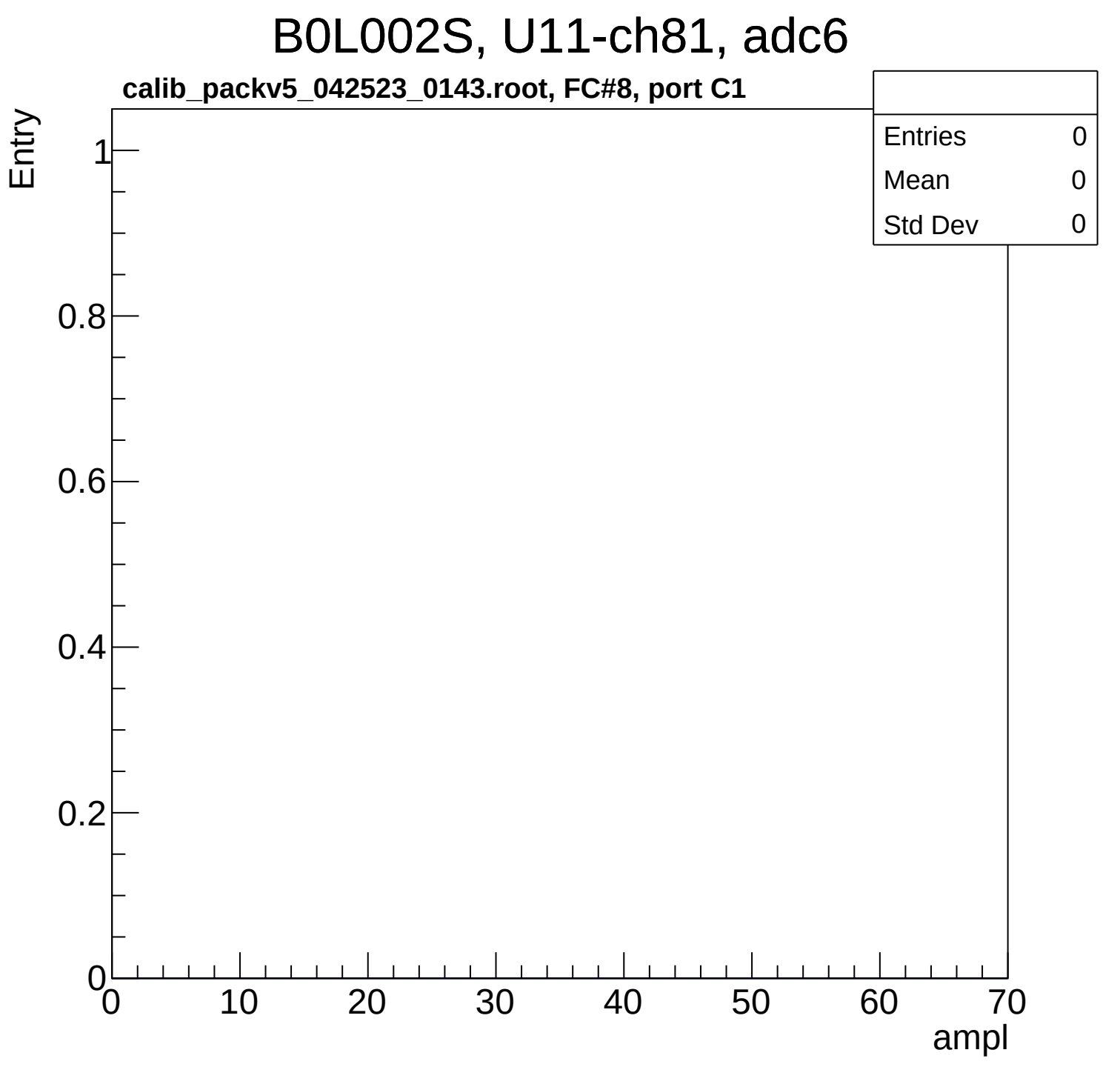
50
60
70
ampl


B0L002S, U11-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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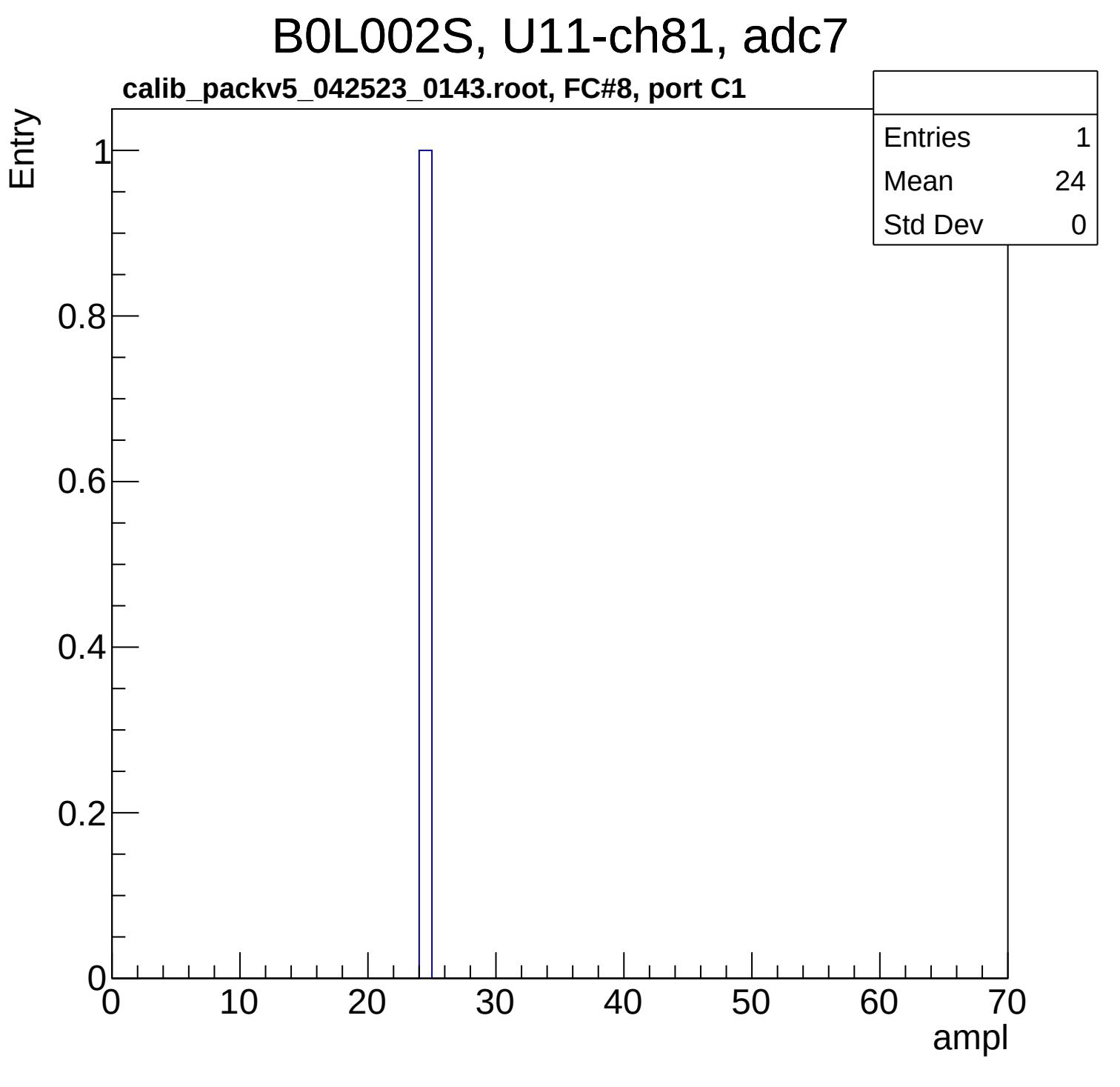
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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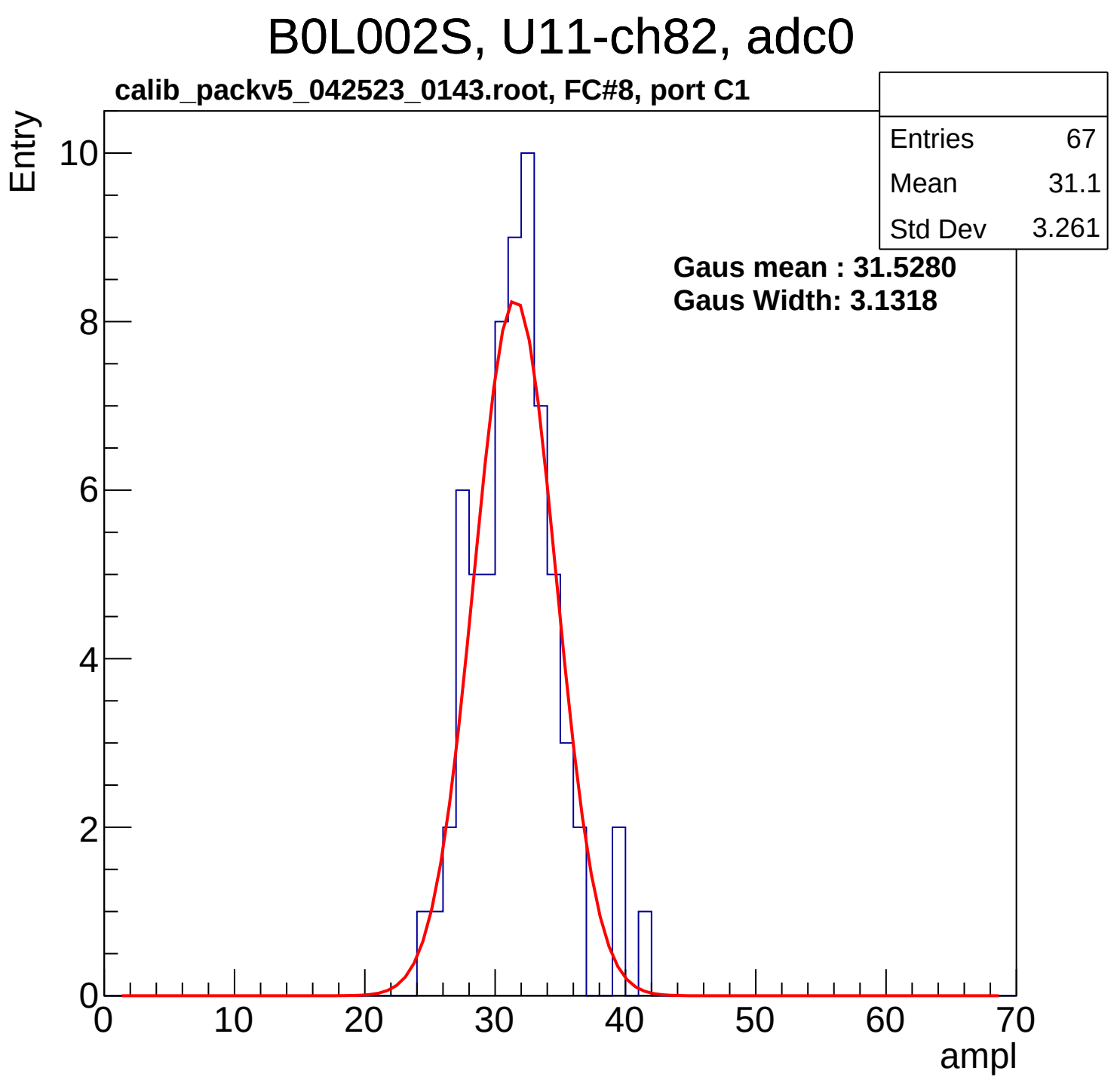
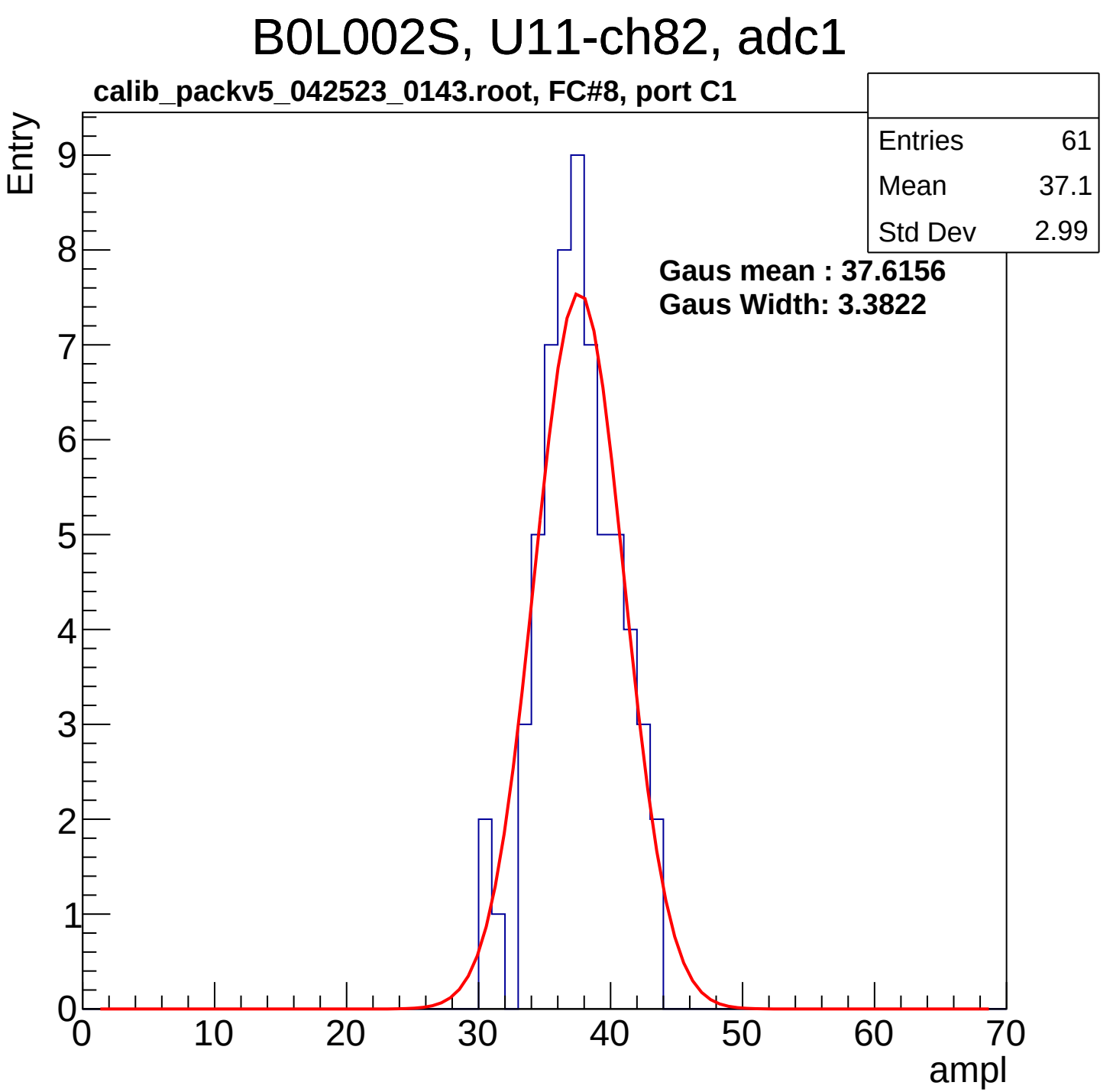
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.1
Std Dev 3.261
Gaus mean : 31.5280
Gaus Width: 3.1318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.1
Std Dev 2.99
Gaus mean : 37.6156
Gaus Width: 3.3822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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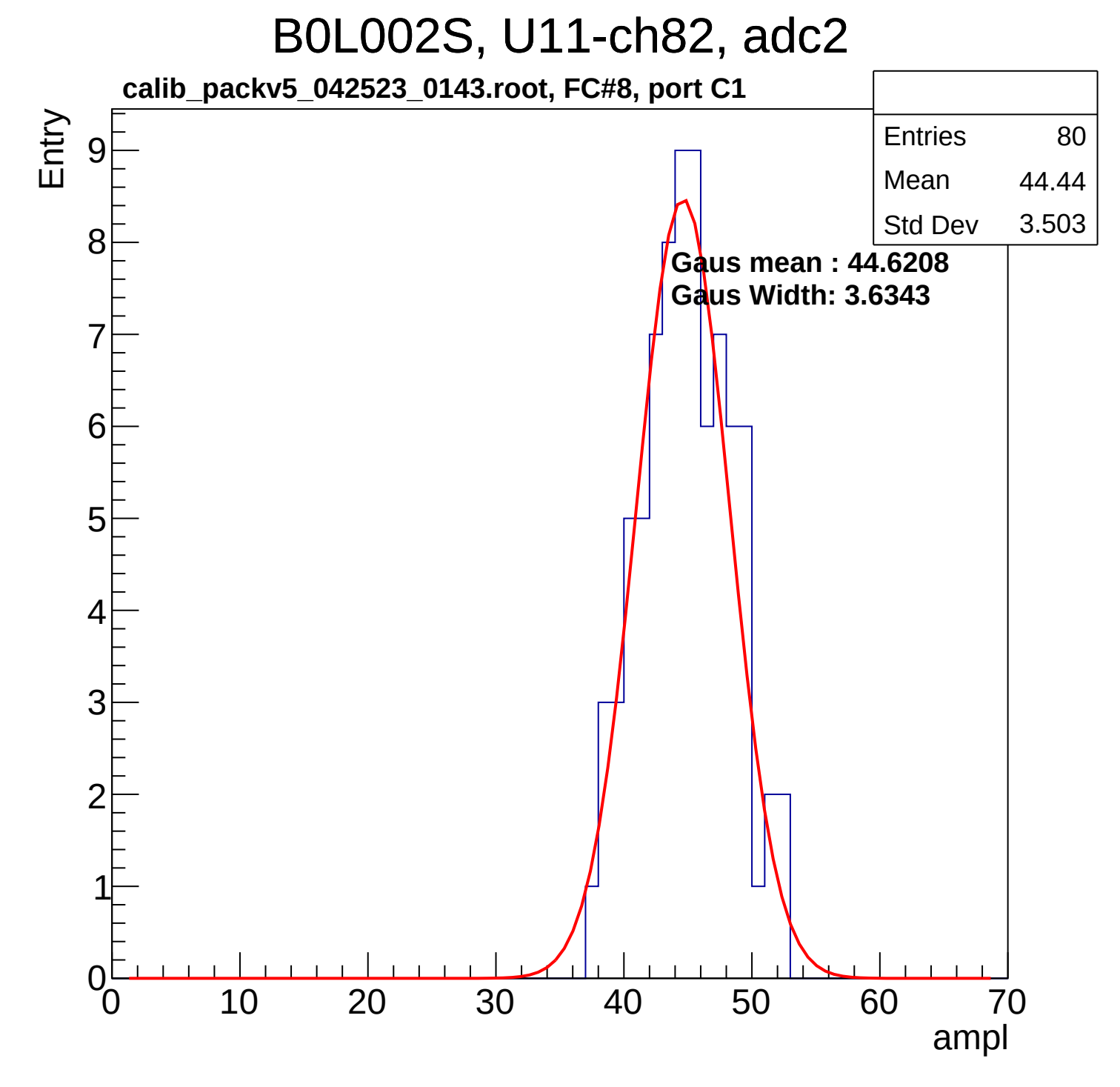
B0L002S, U11-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 44.44
Std Dev 3.503
Gaus mean : 44.6208
Gaus Width: 3.6343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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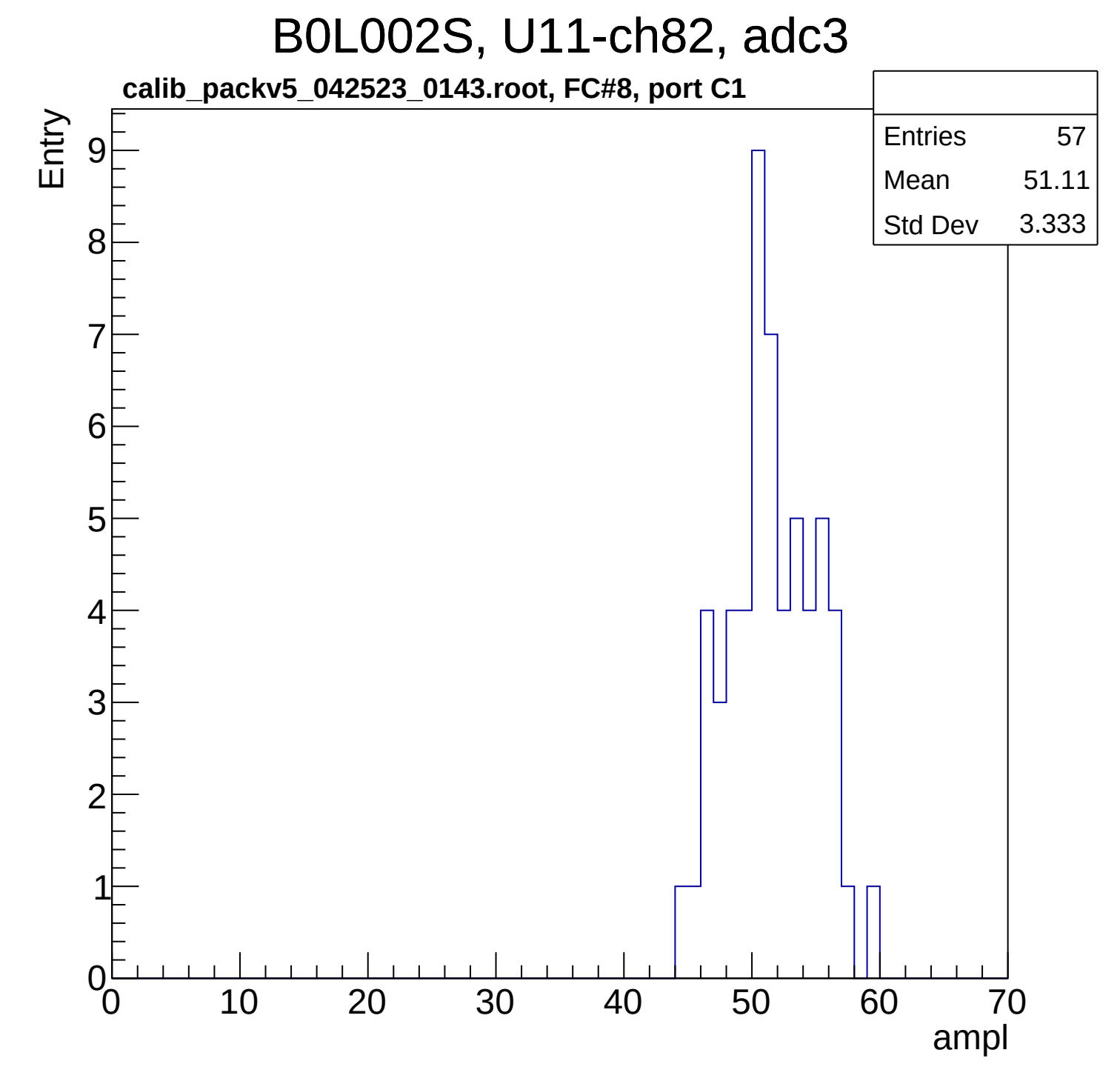
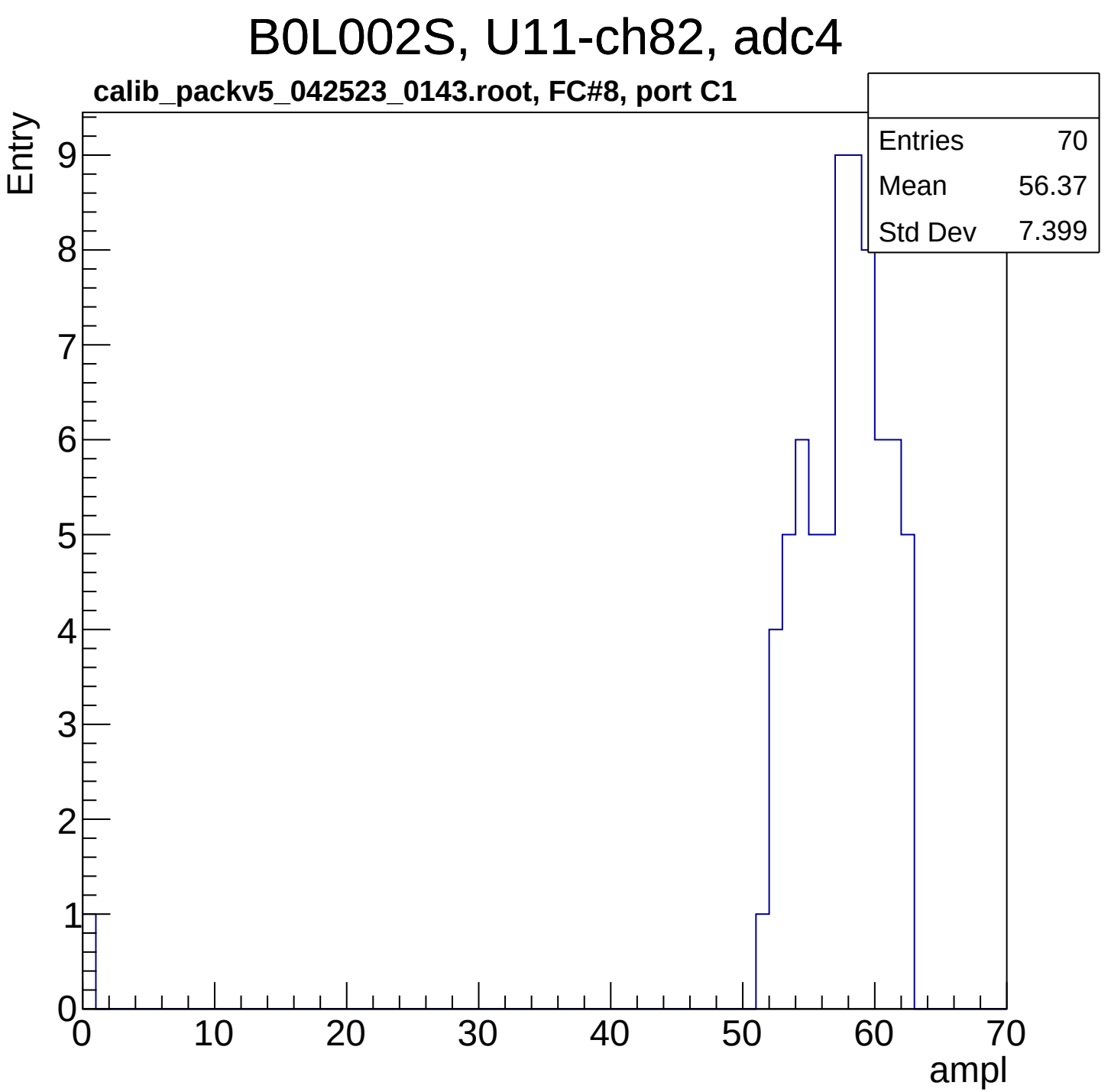

B0L002S, U11-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 51.11
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 56.37
Std Dev 7.399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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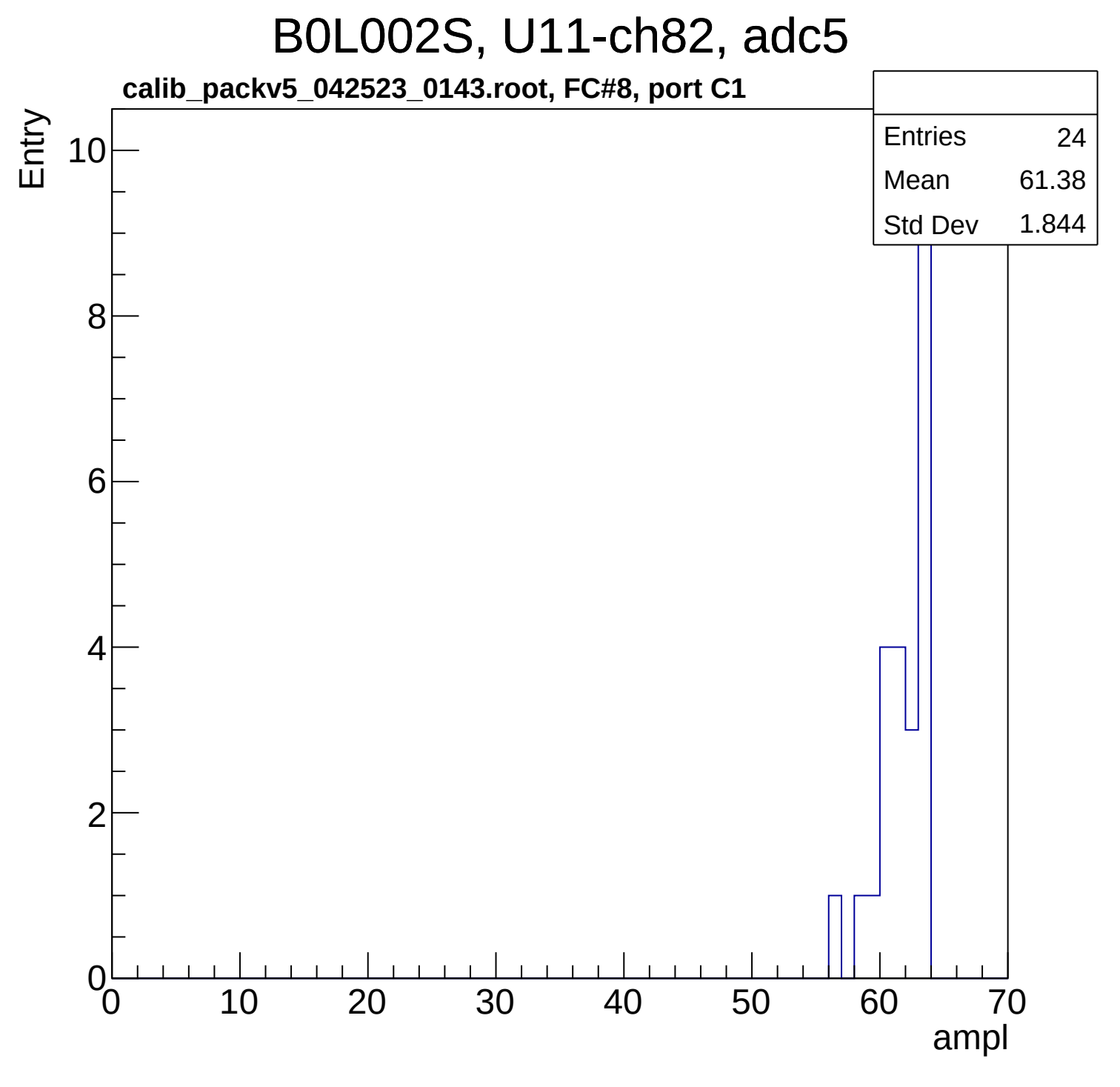
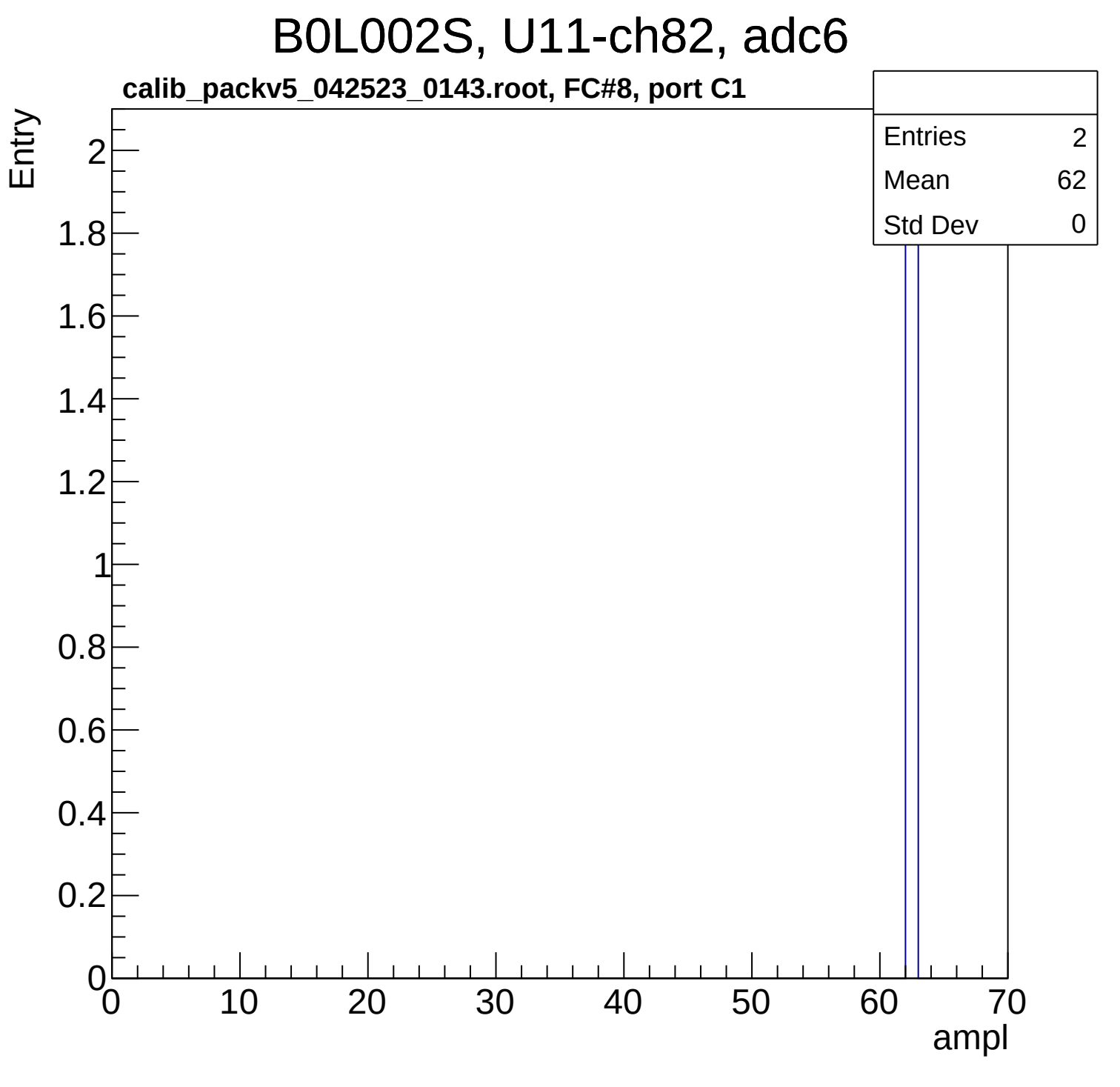
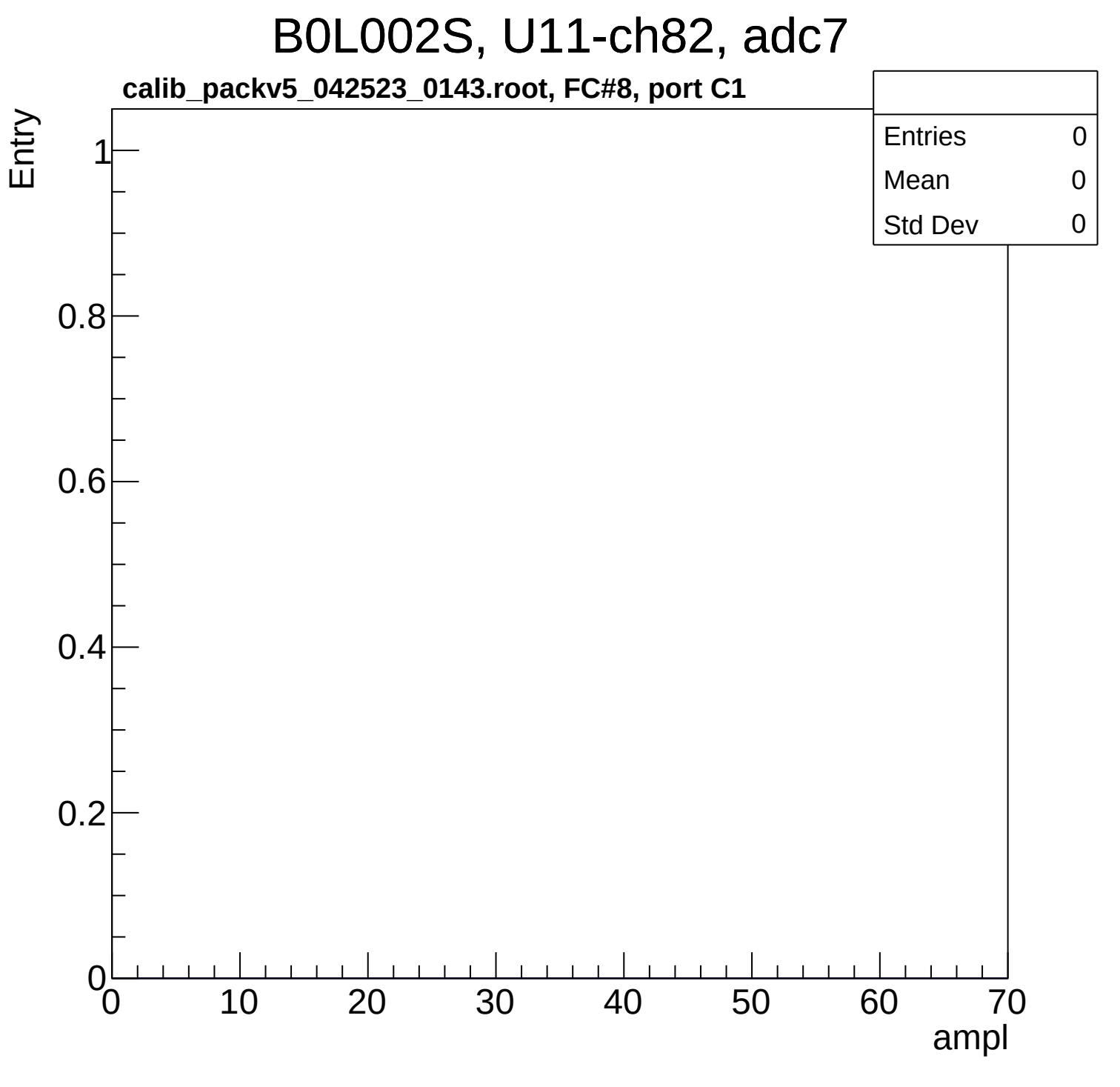
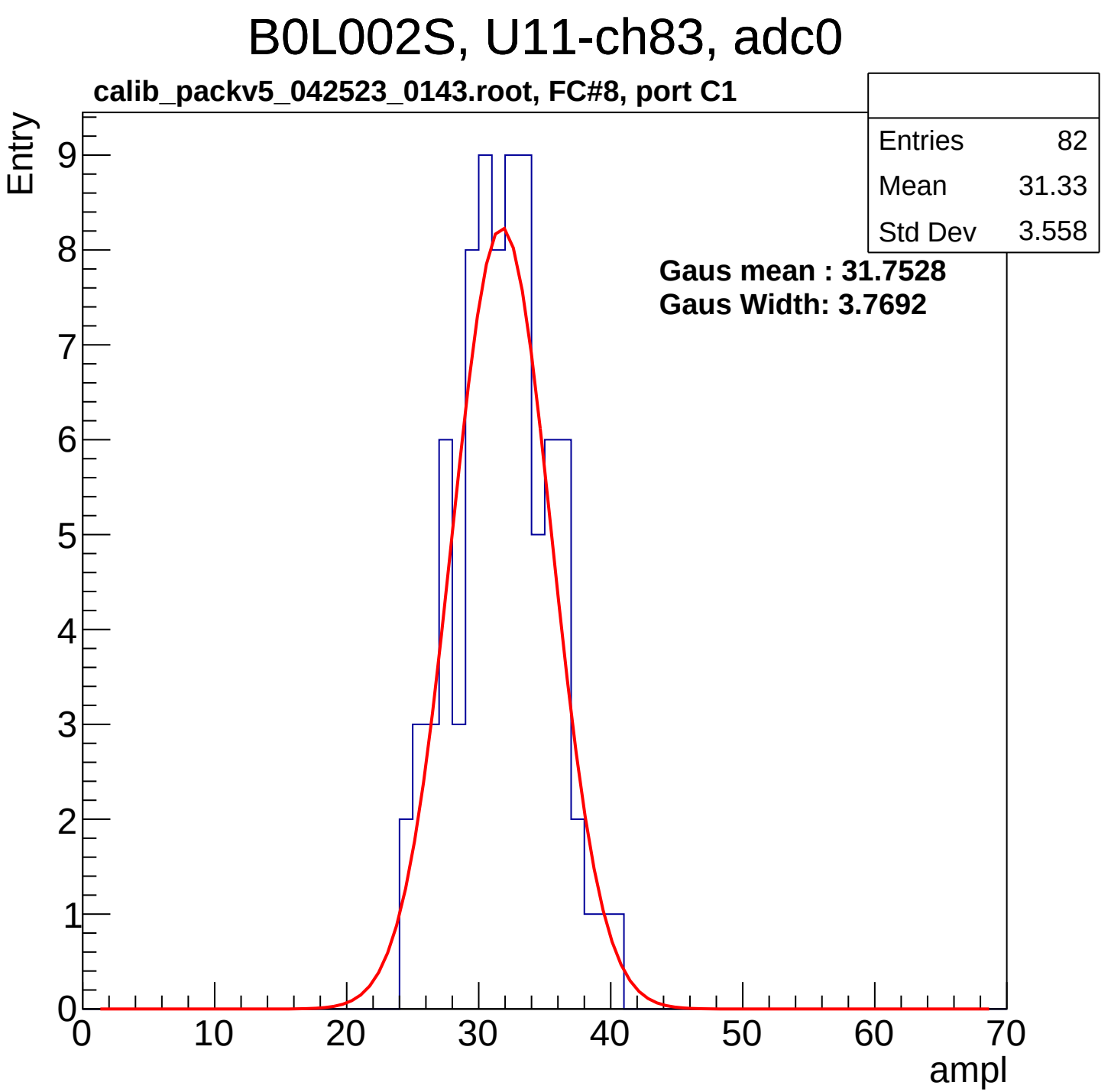
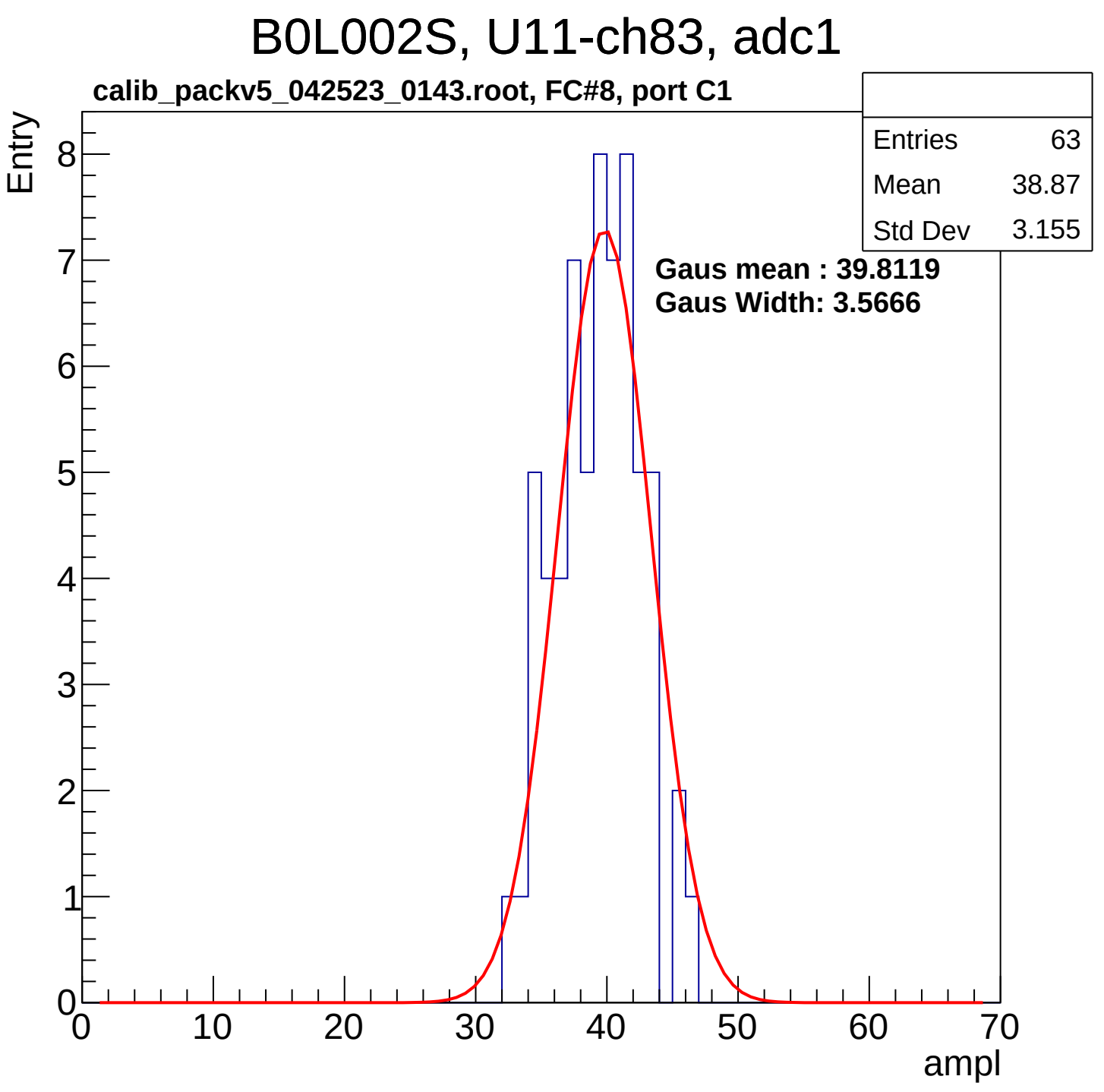
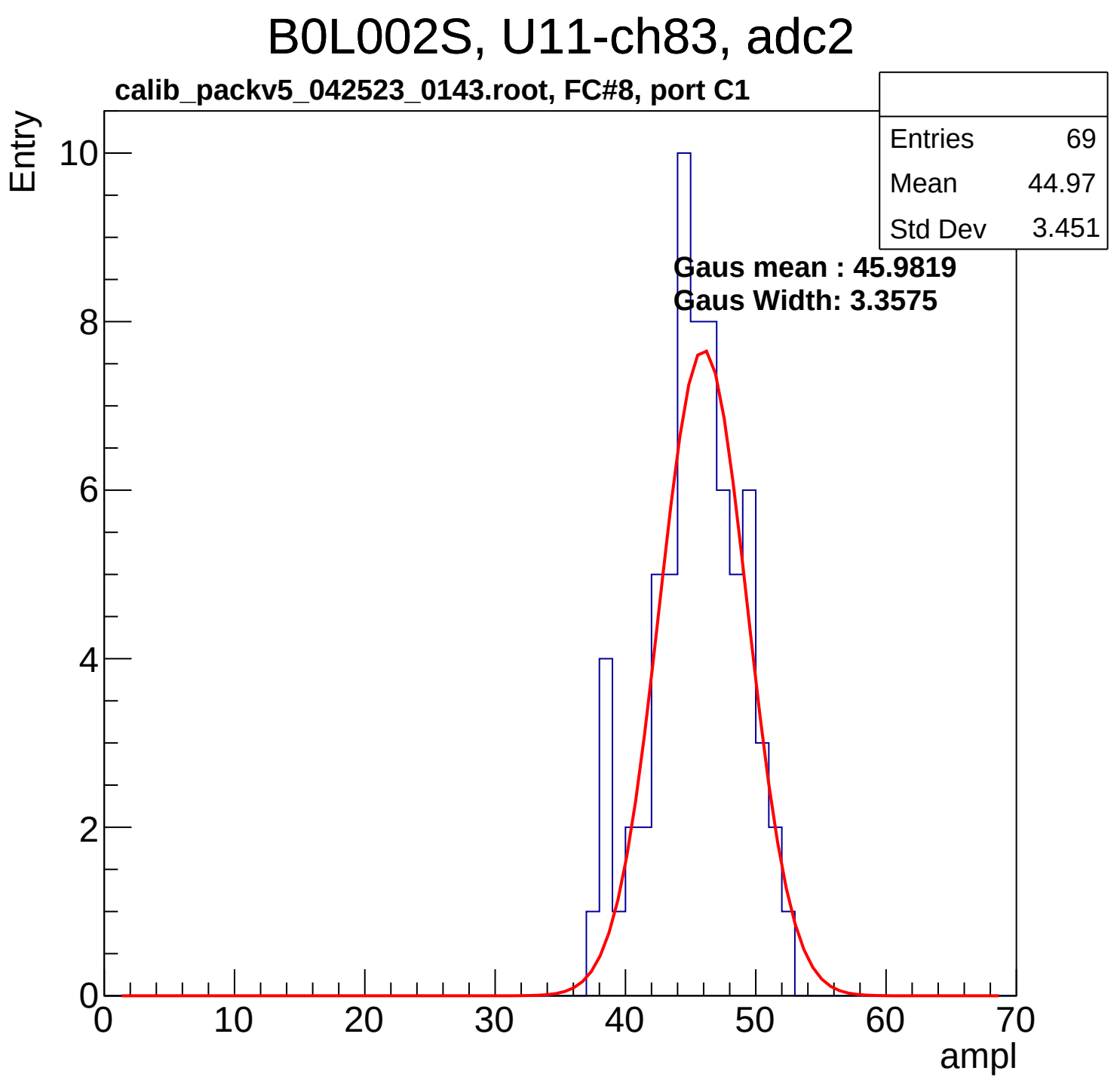

B0L002S, U11-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.38
Std Dev 1.844
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 31.33
Std Dev 3.558
Gaus mean : 31.7528
Gaus Width: 3.7692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.87
Std Dev 3.155
Gaus mean : 39.8119
Gaus Width: 3.5666
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.97
Std Dev 3.451
Gaus mean : 45.9819
Gaus Width: 3.3575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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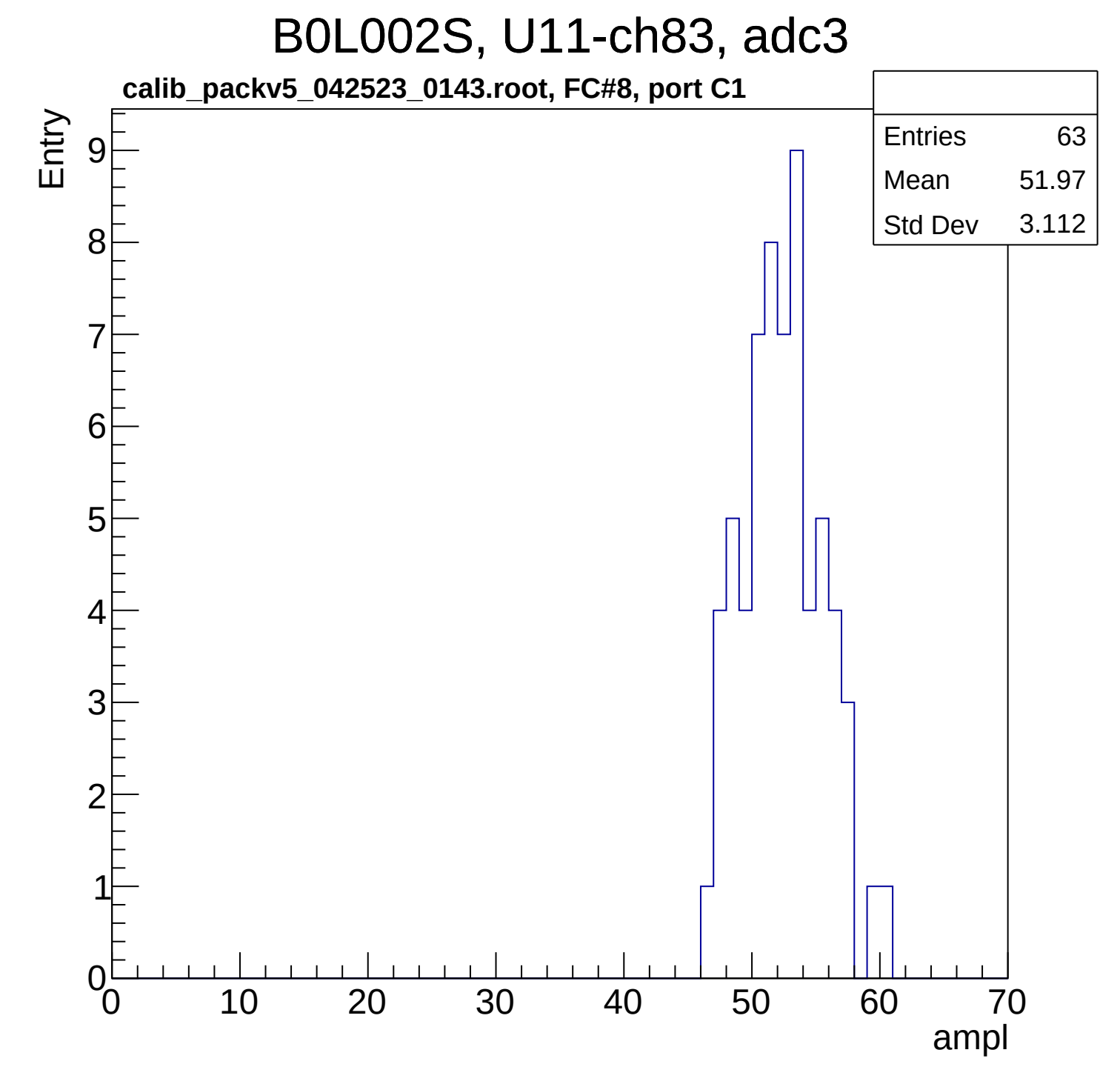
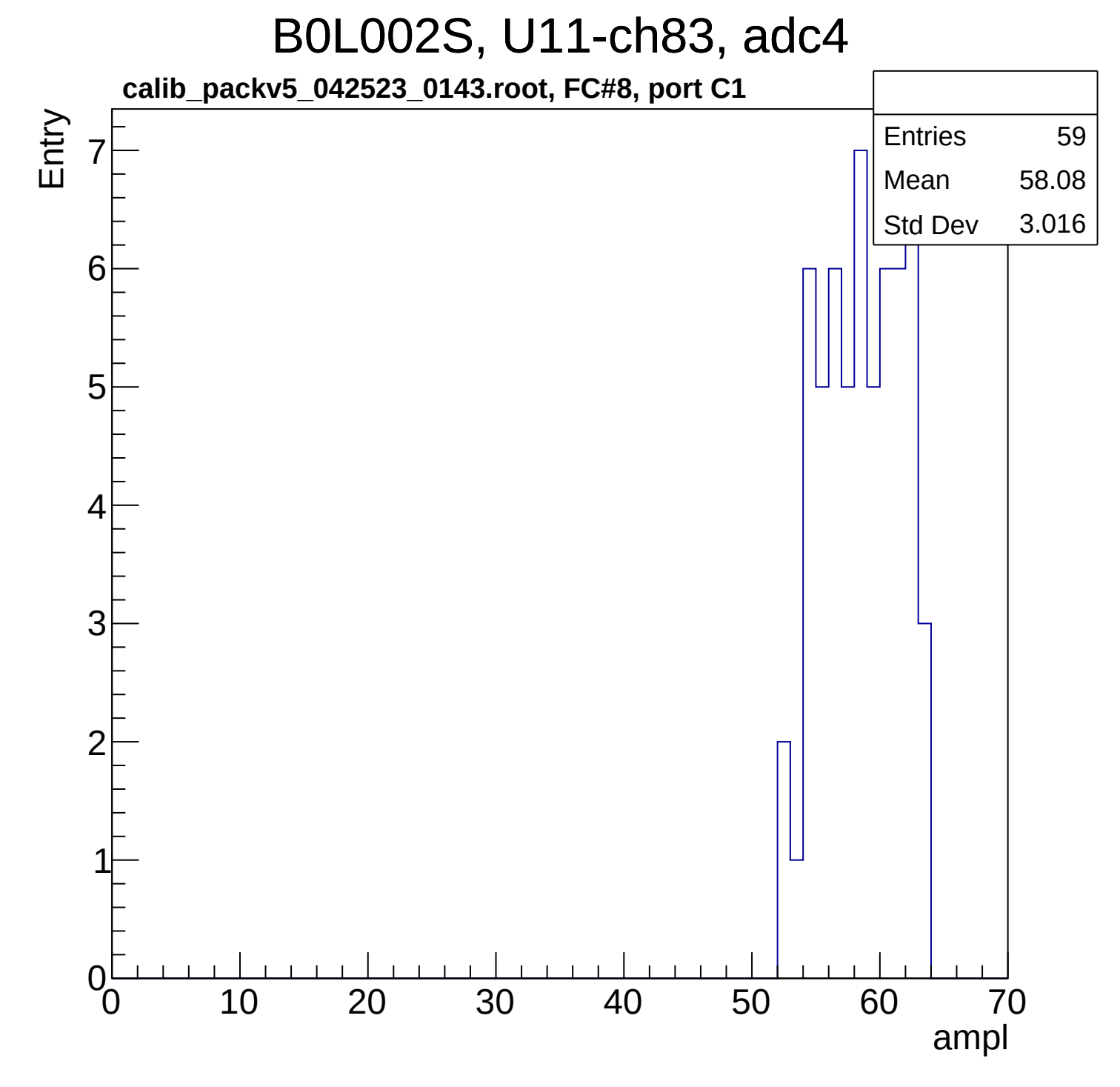
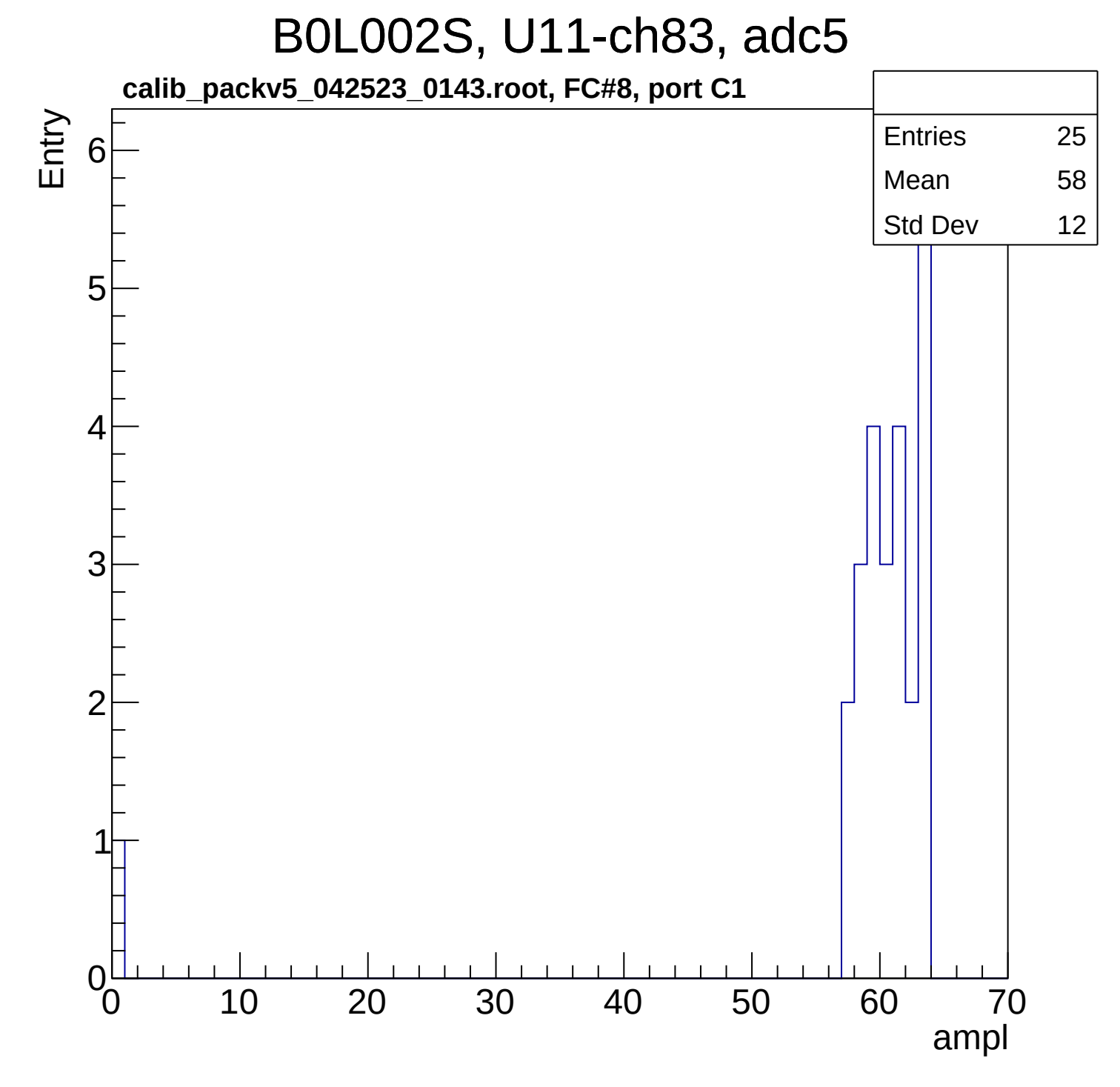

B0L002S, U11-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.97
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 58.08
Std Dev 3.016
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58
Std Dev 12
Entry
0
1
2
3
4
5
6
0
10
20
30
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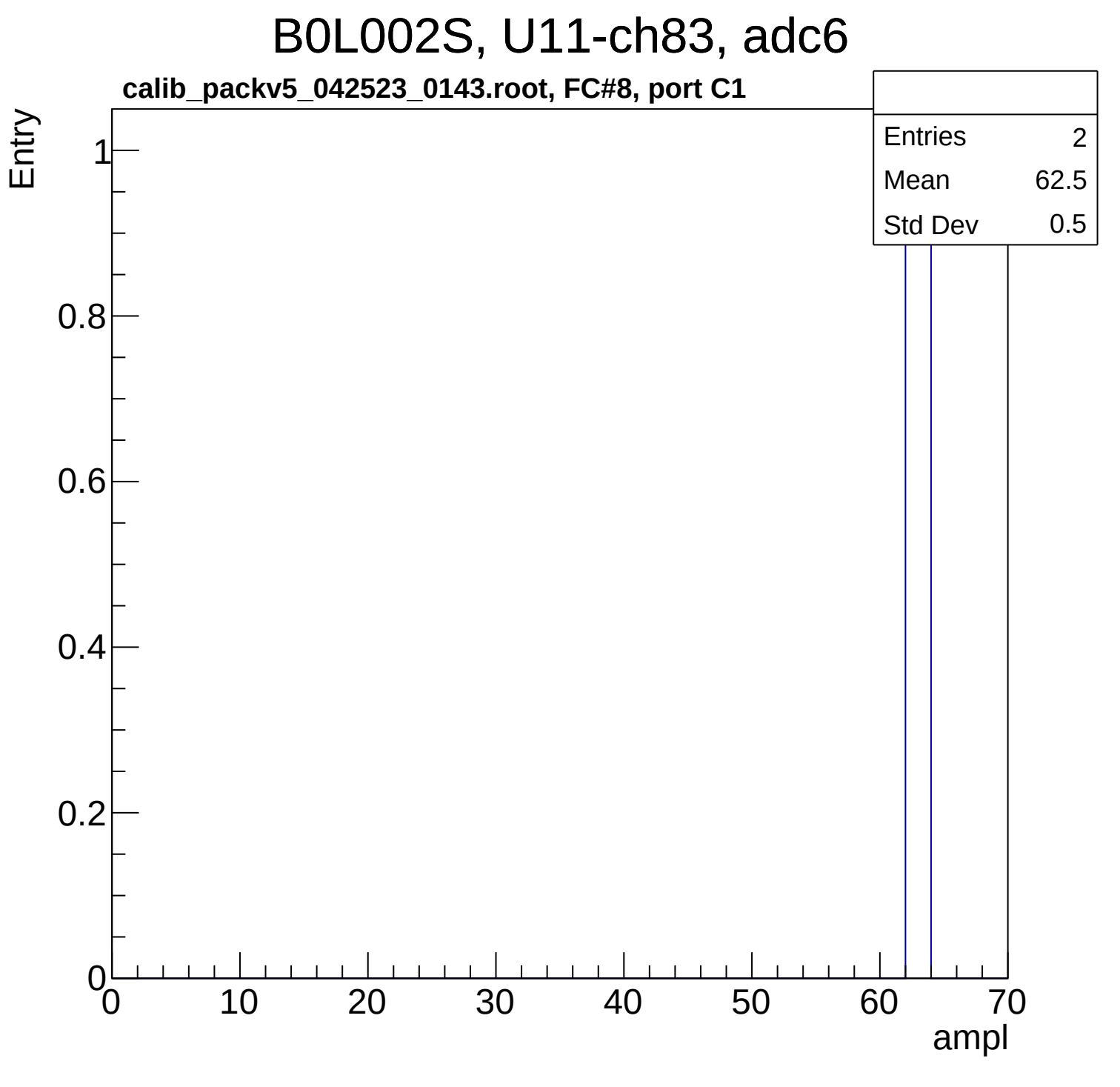
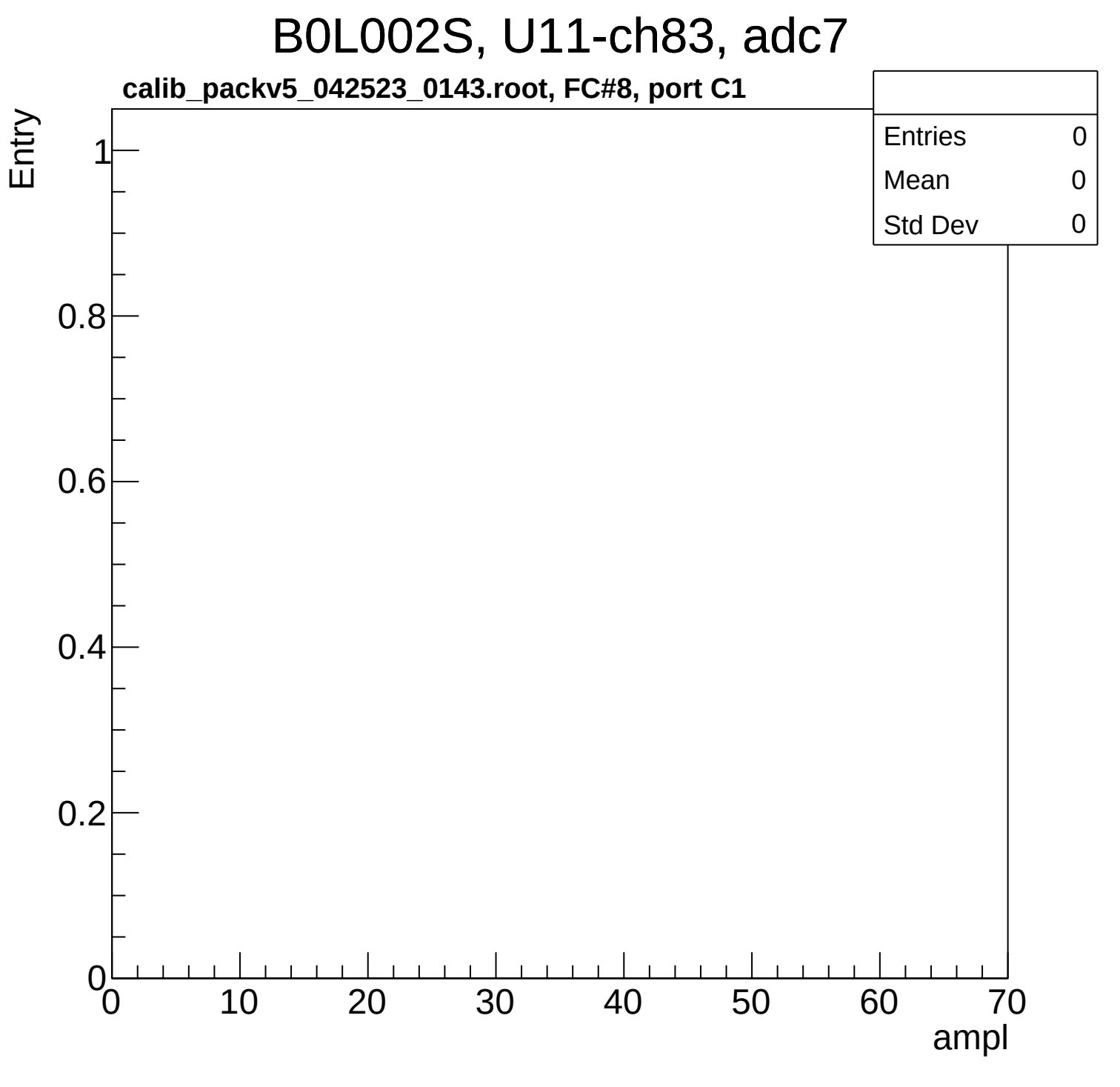
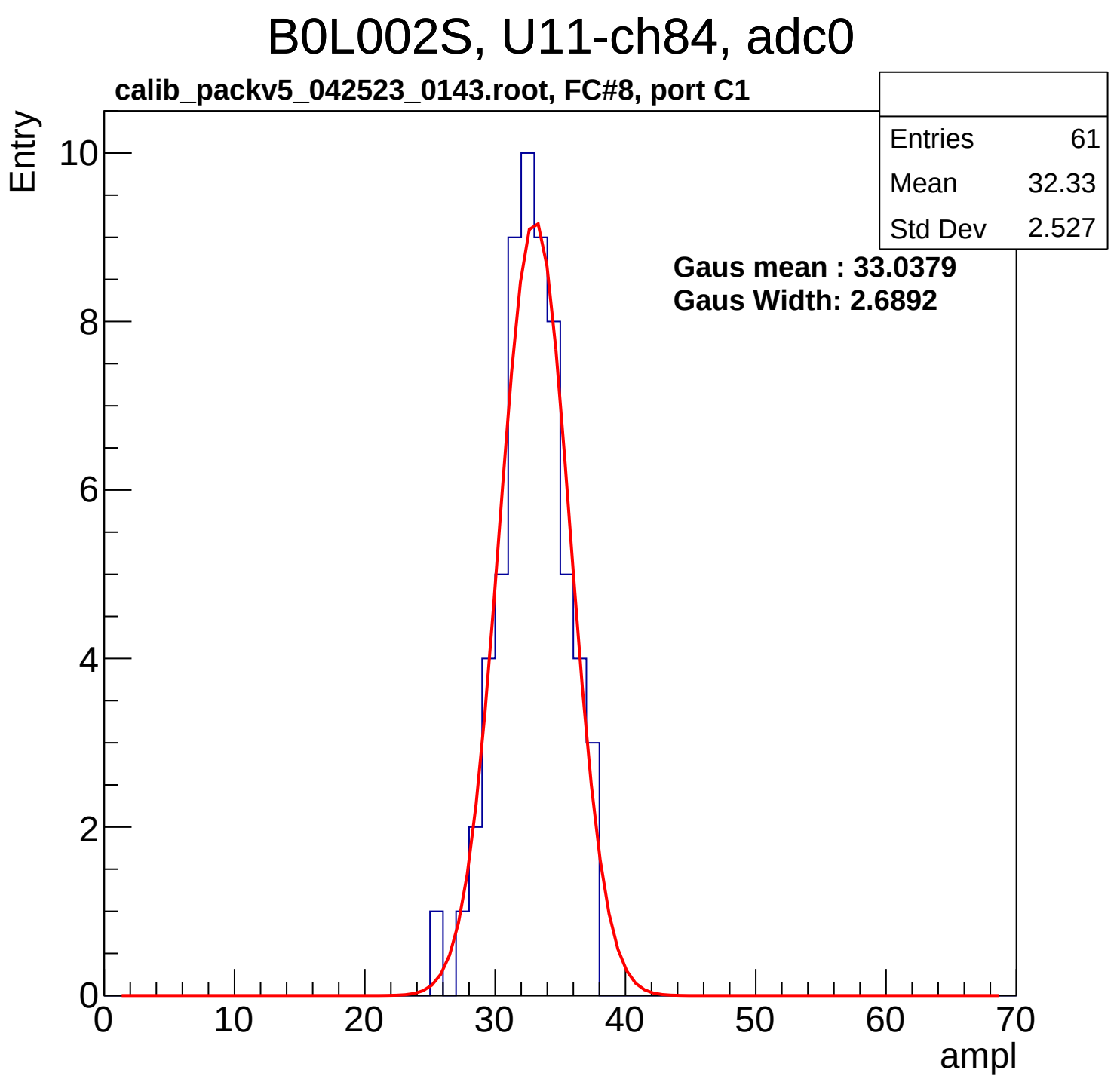
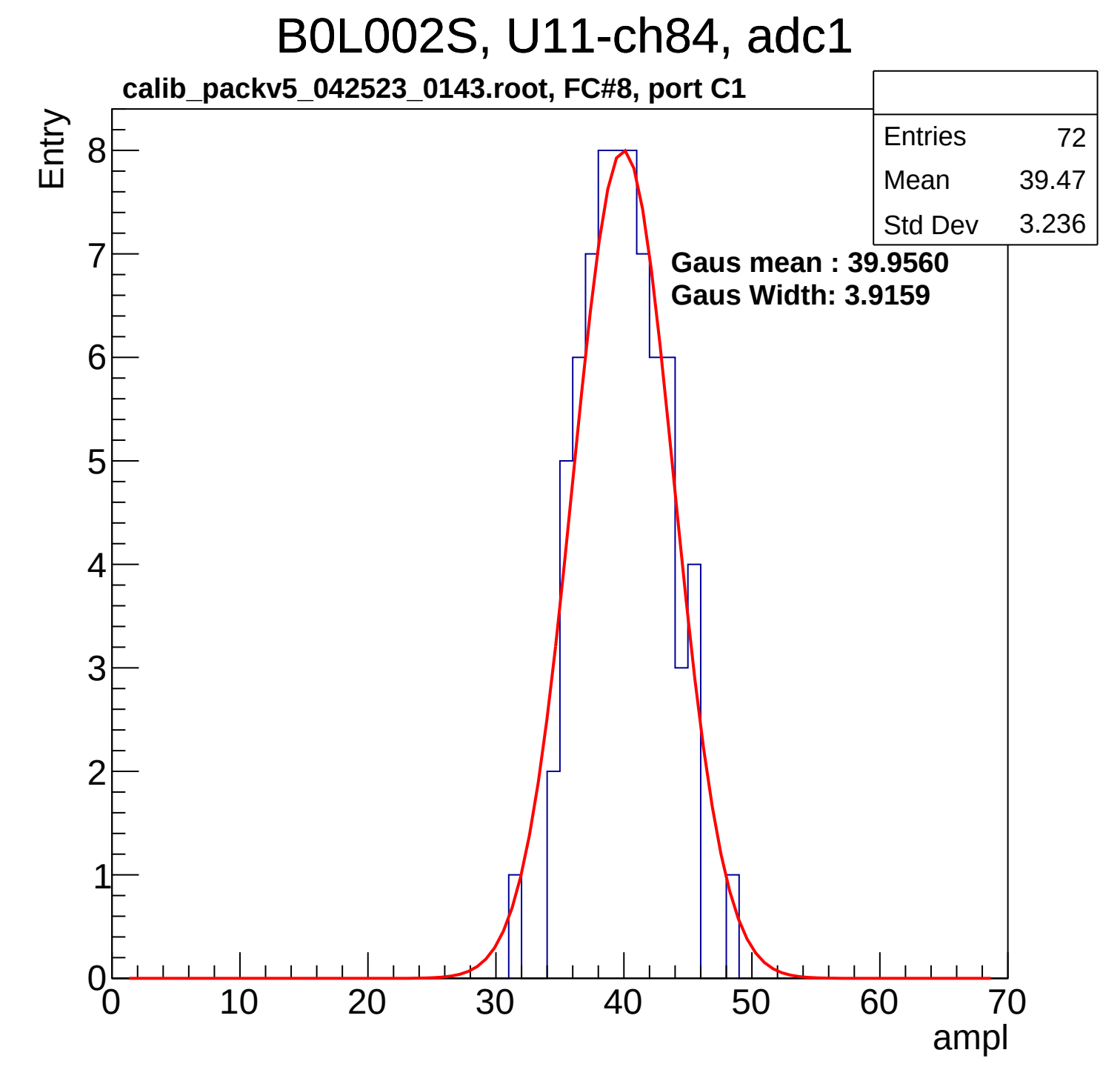
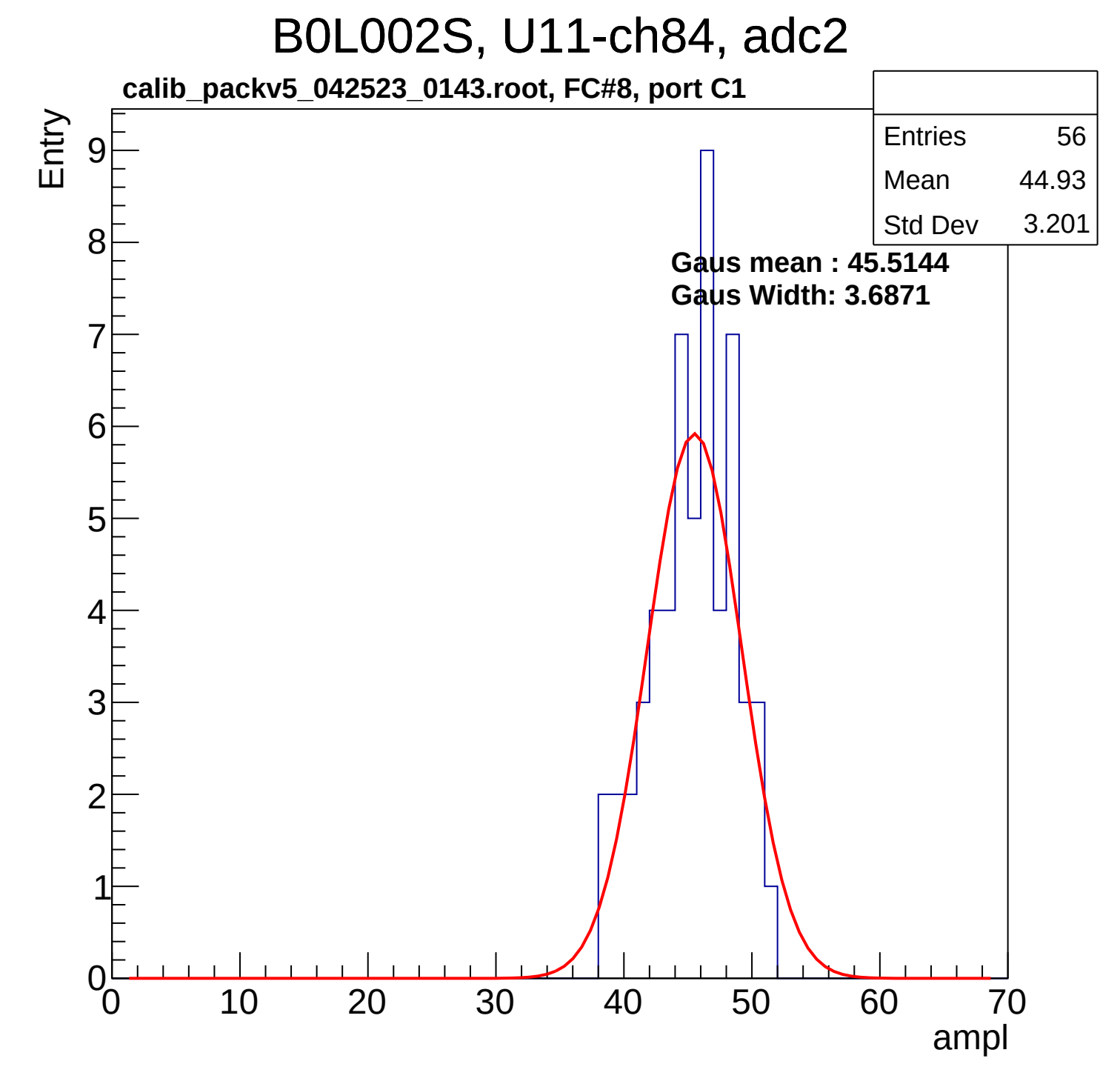
50
60
70
ampl


B0L002S, U11-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 32.33
Std Dev 2.527
Gaus mean : 33.0379
Gaus Width: 2.6892
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 39.47
Std Dev 3.236
Gaus mean : 39.9560
Gaus Width: 3.9159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.93
Std Dev 3.201
Gaus mean : 45.5144
Gaus Width: 3.6871
Entry
ampl
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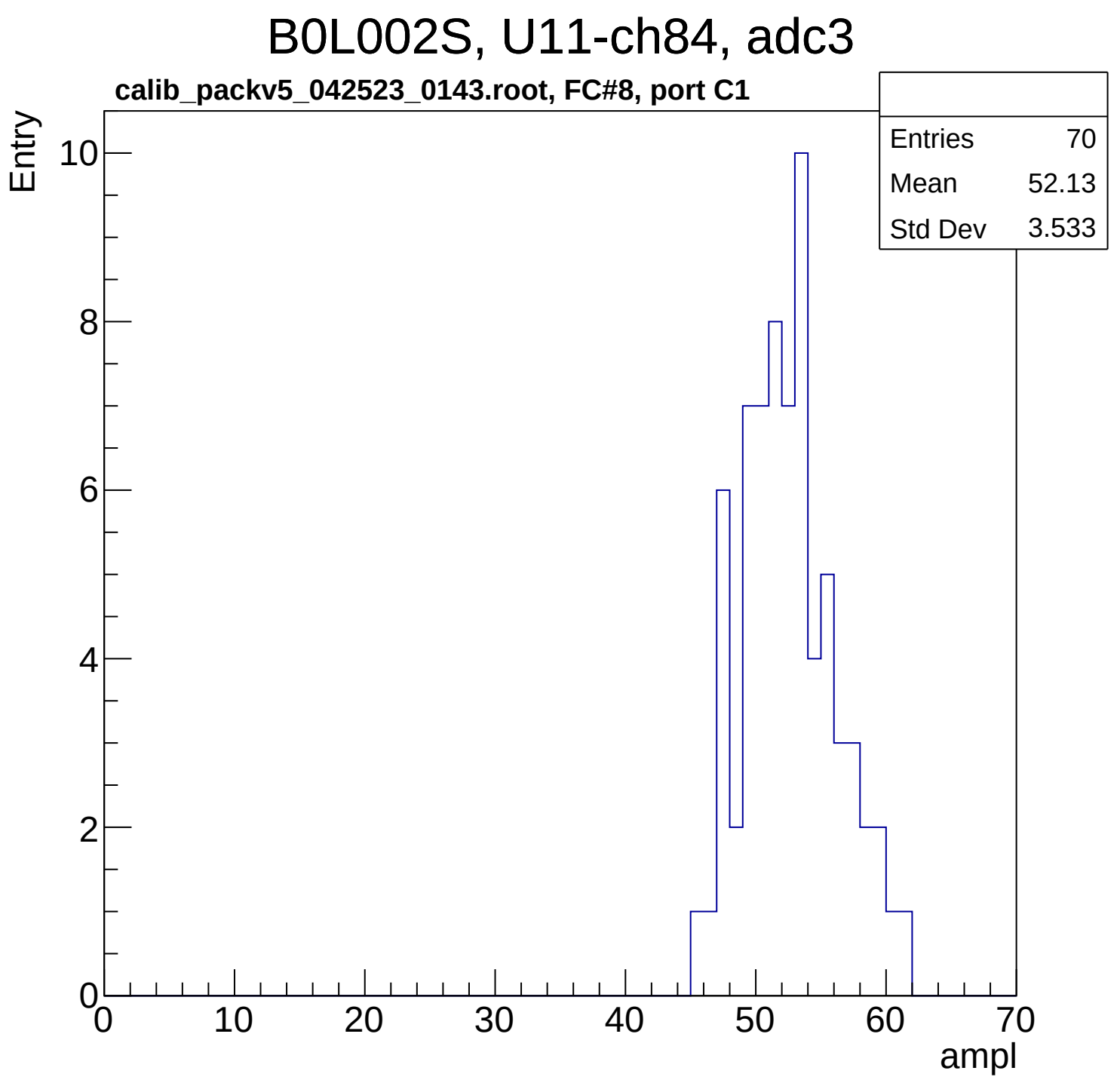
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 52.13
Std Dev 3.533
Entry
ampl
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0
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60
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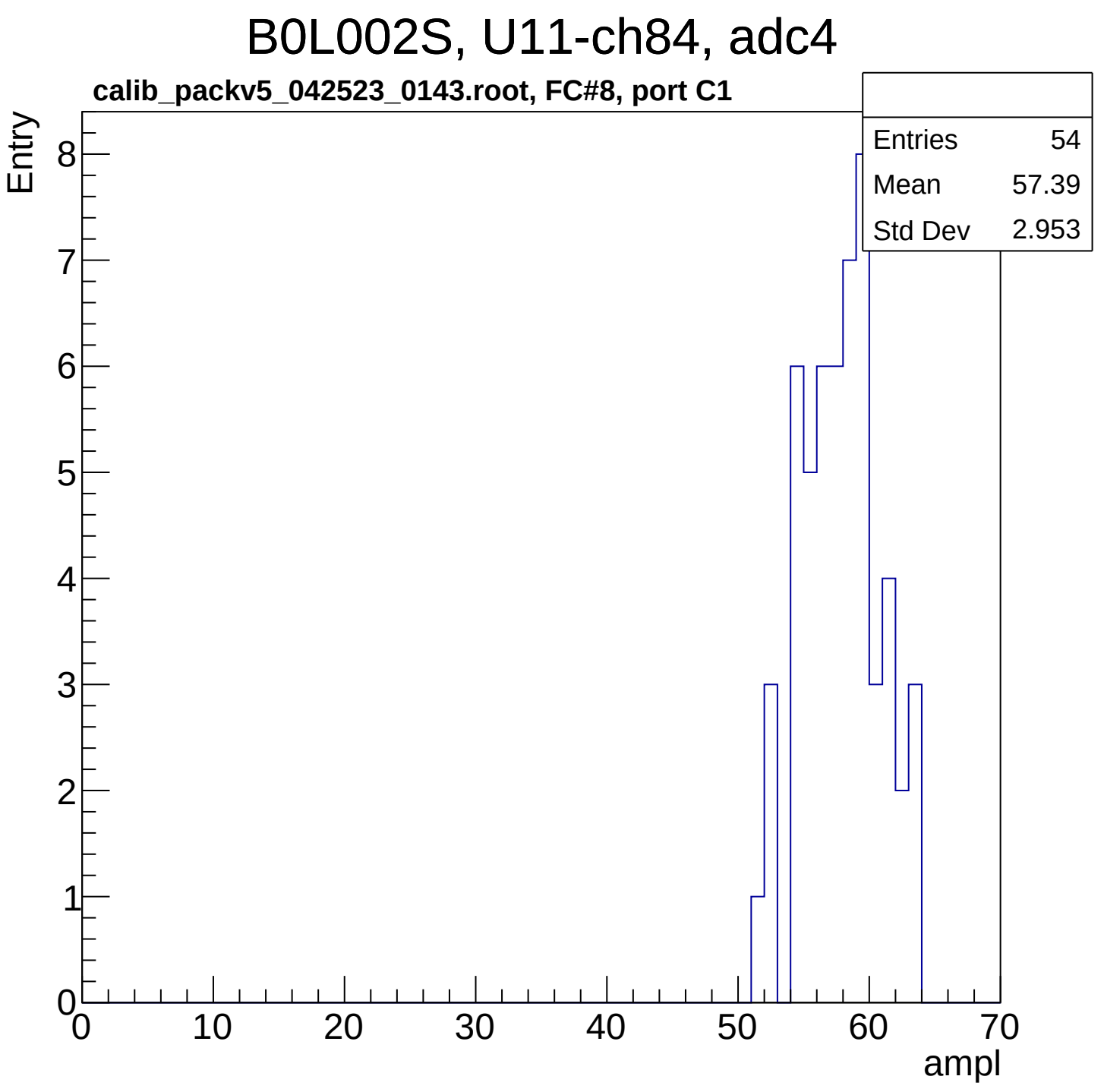
B0L002S, U11-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.39
Std Dev 2.953
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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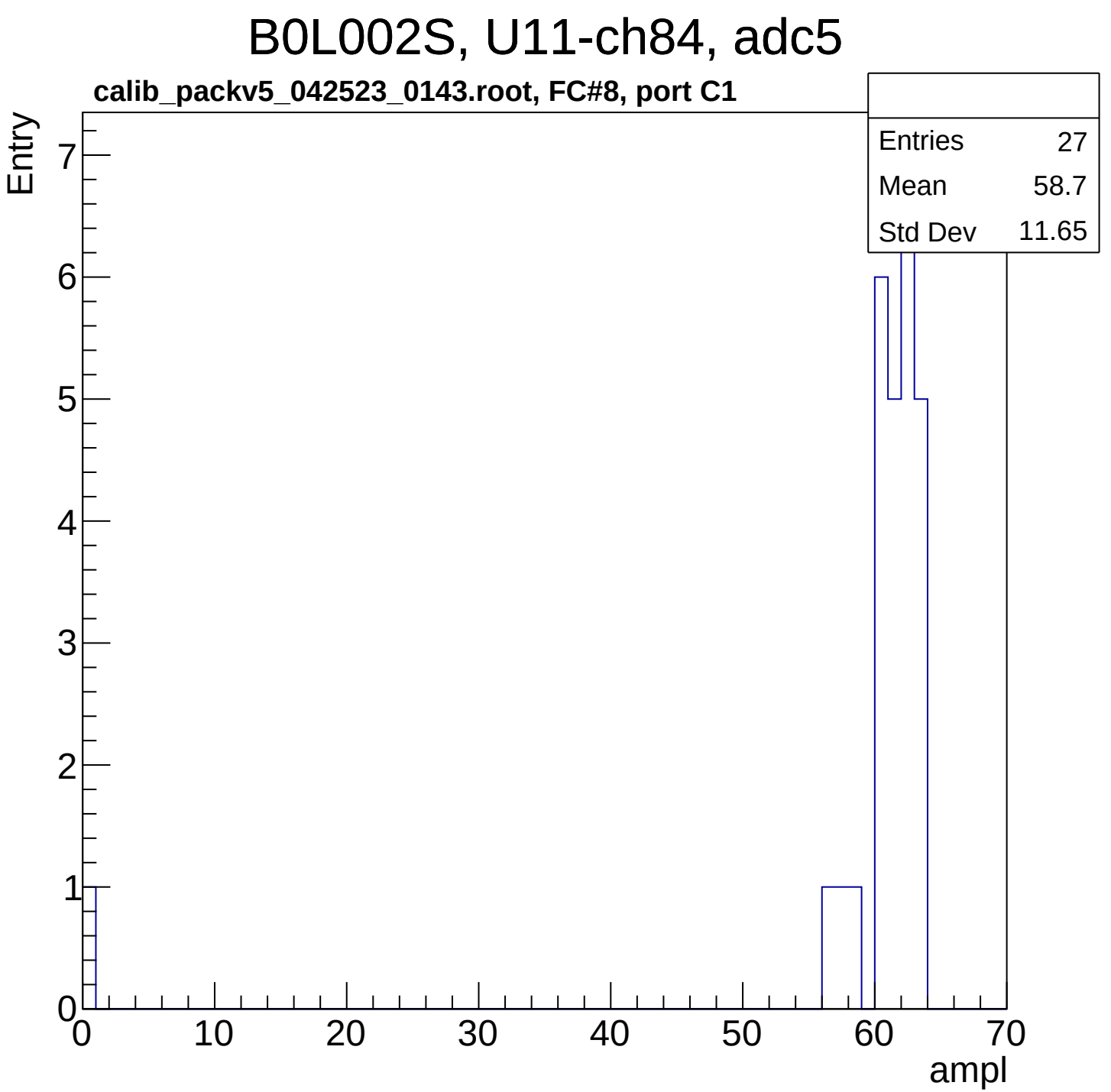
B0L002S, U11-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.7
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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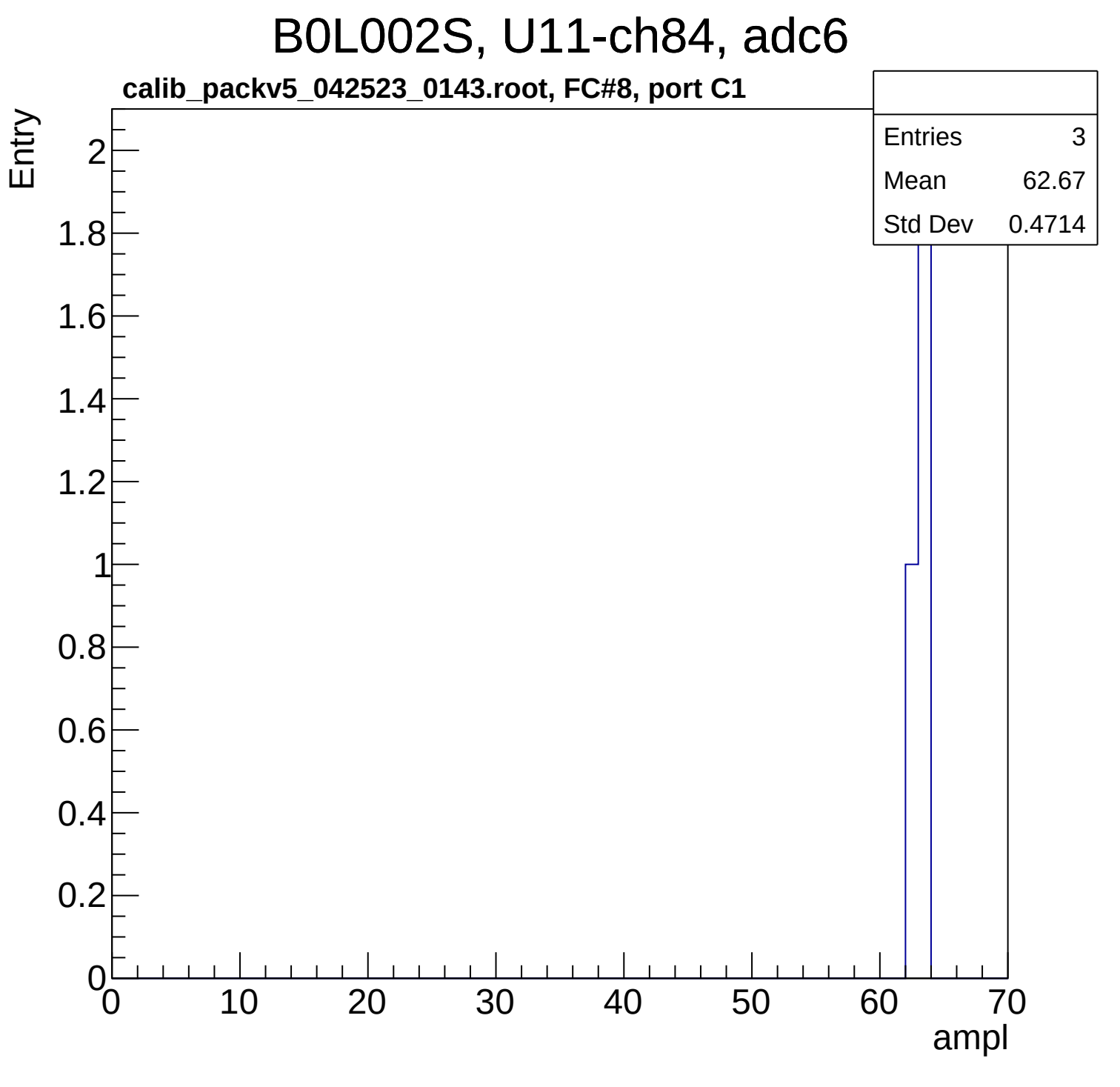
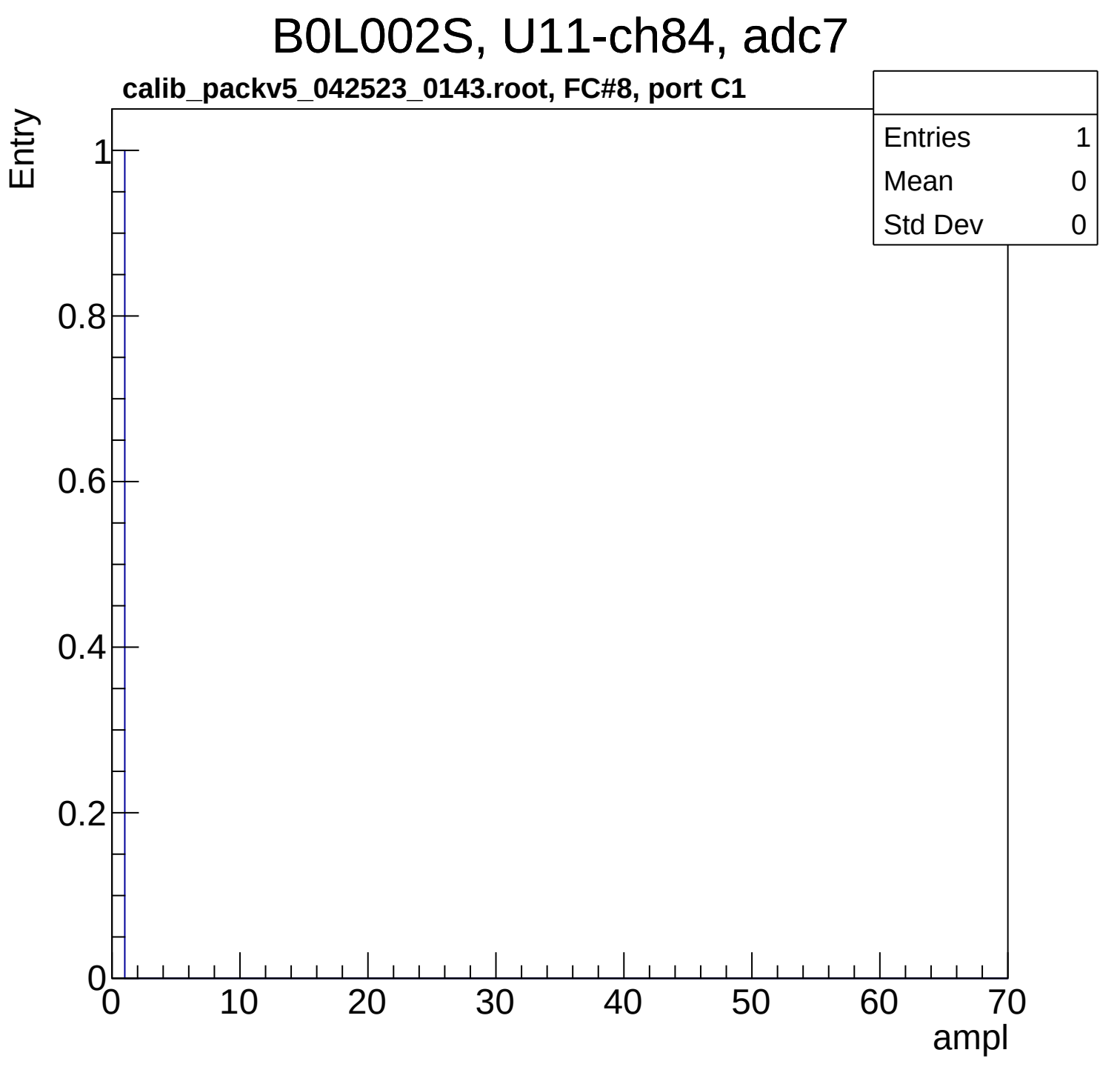

B0L002S, U11-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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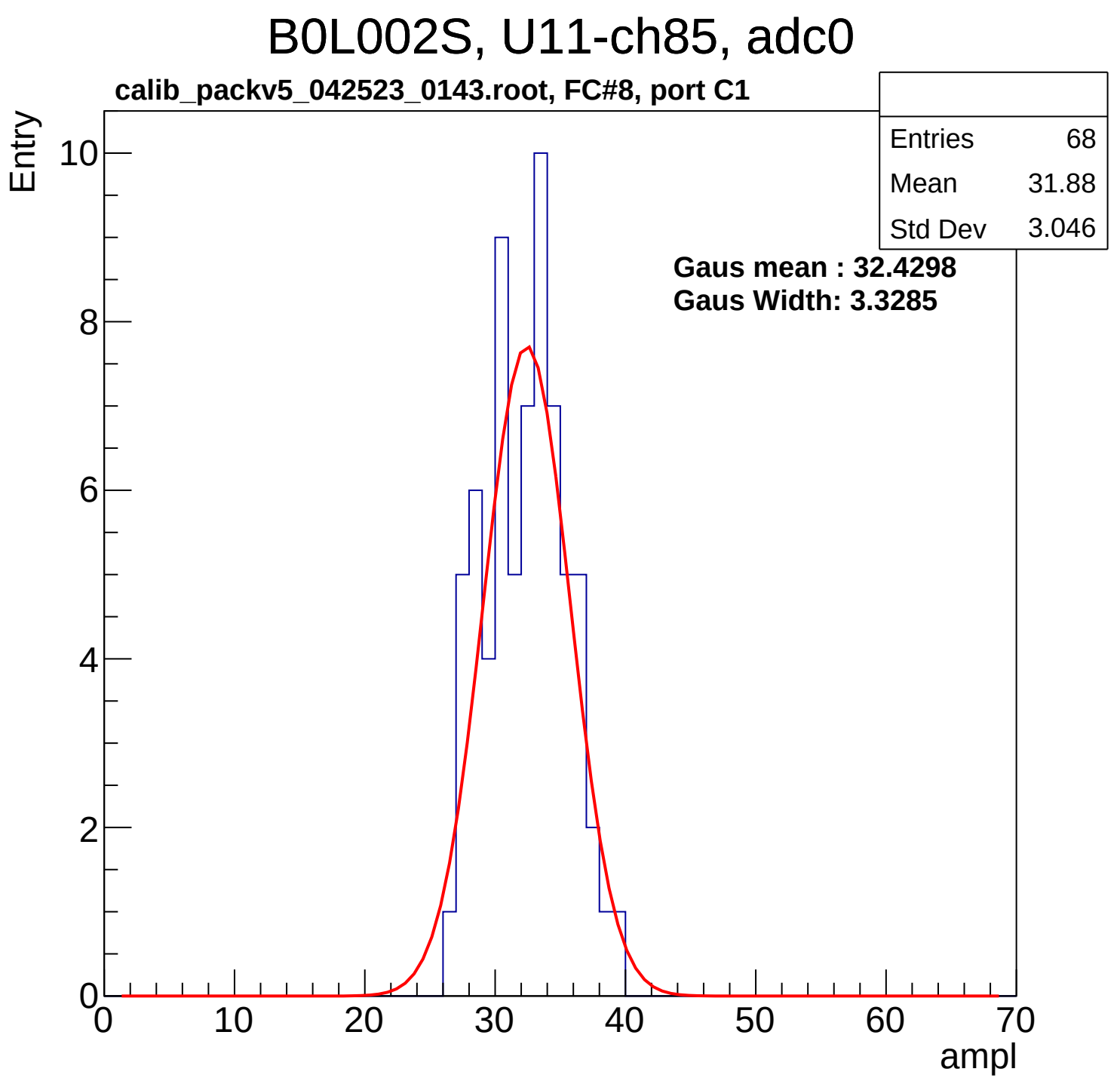
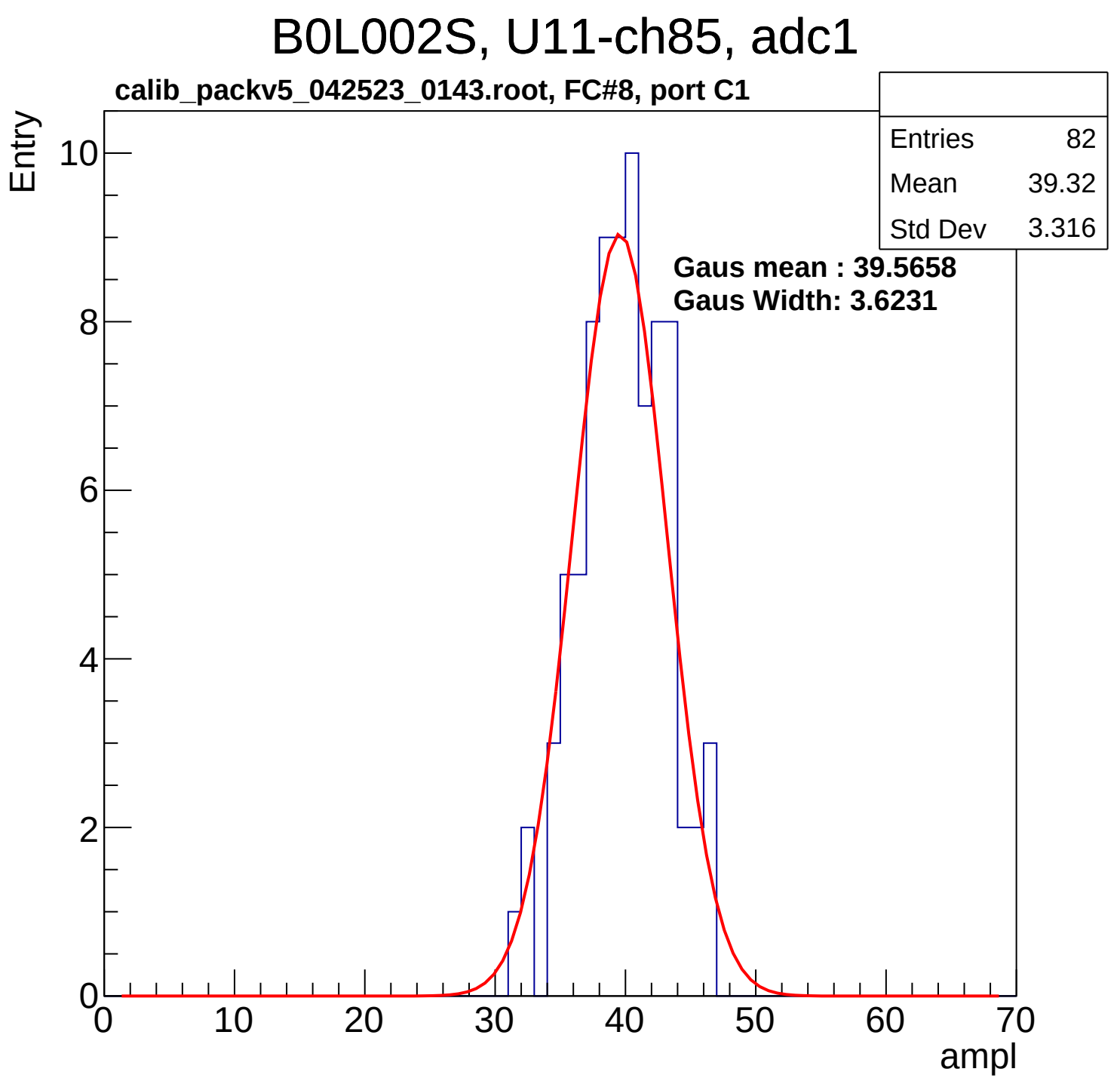
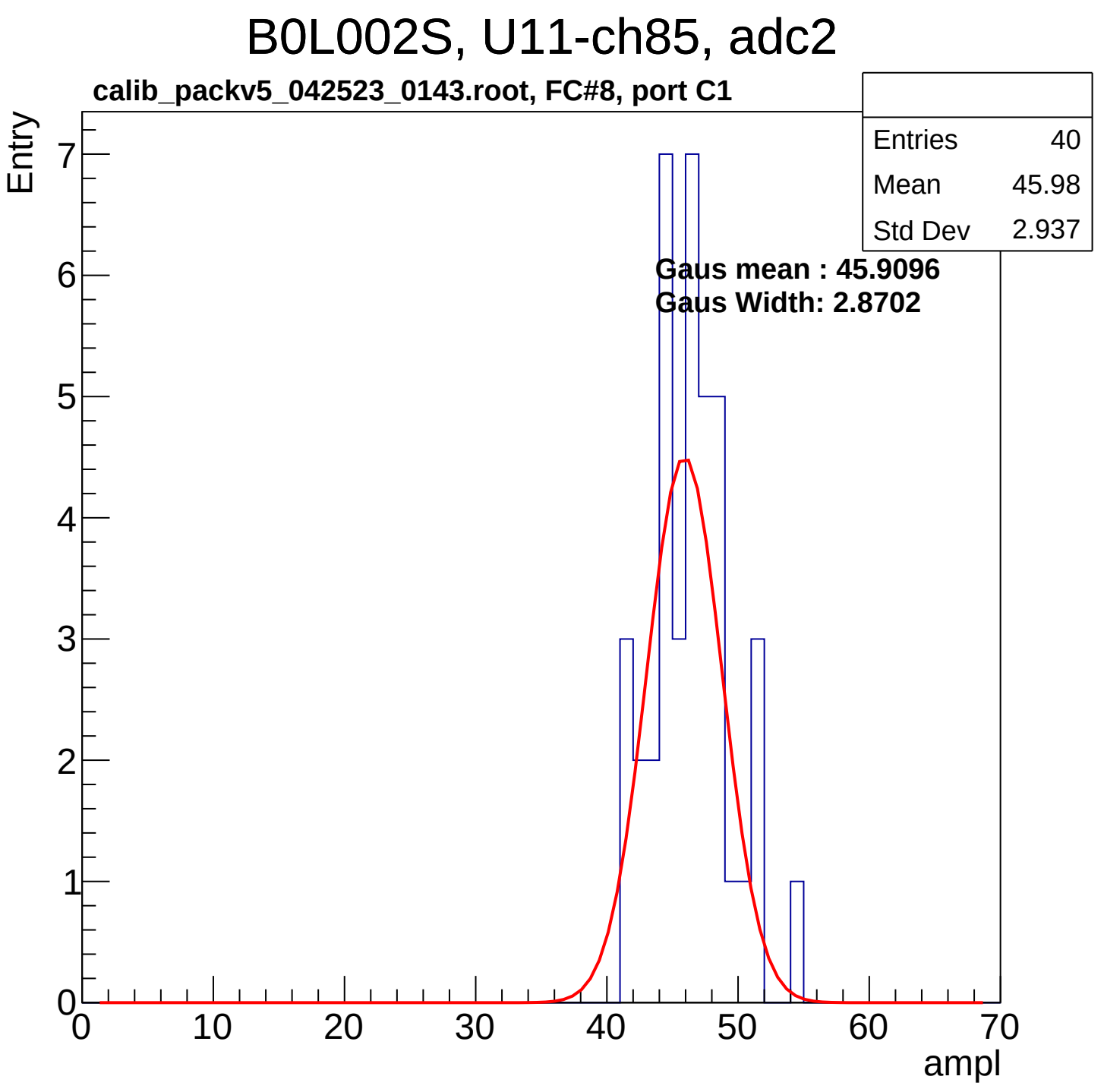
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.88
Std Dev 3.046
Gaus mean : 32.4298
Gaus Width: 3.3285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 39.32
Std Dev 3.316
Gaus mean : 39.5658
Gaus Width: 3.6231
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 45.98
Std Dev 2.937
Gaus mean : 45.9096
Gaus Width: 2.8702
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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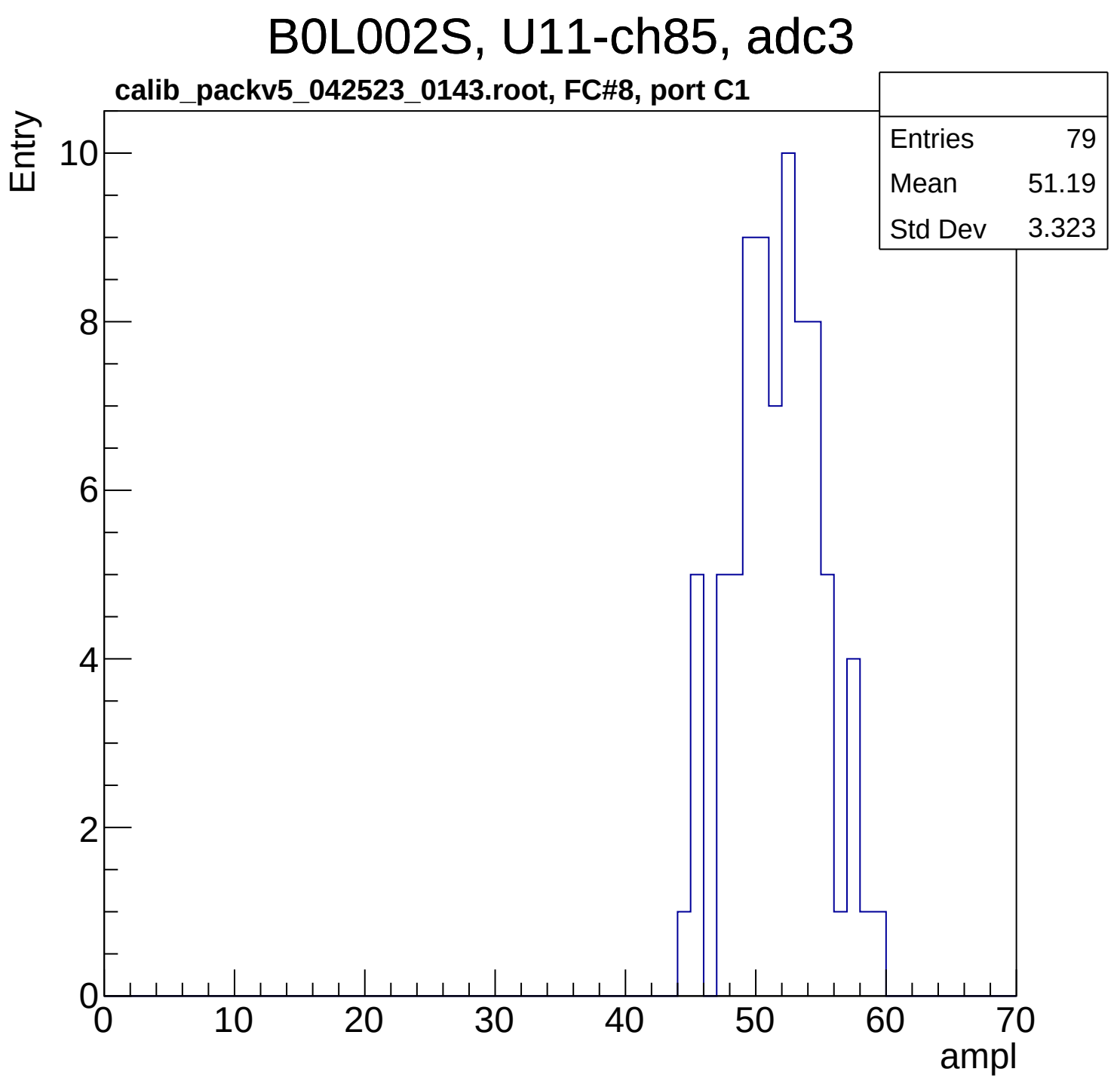
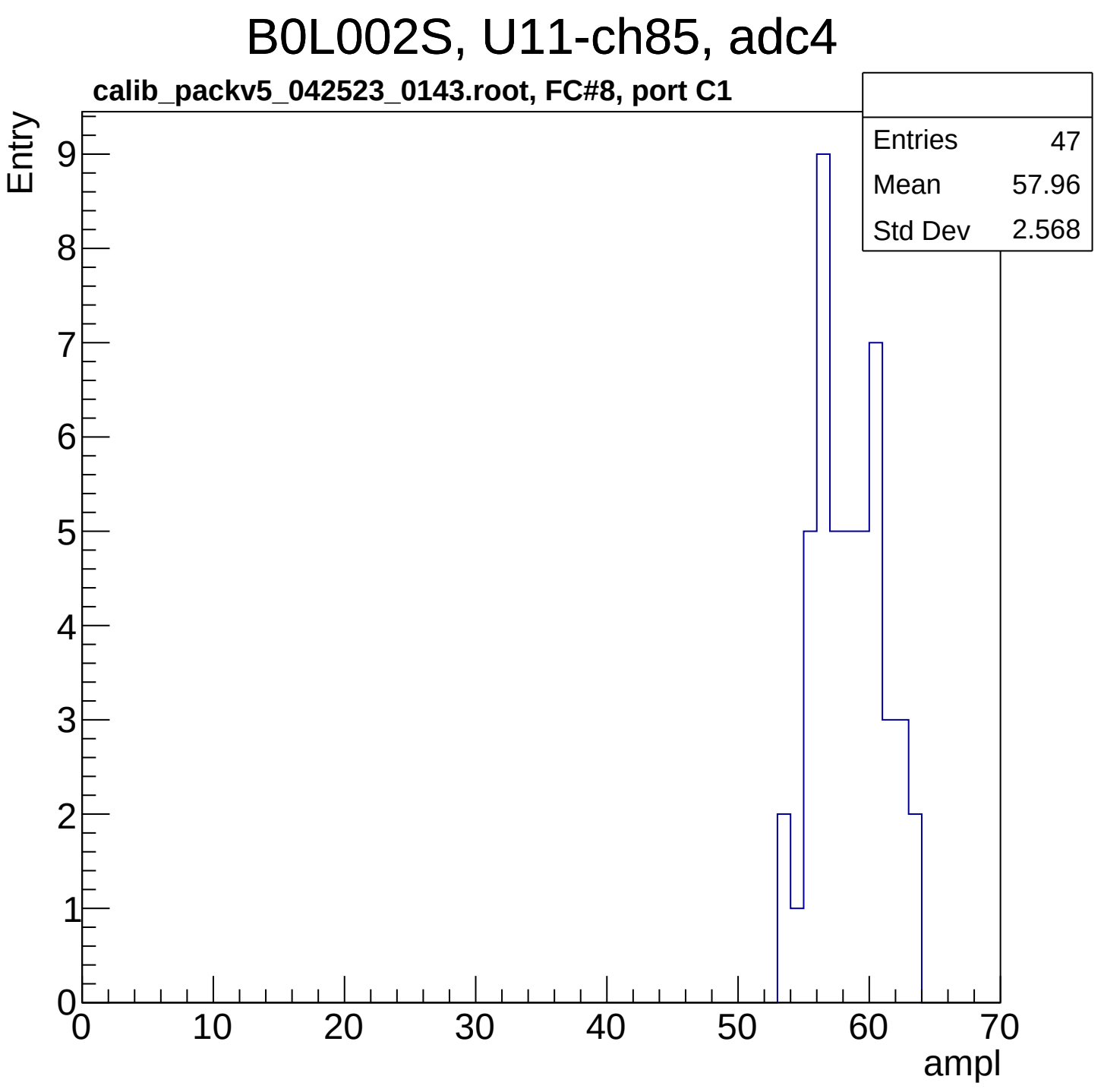

B0L002S, U11-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 51.19
Std Dev 3.323
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.96
Std Dev 2.568
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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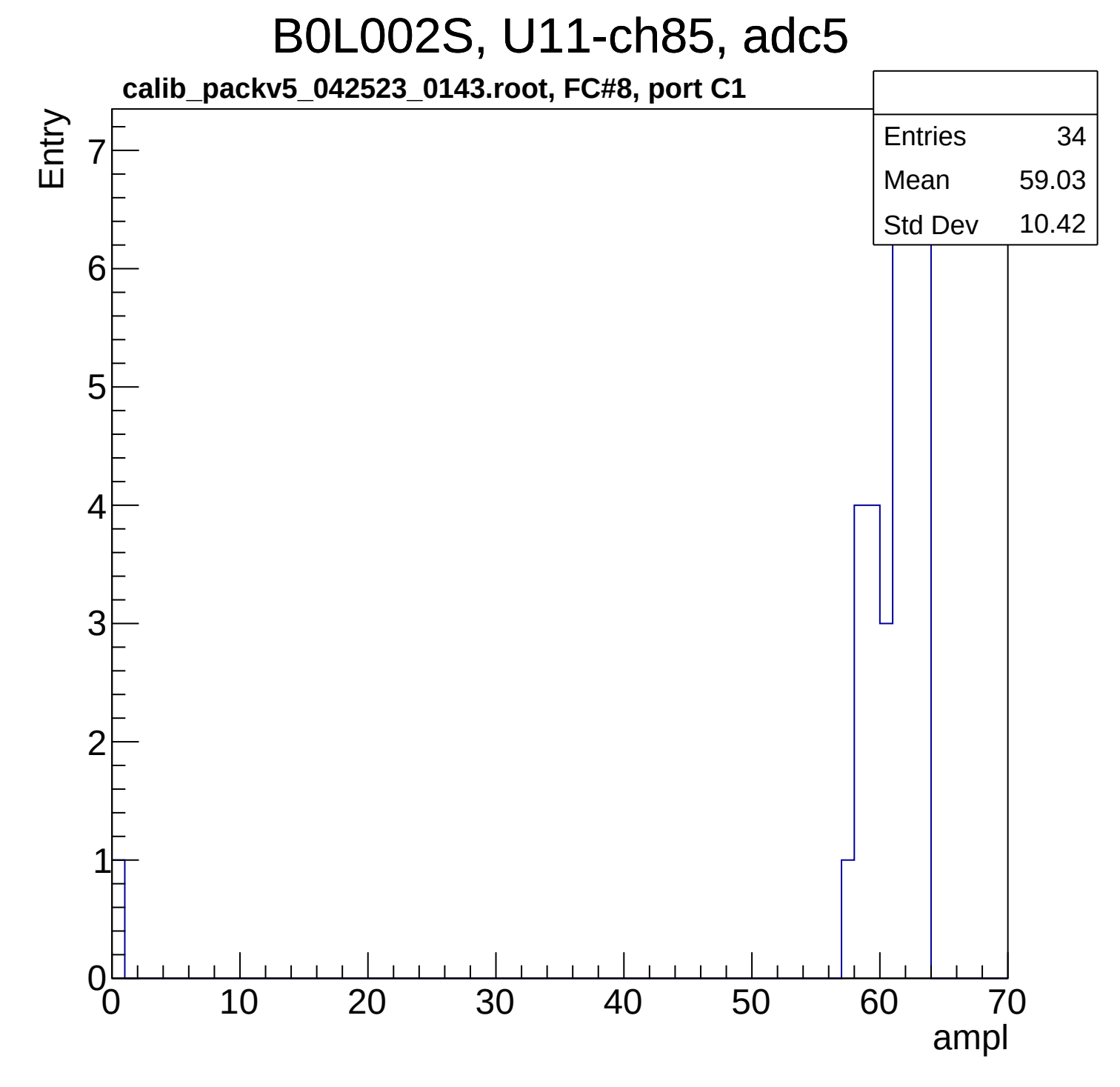
B0L002S, U11-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.03
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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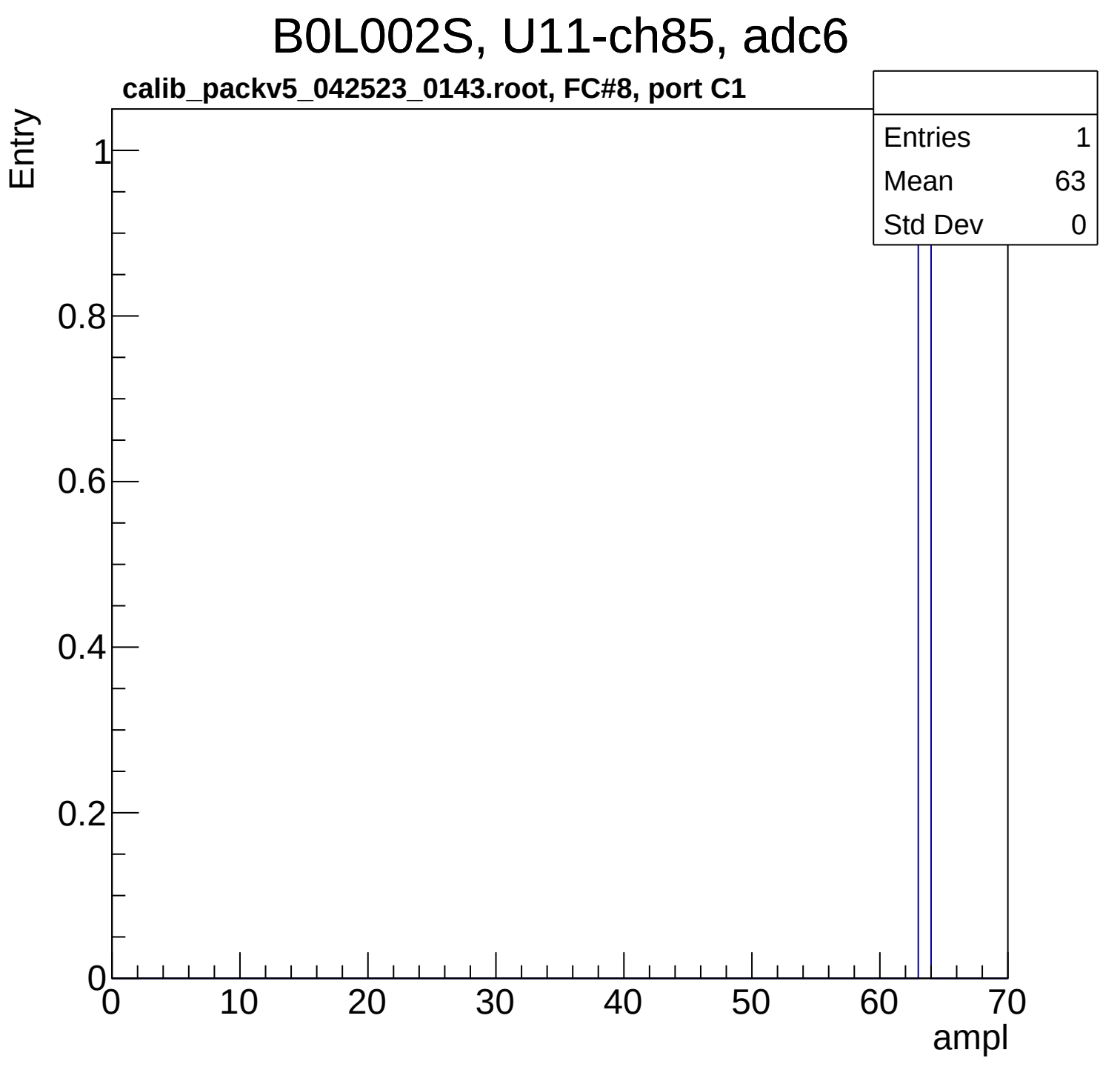
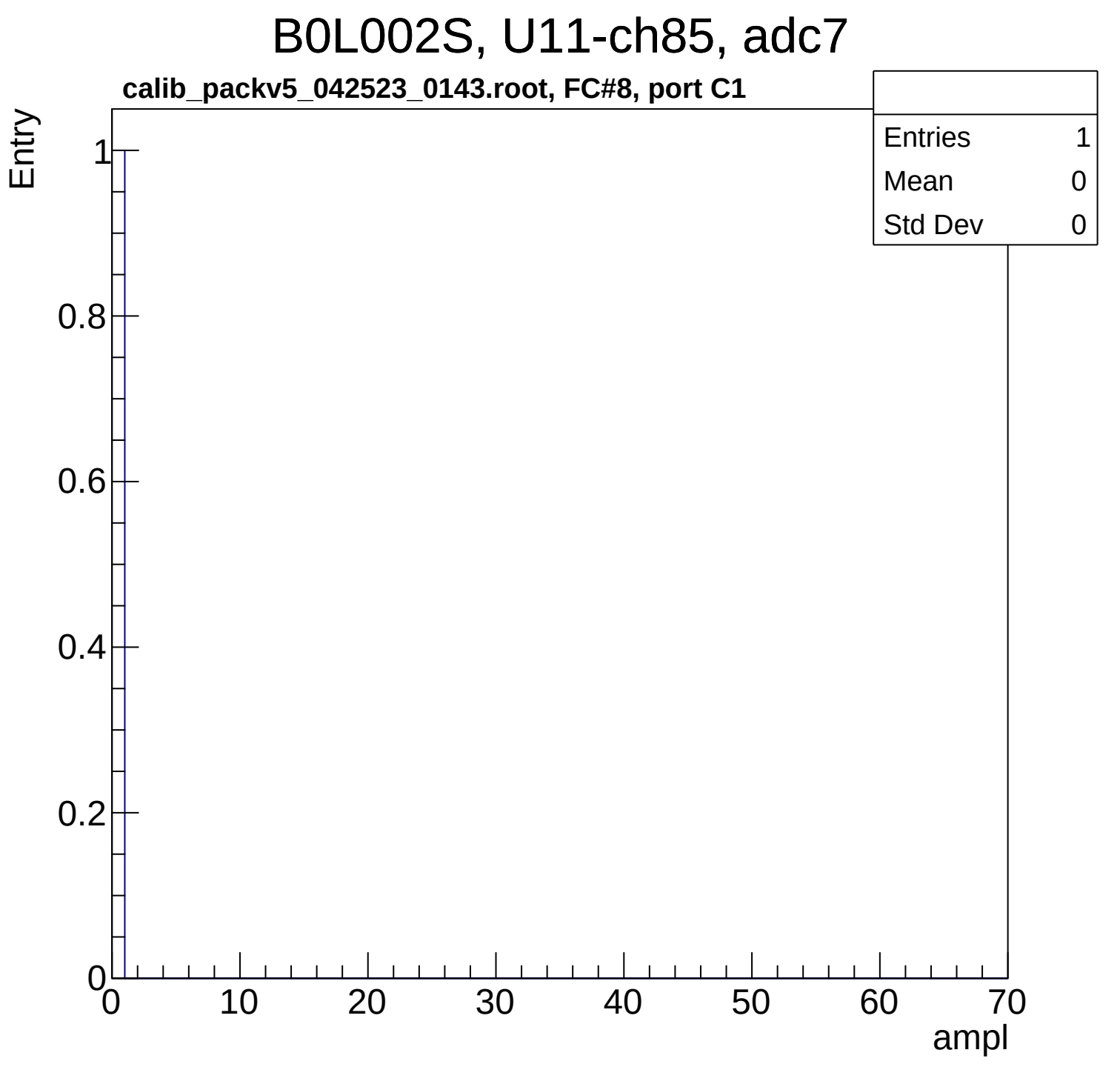
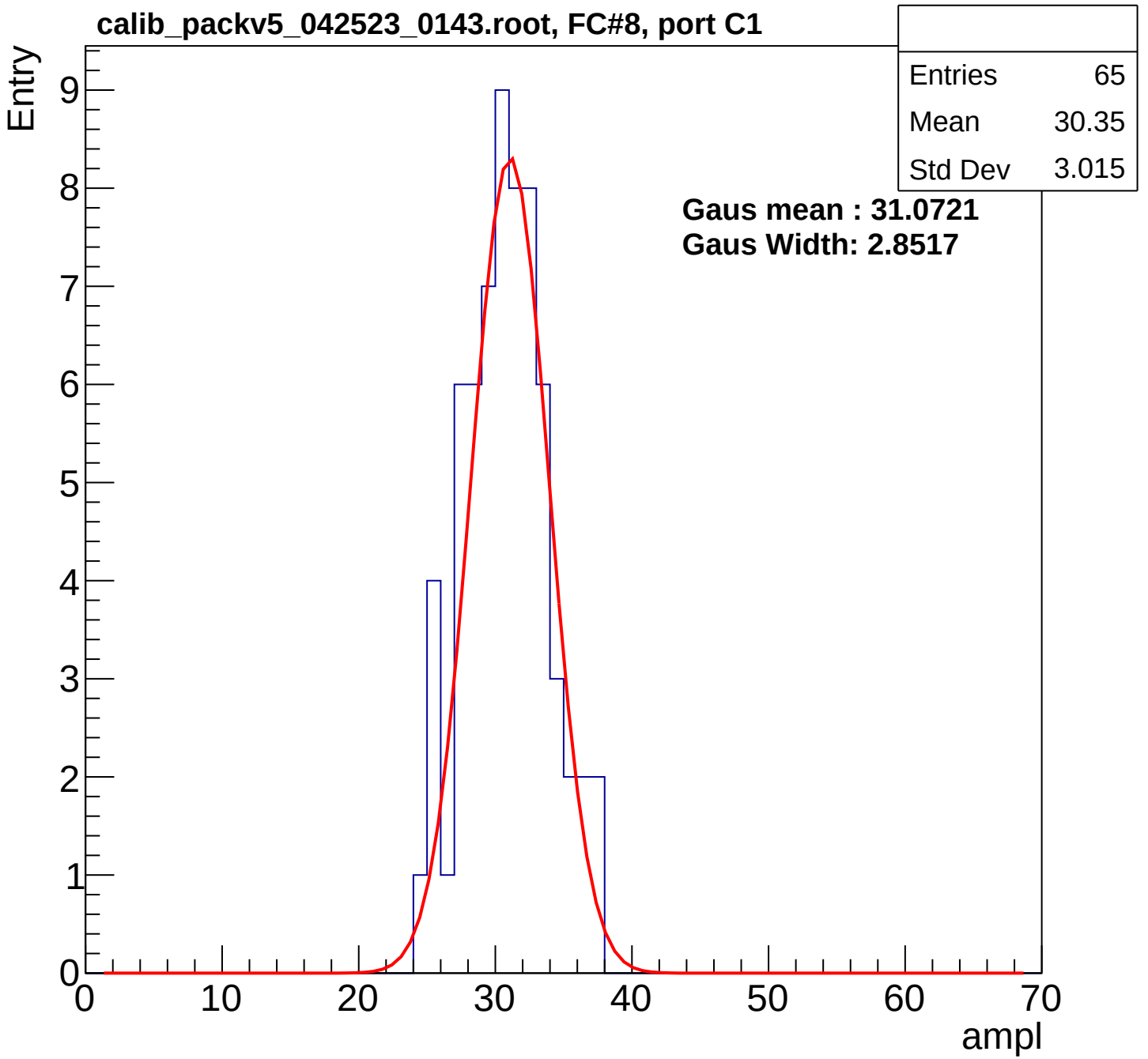
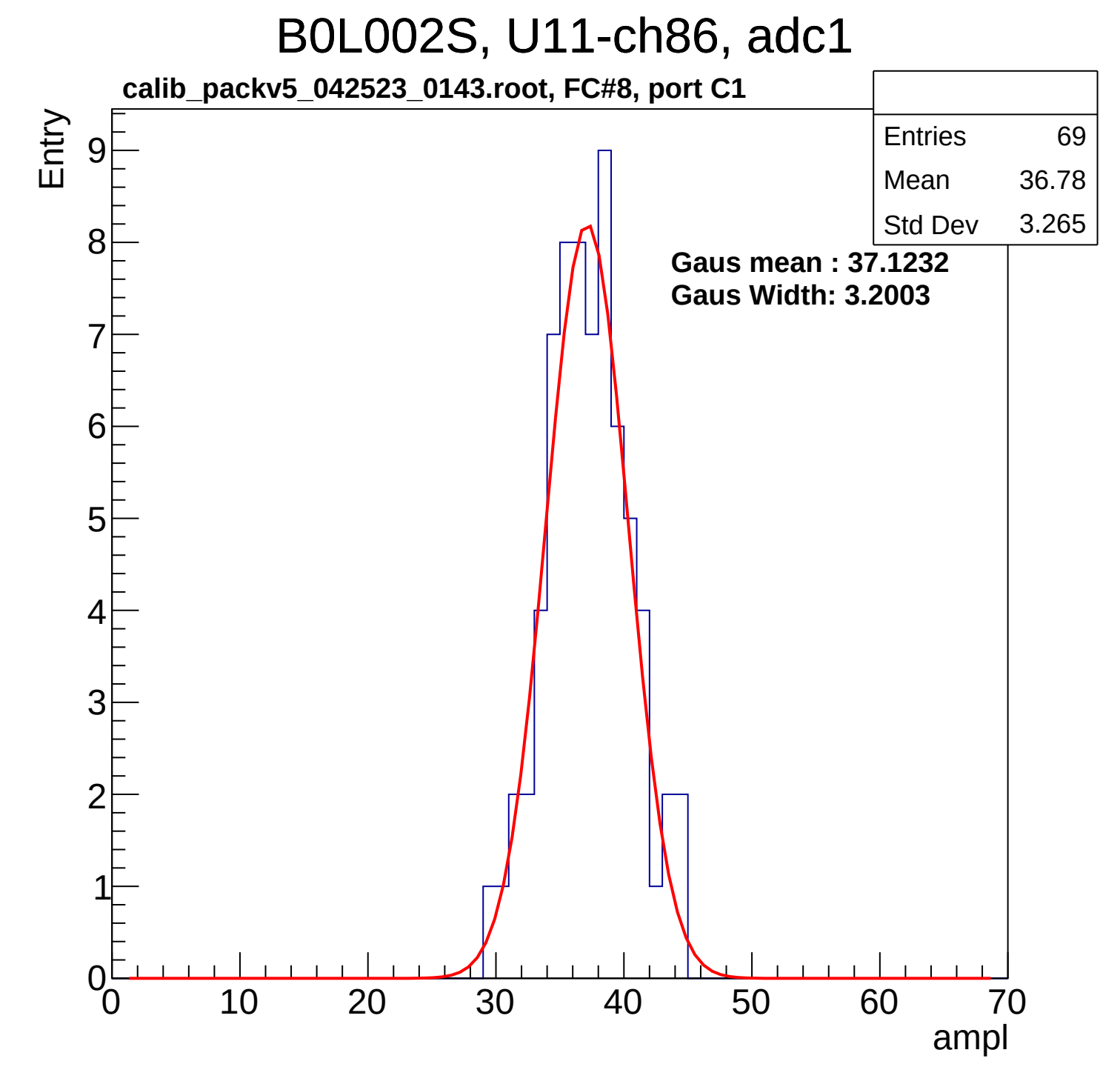

B0L002S, U11-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.35
Std Dev 3.015
Gaus mean : 31.0721
Gaus Width: 2.8517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.78
Std Dev 3.265
Gaus mean : 37.1232
Gaus Width: 3.2003
Entry
ampl
0
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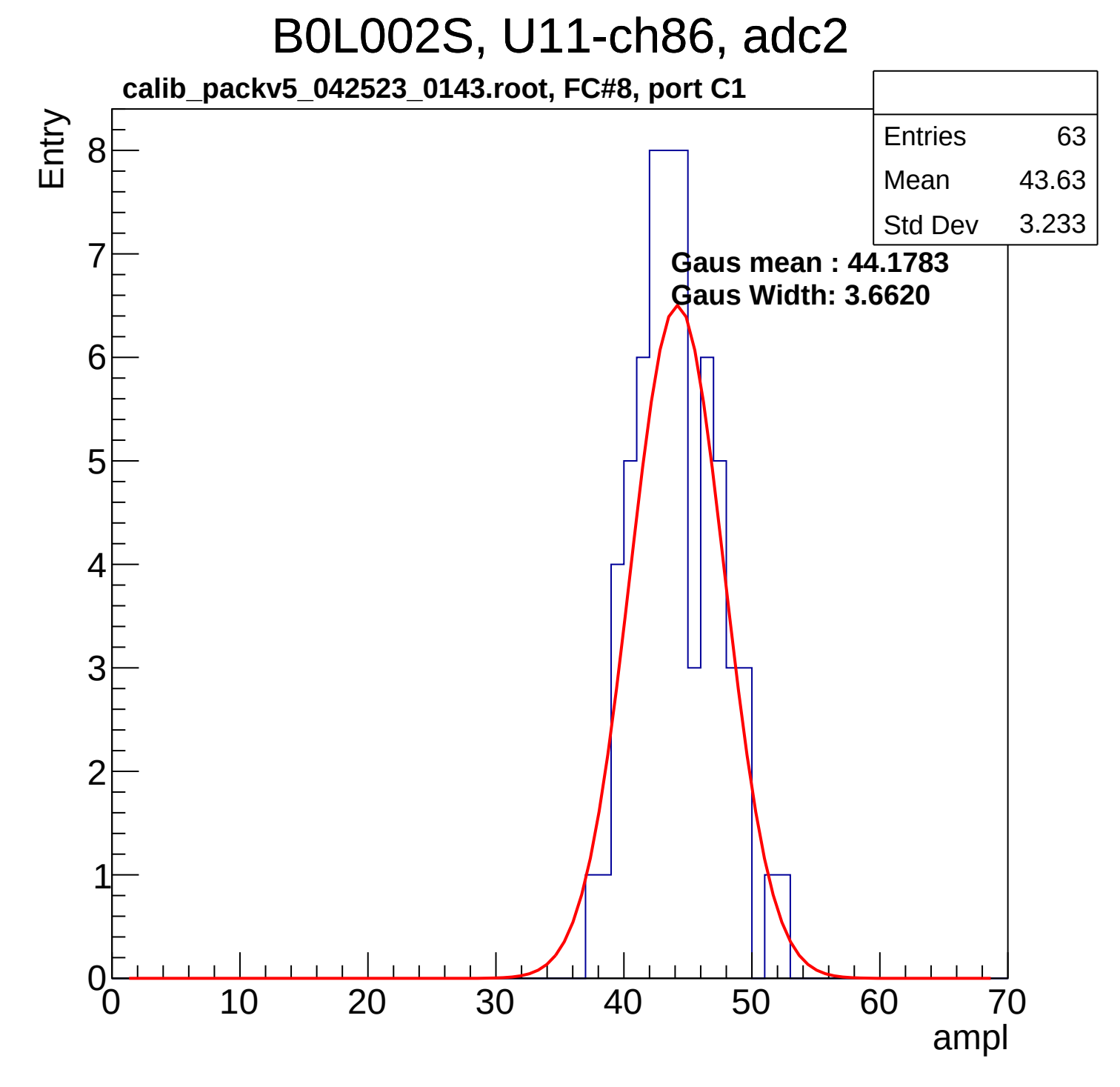
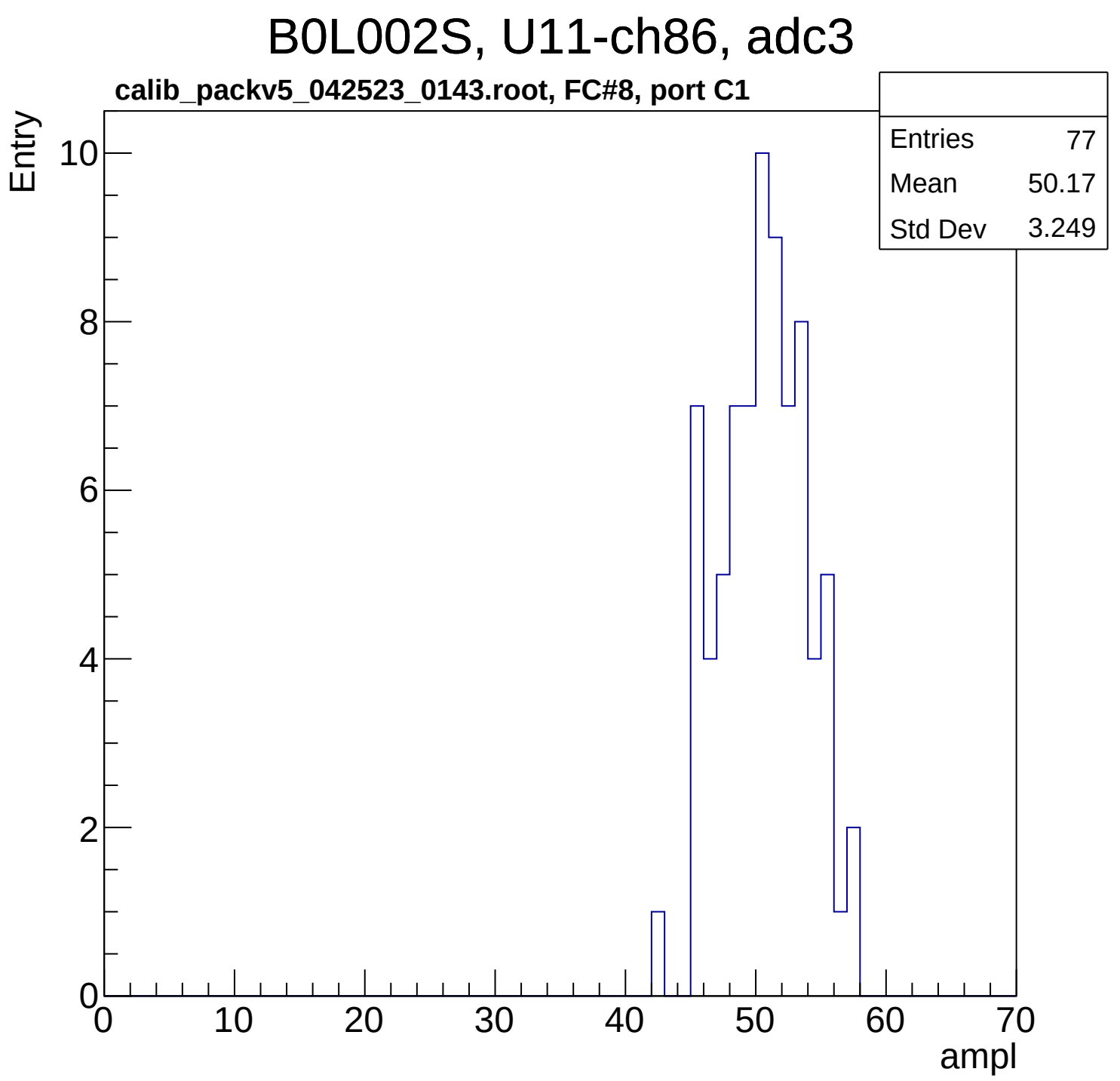
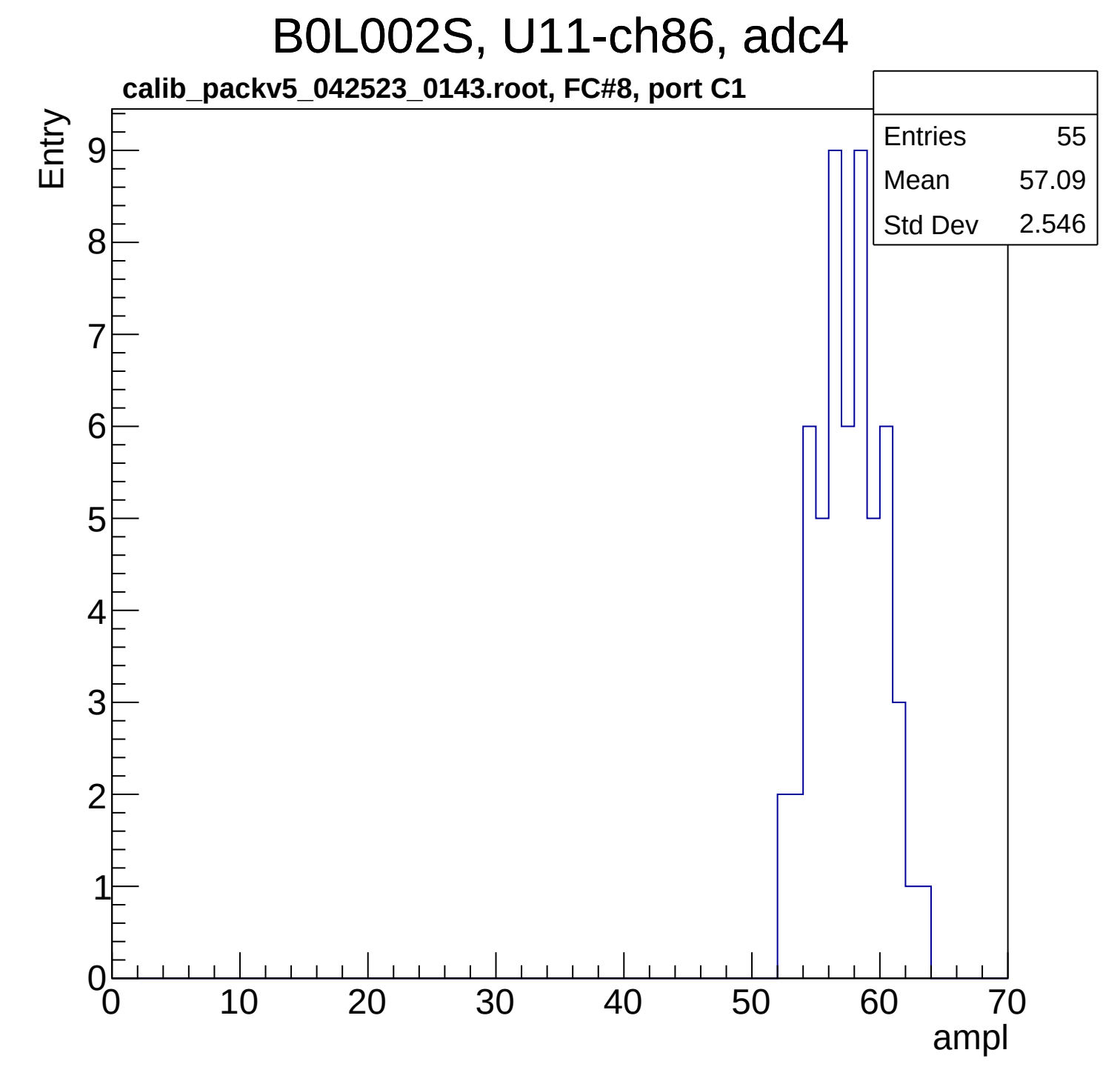
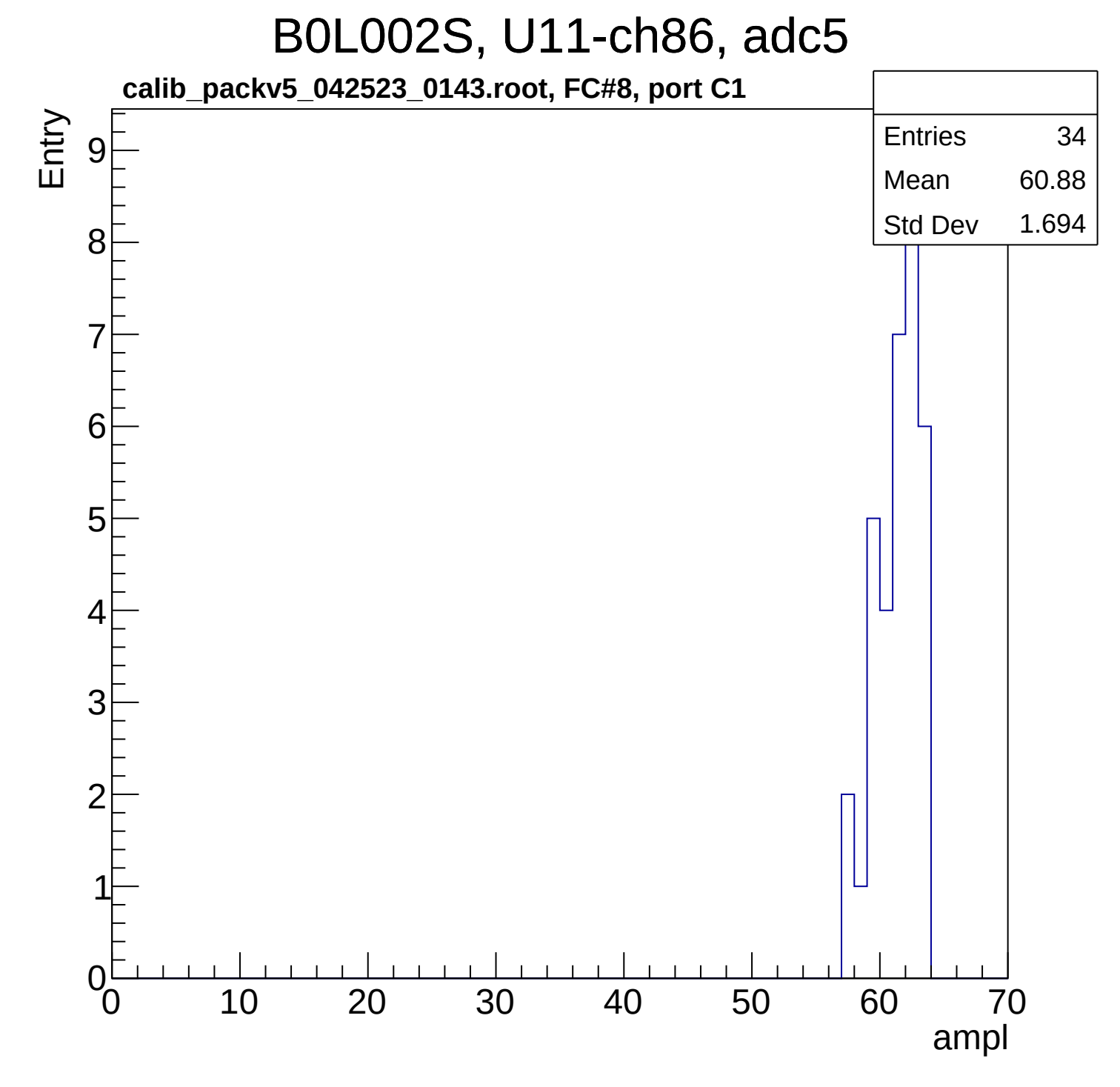
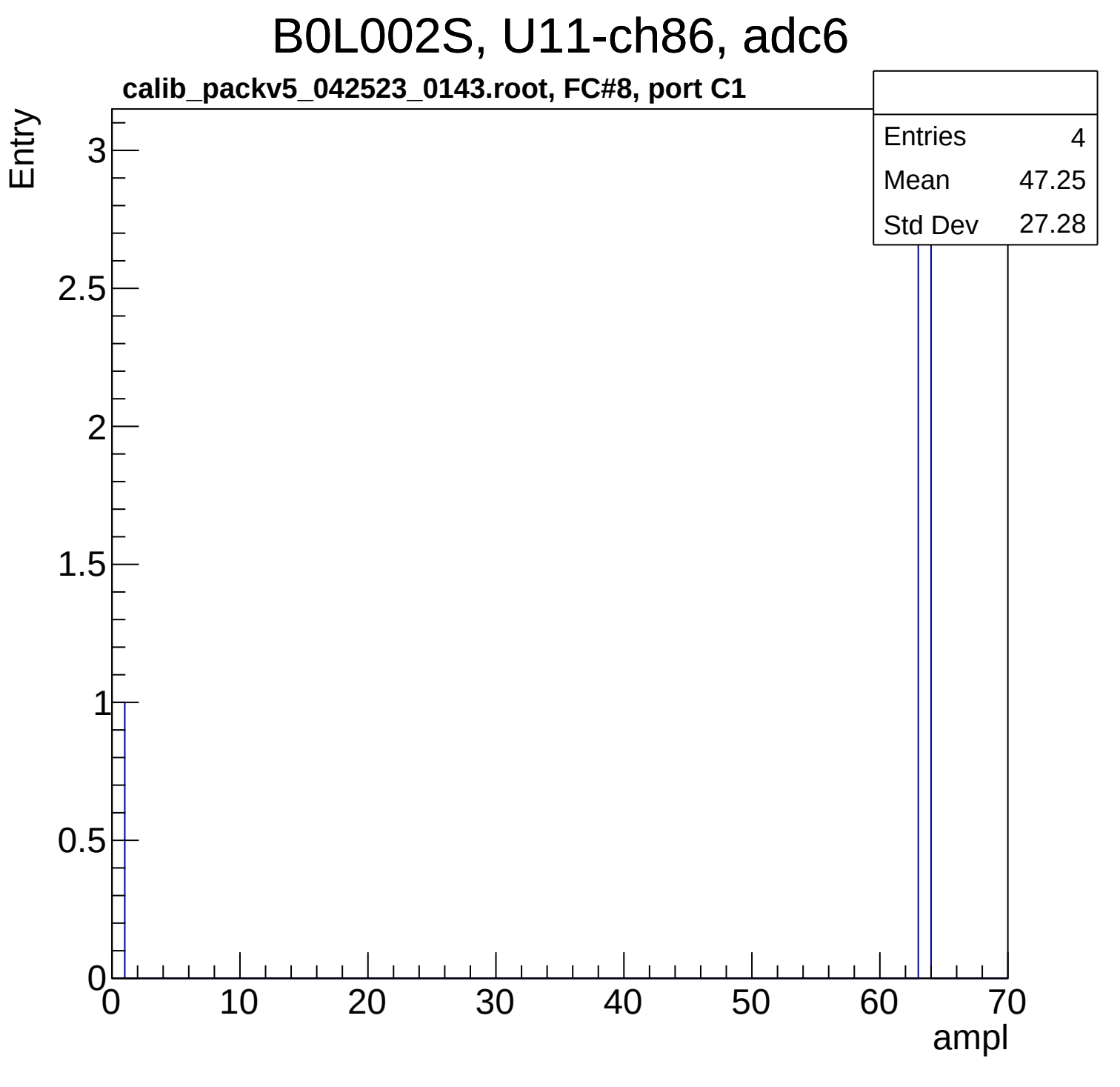
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

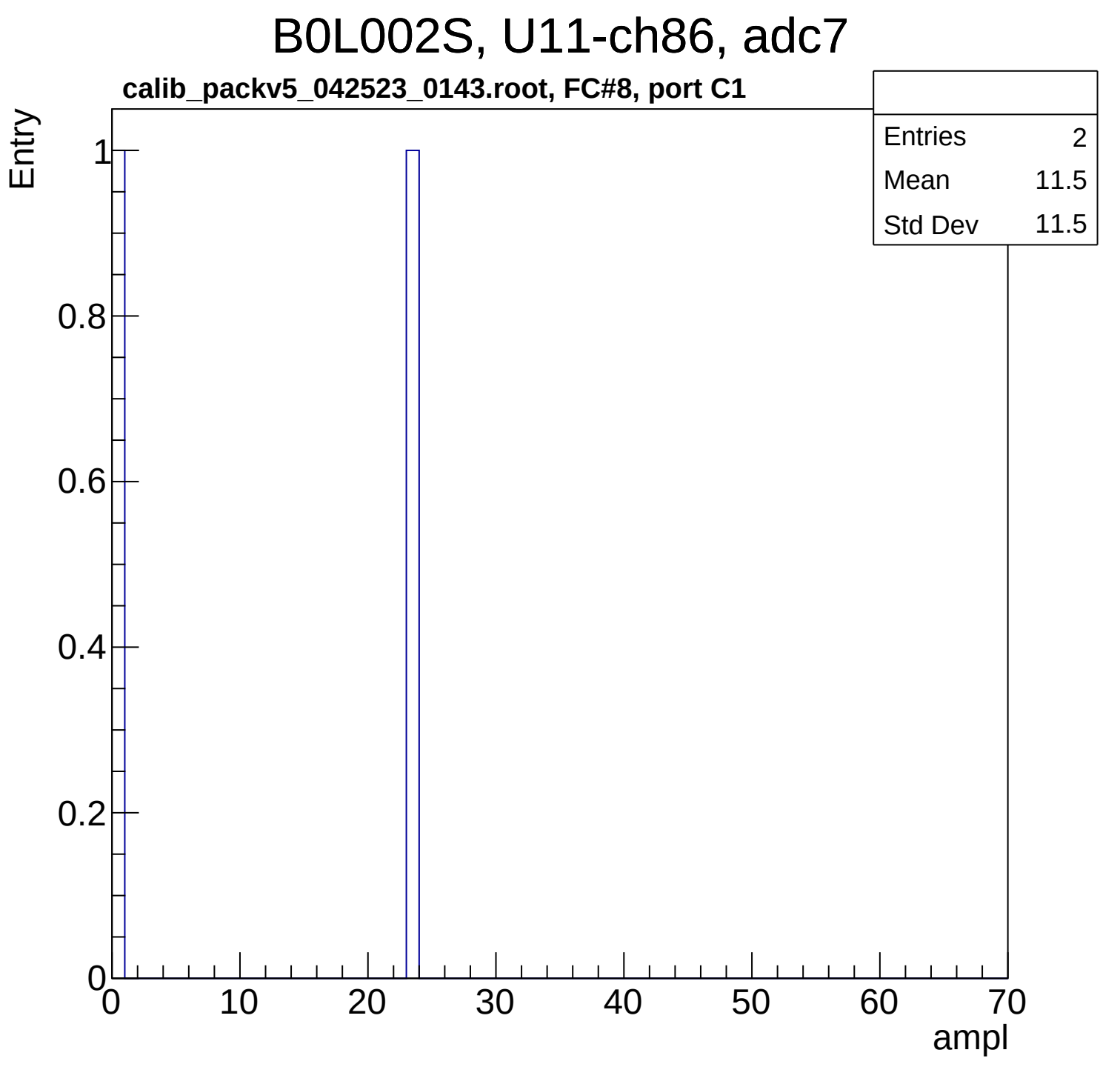

B0L002S, U11-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 43.63
Std Dev 3.233
Gaus mean : 44.1783
Gaus Width: 3.6620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

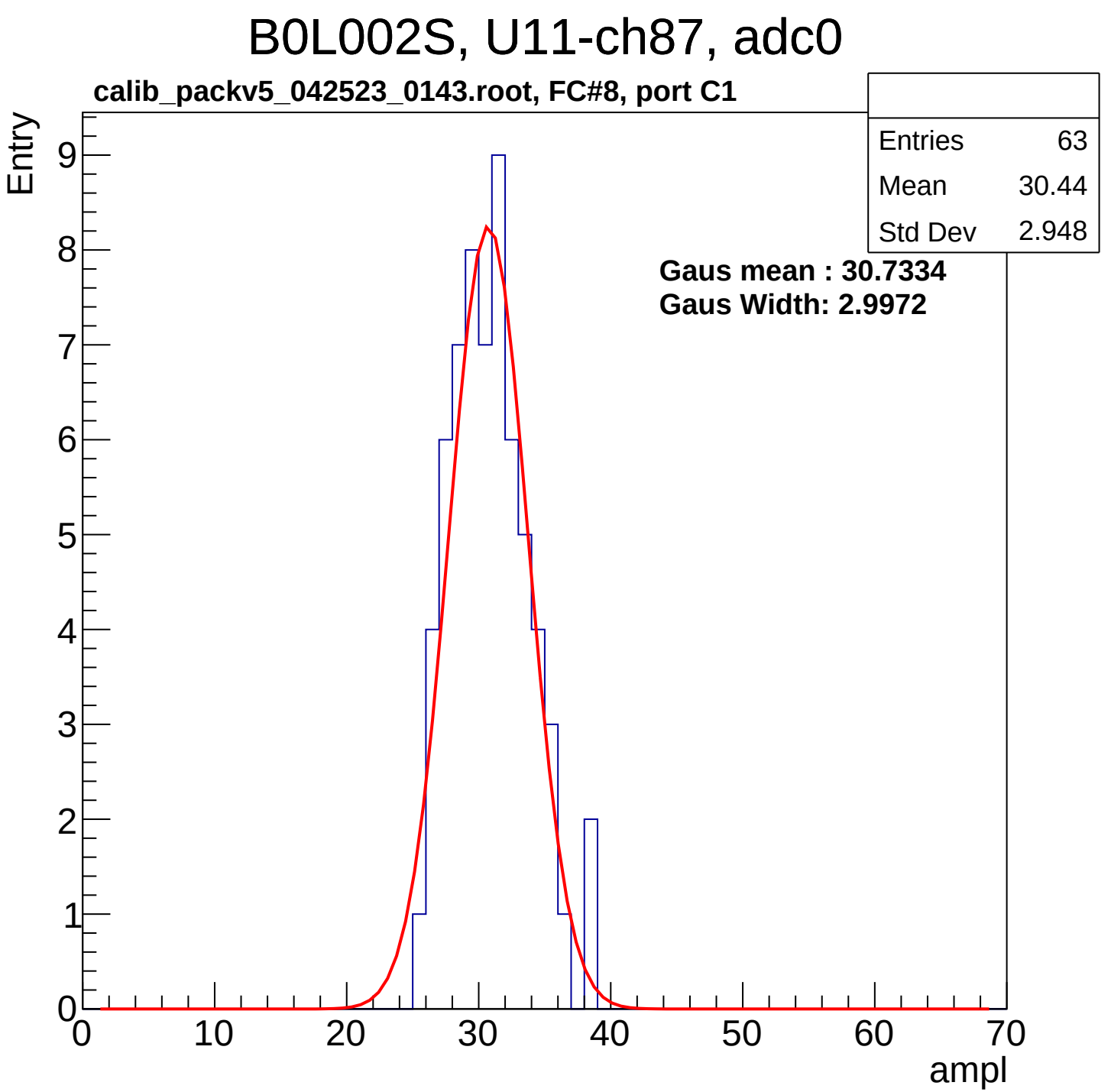

B0L002S, U11-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 50.17
Std Dev 3.249
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

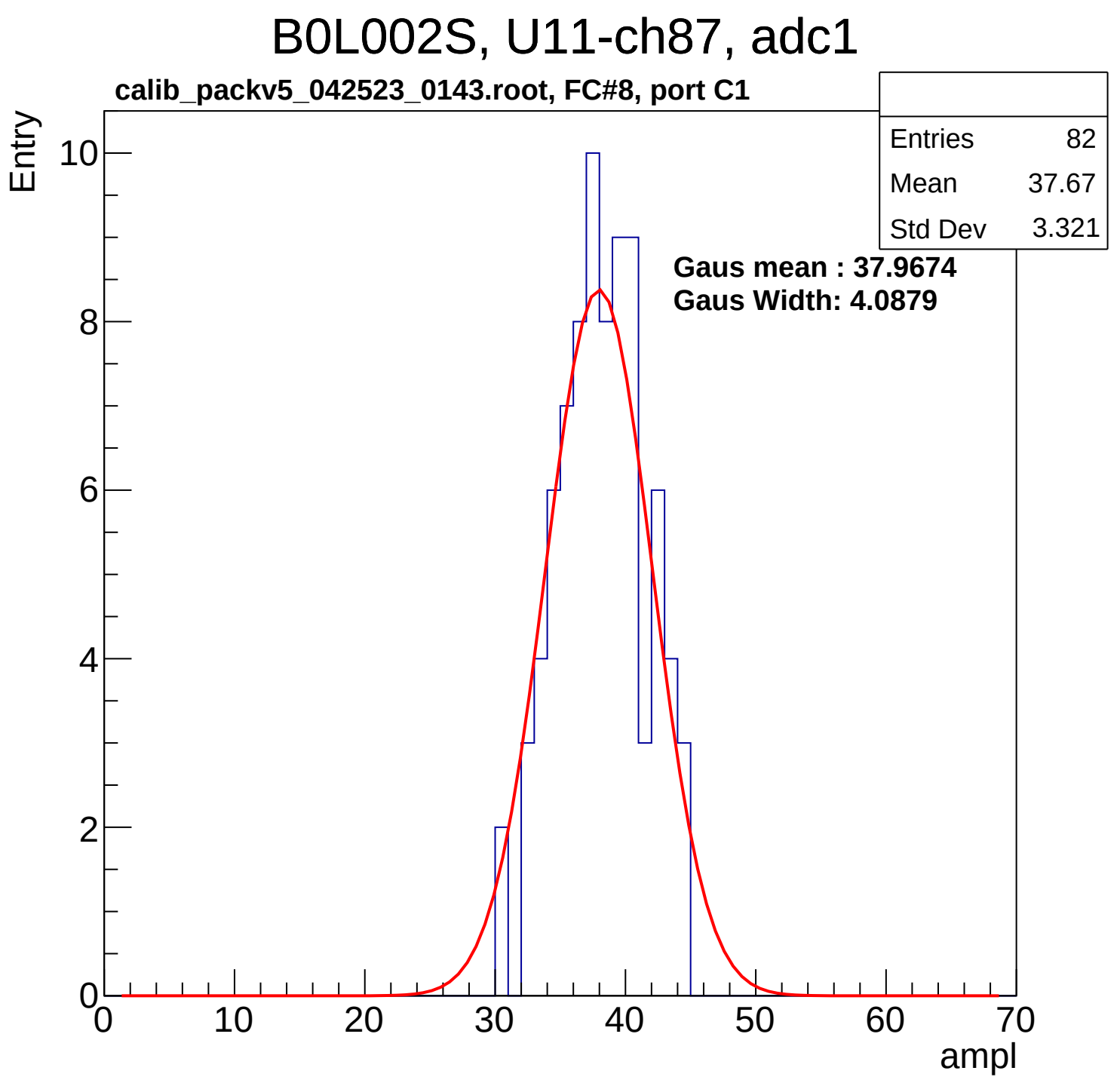

B0L002S, U11-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.09
Std Dev 2.546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

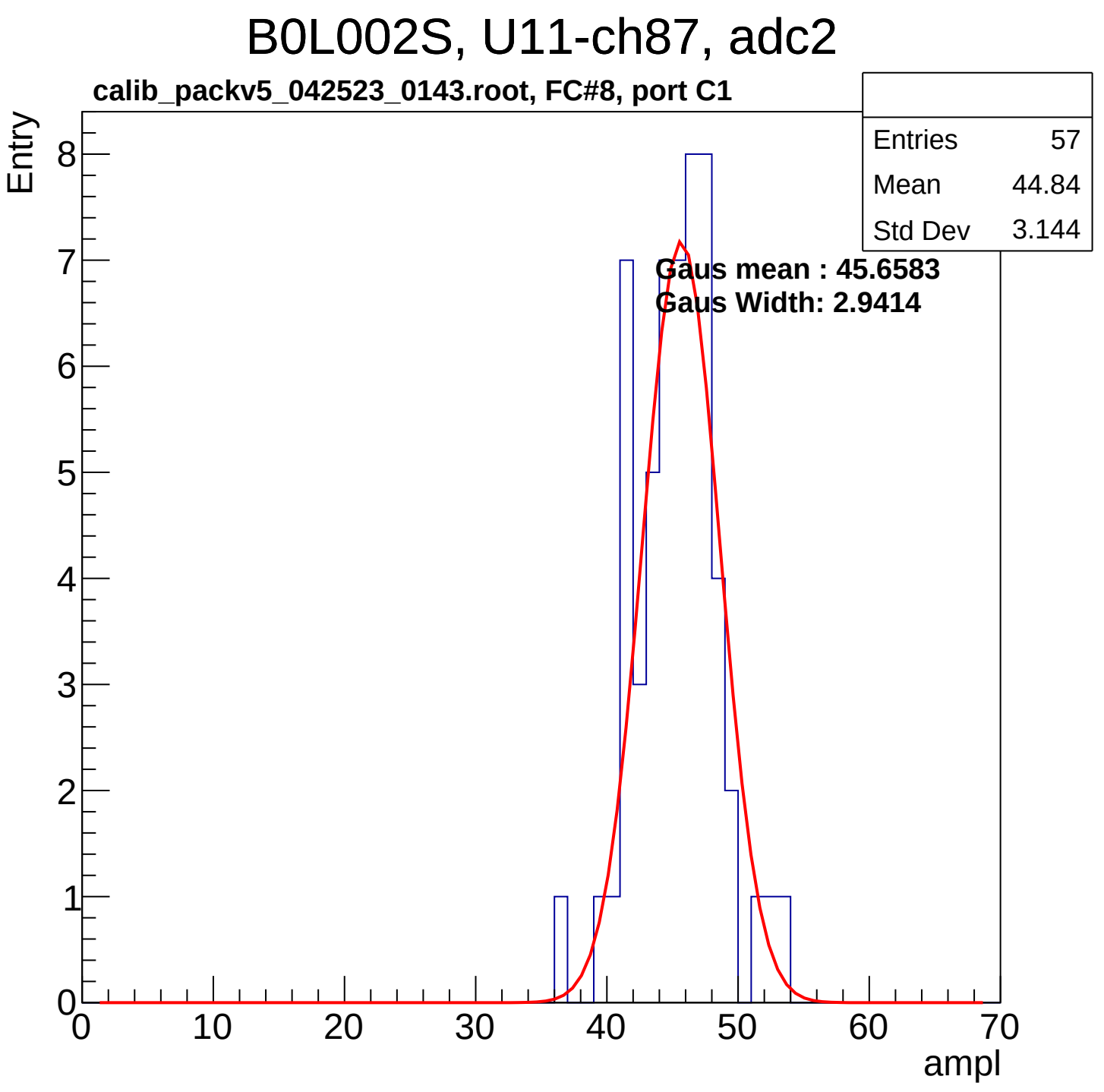

B0L002S, U11-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.88
Std Dev 1.694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

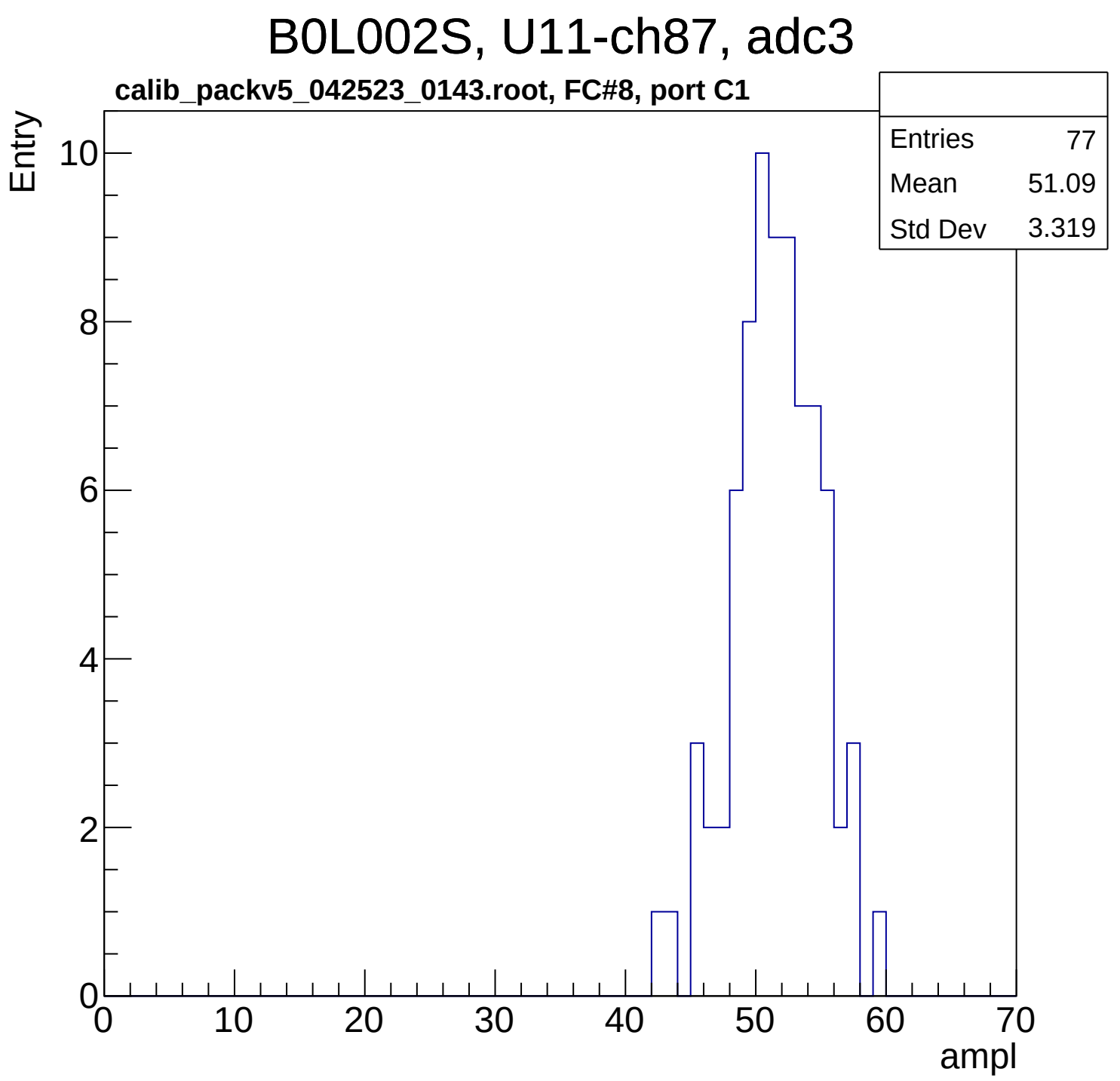

B0L002S, U11-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

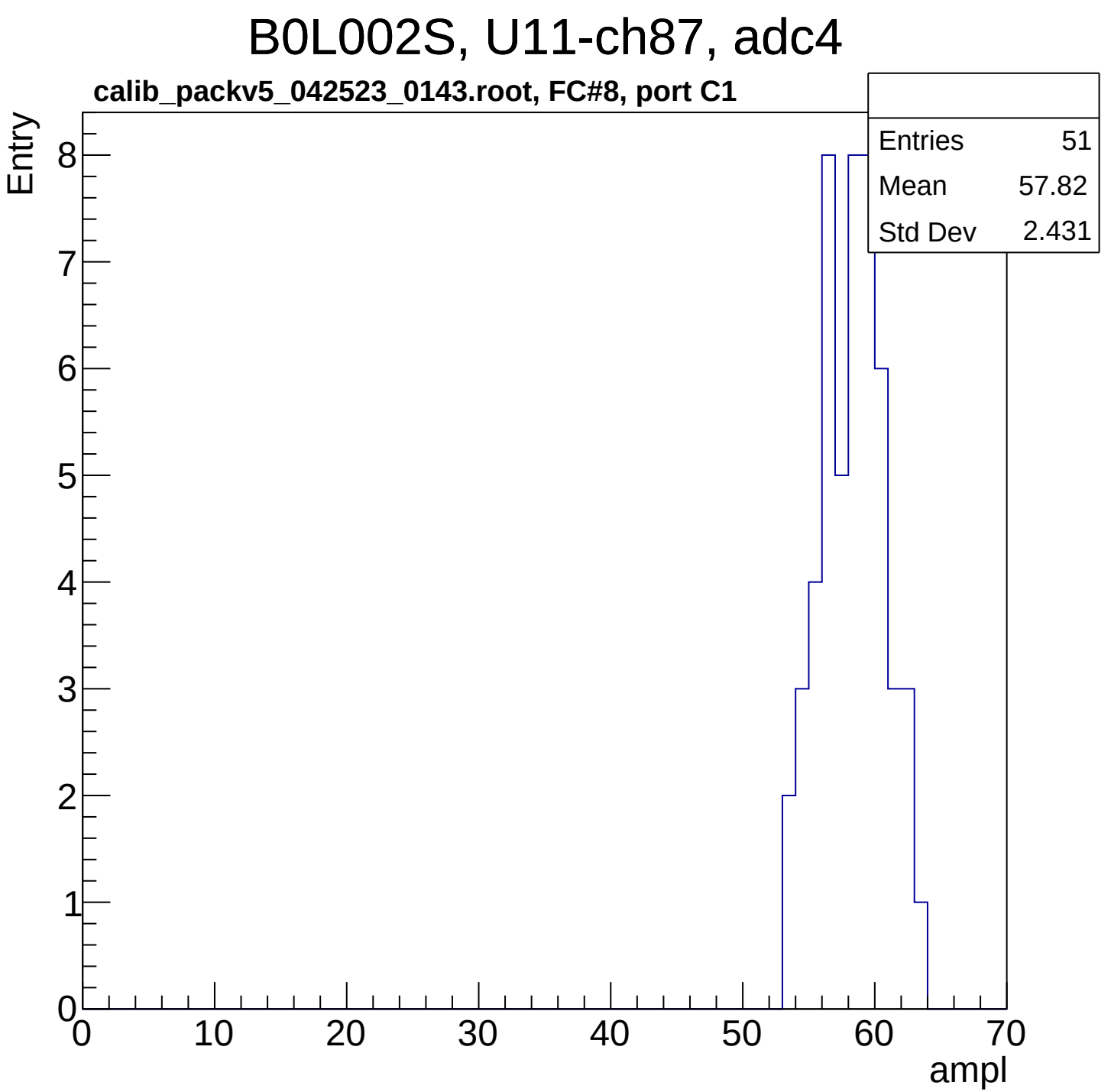

B0L002S, U11-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.44
Std Dev 2.948
Gaus mean : 30.7334
Gaus Width: 2.9972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 37.67
Std Dev 3.321
Gaus mean : 37.9674
Gaus Width: 4.0879
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 44.84
Std Dev 3.144
Gaus mean : 45.6583
Gaus Width: 2.9414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.09
Std Dev 3.319
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.82
Std Dev 2.431
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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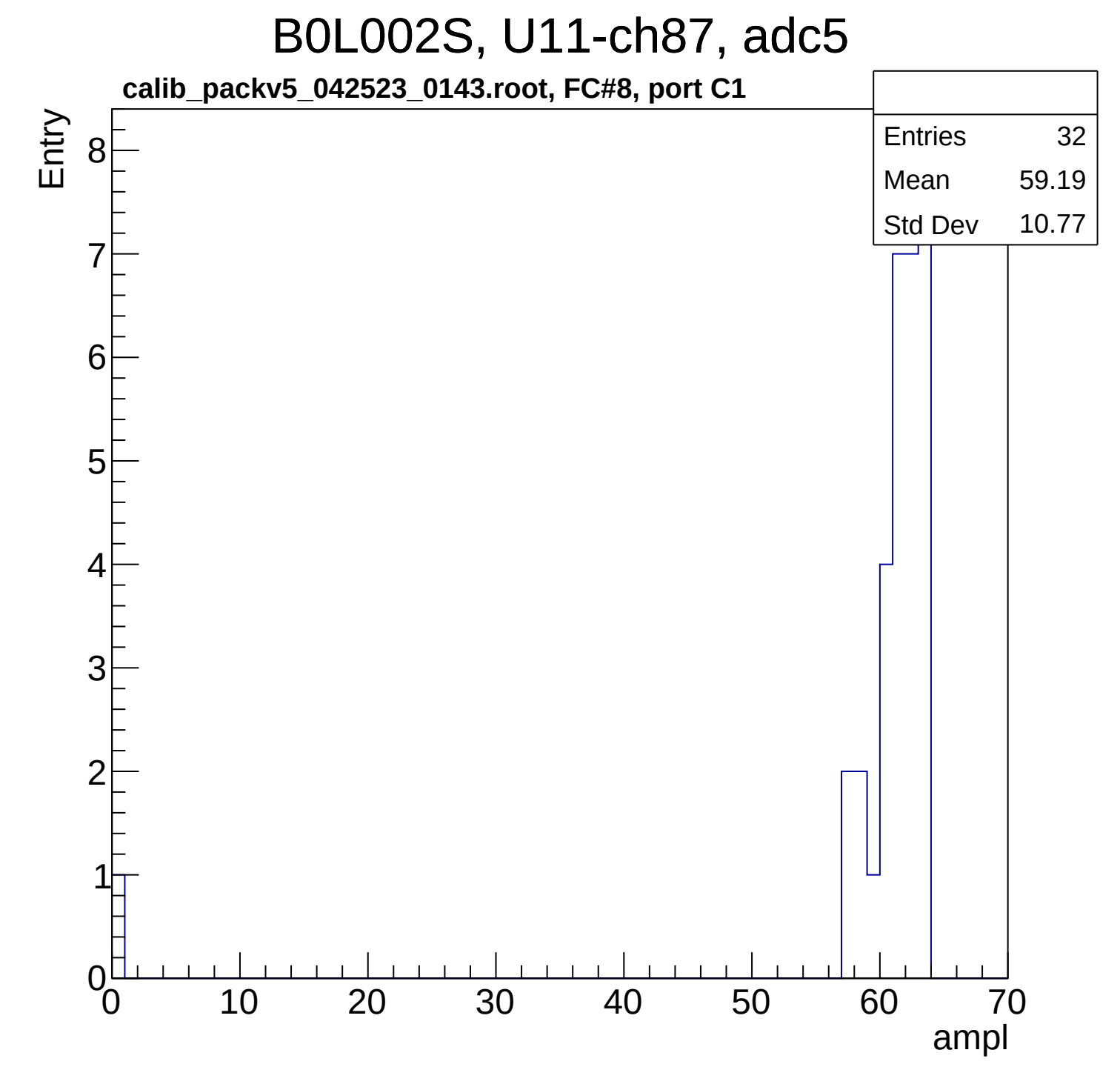
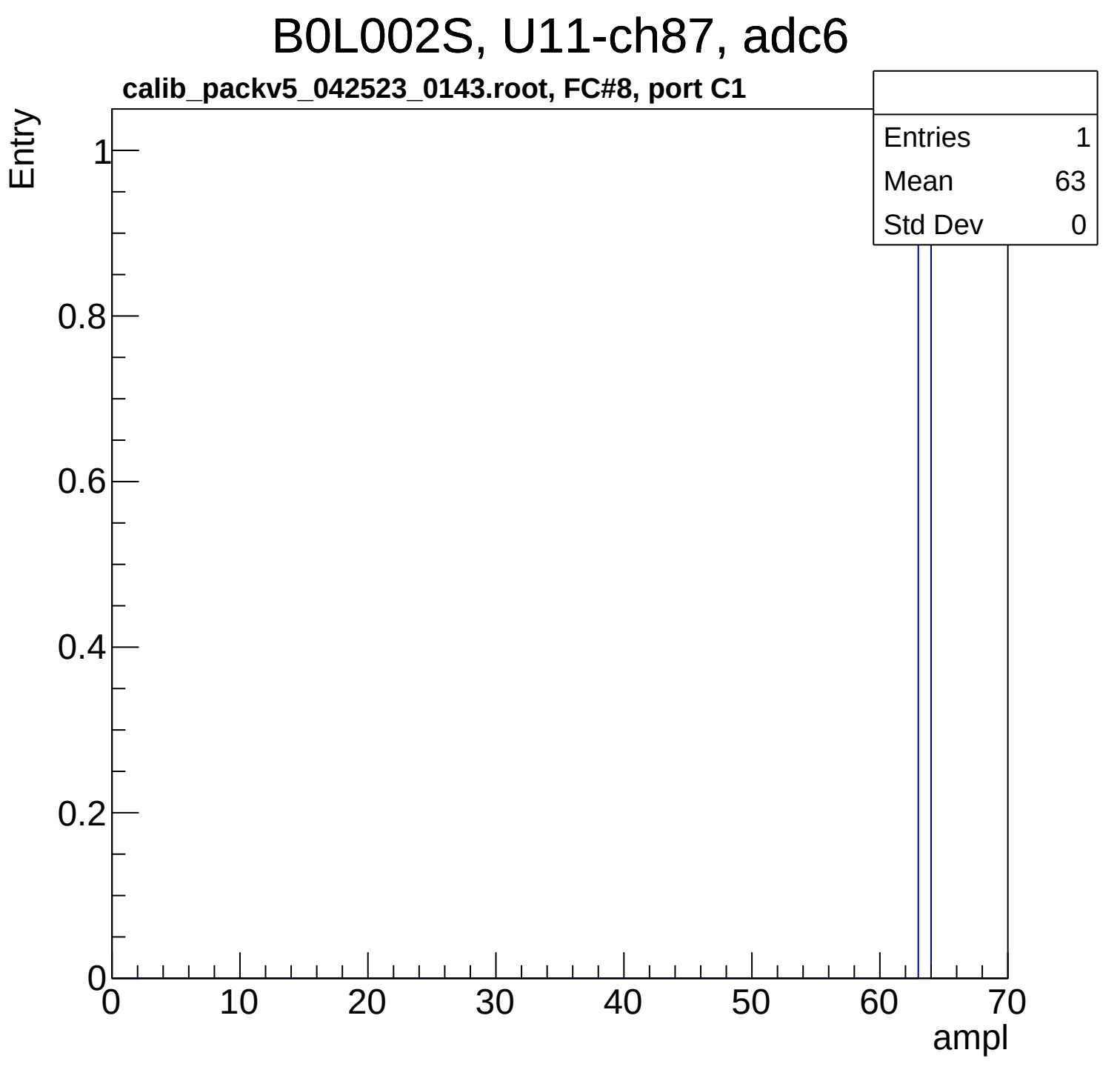
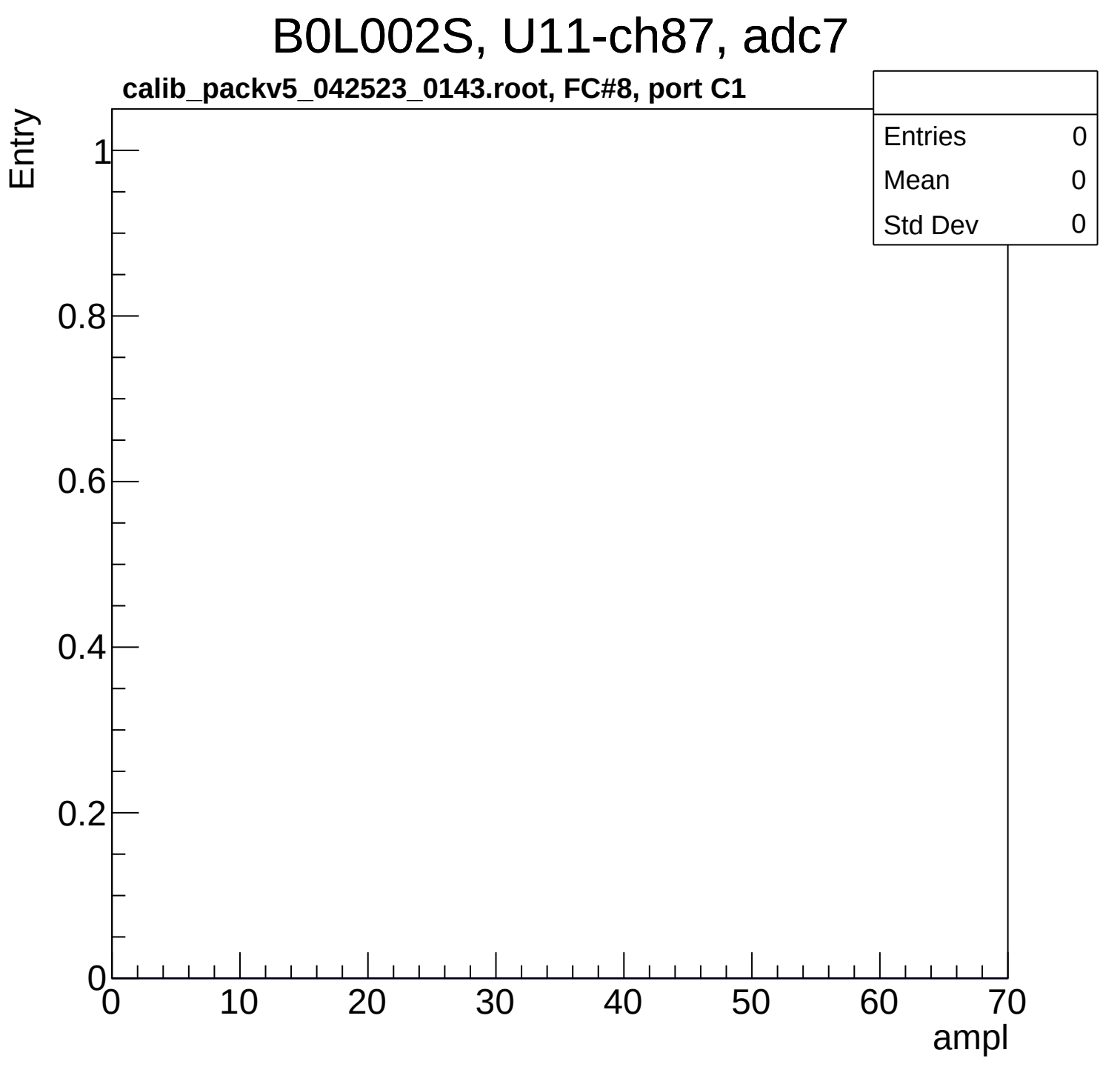
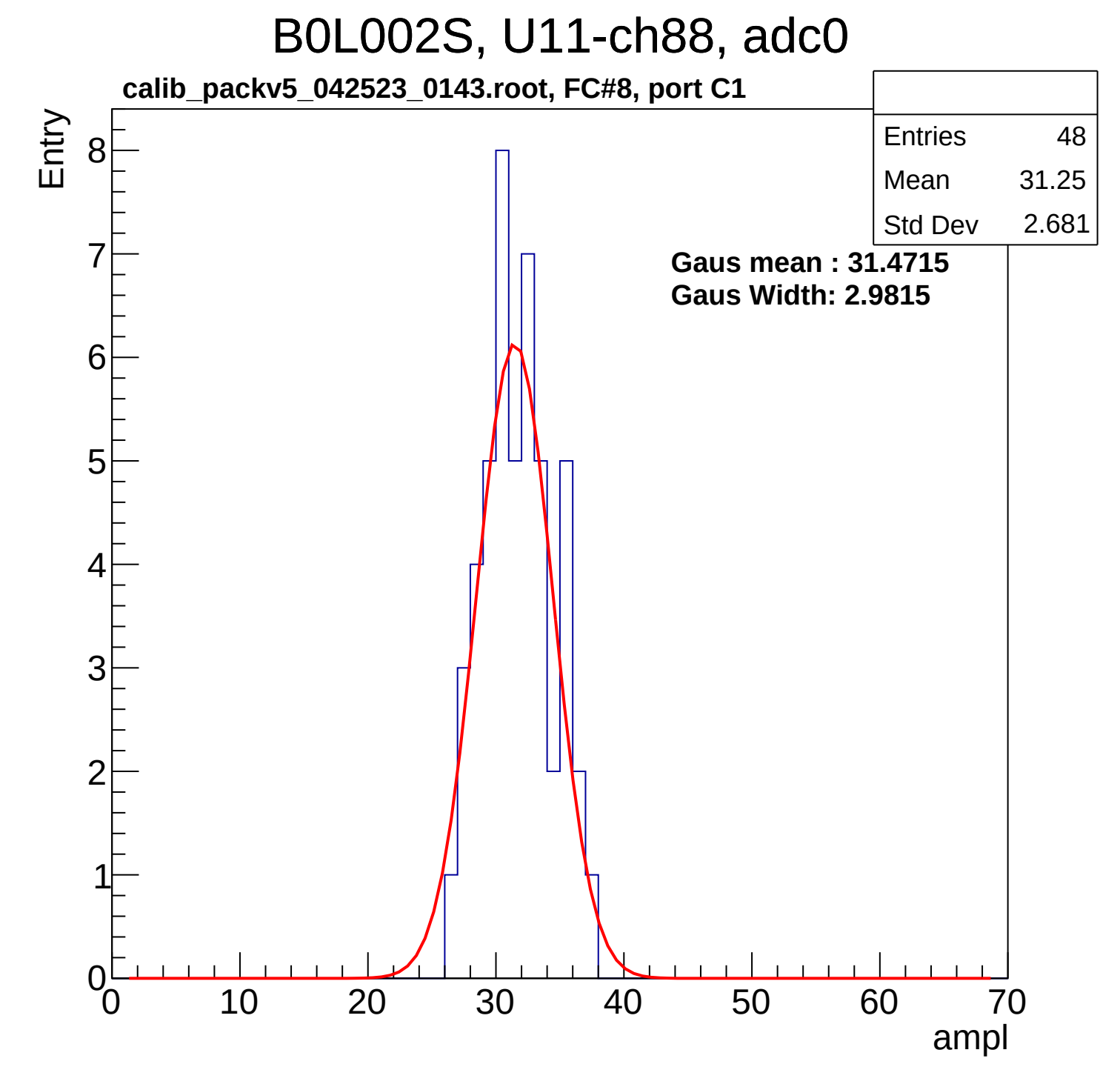
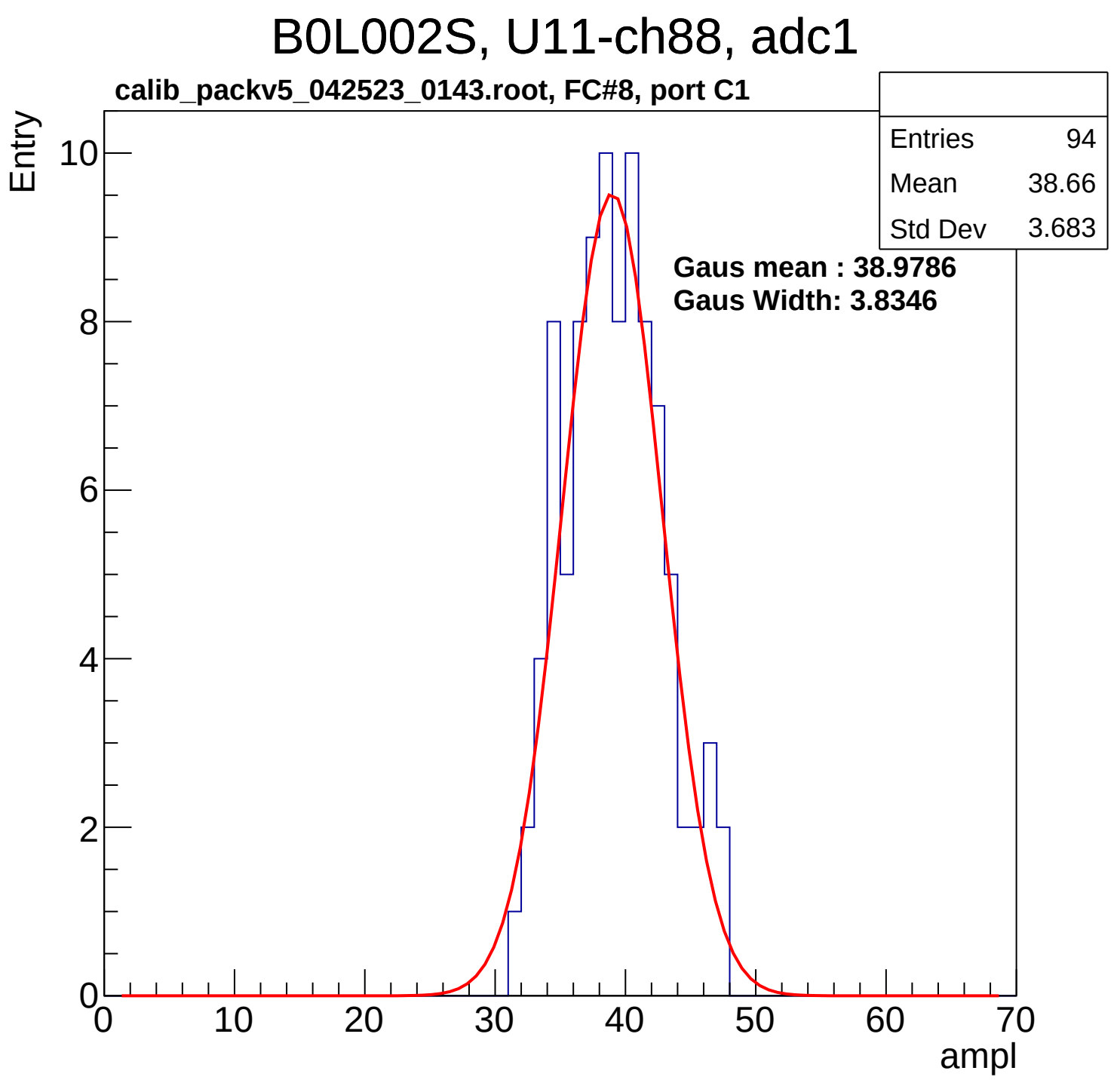
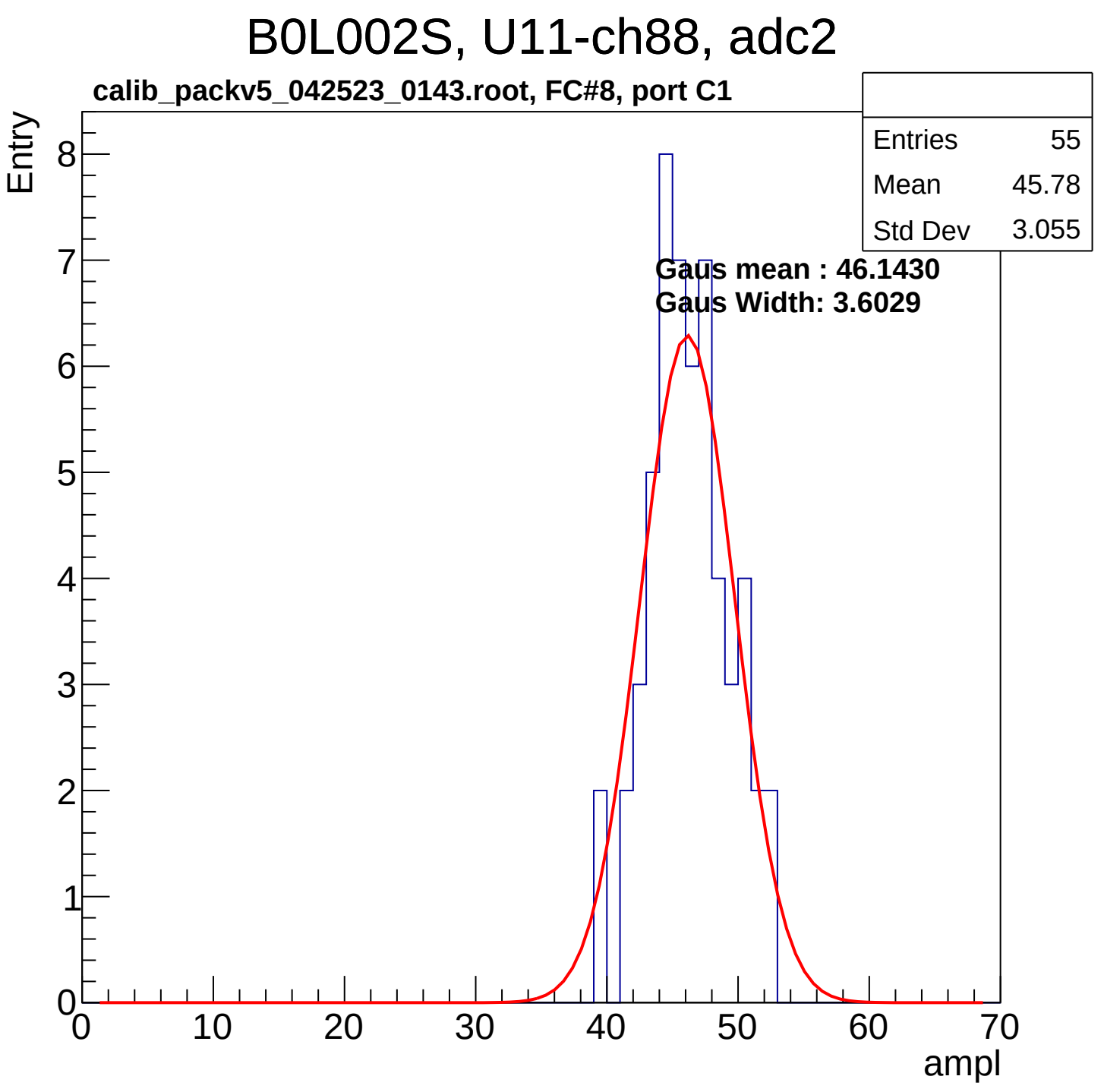

B0L002S, U11-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.19
Std Dev 10.77
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 31.25
Std Dev 2.681
Gaus mean : 31.4715
Gaus Width: 2.9815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 94
Mean 38.66
Std Dev 3.683
Gaus mean : 38.9786
Gaus Width: 3.8346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.78
Std Dev 3.055
Gaus mean : 46.1430
Gaus Width: 3.6029
Entry
ampl
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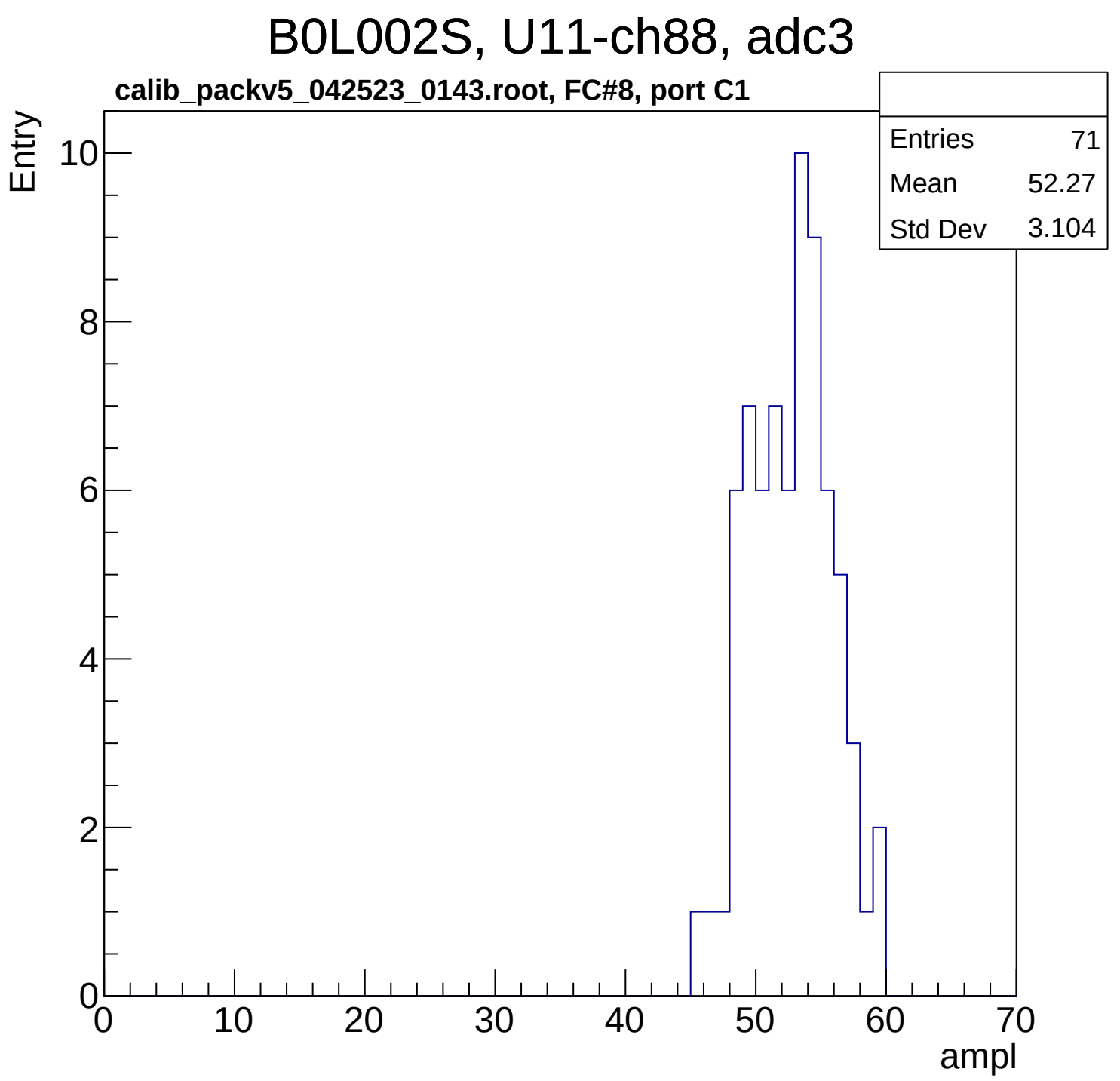
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.27
Std Dev 3.104
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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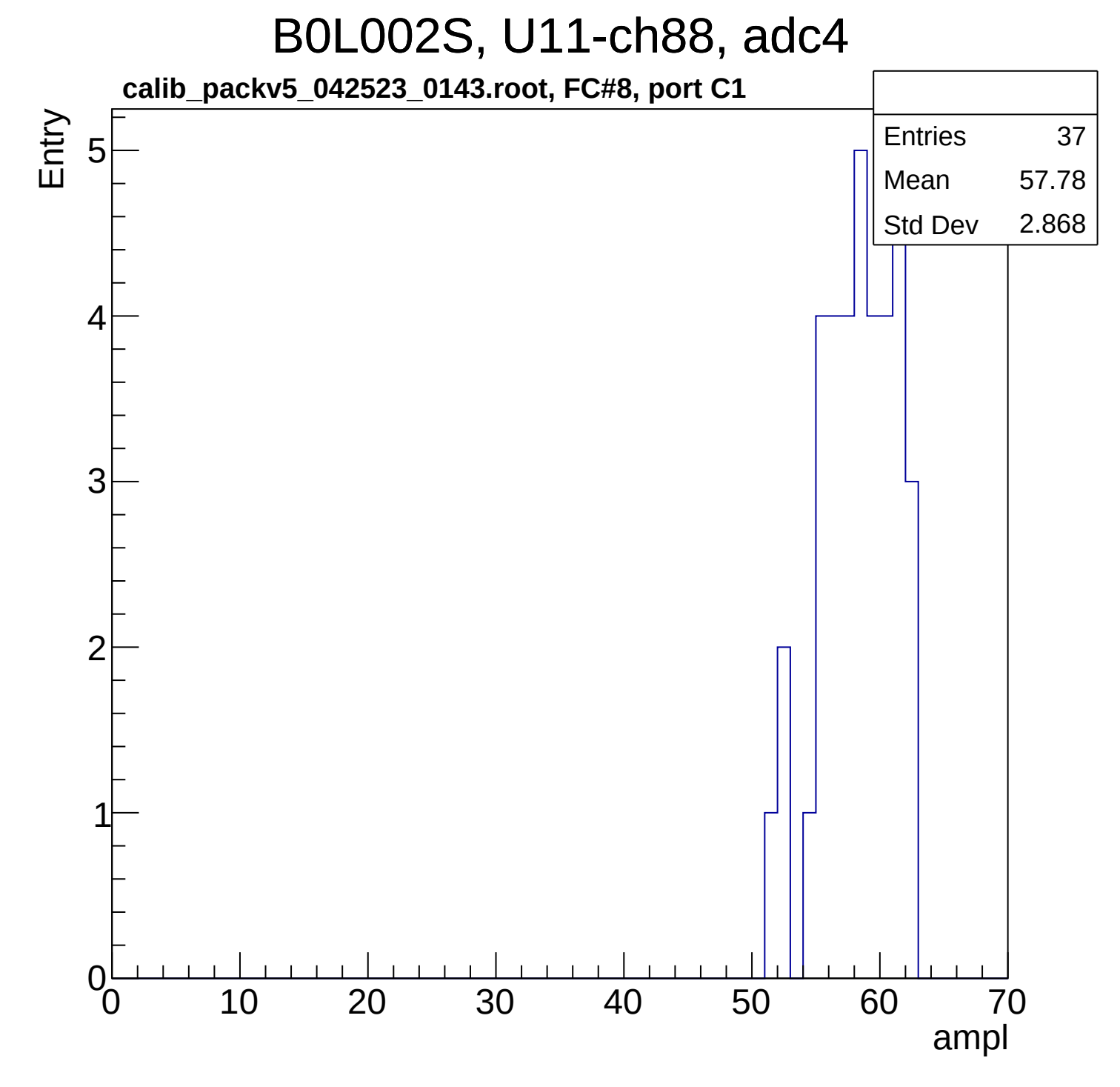
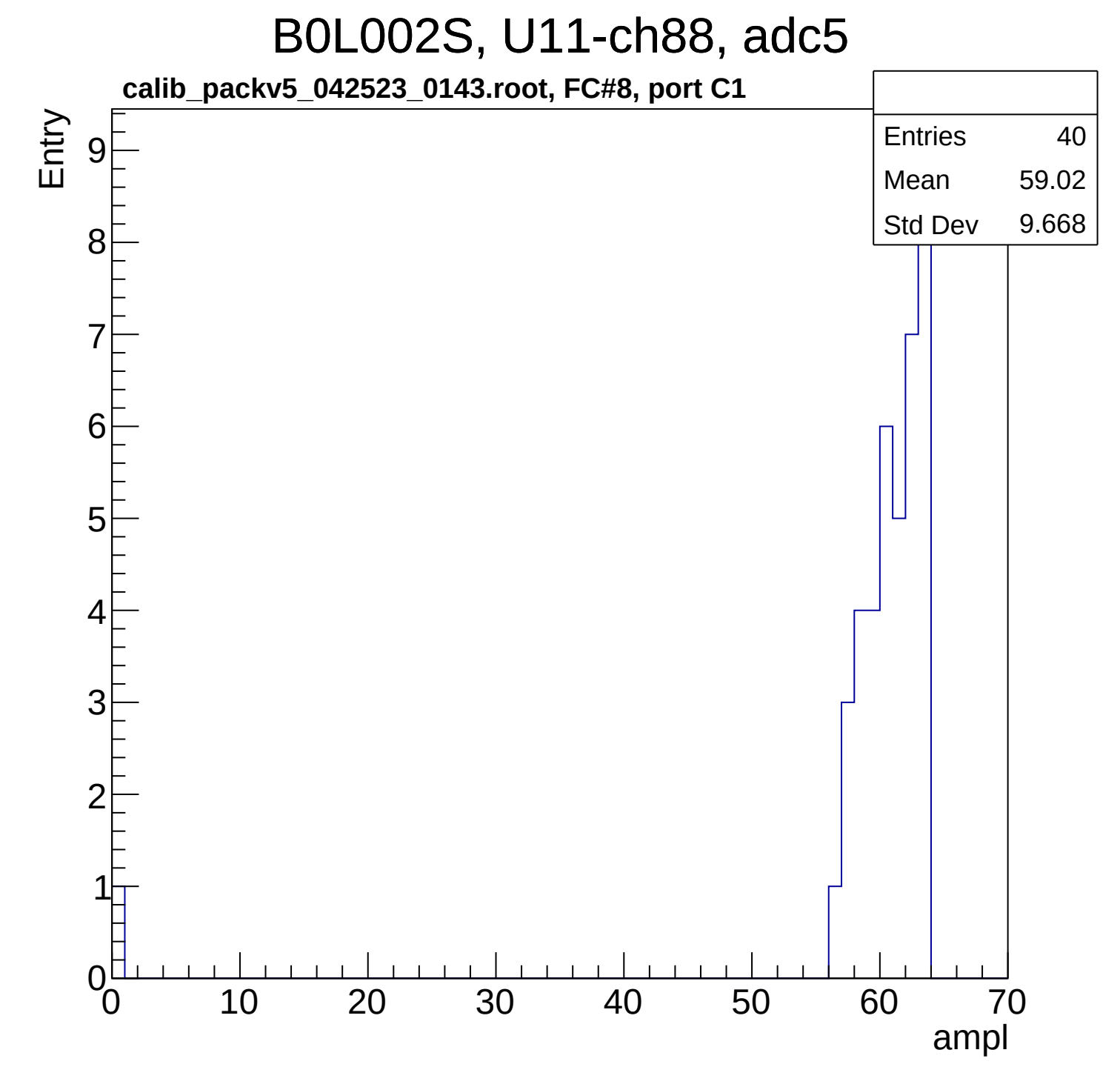

B0L002S, U11-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 57.78
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L002S, U11-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.02
Std Dev 9.668
Entry
ampl
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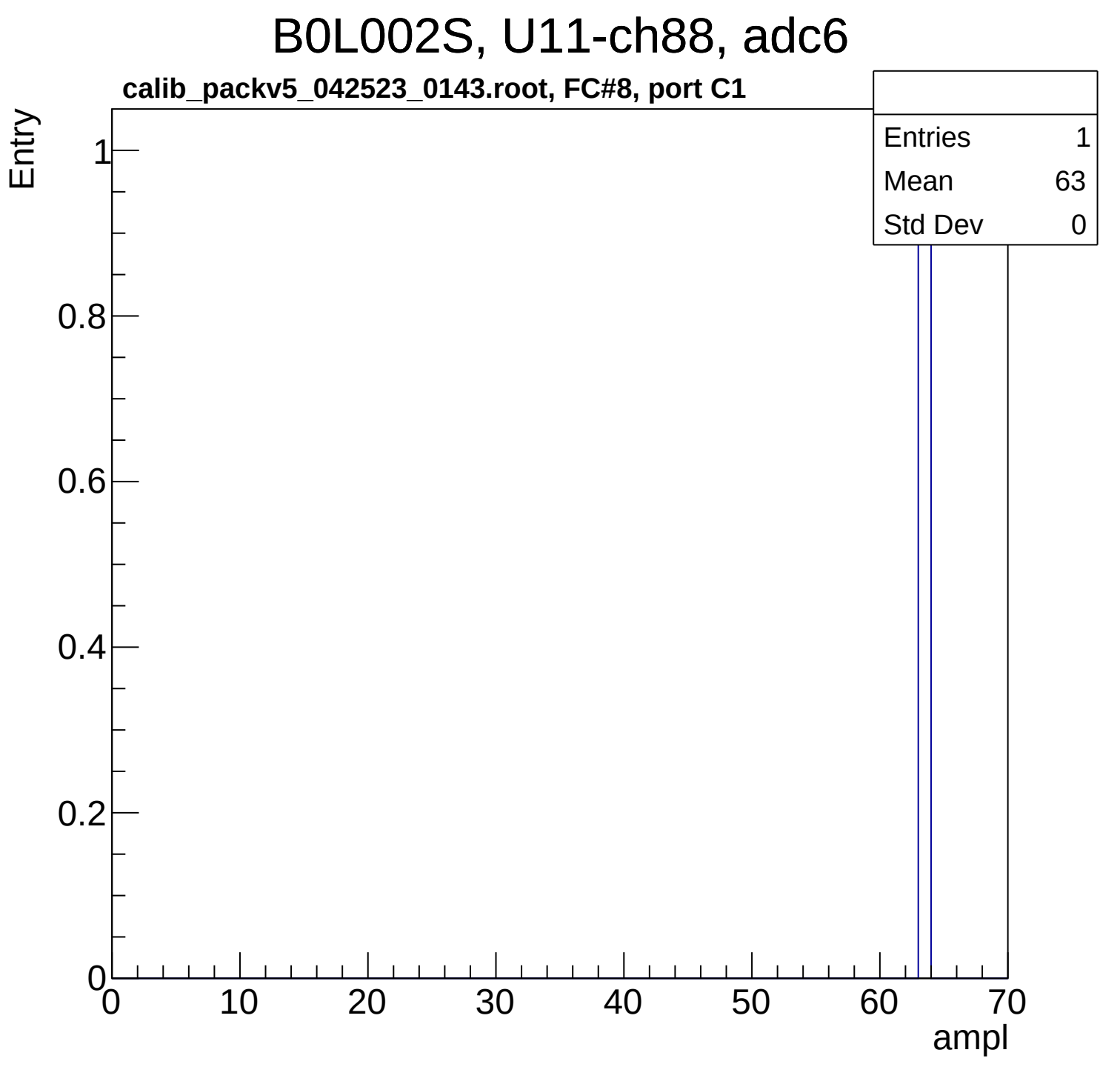
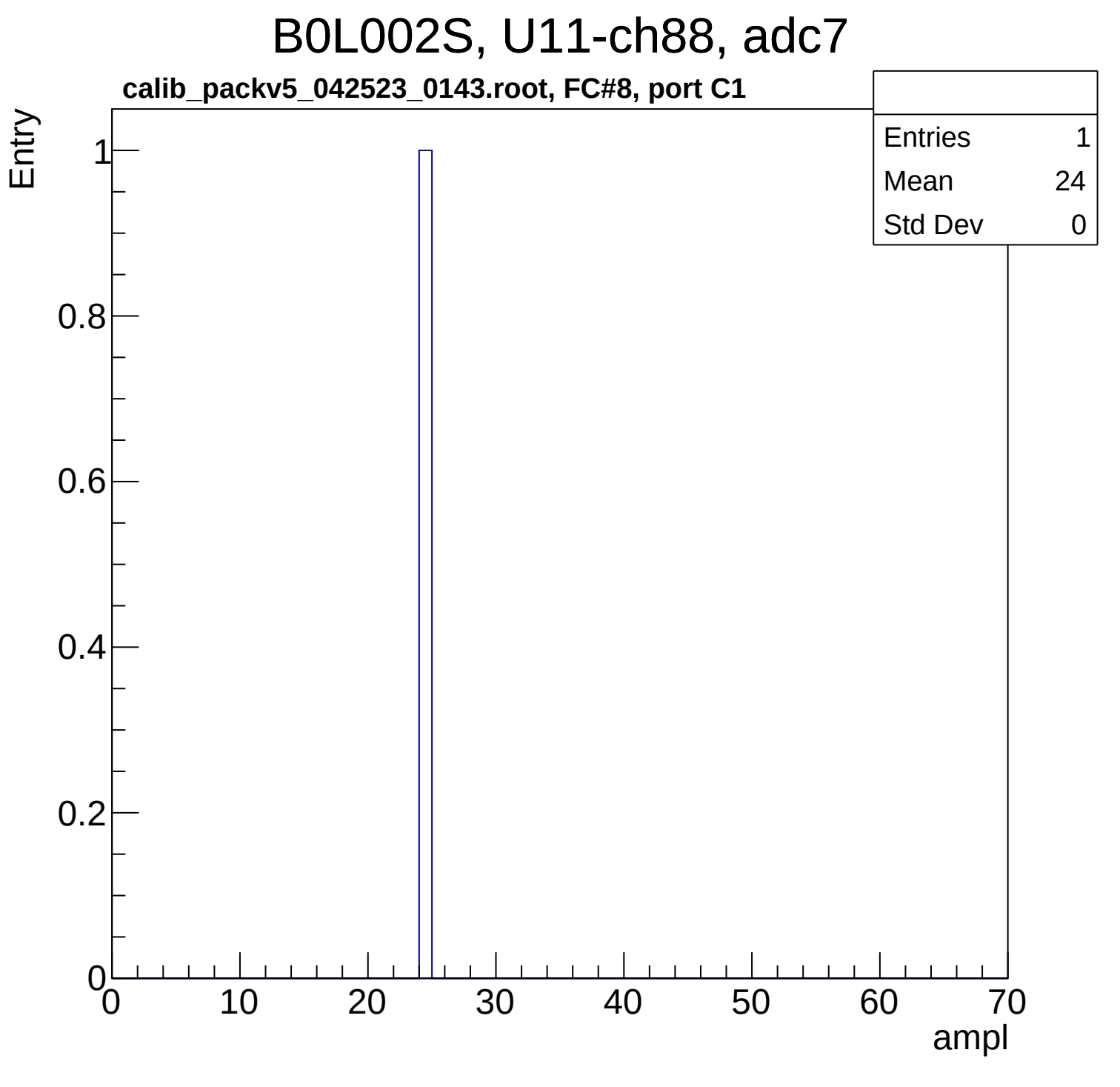
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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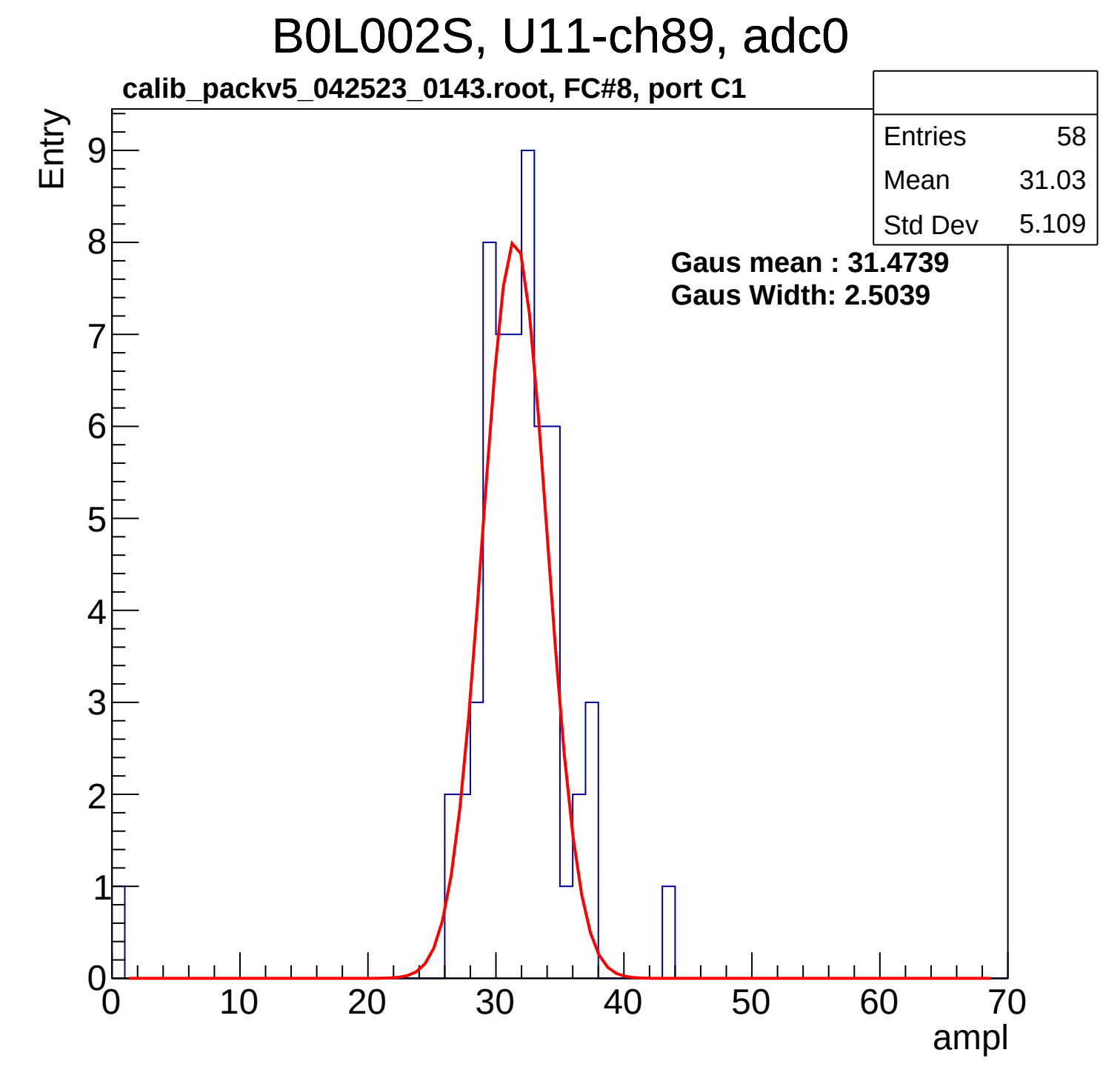
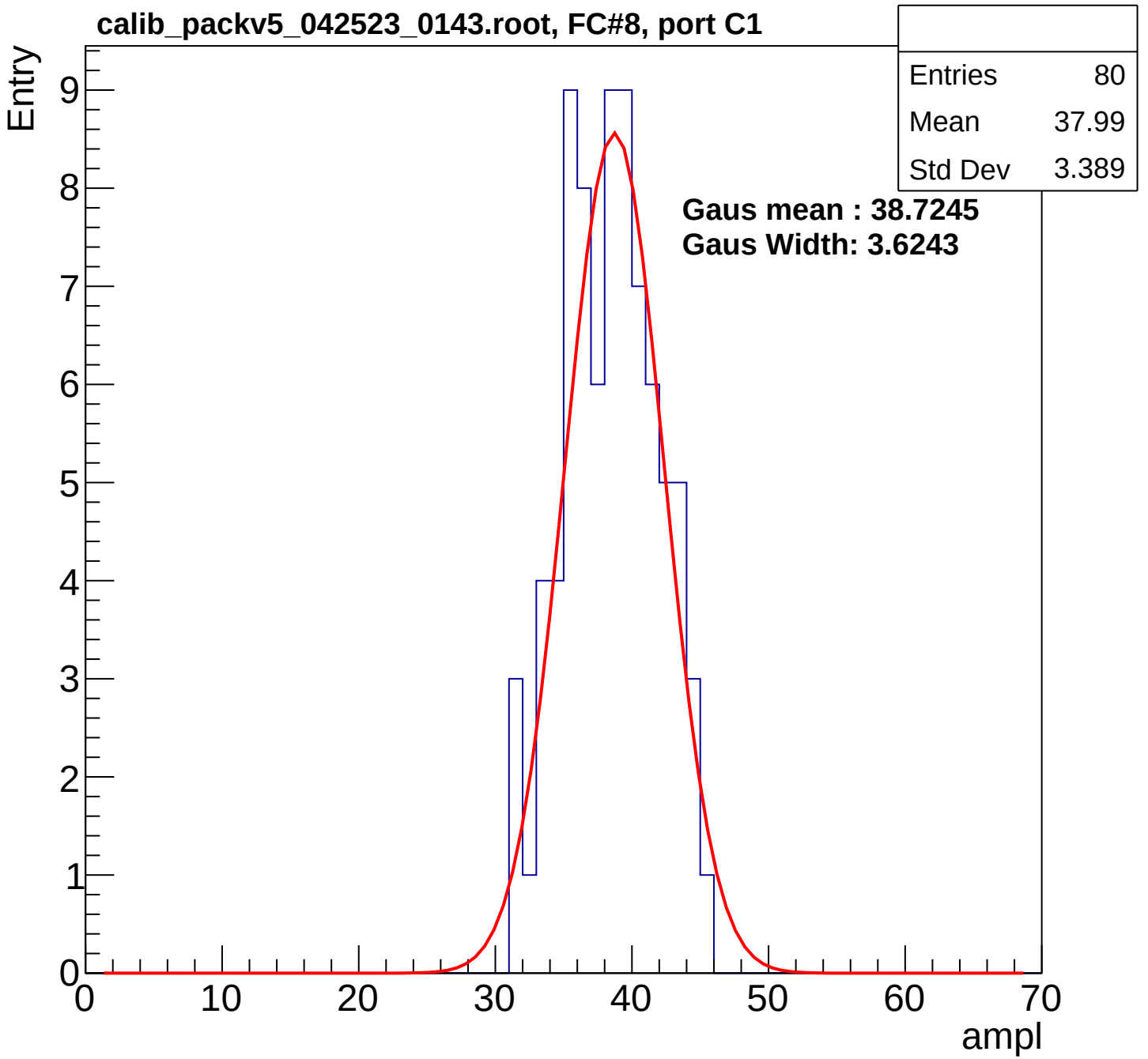
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.03
Std Dev 5.109
Gaus mean : 31.4739
Gaus Width: 2.5039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.99
Std Dev 3.389
Gaus mean : 38.7245
Gaus Width: 3.6243
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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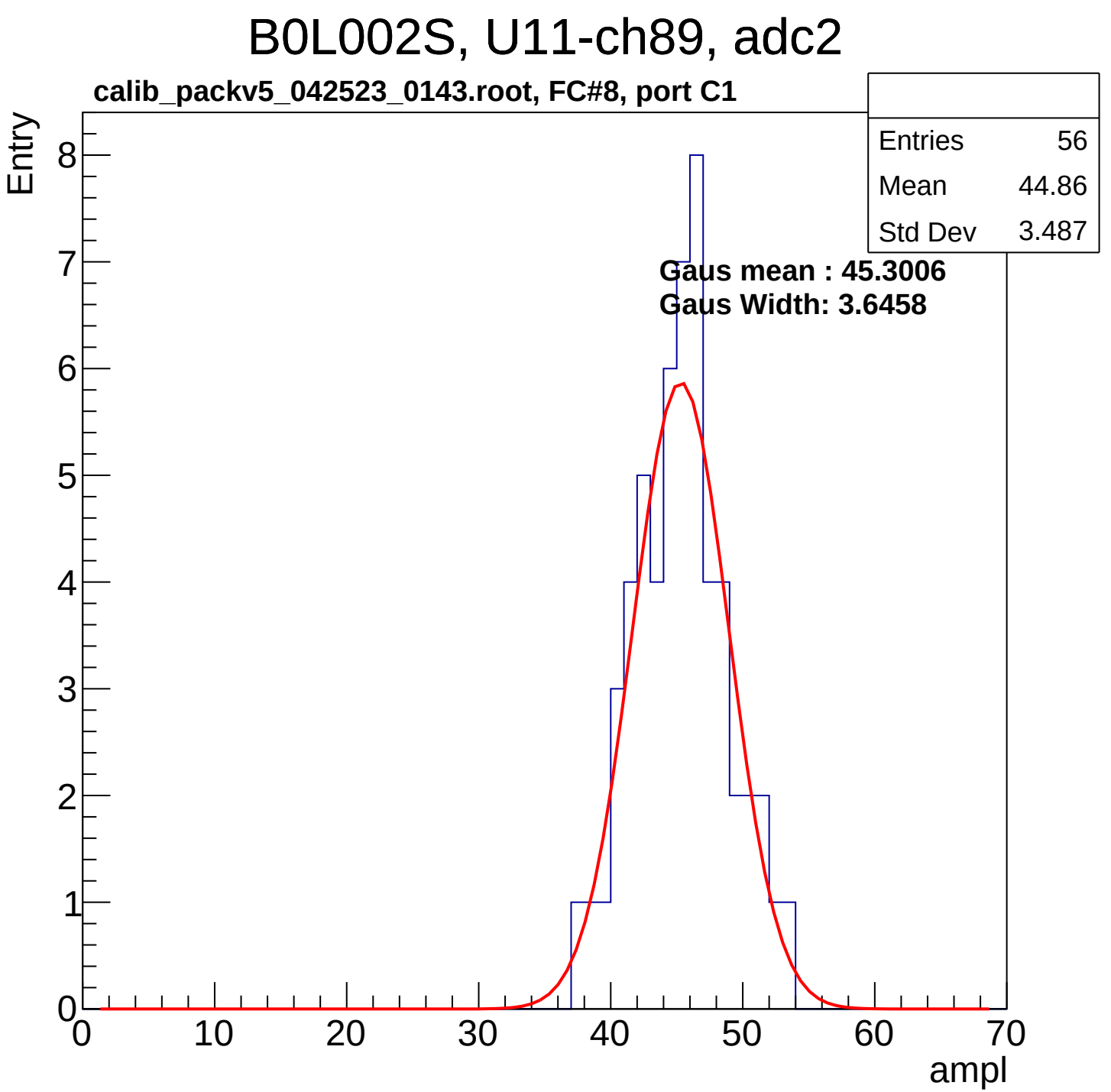
B0L002S, U11-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.86
Std Dev 3.487
Gaus mean : 45.3006
Gaus Width: 3.6458
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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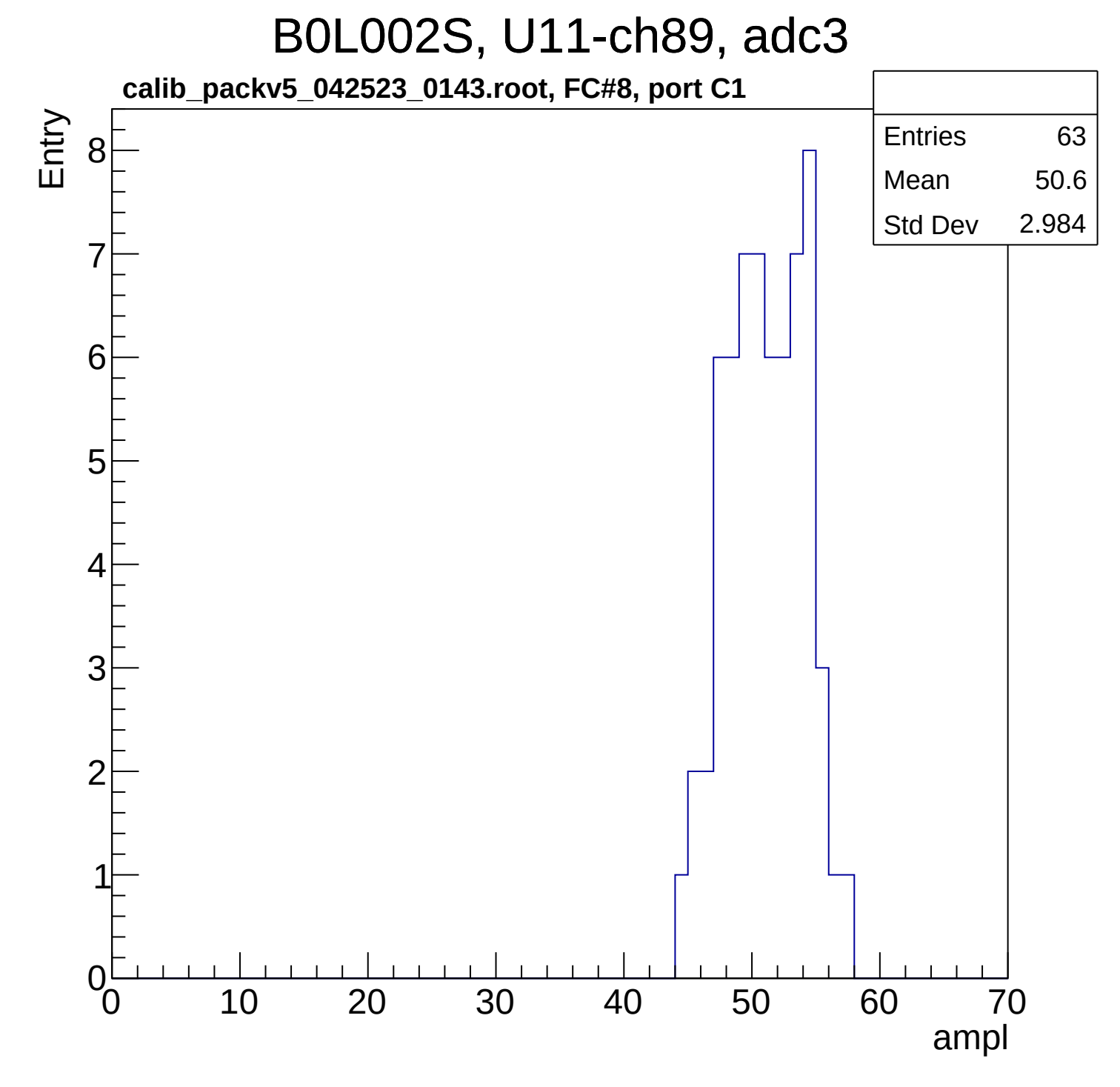
B0L002S, U11-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.6
Std Dev 2.984
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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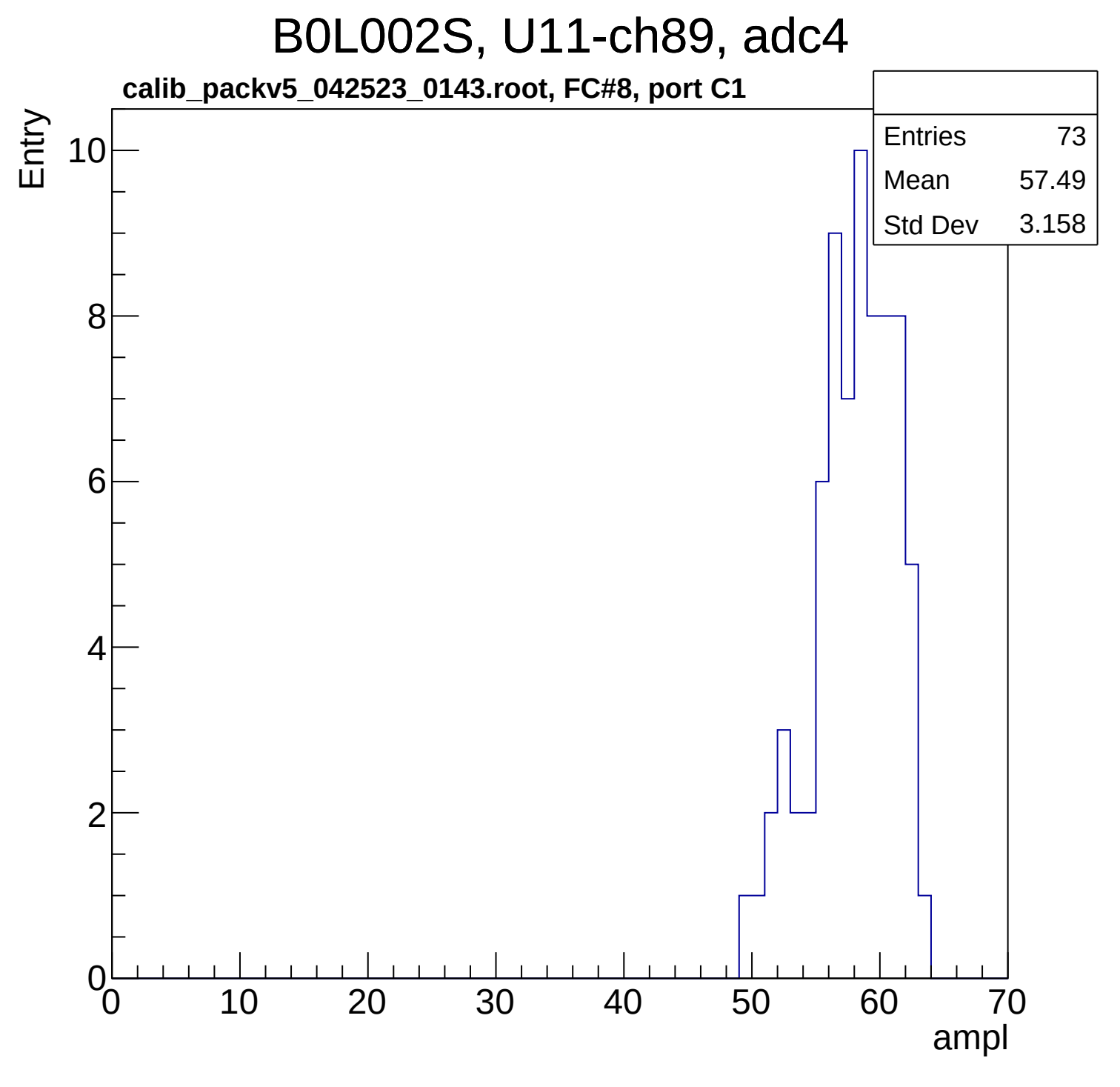
B0L002S, U11-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 57.49
Std Dev 3.158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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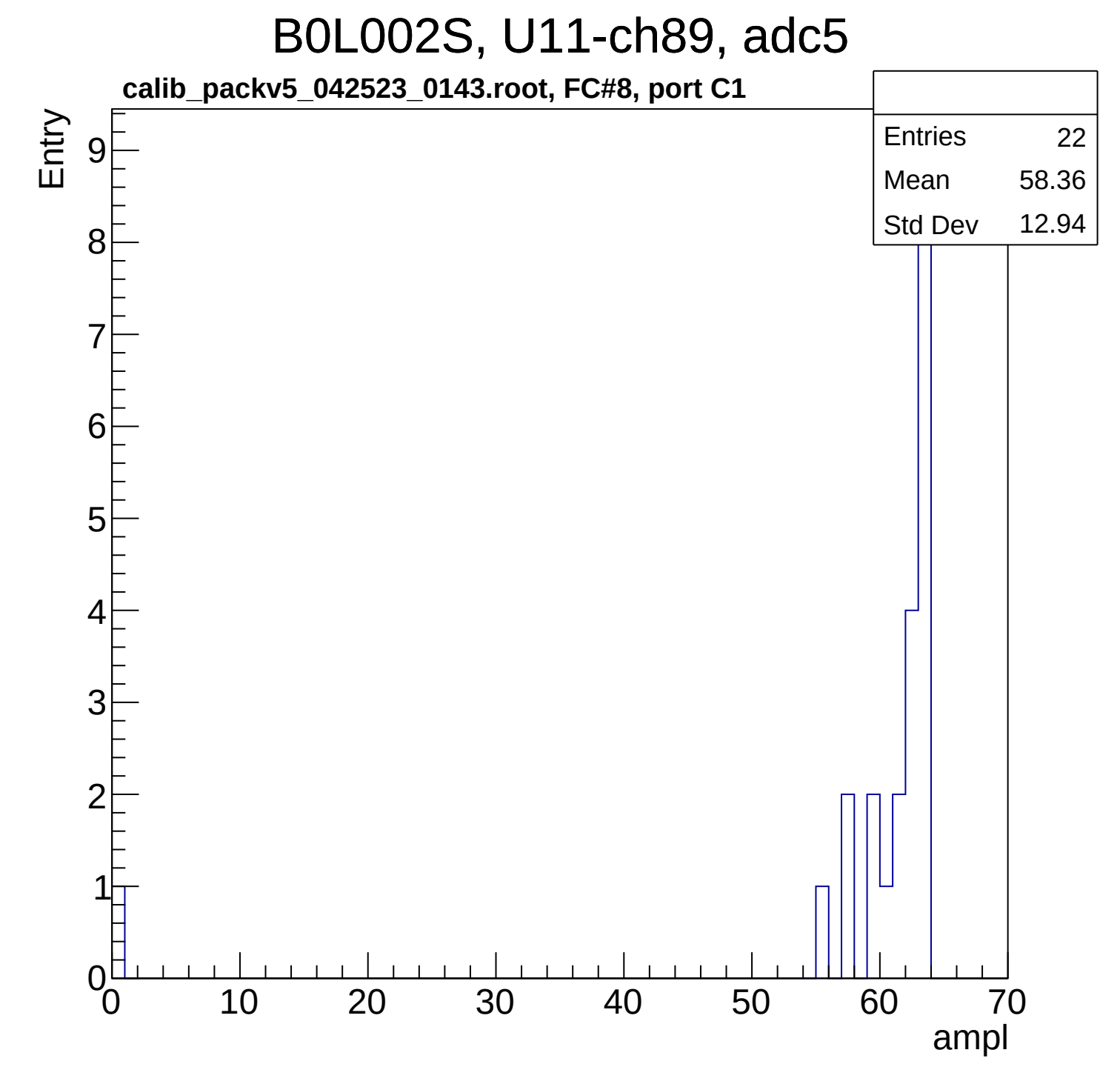
B0L002S, U11-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 58.36
Std Dev 12.94
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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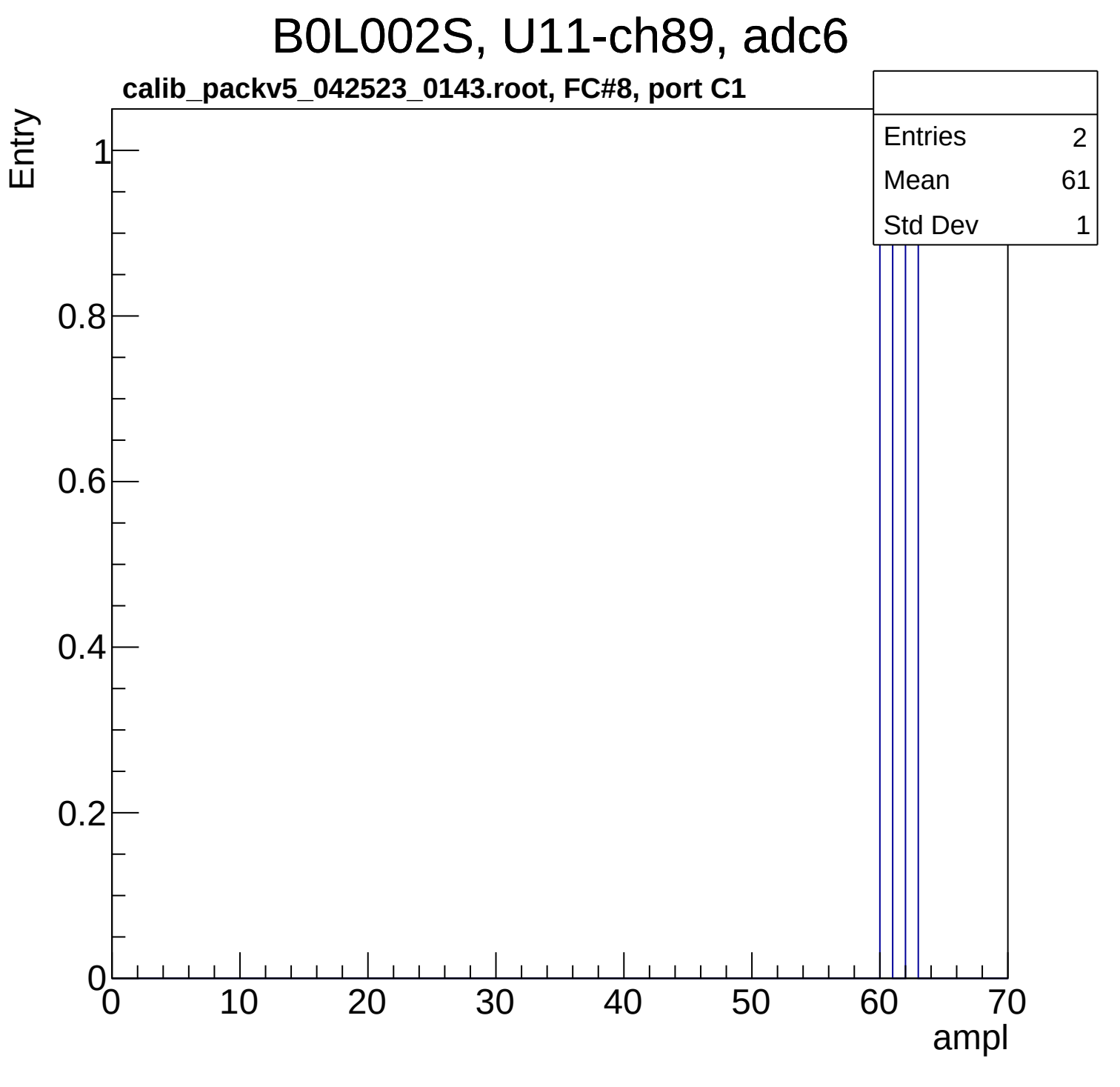
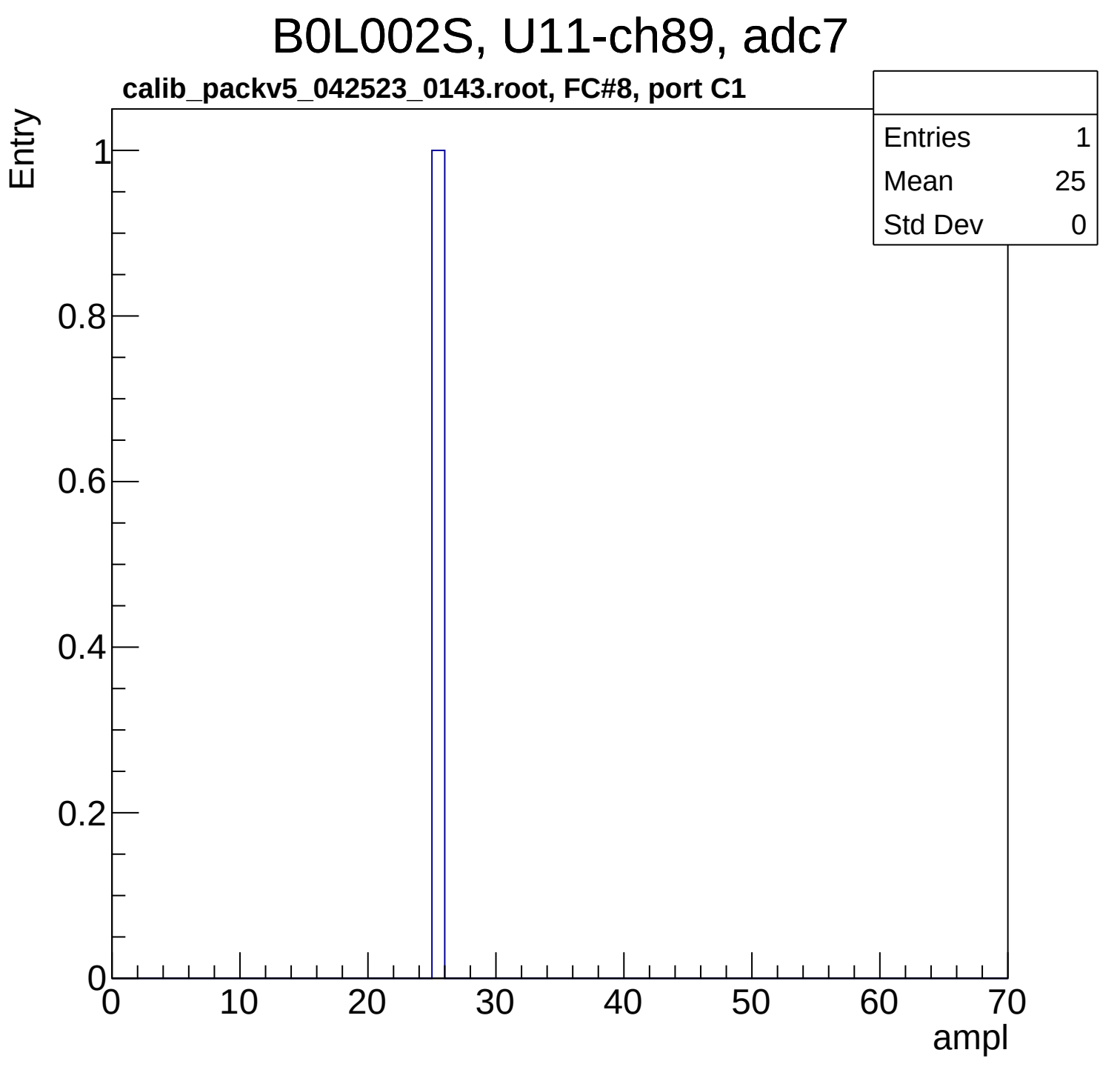
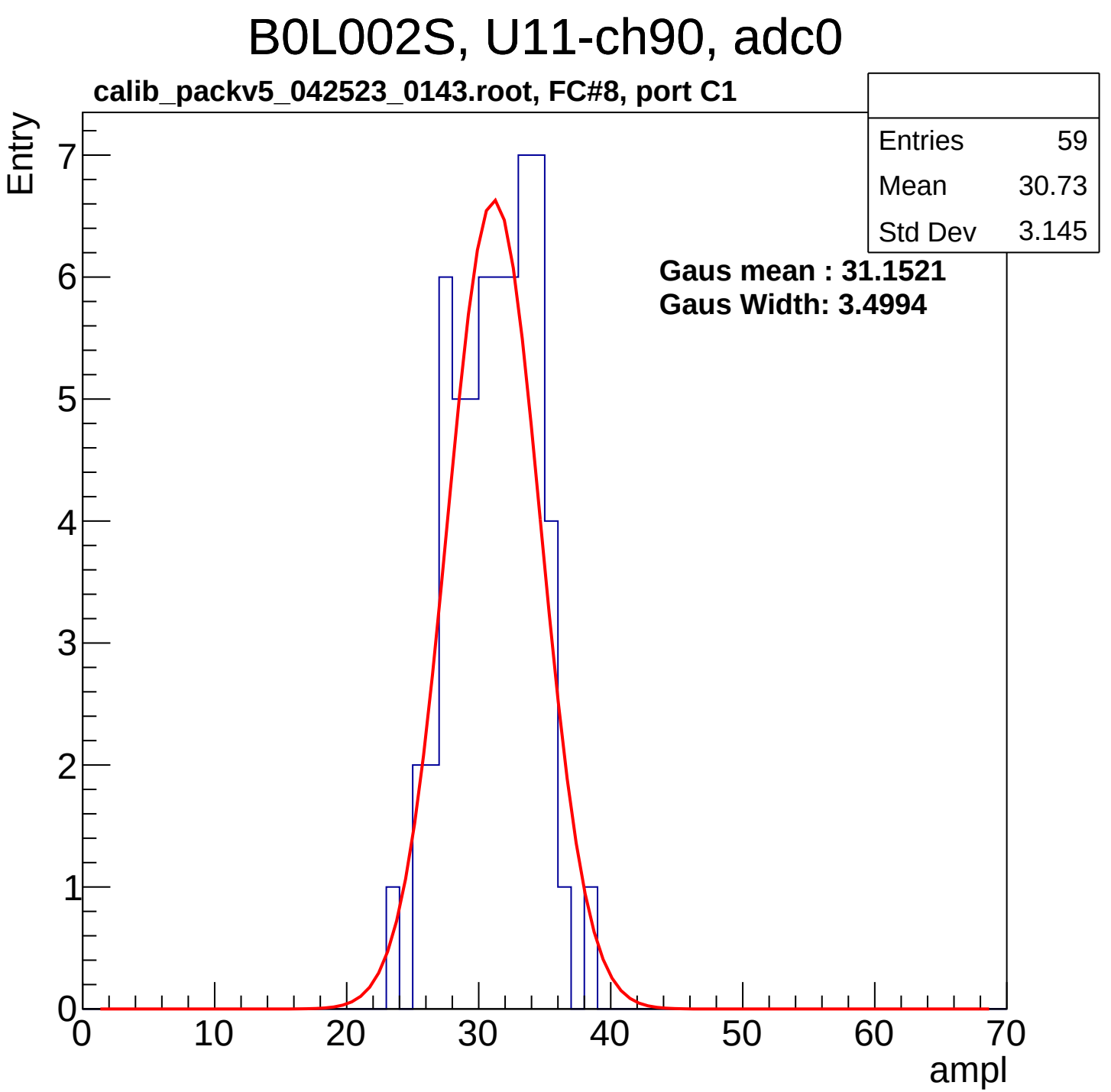
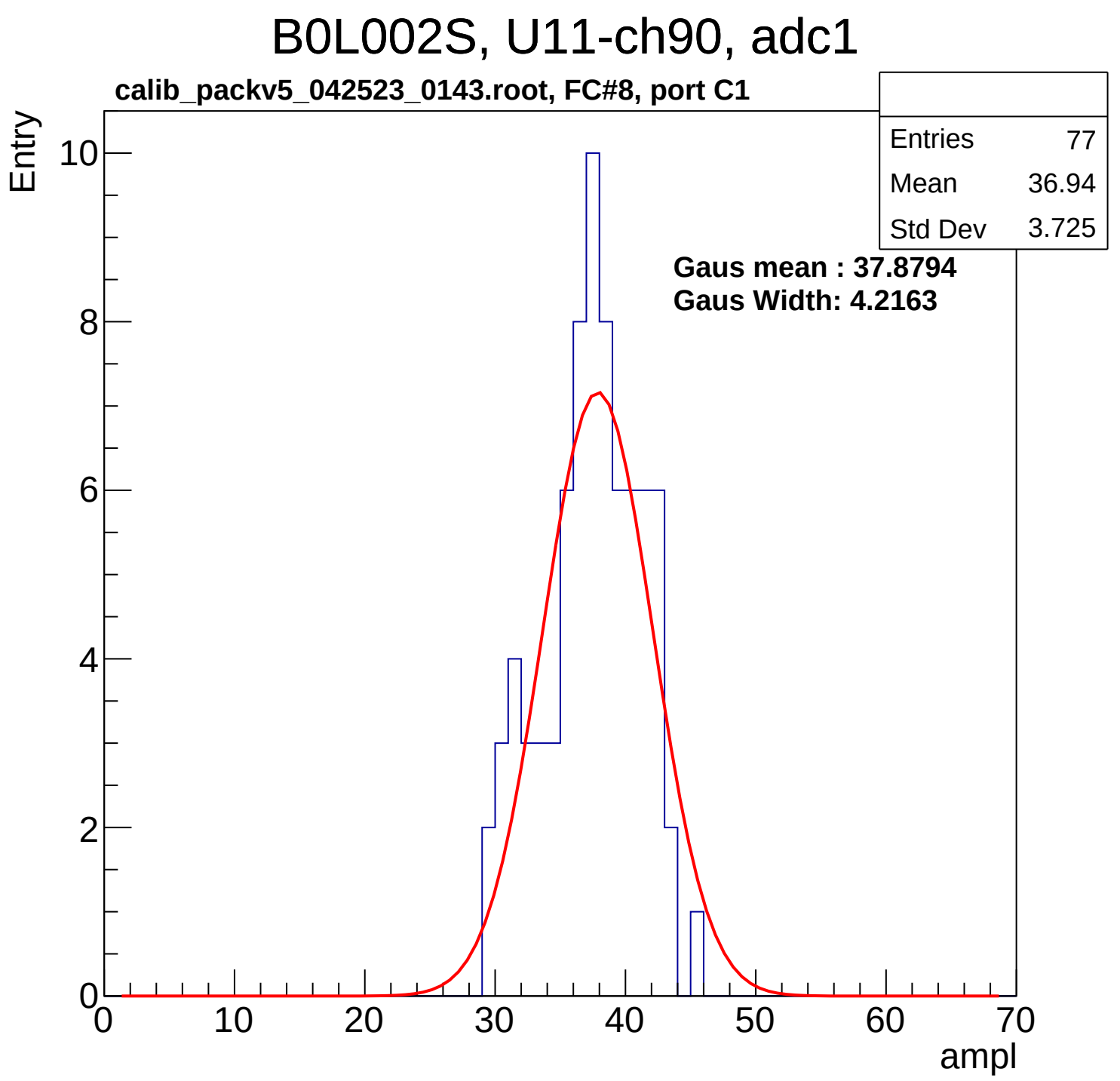
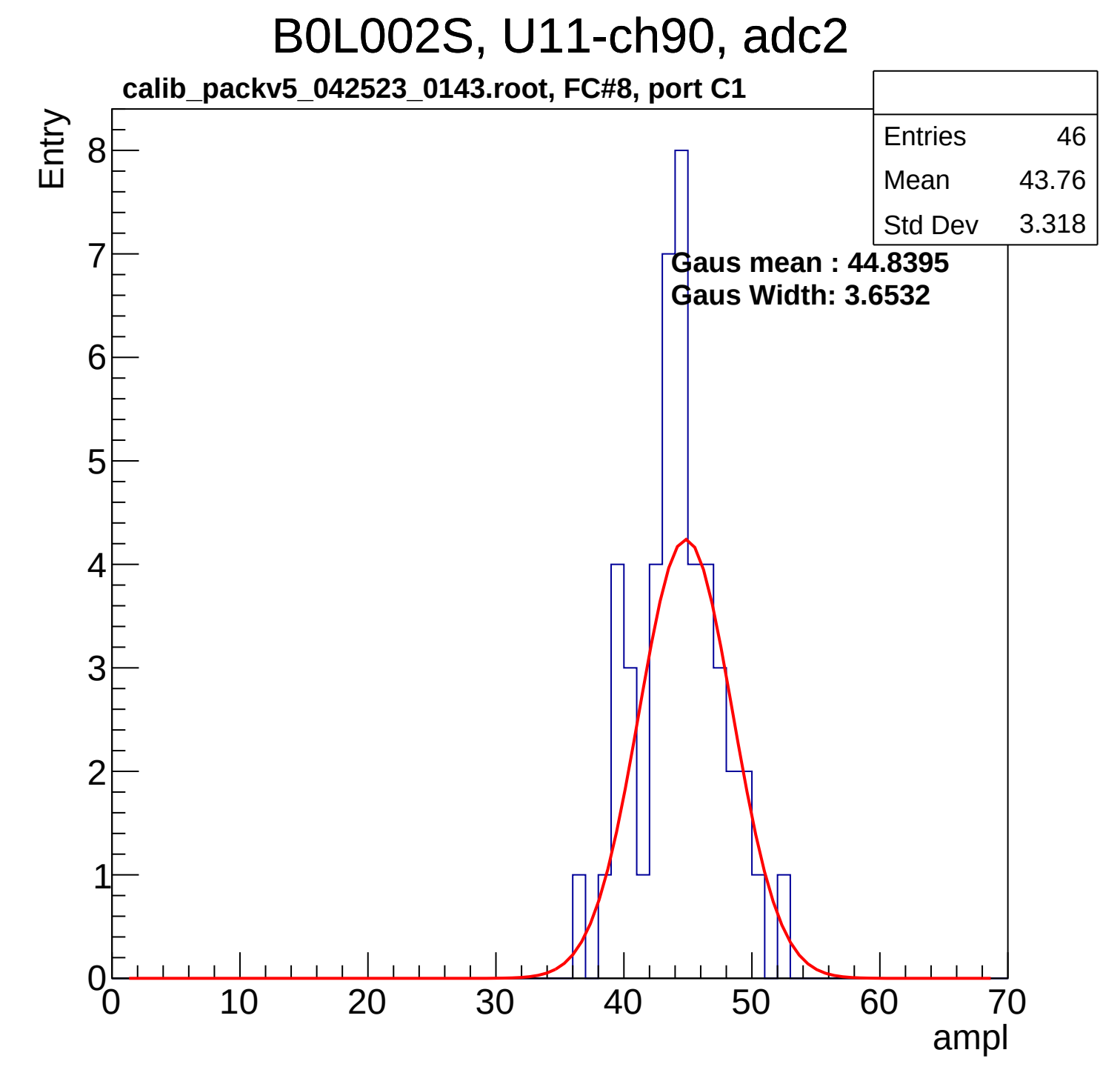
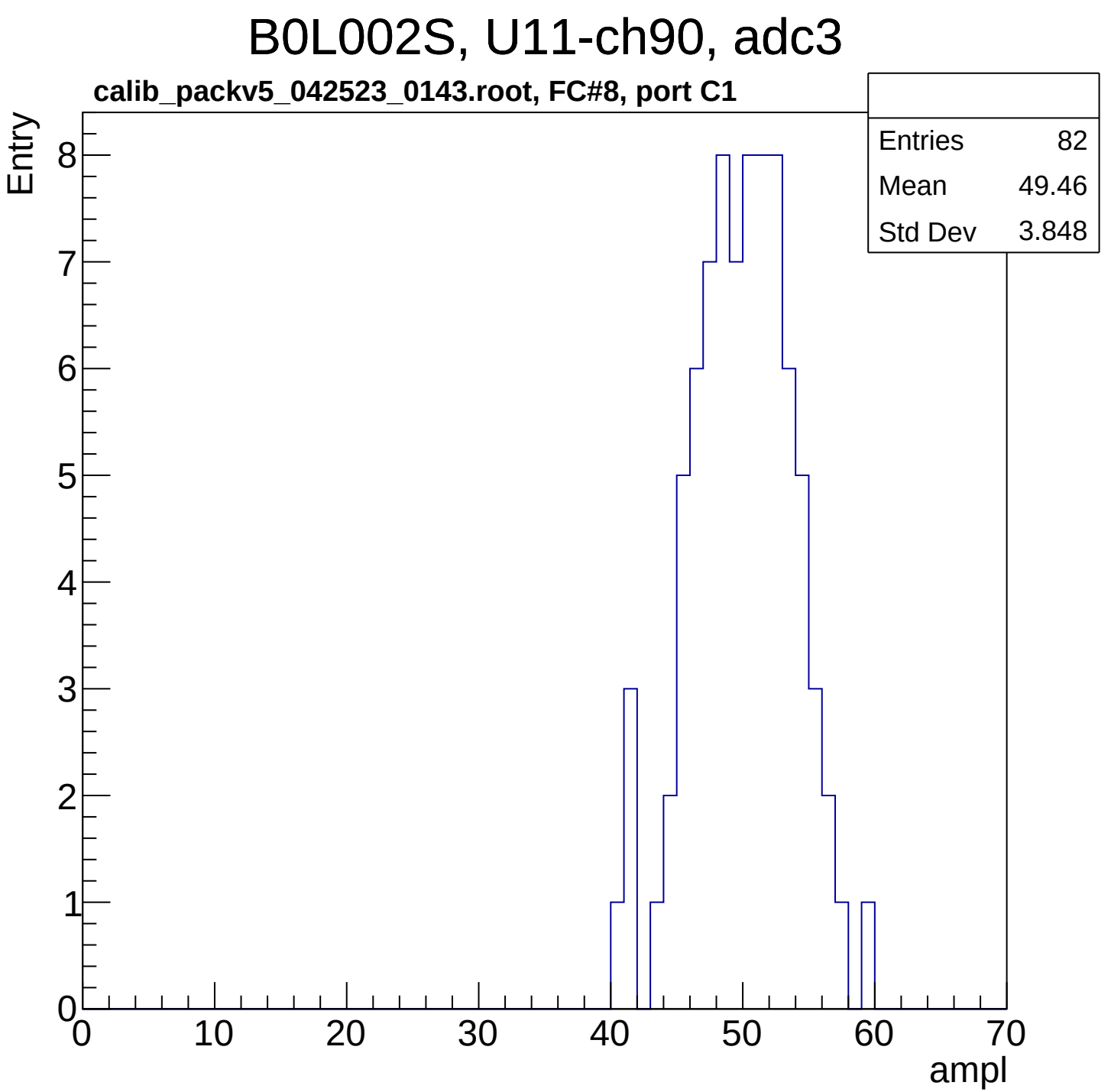

B0L002S, U11-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

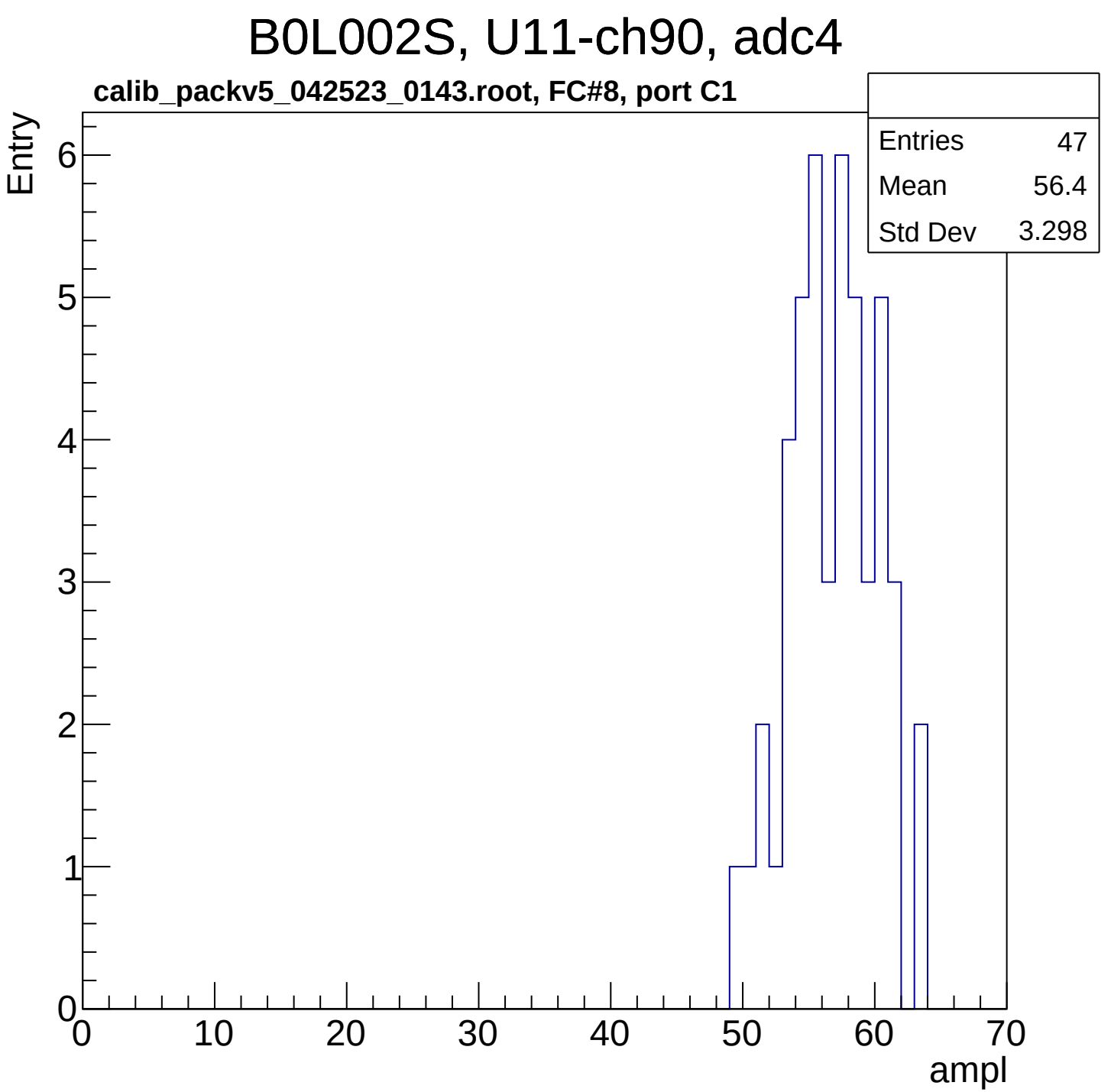

B0L002S, U11-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

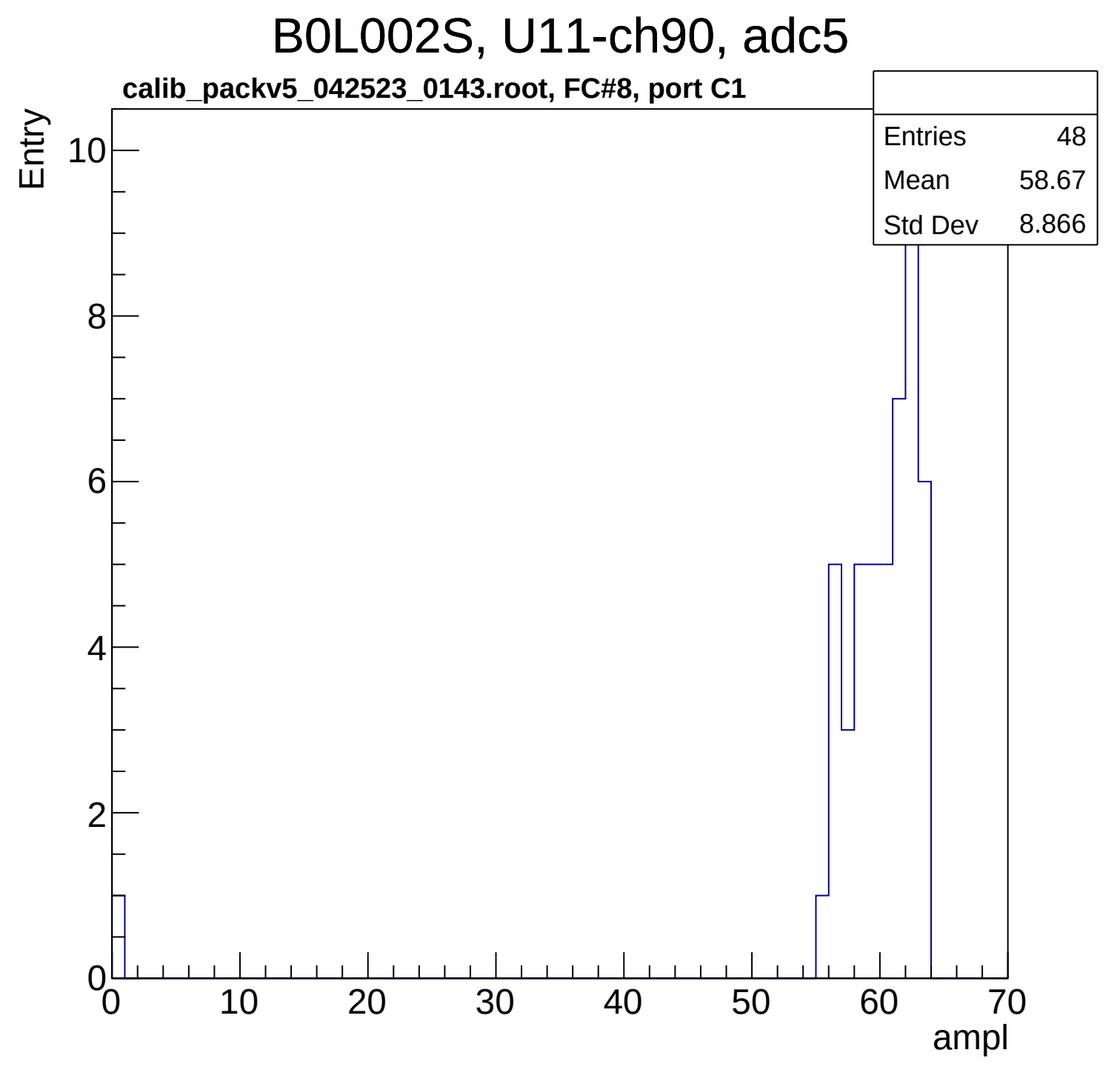

B0L002S, U11-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.73
Std Dev 3.145
Gaus mean : 31.1521
Gaus Width: 3.4994
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

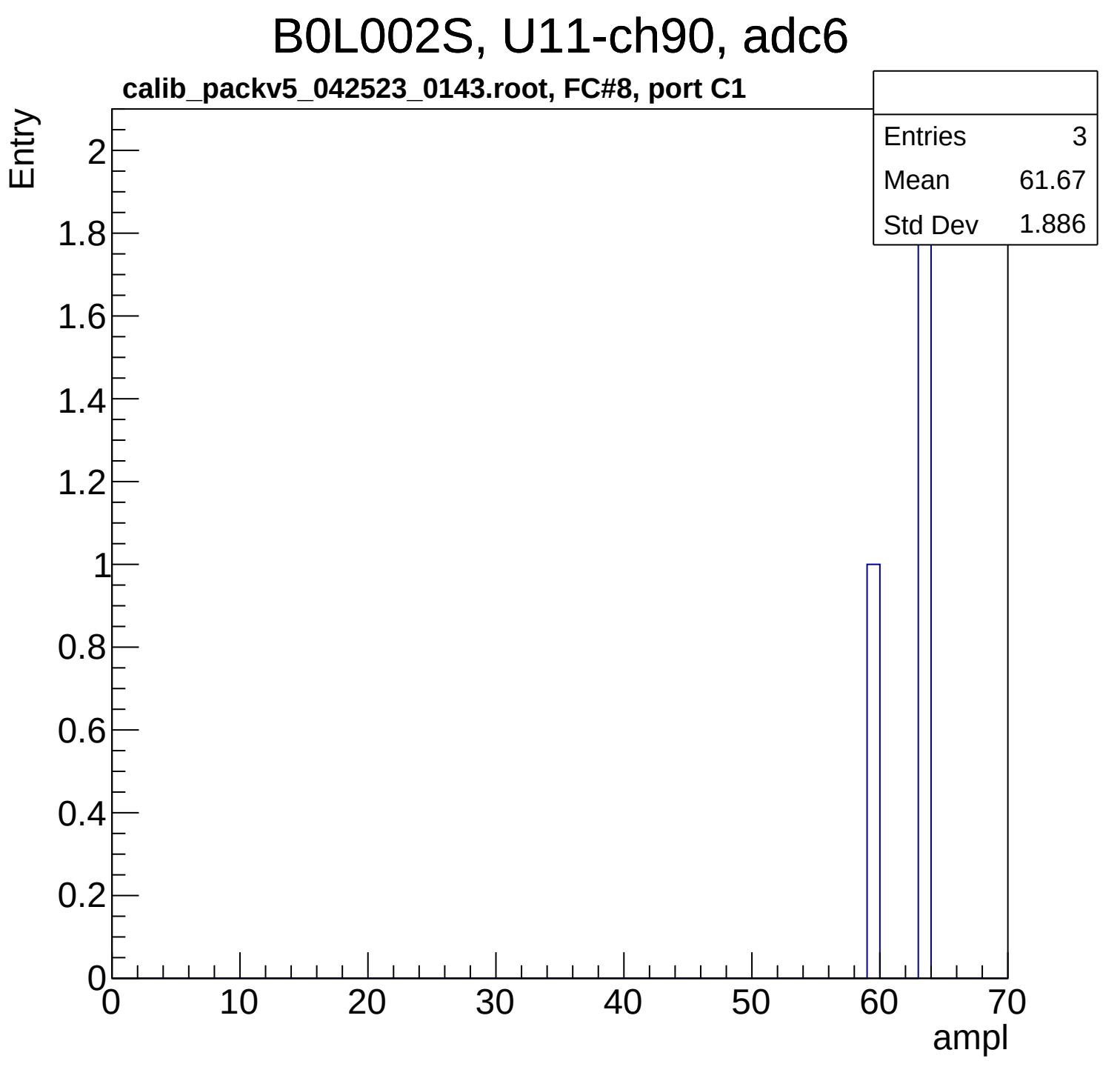

B0L002S, U11-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 36.94
Std Dev 3.725
Gaus mean : 37.8794
Gaus Width: 4.2163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

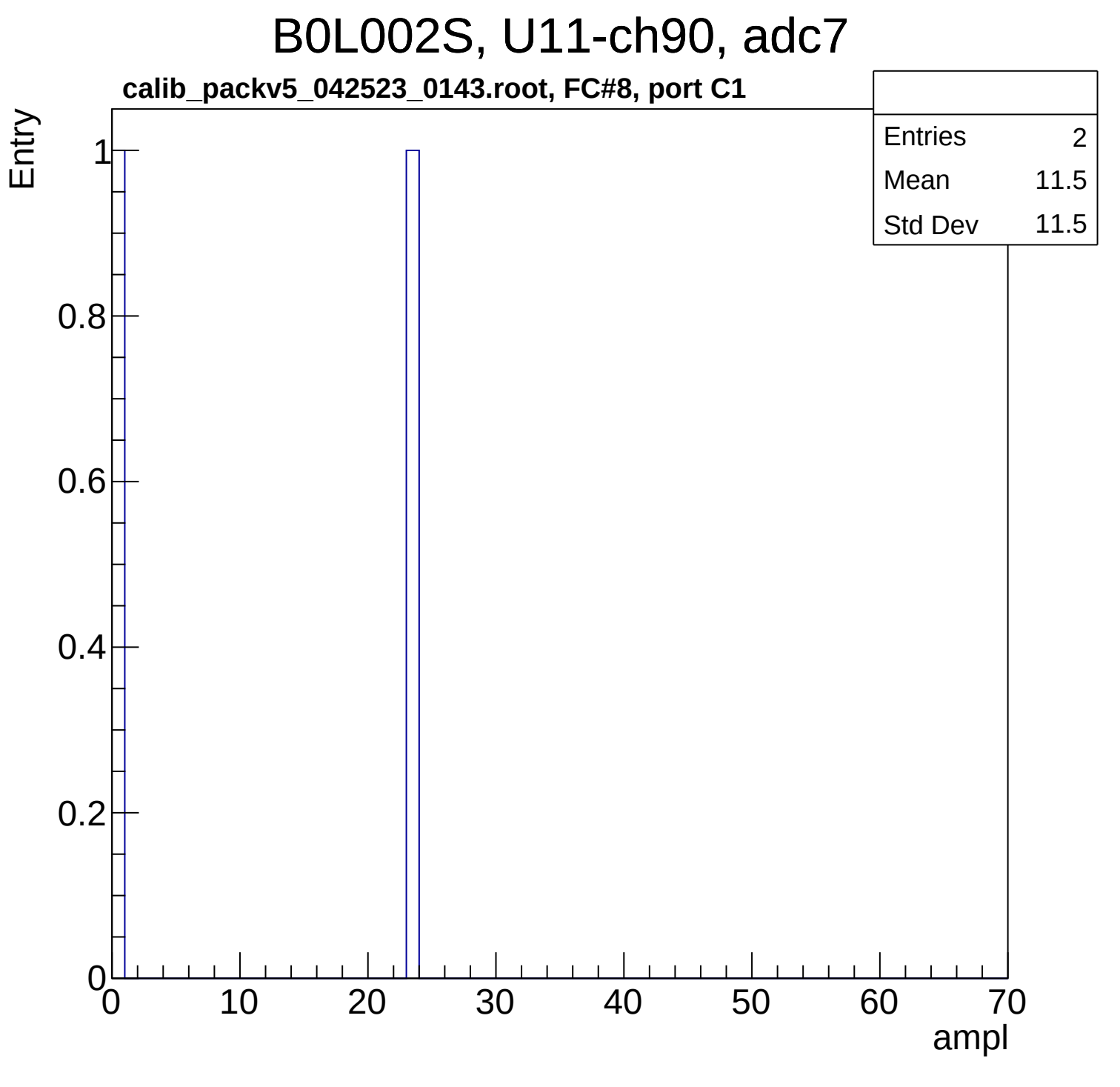

B0L002S, U11-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 43.76
Std Dev 3.318
Gaus mean : 44.8395
Gaus Width: 3.6532
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

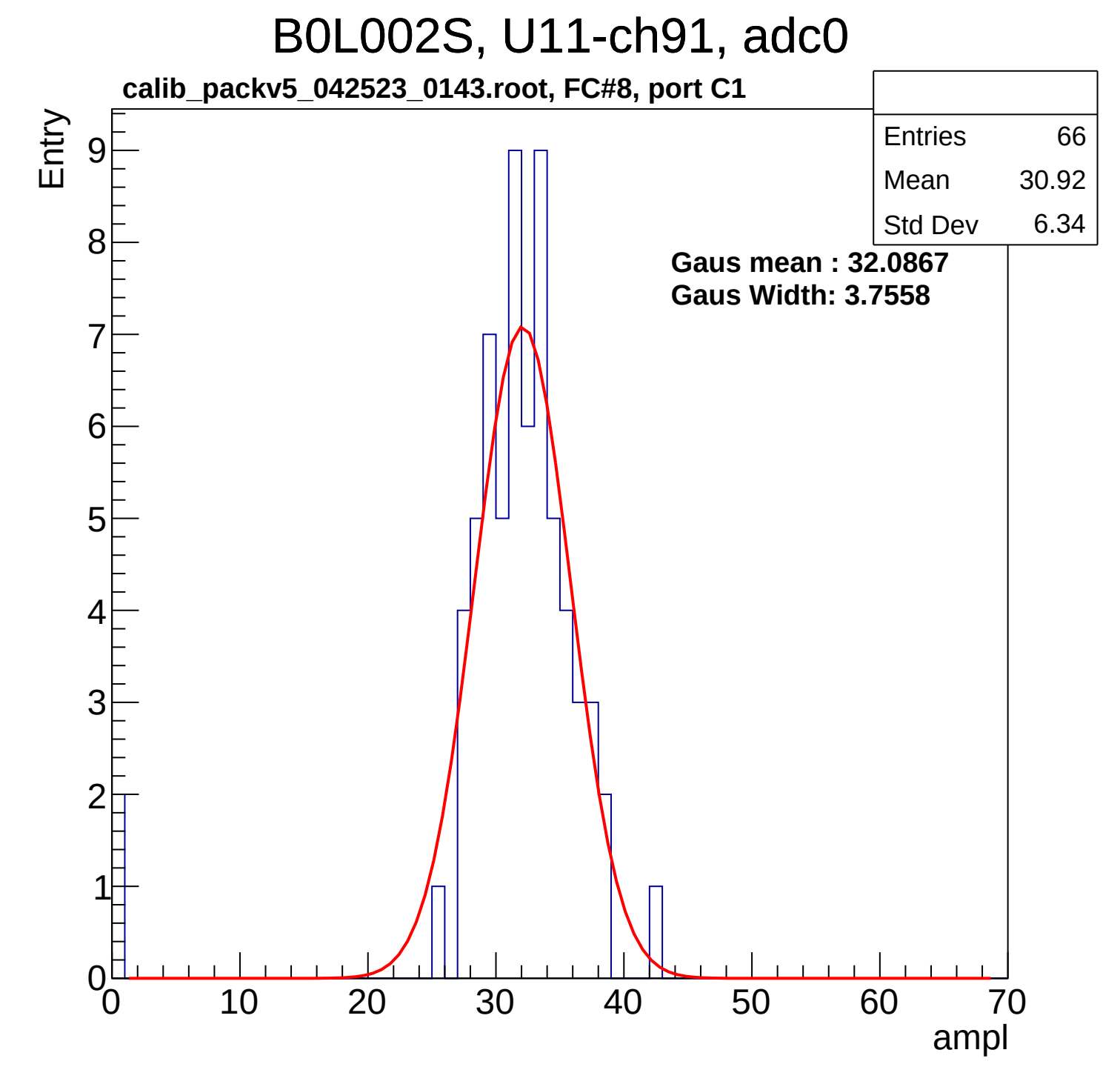

B0L002S, U11-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 49.46
Std Dev 3.848
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

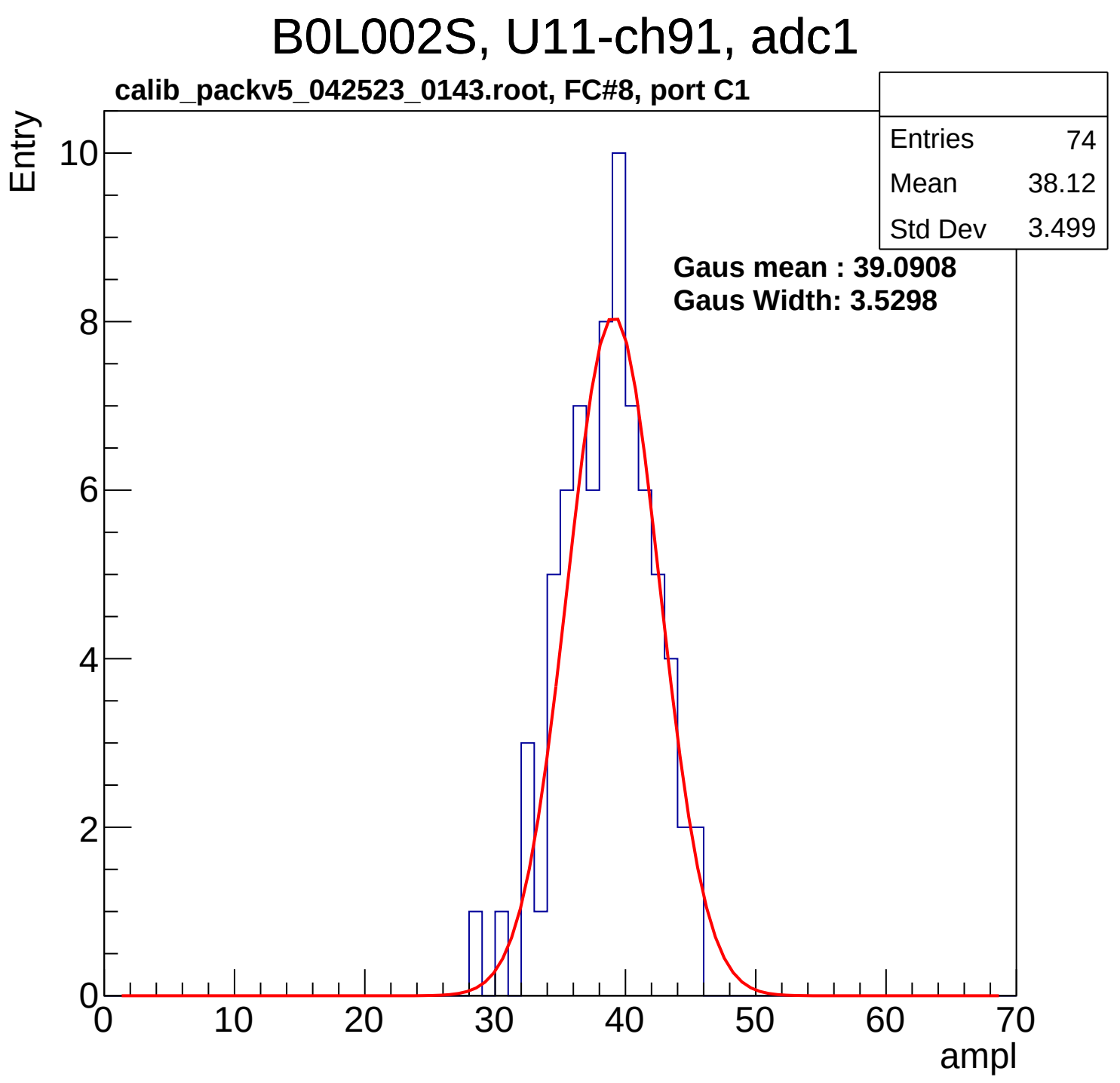

B0L002S, U11-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 56.4
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

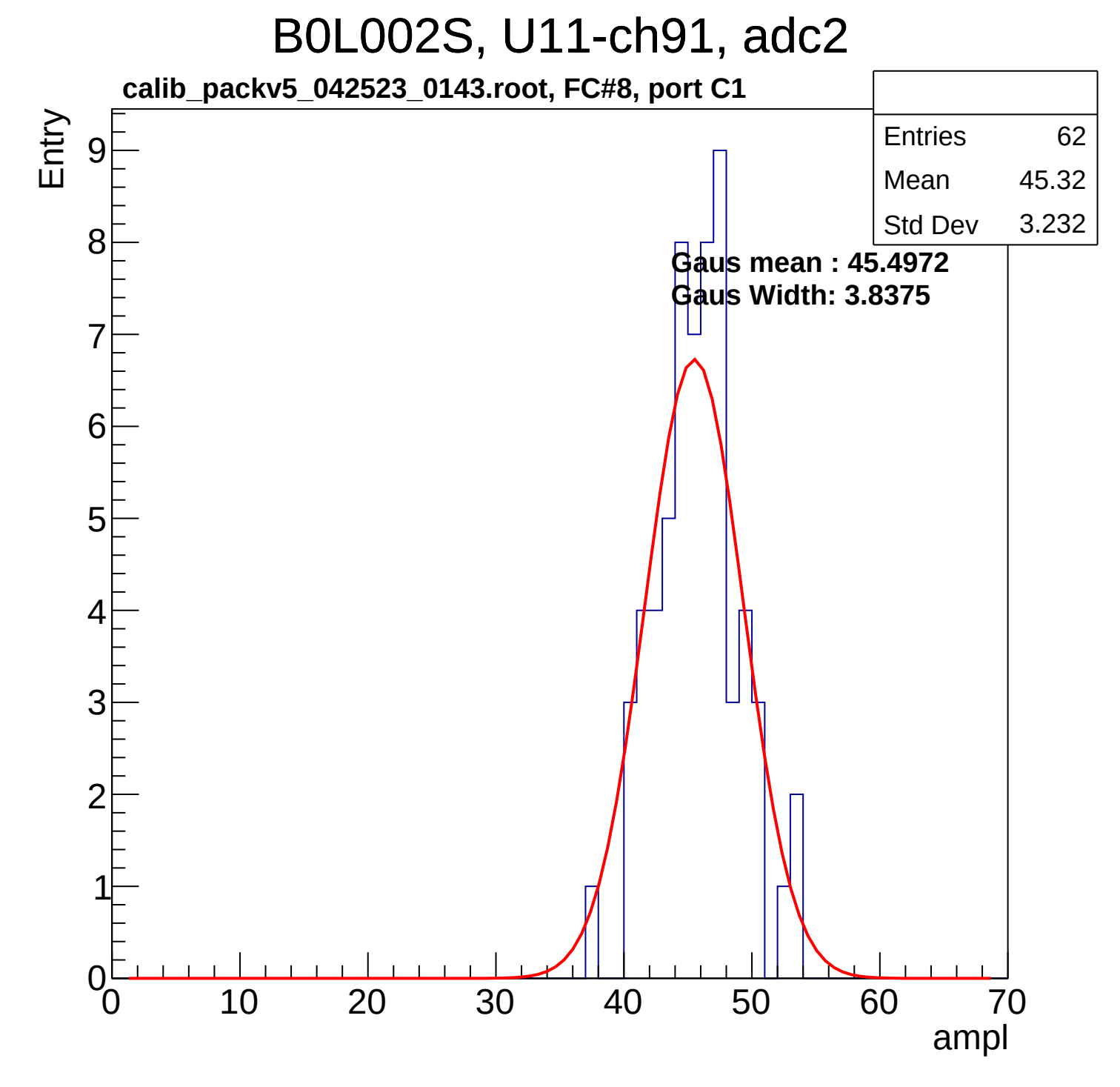

B0L002S, U11-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 58.67
Std Dev 8.866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.92
Std Dev 6.34
Gaus mean : 32.0867
Gaus Width: 3.7558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.12
Std Dev 3.499
Gaus mean : 39.0908
Gaus Width: 3.5298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.32
Std Dev 3.232
Gaus mean : 45.4972
Gaus Width: 3.8375
Entry
ampl
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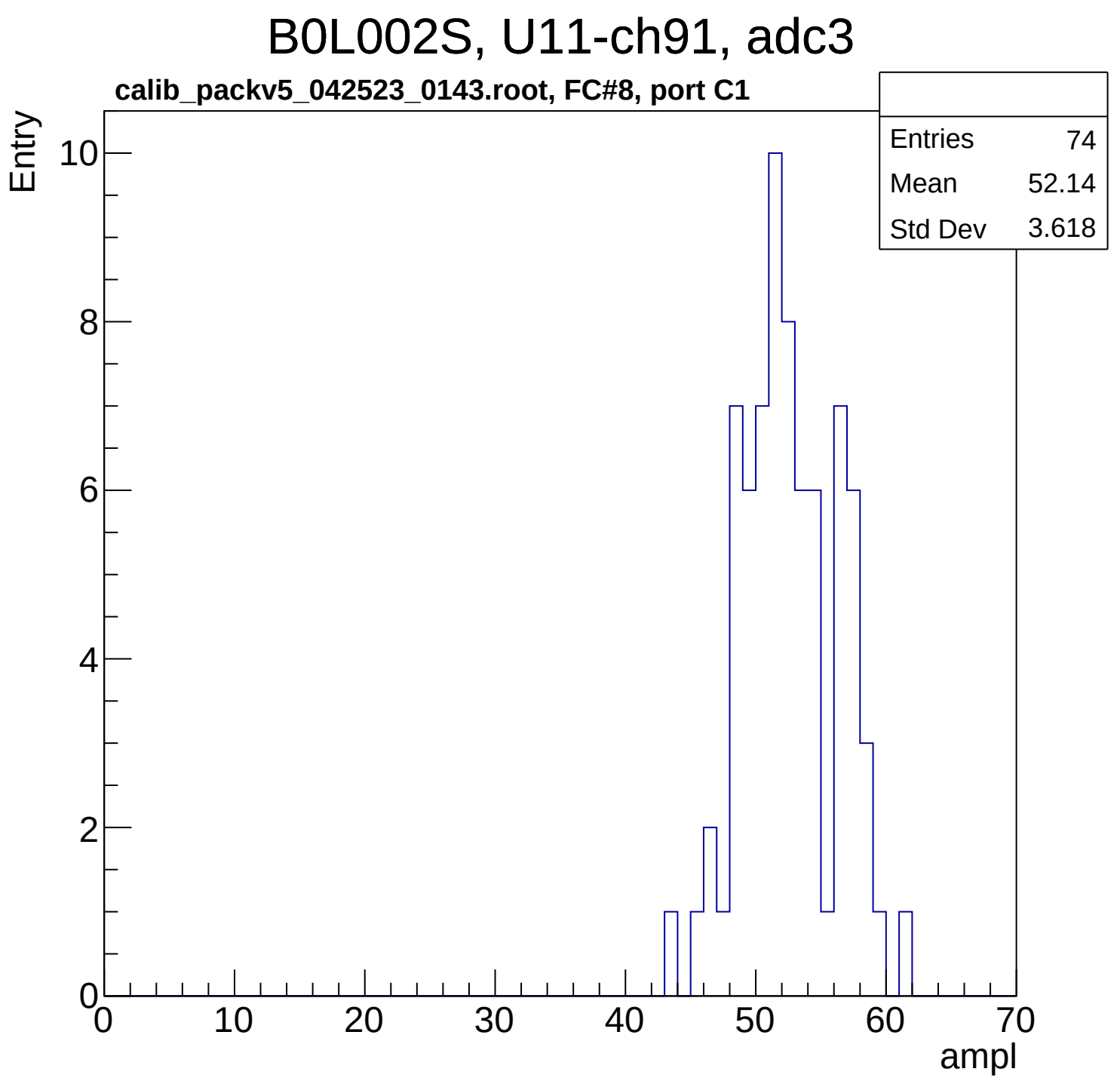
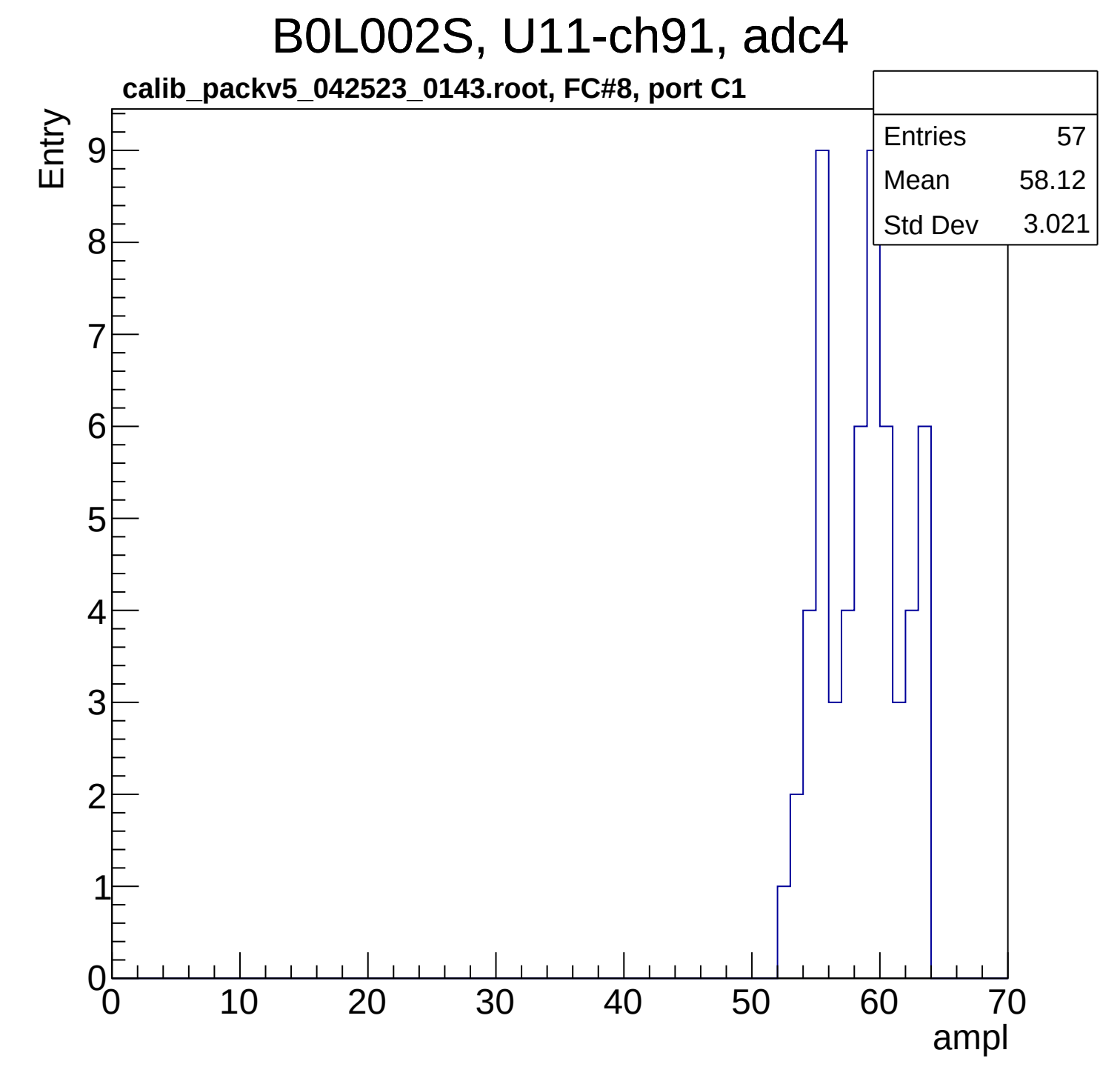
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 52.14
Std Dev 3.618
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 58.12
Std Dev 3.021
Entry
ampl
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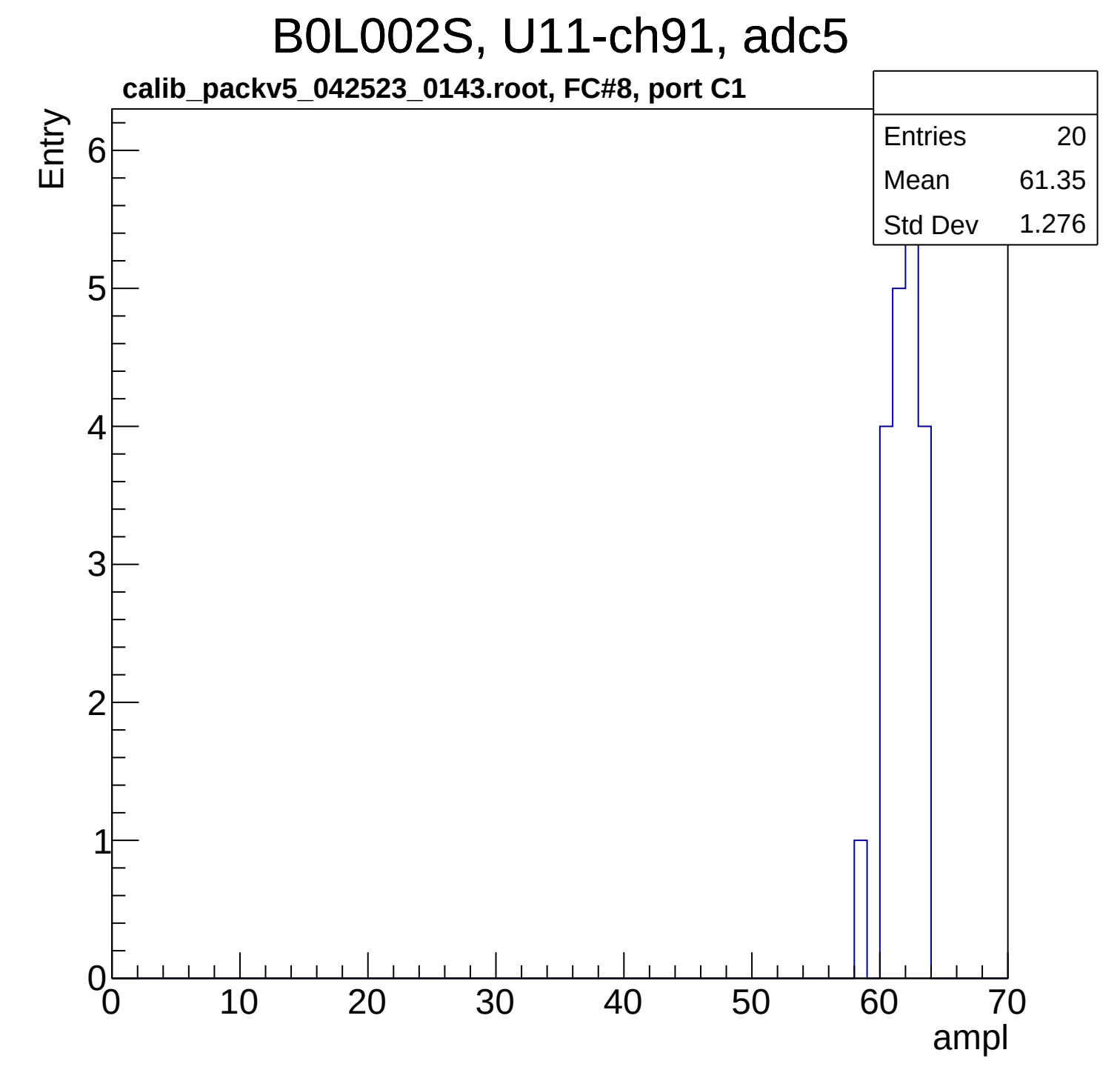
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 61.35
Std Dev 1.276
Entry
0
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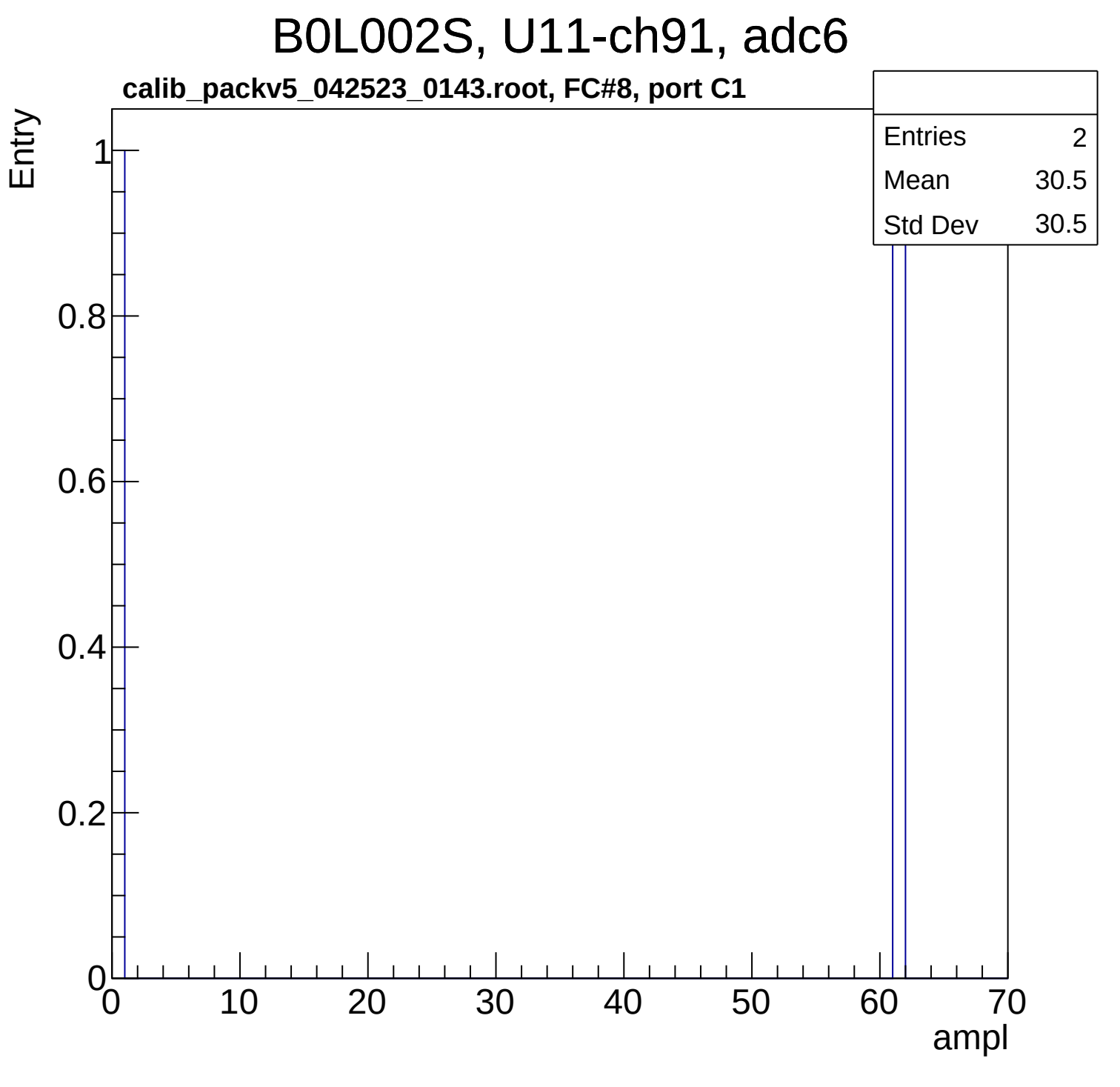
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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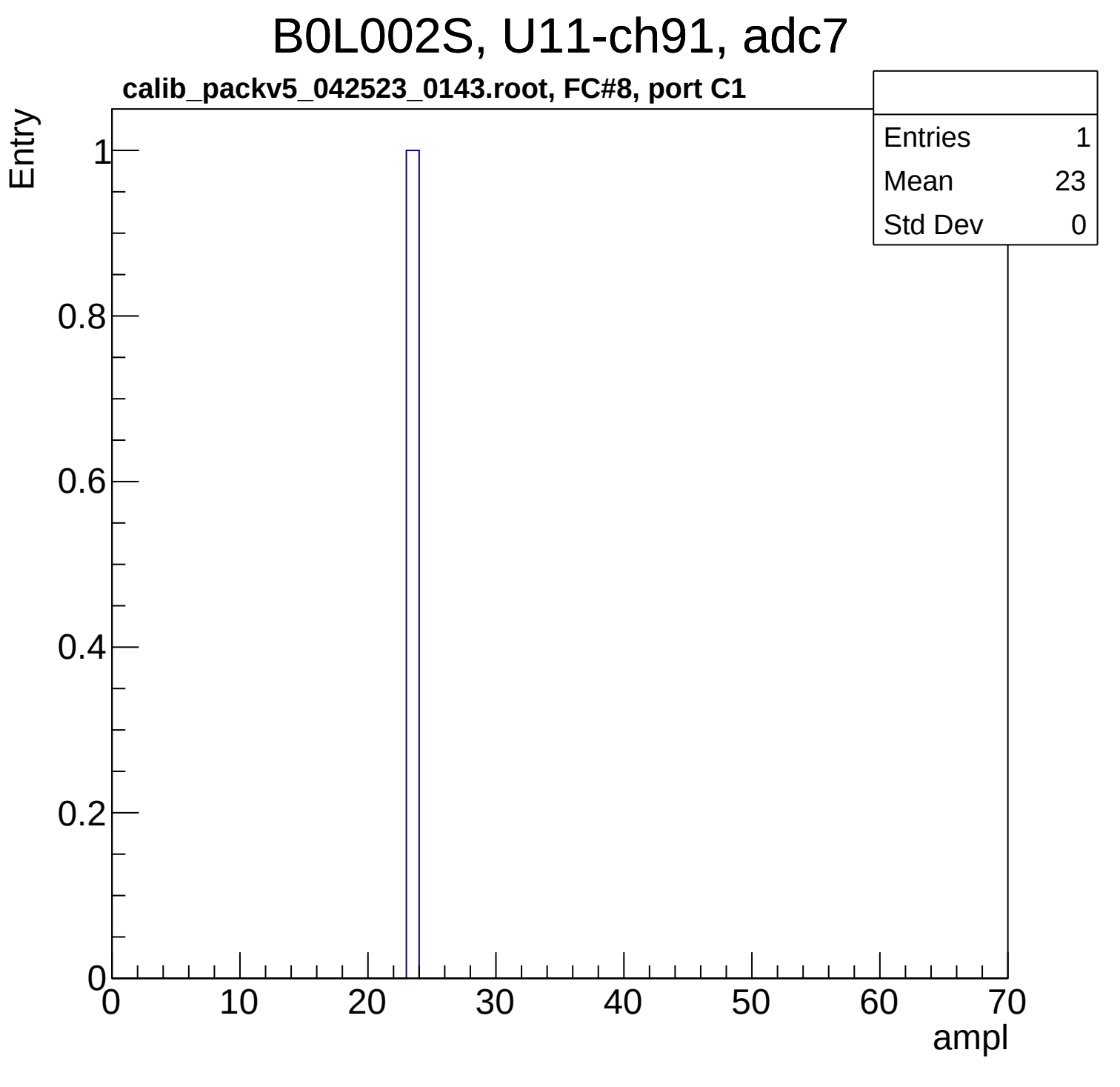
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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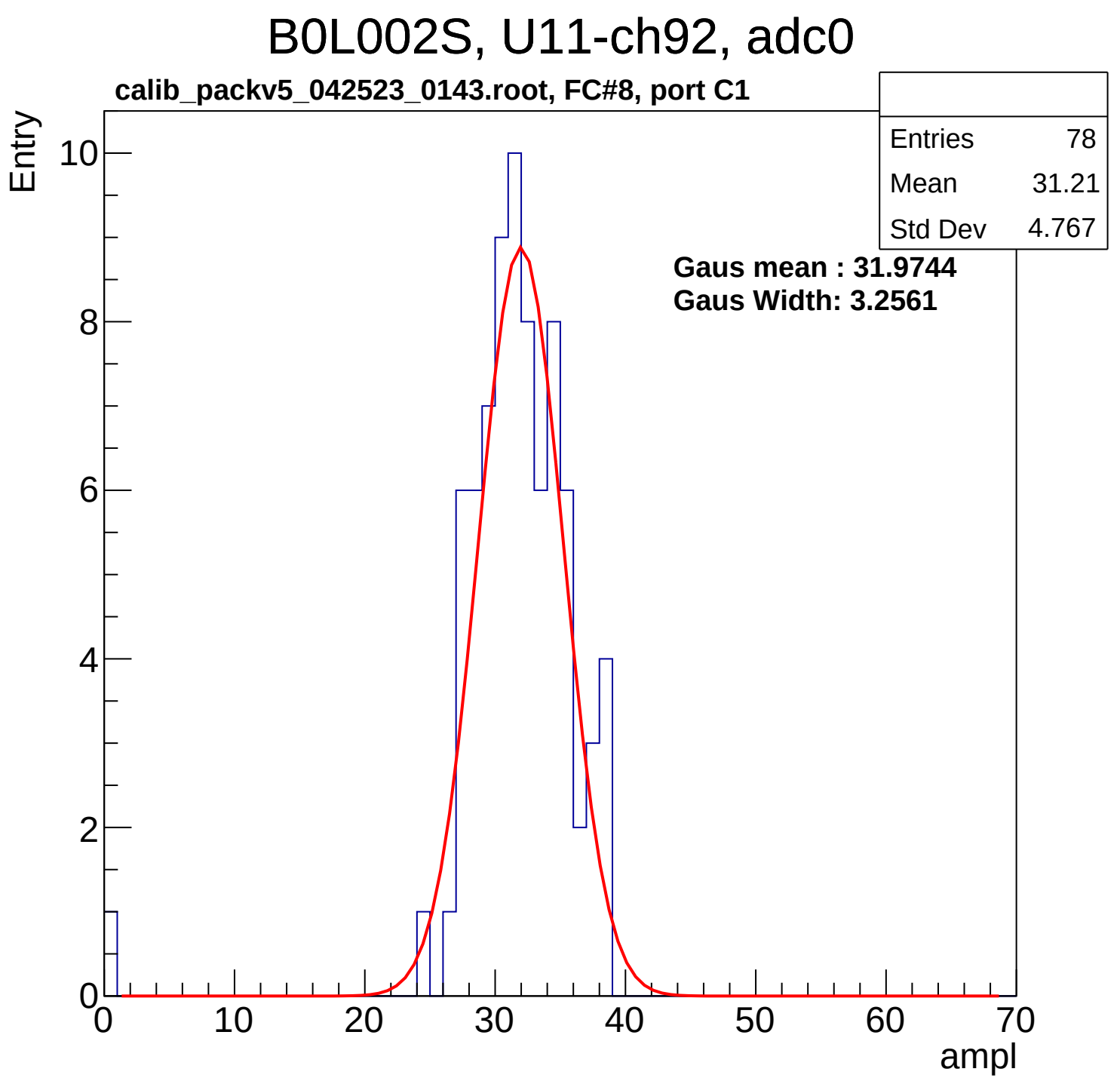
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 31.21
Std Dev 4.767
Gaus mean : 31.9744
Gaus Width: 3.2561
Entry
ampl
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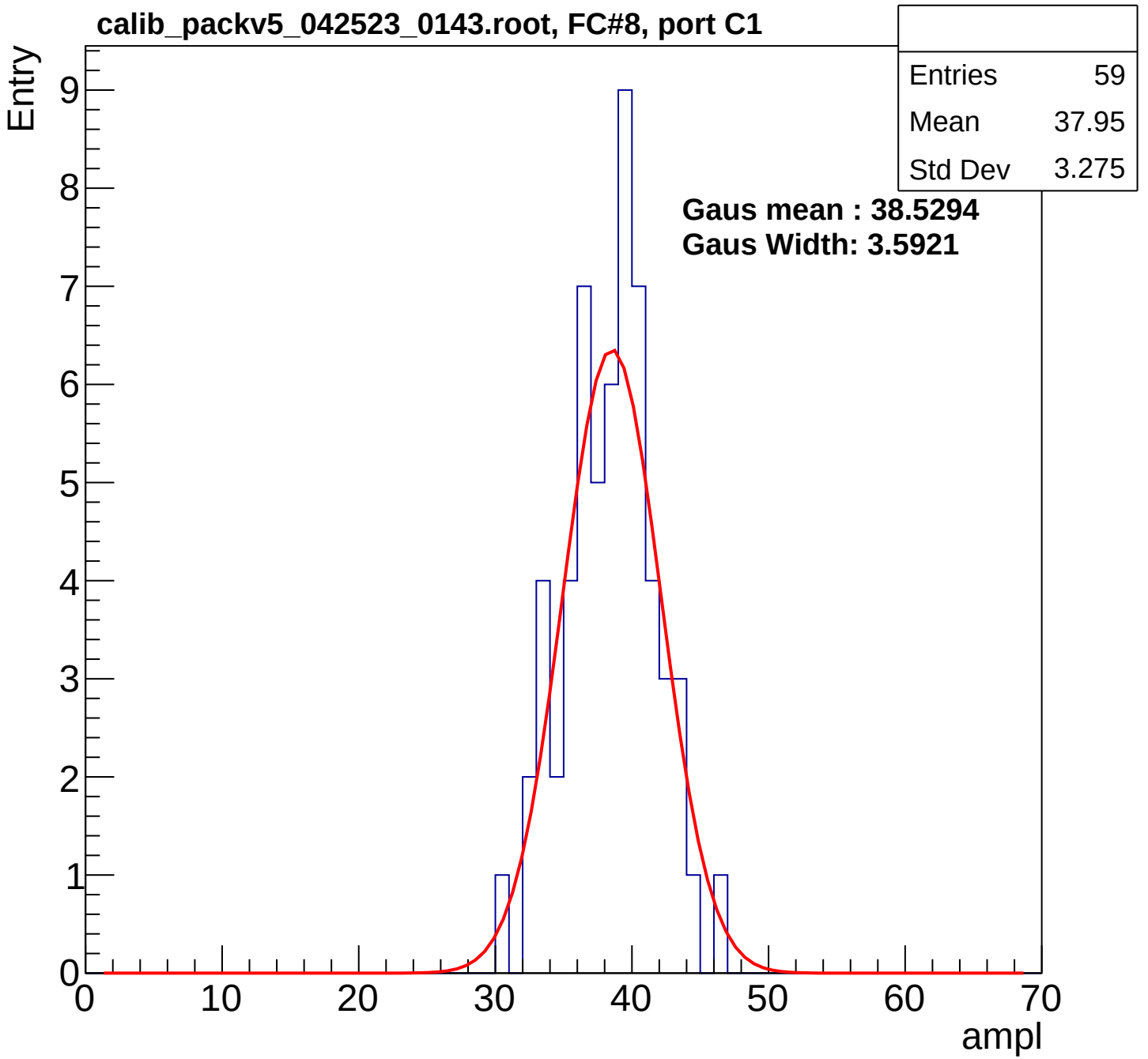
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.95
Std Dev 3.275
Gaus mean : 38.5294
Gaus Width: 3.5921
Entry
ampl
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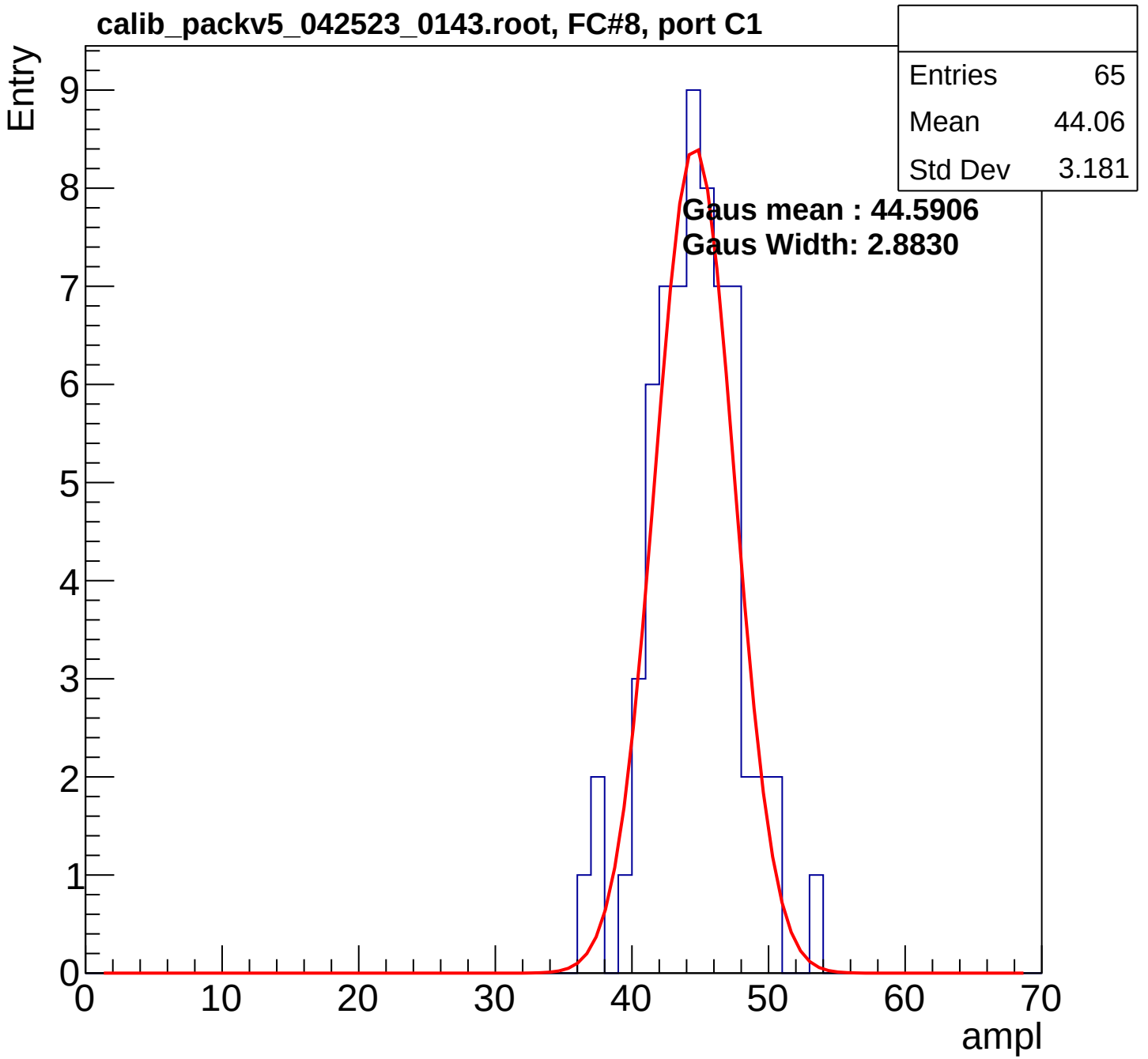
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.06
Std Dev 3.181
Gaus mean : 44.5906
Gaus Width: 2.8830
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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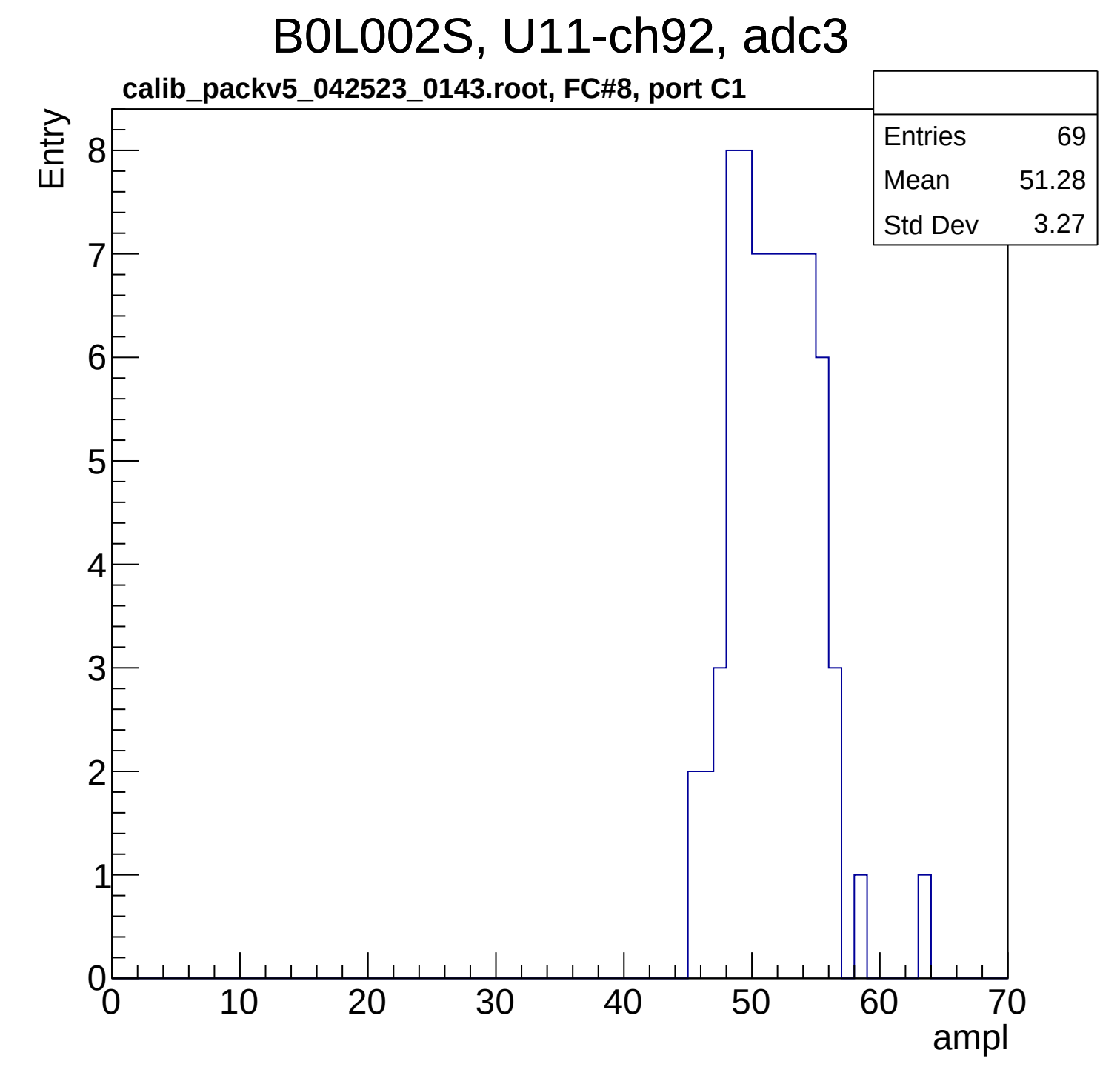
B0L002S, U11-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.28
Std Dev 3.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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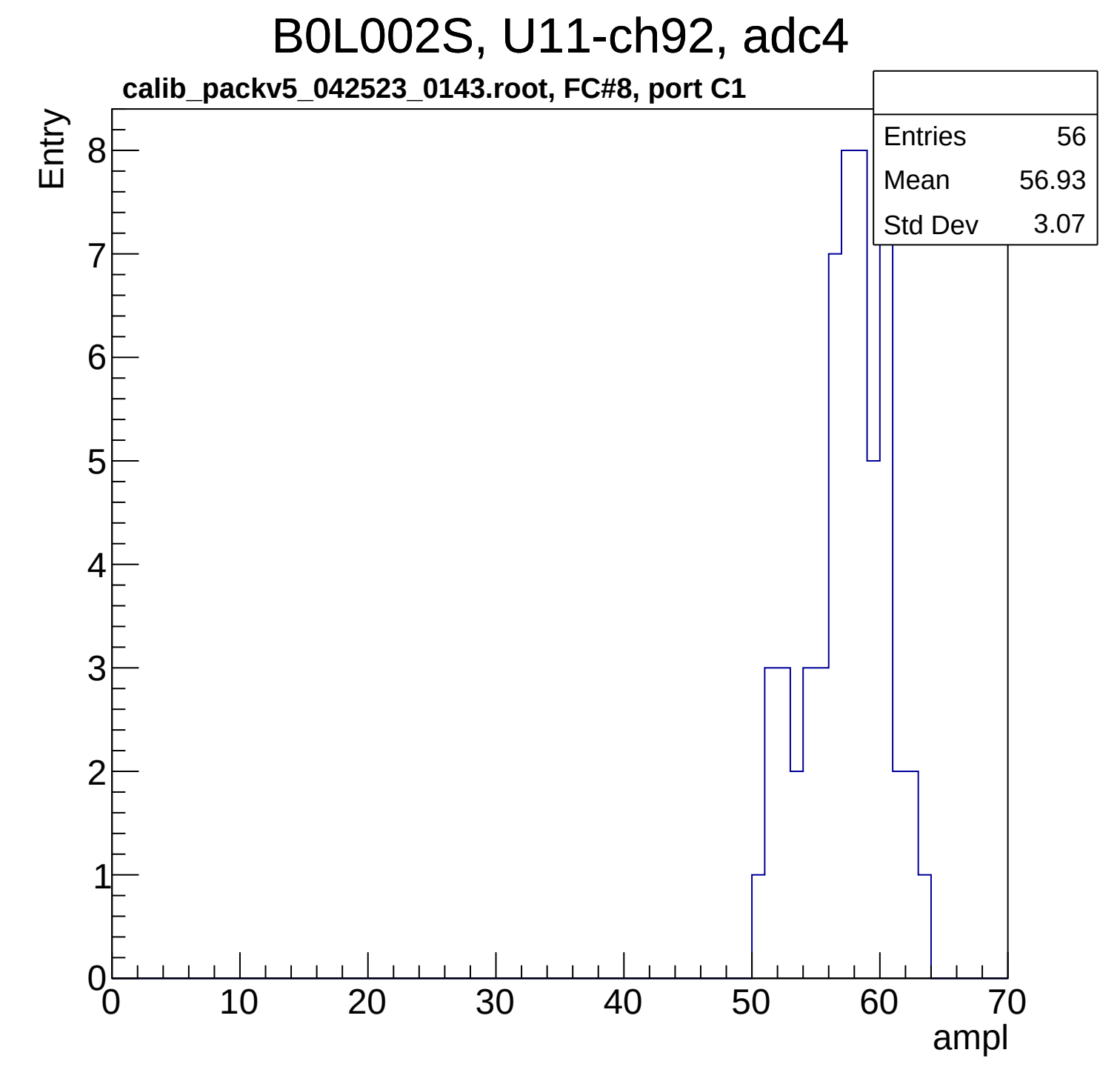
B0L002S, U11-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.93
Std Dev 3.07
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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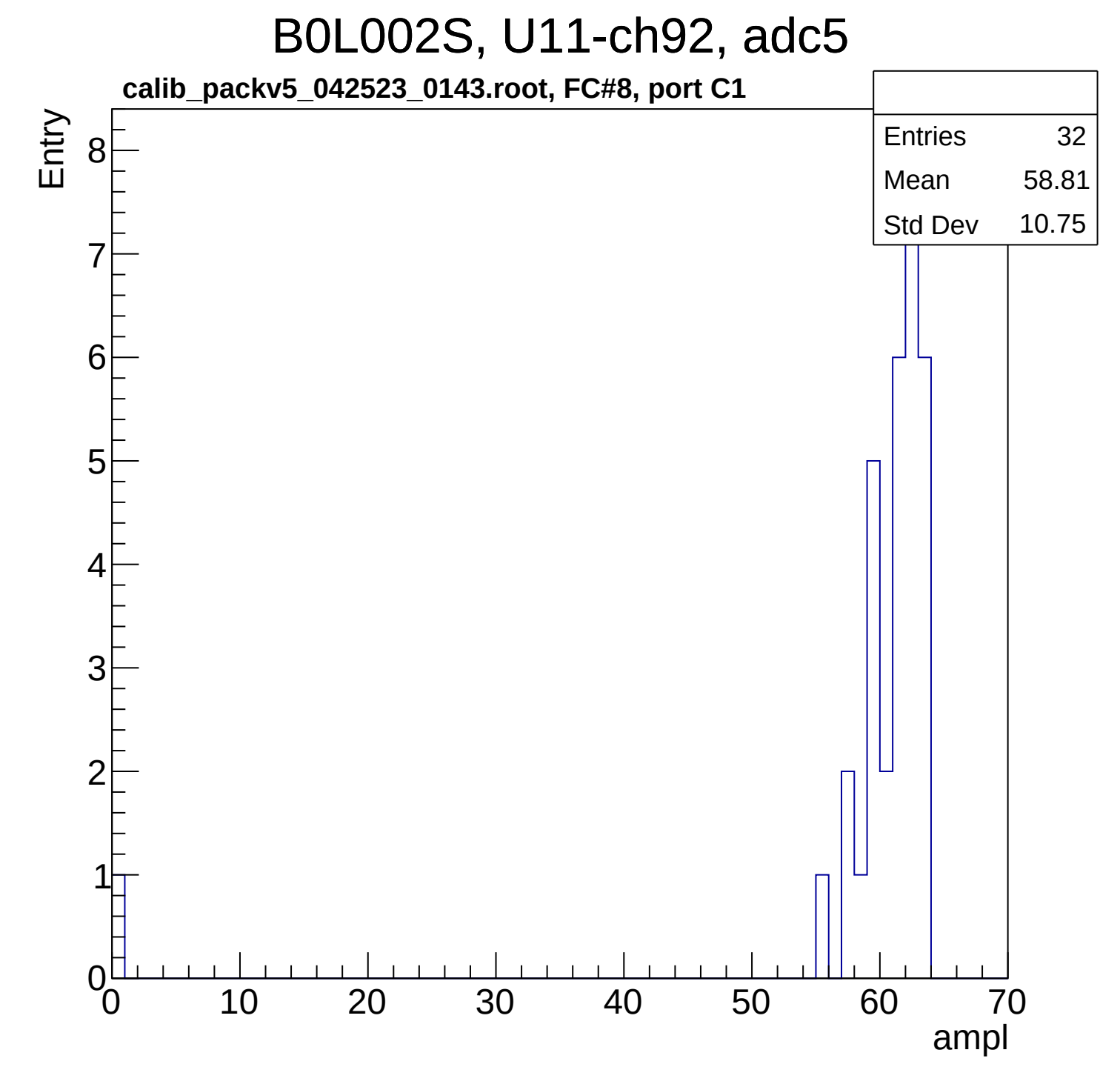
B0L002S, U11-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.81
Std Dev 10.75
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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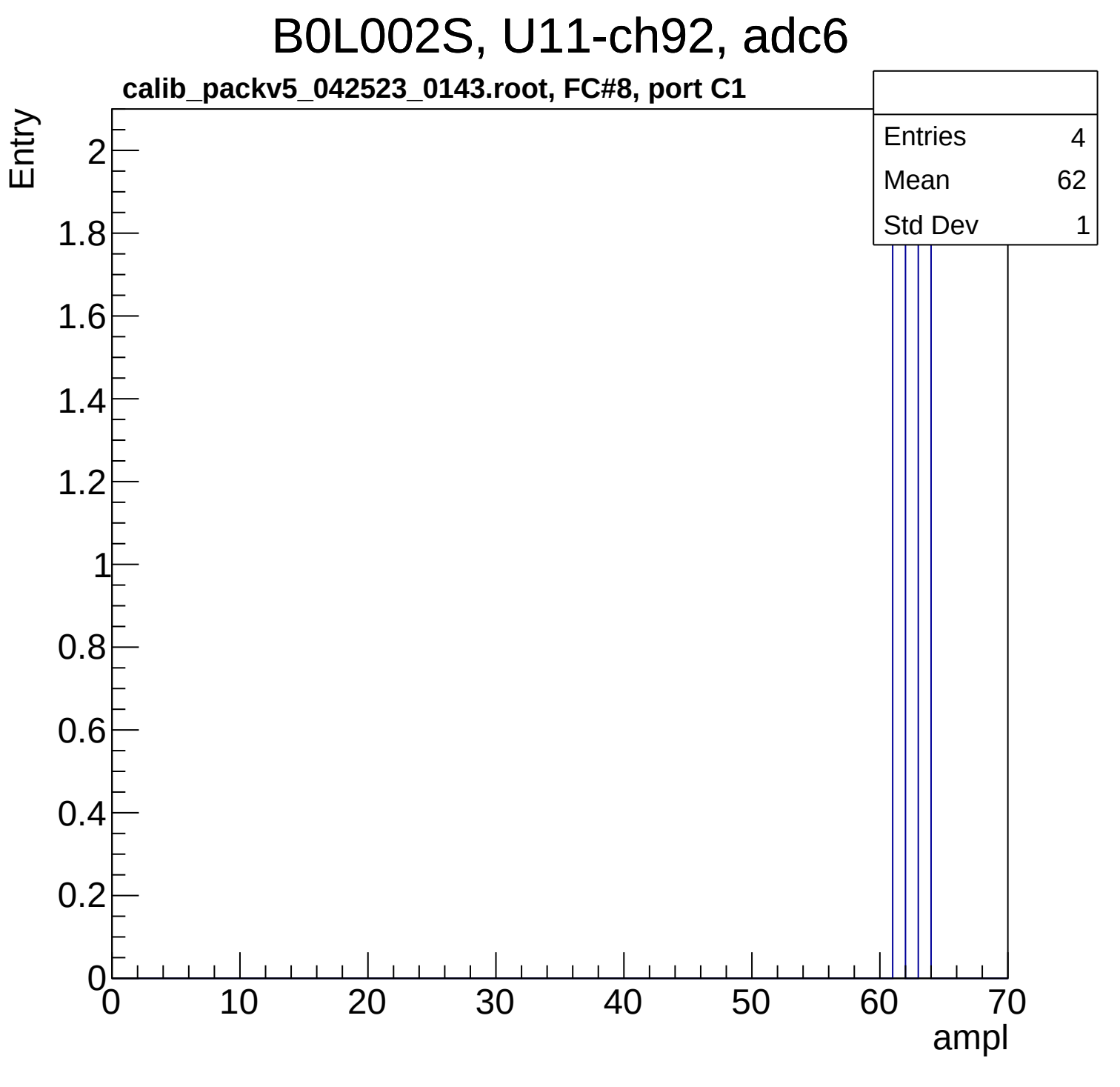
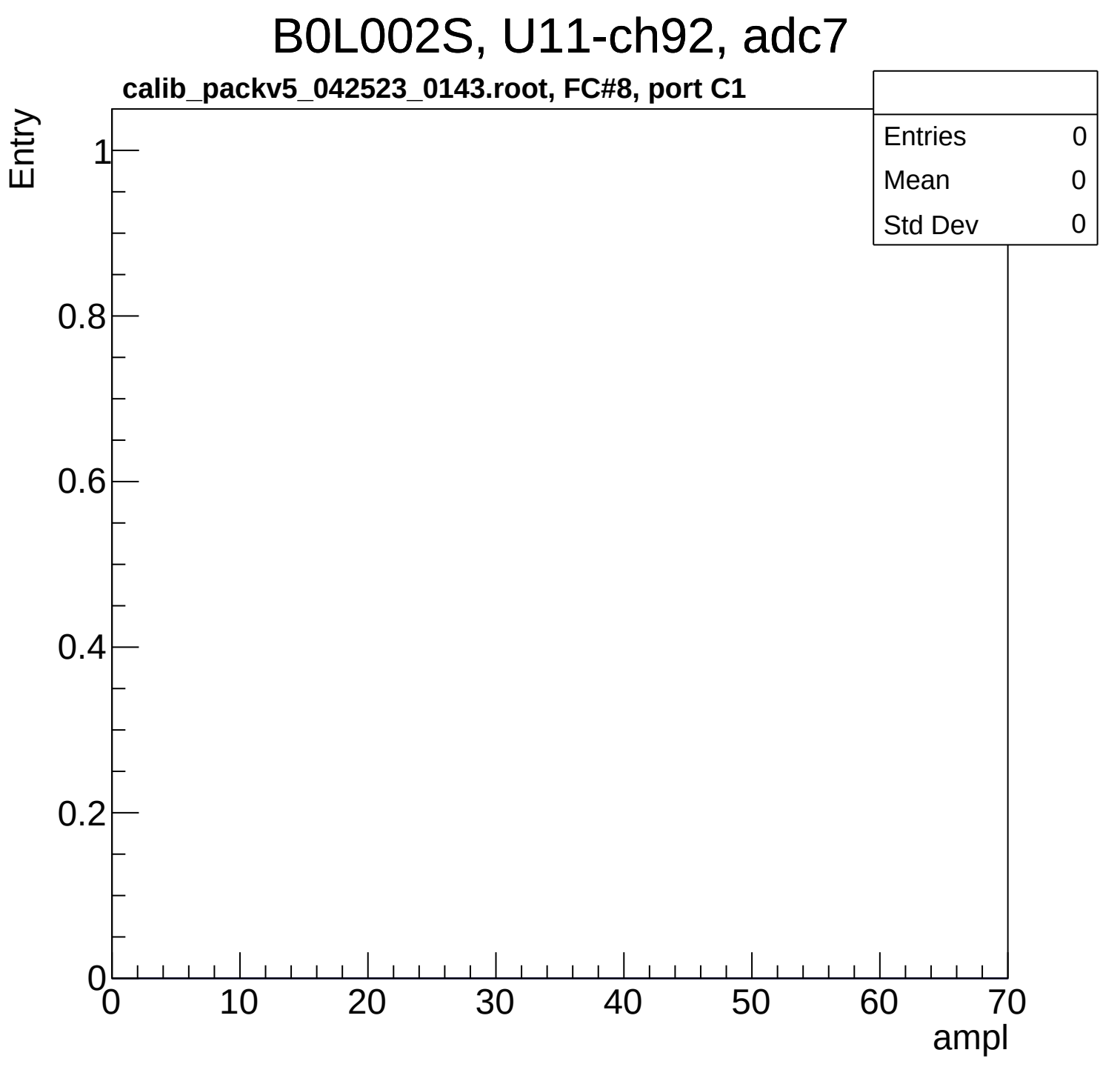
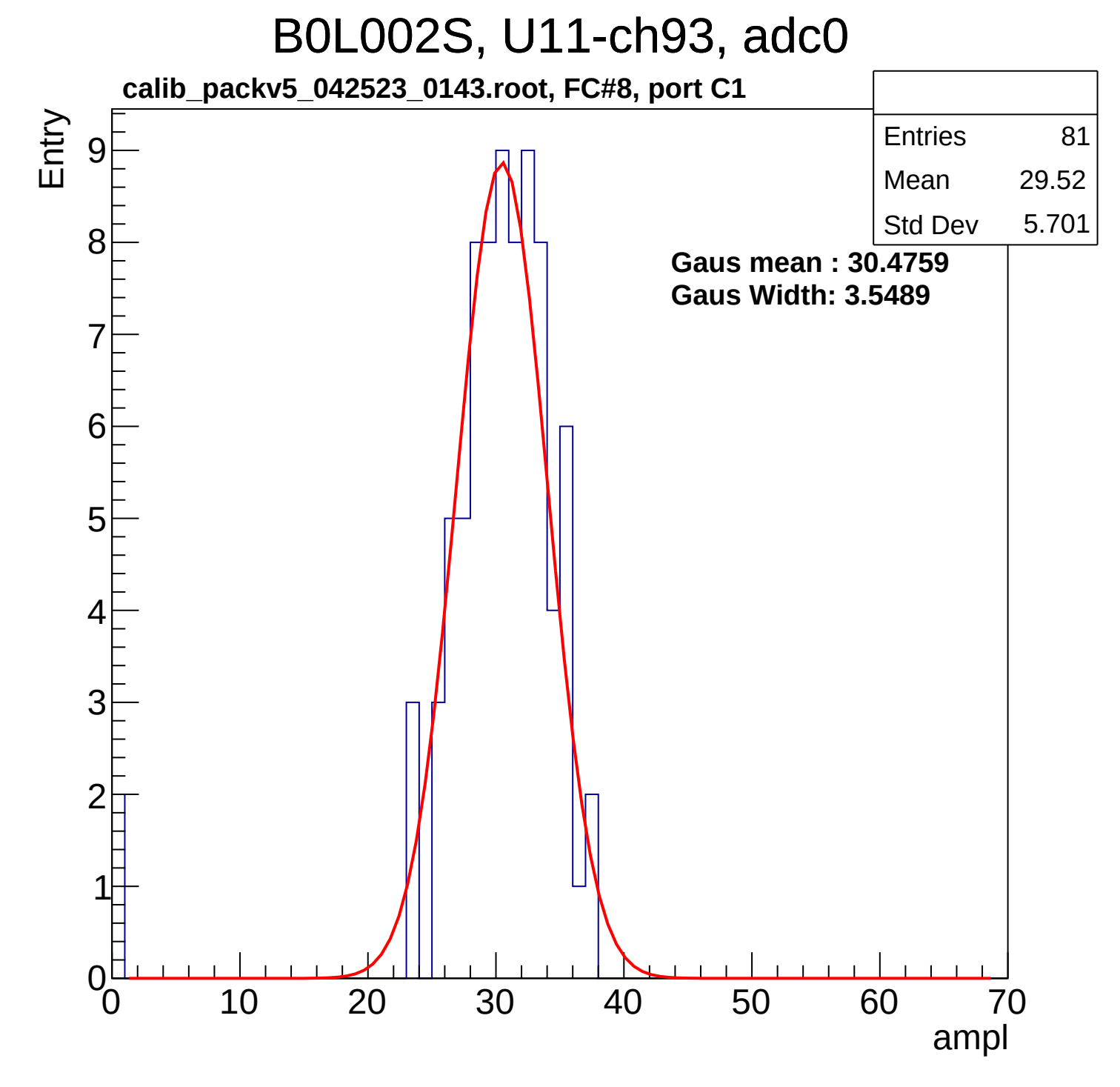

B0L002S, U11-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 29.52
Std Dev 5.701
Gaus mean : 30.4759
Gaus Width: 3.5489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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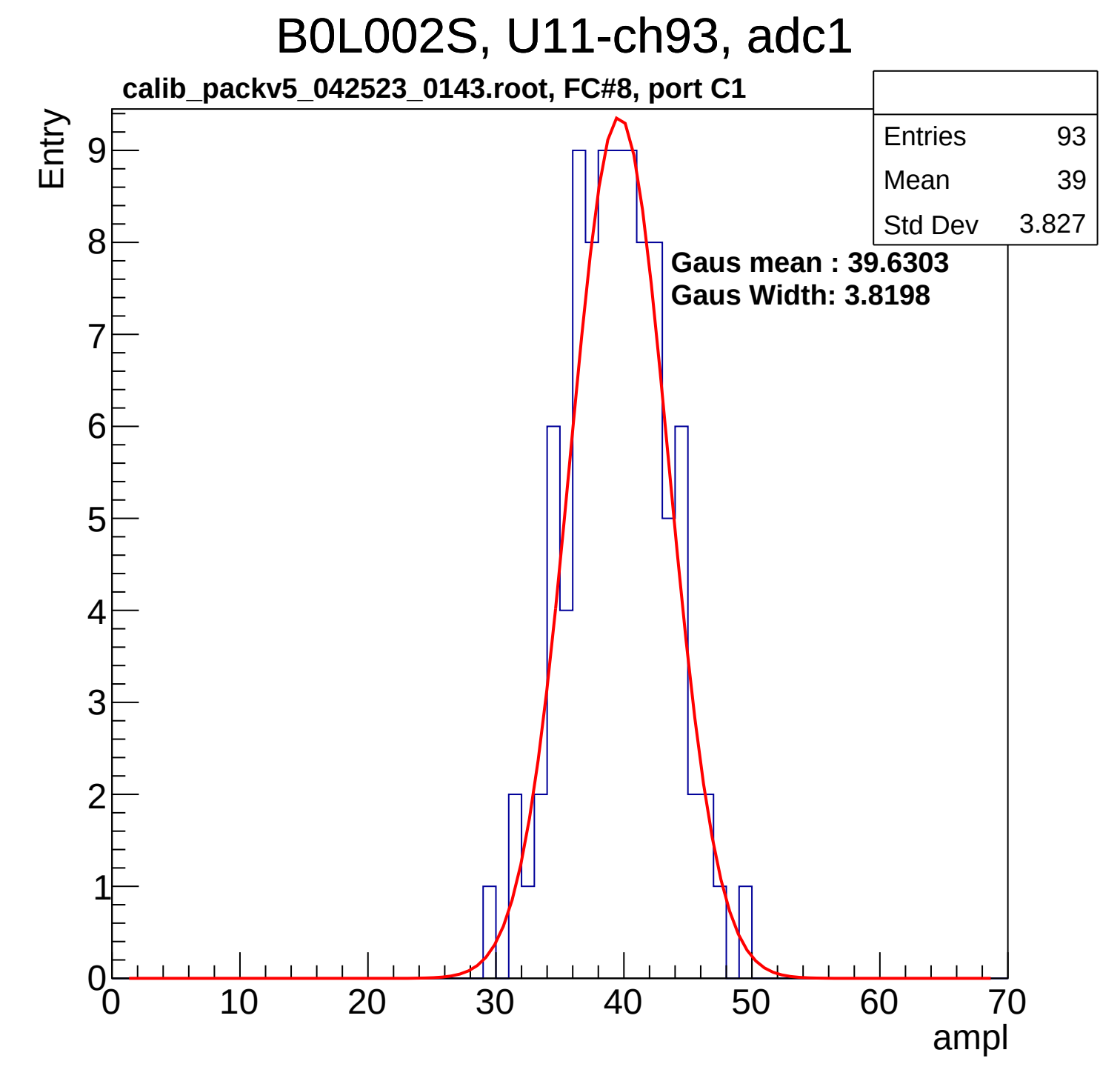
B0L002S, U11-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 93
Mean 39
Std Dev 3.827
Gaus mean : 39.6303
Gaus Width: 3.8198
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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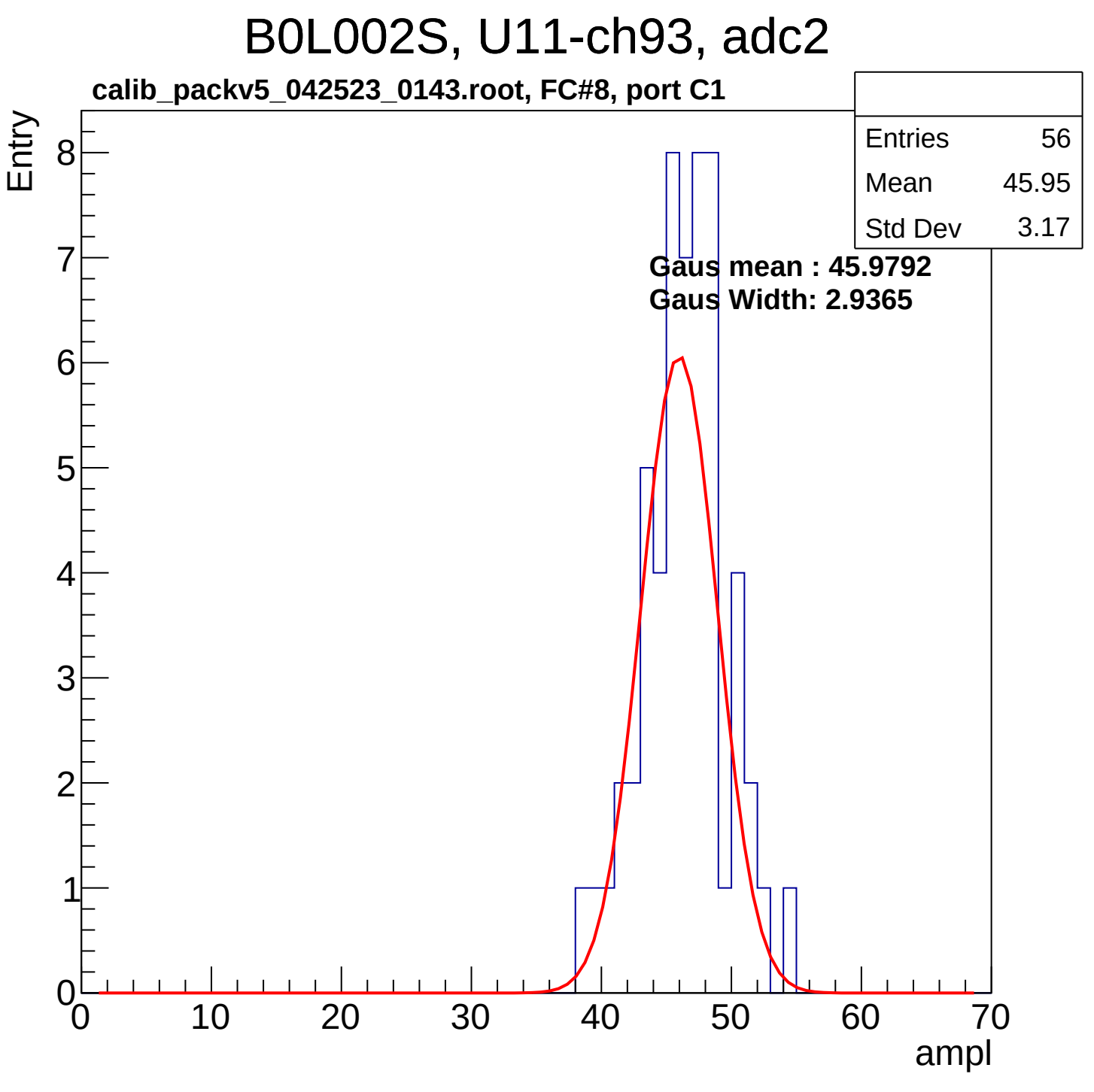
B0L002S, U11-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.95
Std Dev 3.17
Gaus mean : 45.9792
Gaus Width: 2.9365
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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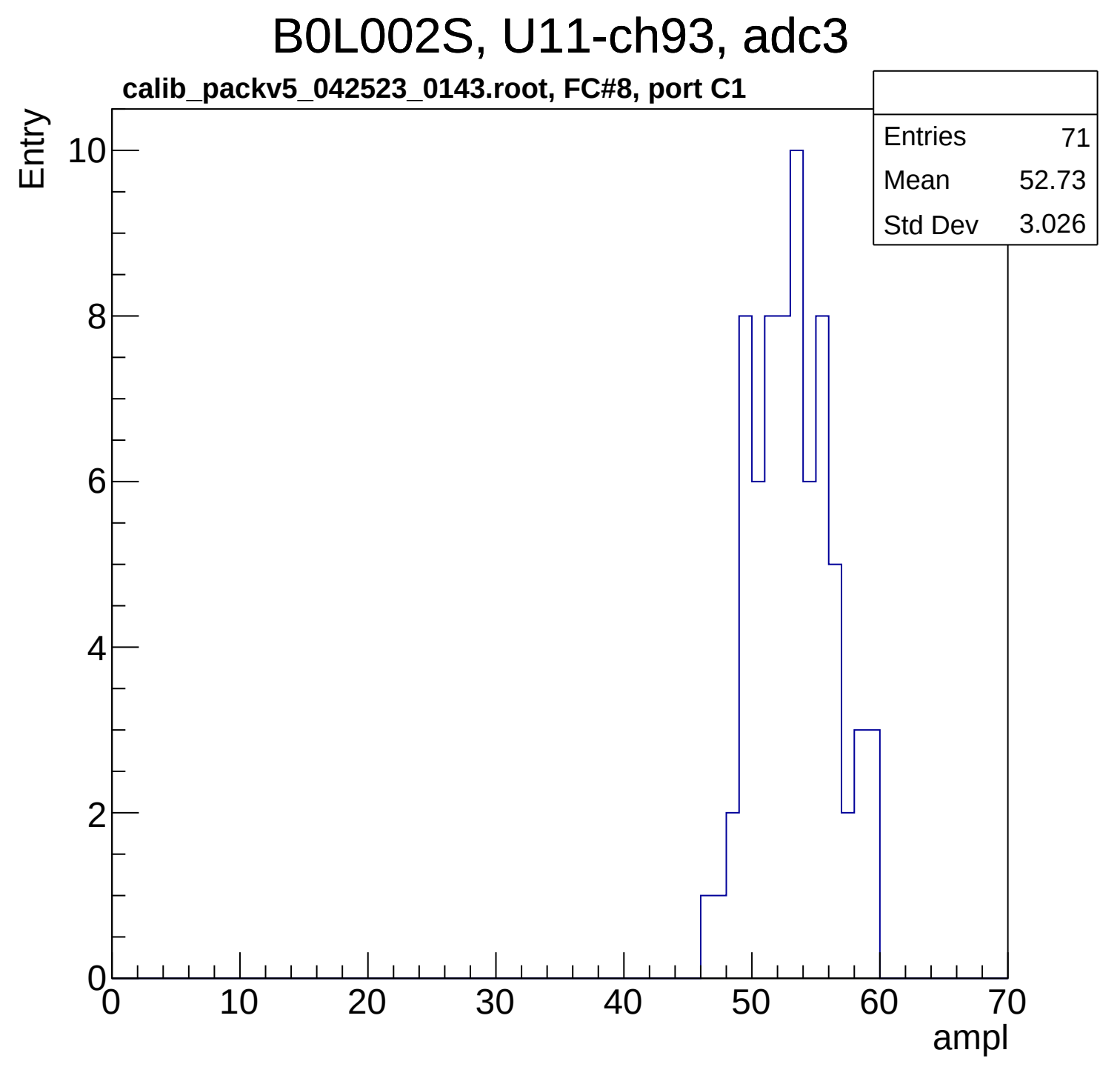
B0L002S, U11-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.73
Std Dev 3.026
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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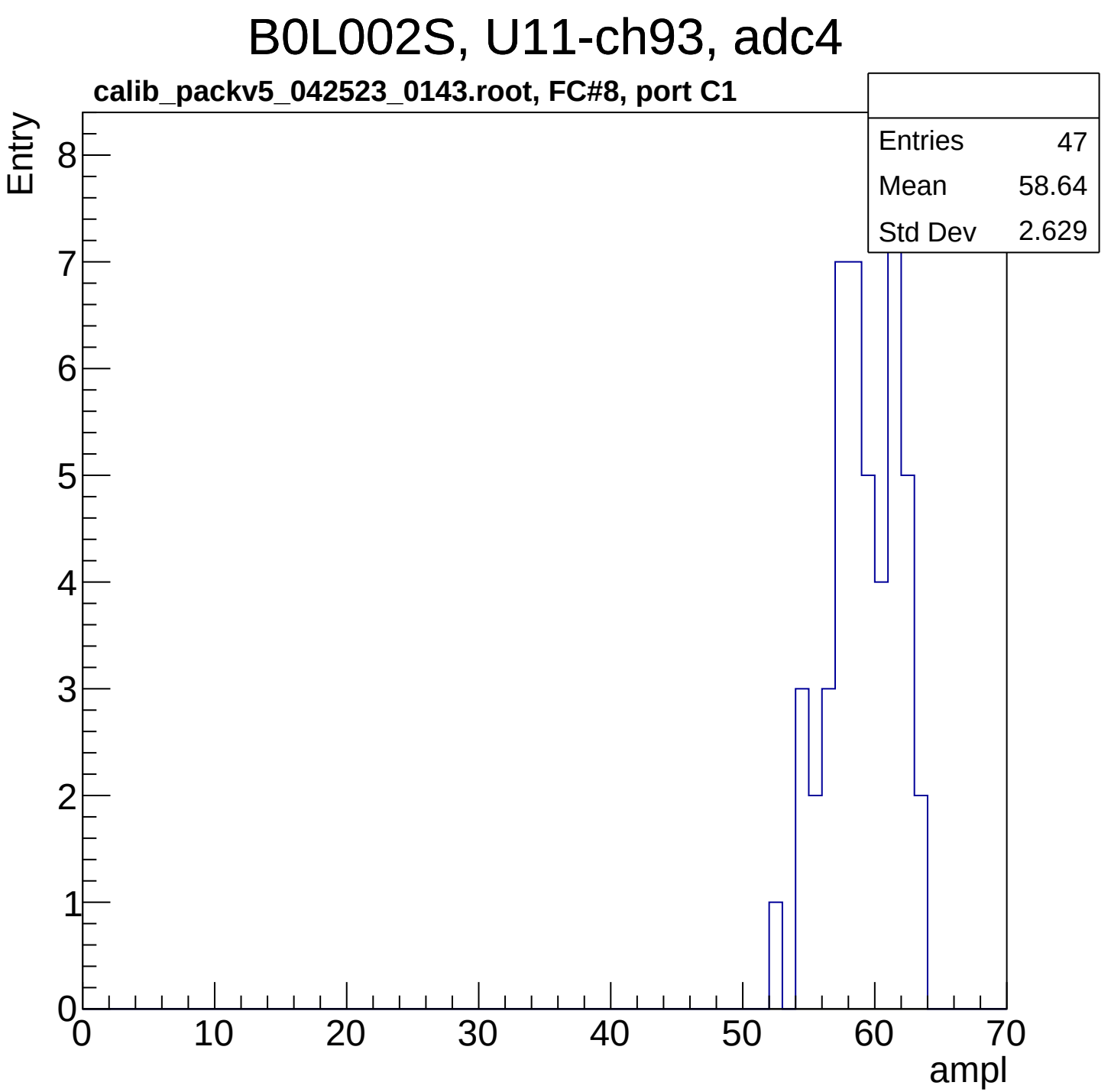
B0L002S, U11-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.64
Std Dev 2.629
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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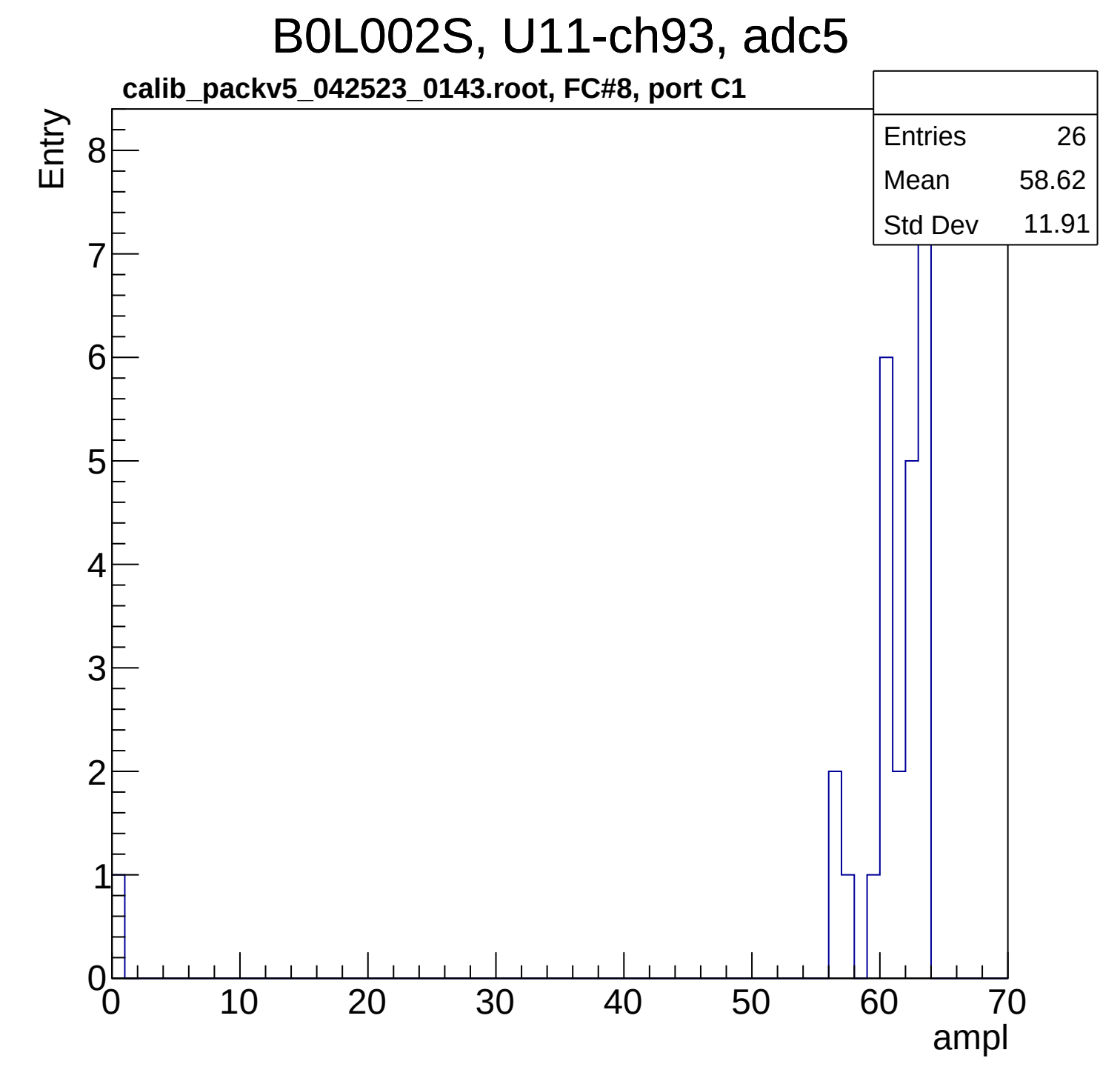
B0L002S, U11-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.62
Std Dev 11.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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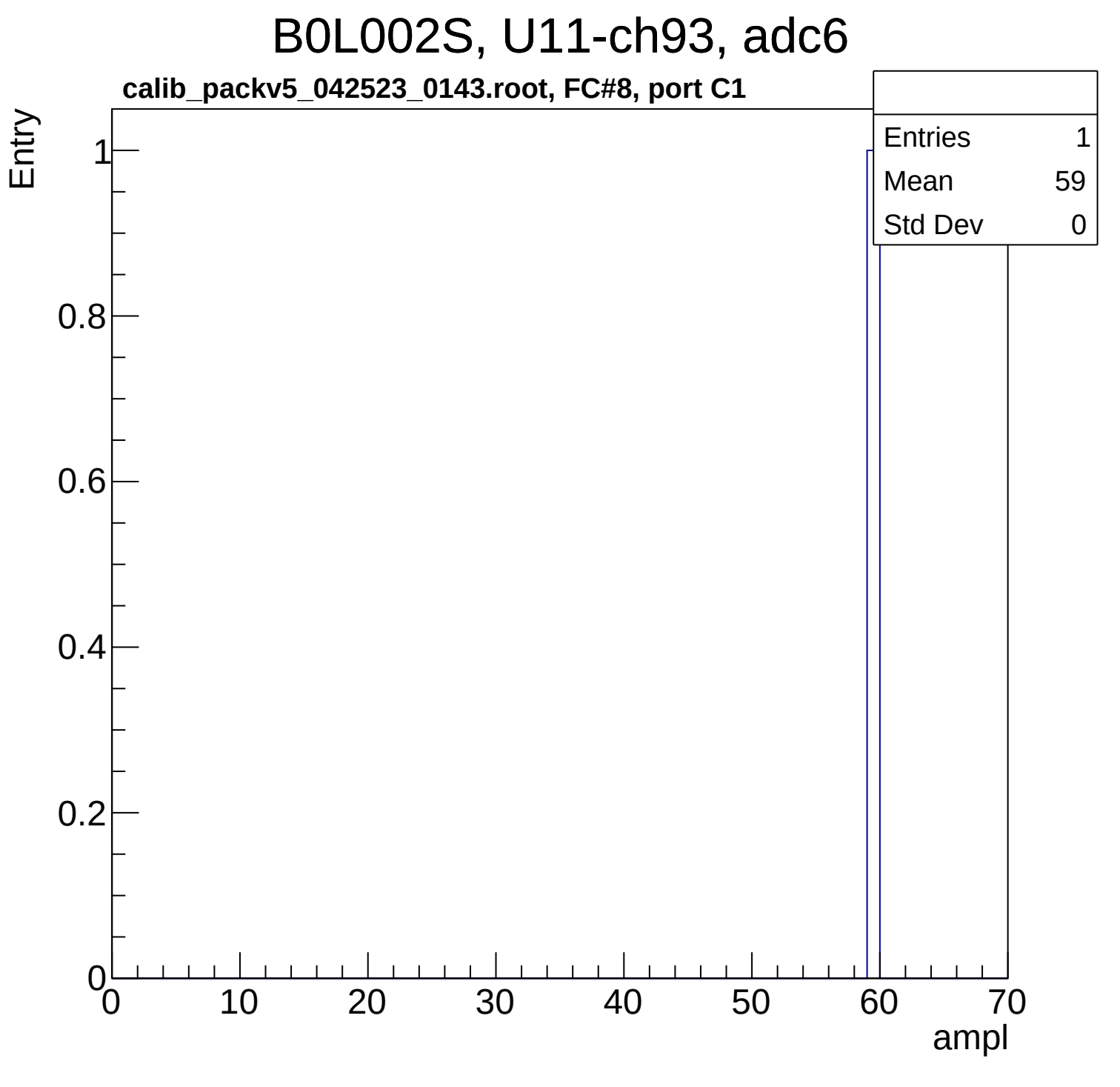
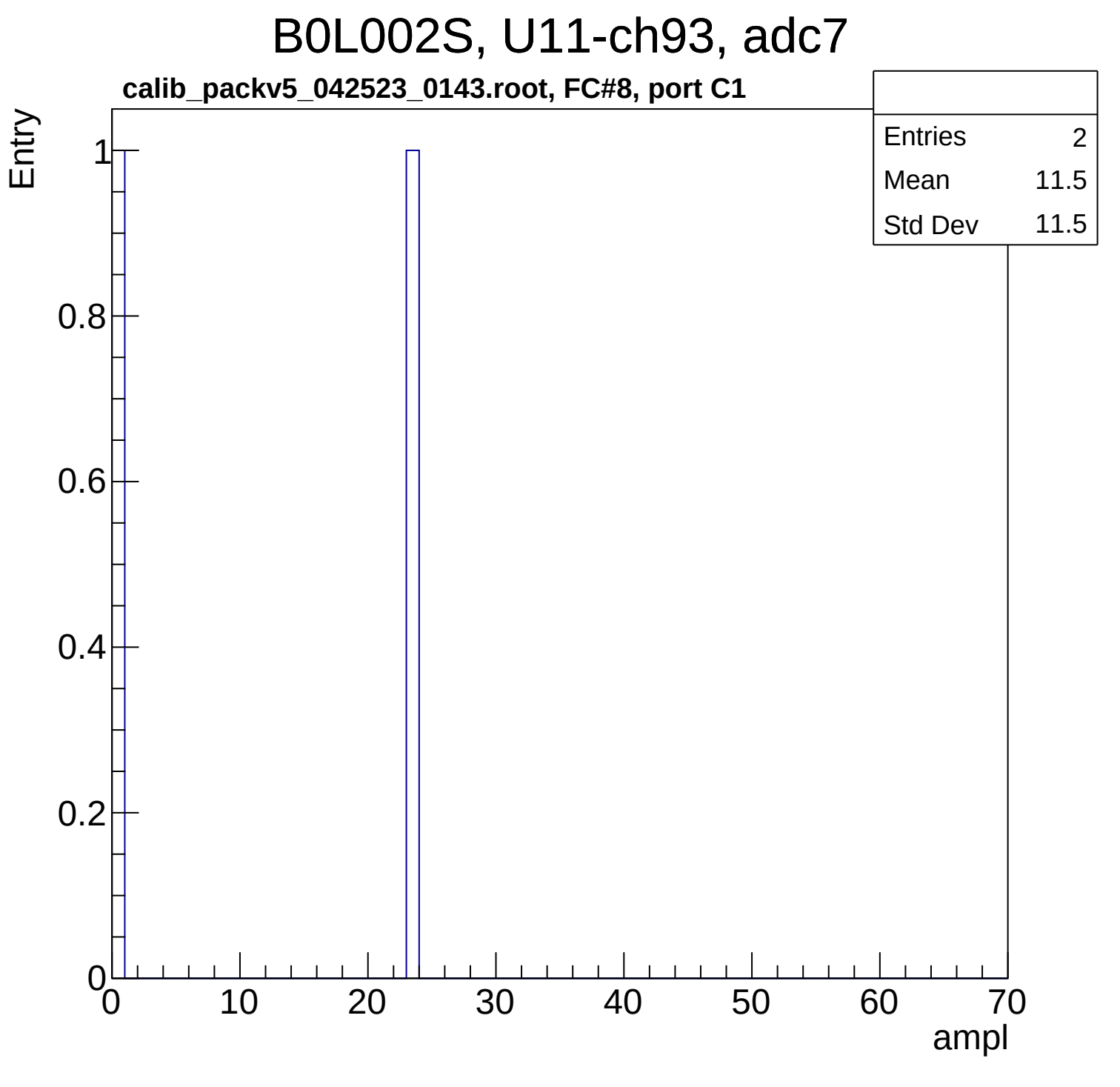
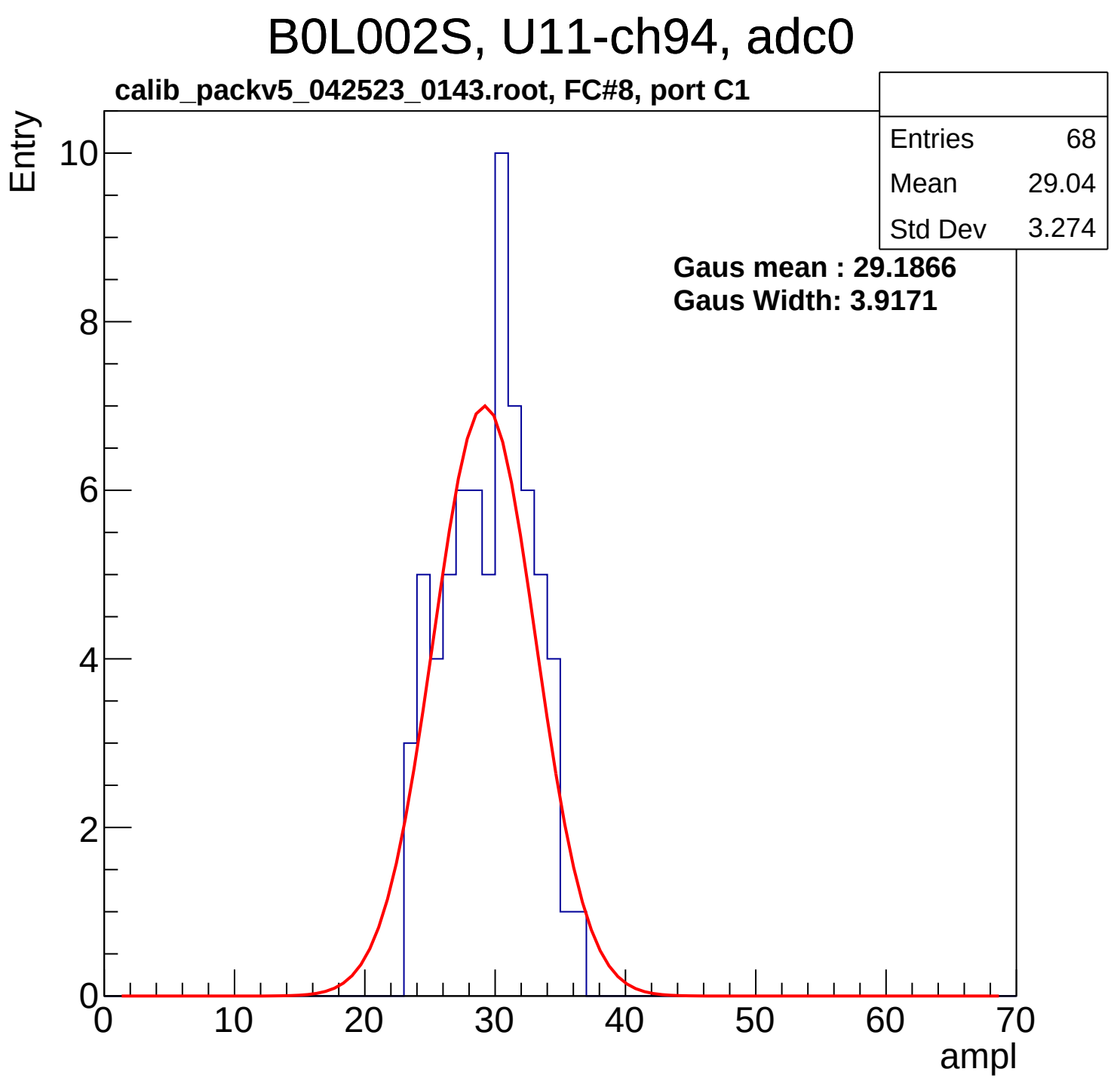
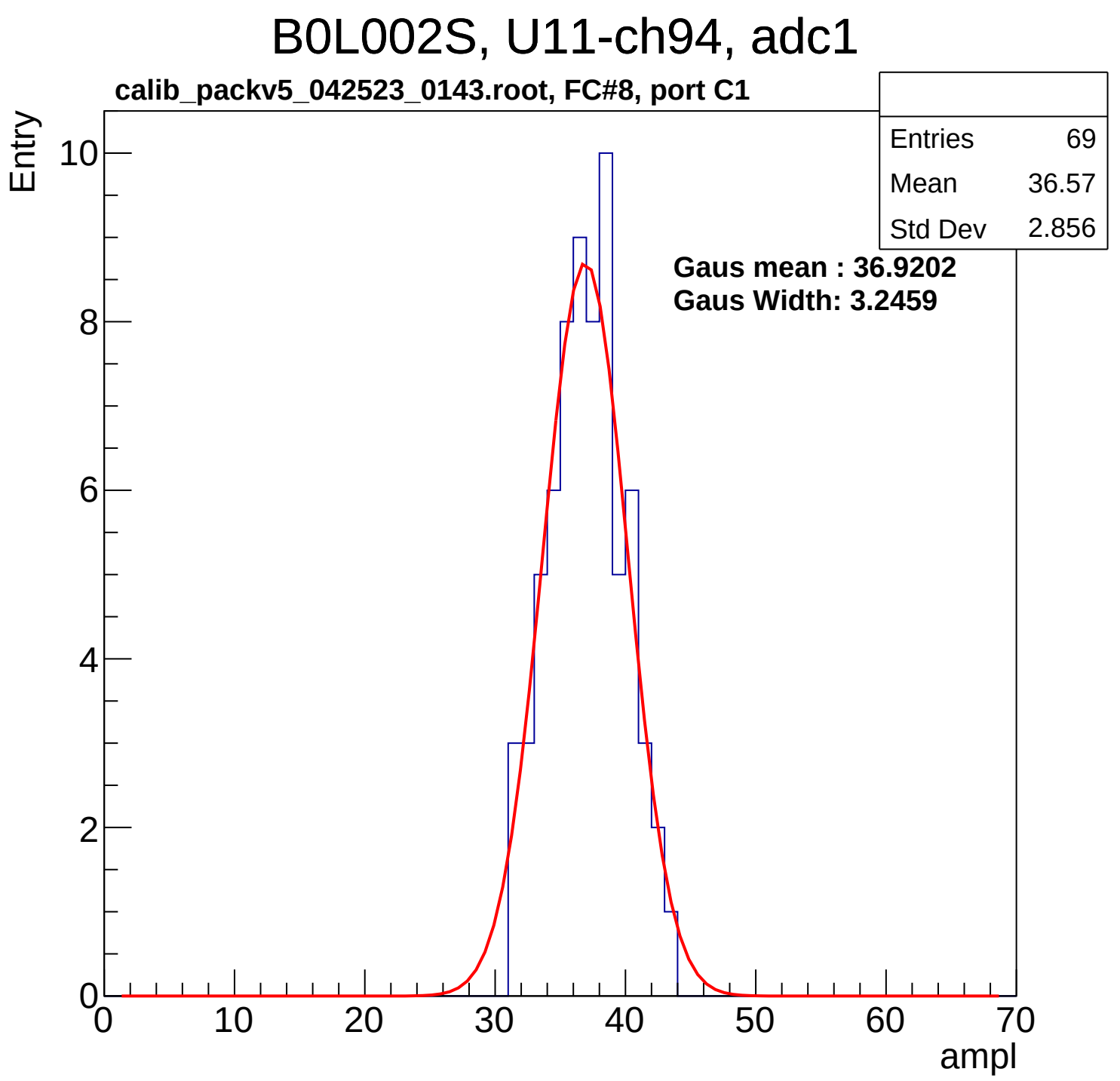
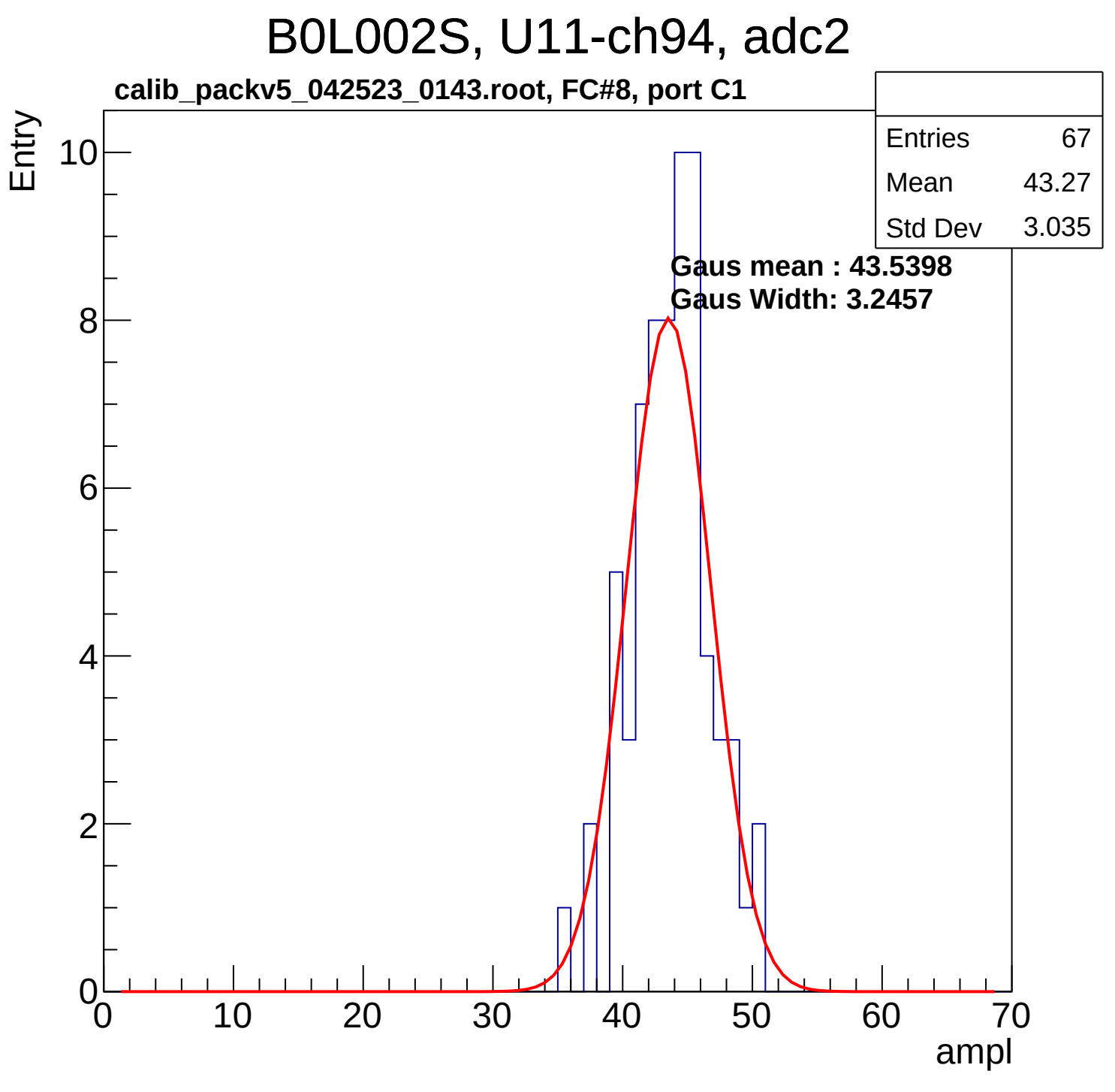
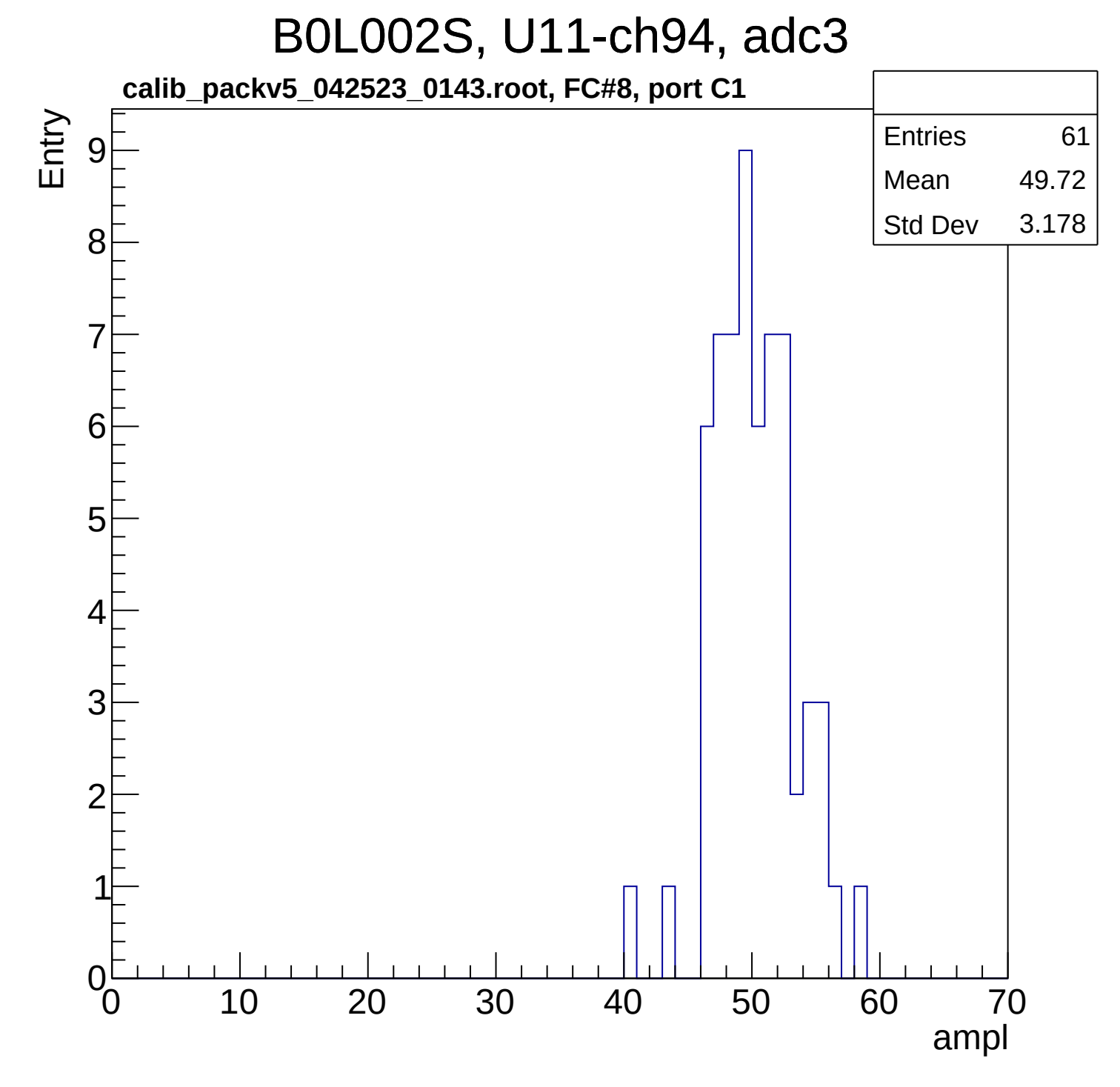

B0L002S, U11-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

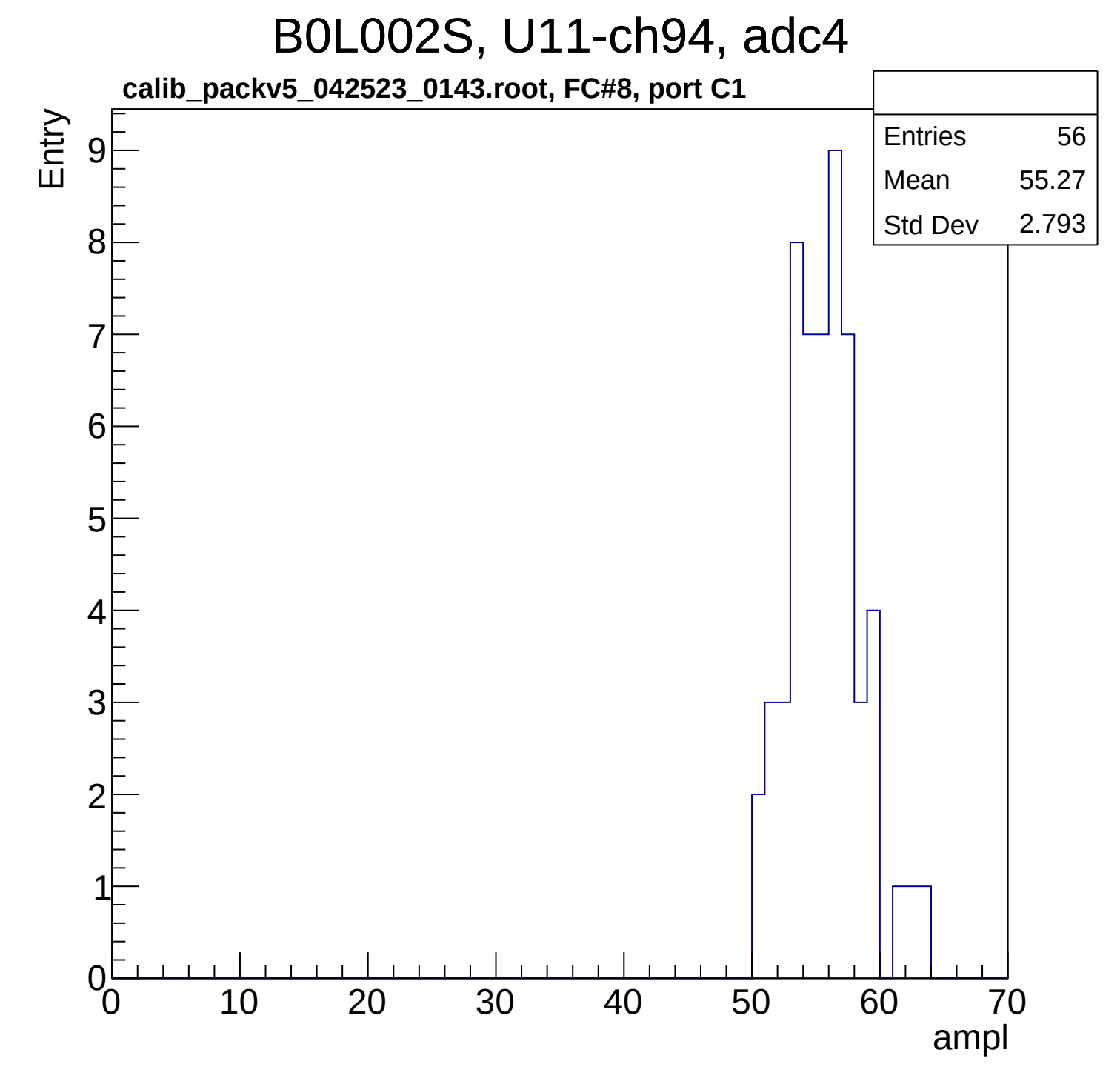

B0L002S, U11-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

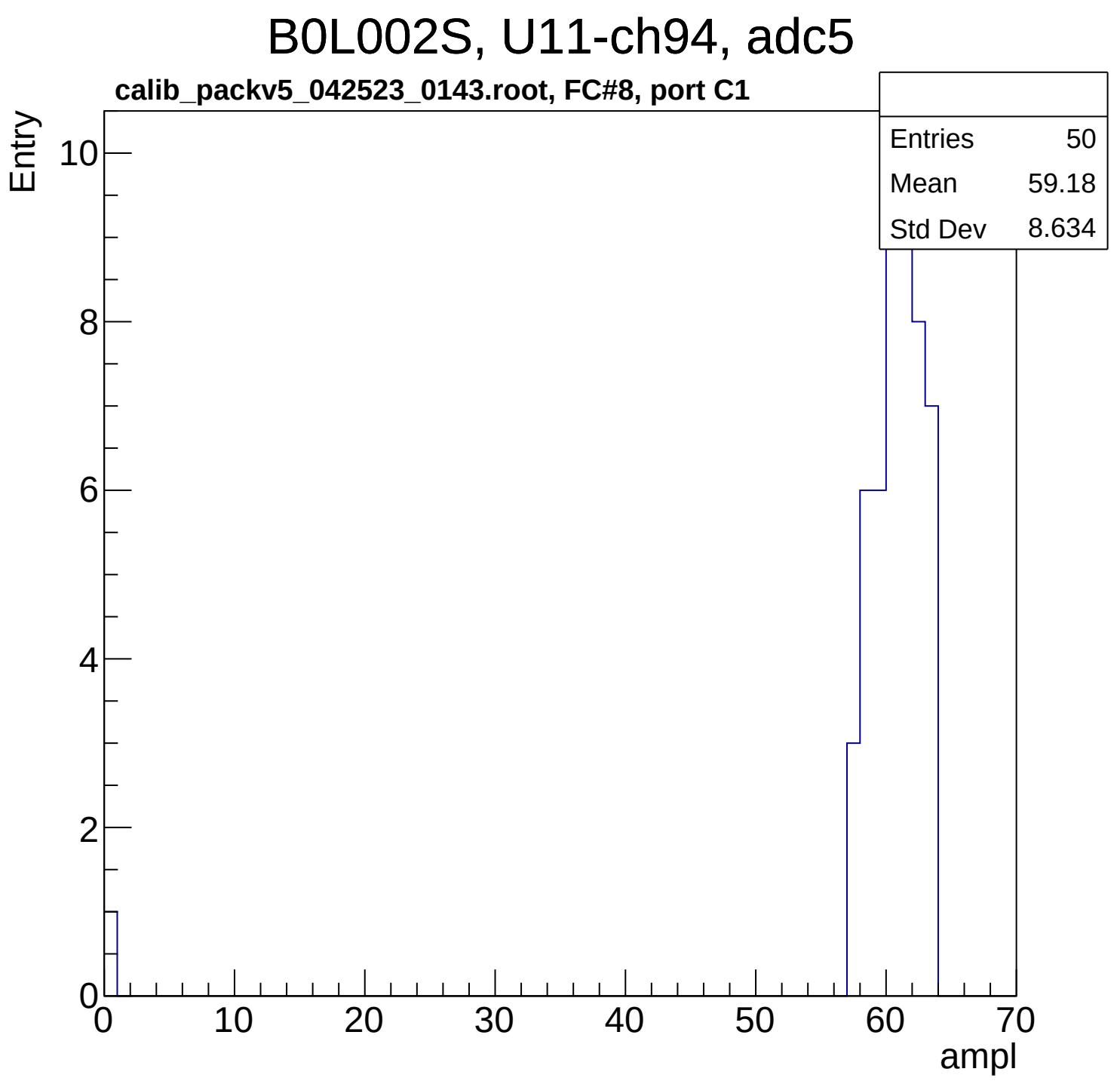

B0L002S, U11-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.04
Std Dev 3.274
Gaus mean : 29.1866
Gaus Width: 3.9171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

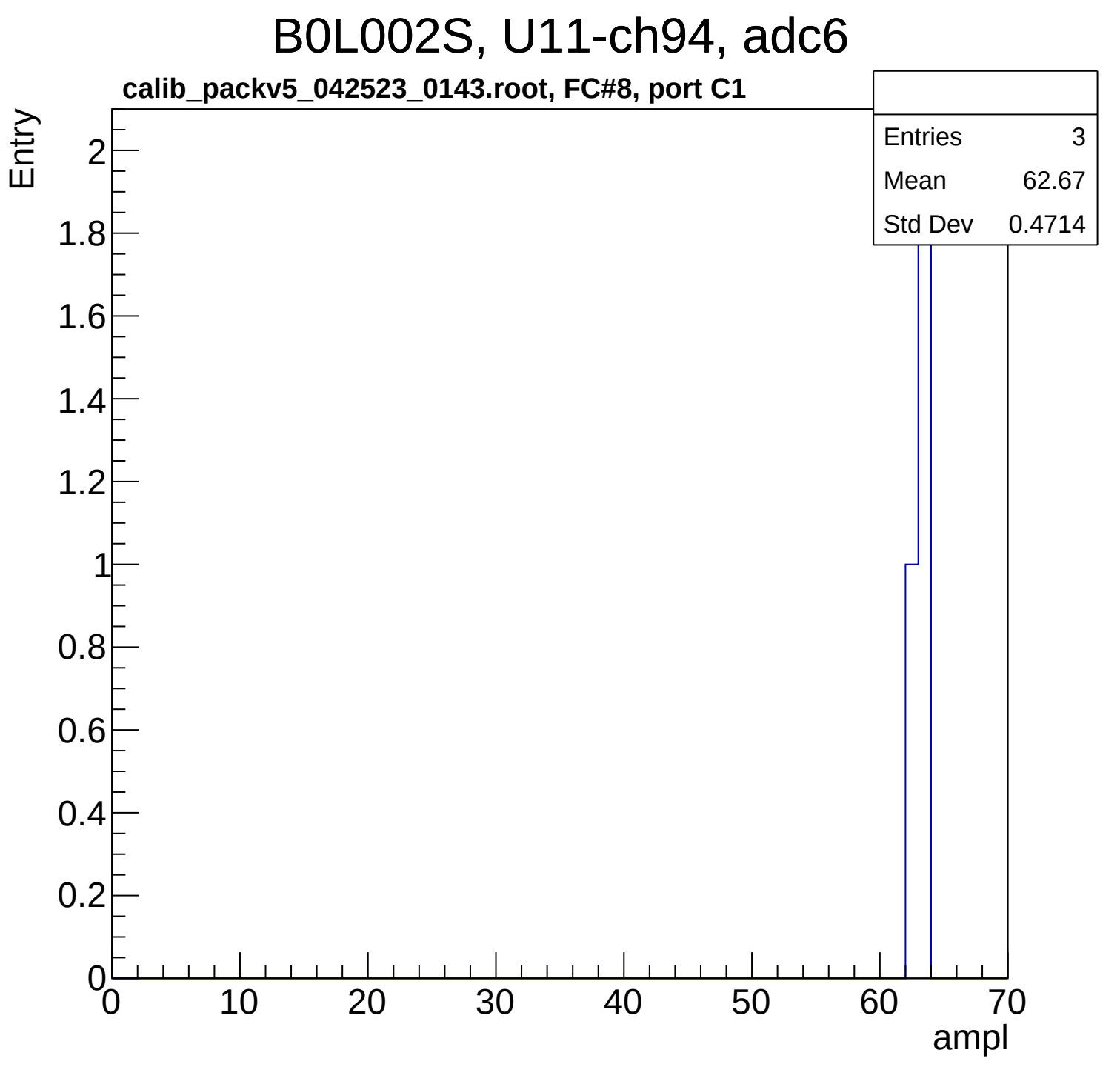

B0L002S, U11-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.57
Std Dev 2.856
Gaus mean : 36.9202
Gaus Width: 3.2459
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

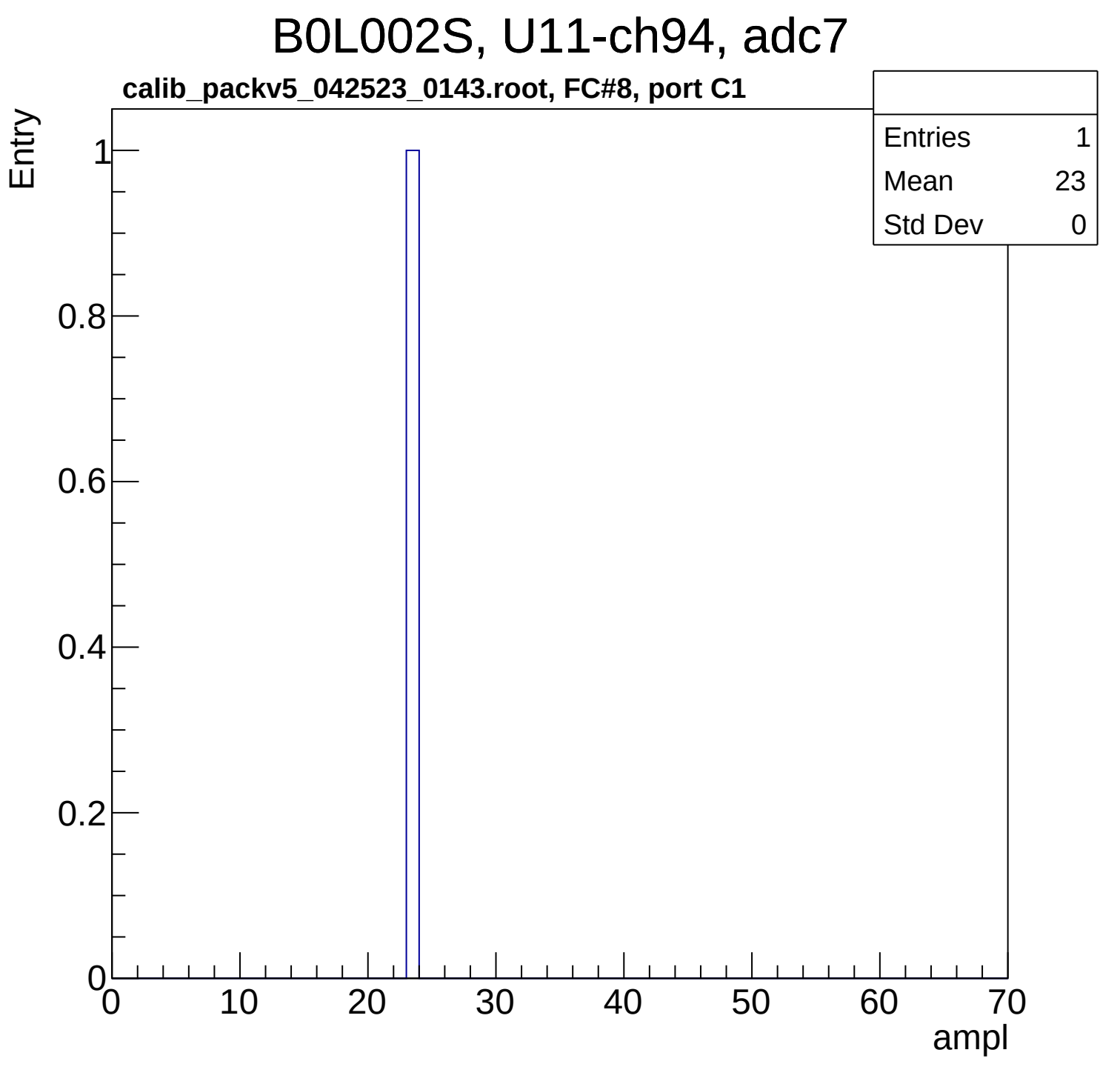

B0L002S, U11-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.27
Std Dev 3.035
Gaus mean : 43.5398
Gaus Width: 3.2457
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

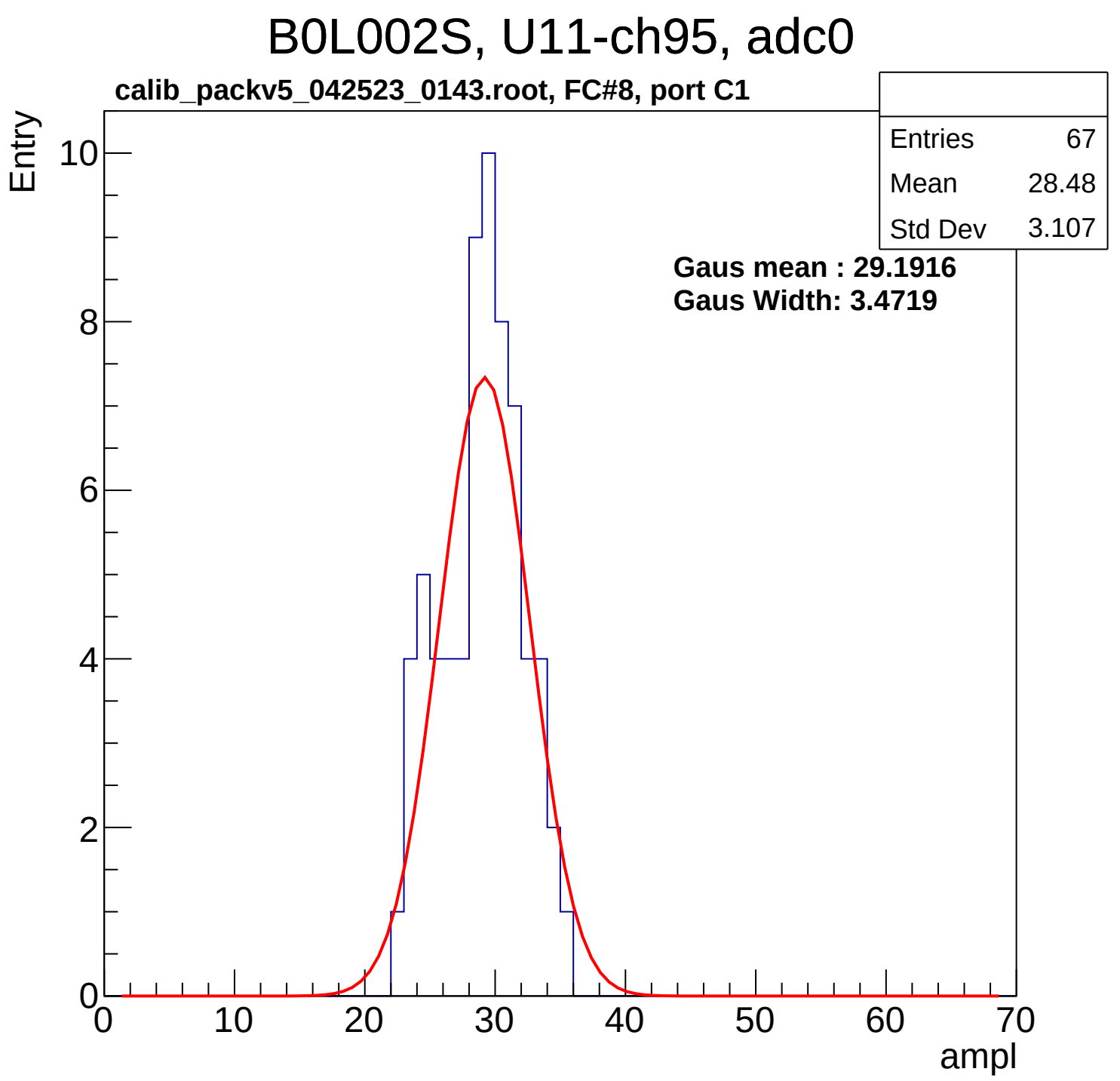

B0L002S, U11-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 49.72
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

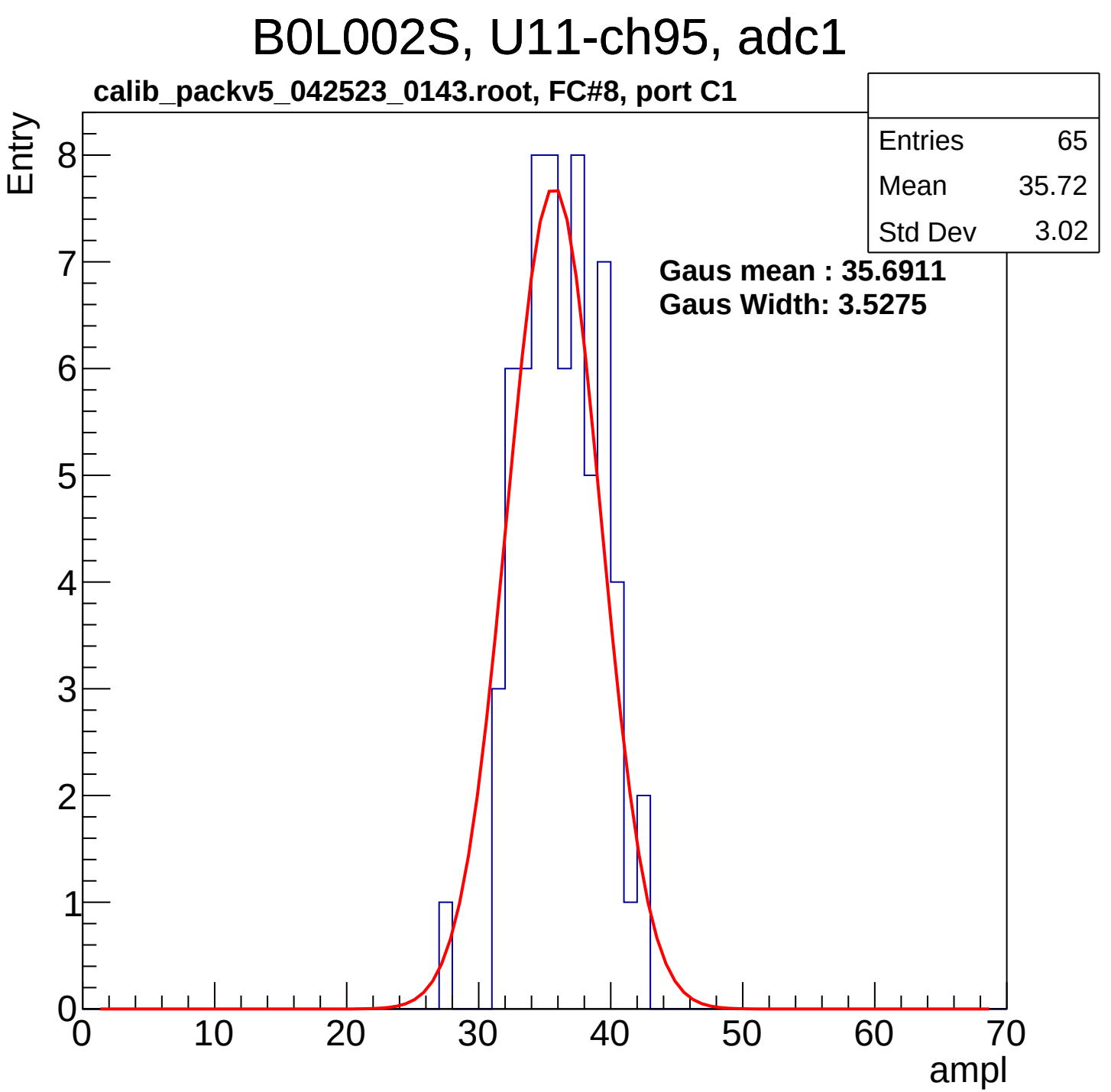

B0L002S, U11-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.27
Std Dev 2.793
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

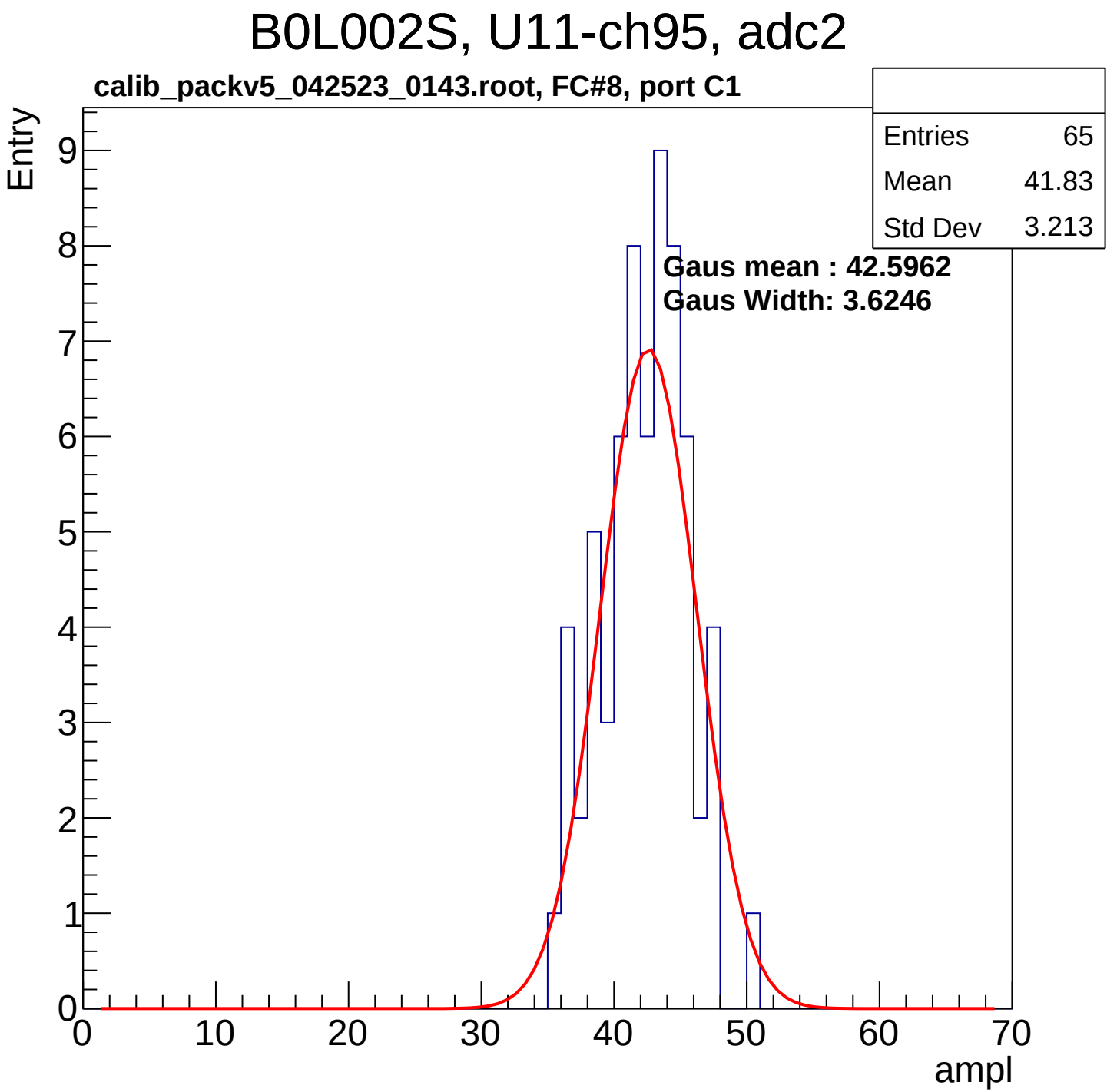

B0L002S, U11-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 59.18
Std Dev 8.634
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 28.48
Std Dev 3.107
Gaus mean : 29.1916
Gaus Width: 3.4719
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 35.72
Std Dev 3.02
Gaus mean : 35.6911
Gaus Width: 3.5275
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 41.83
Std Dev 3.213
Gaus mean : 42.5962
Gaus Width: 3.6246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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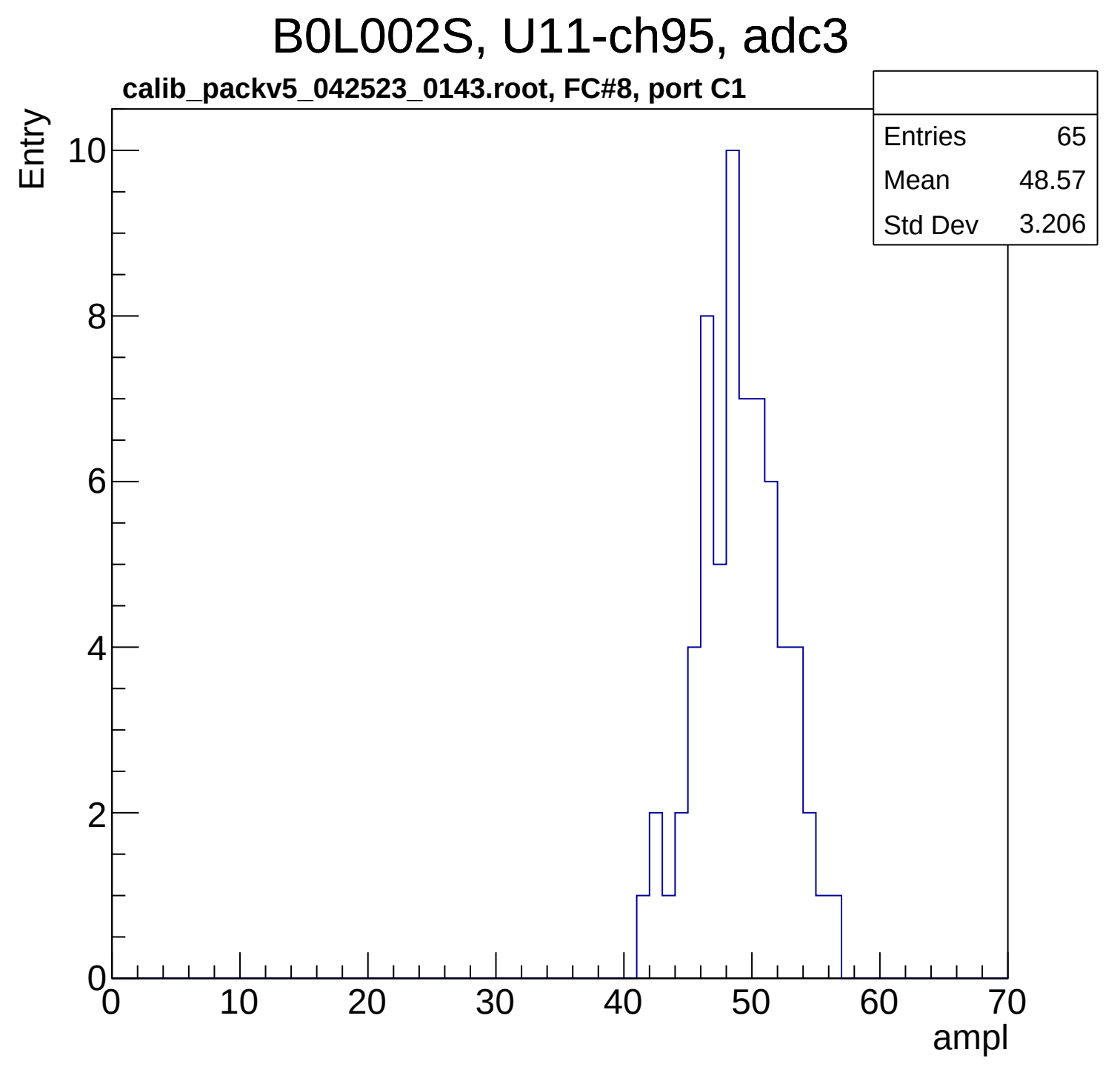
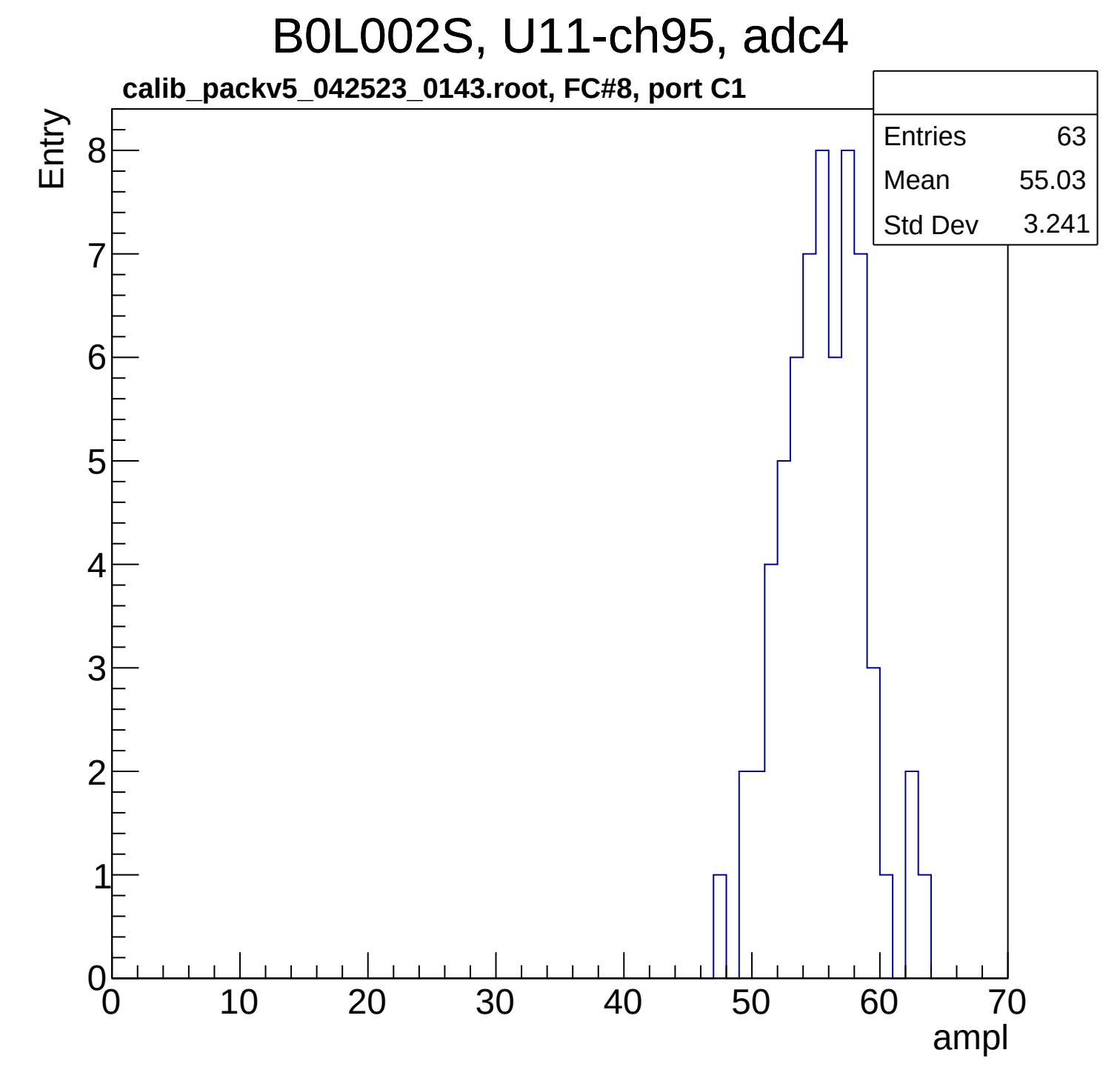

B0L002S, U11-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 48.57
Std Dev 3.206
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.03
Std Dev 3.241
Entry
ampl
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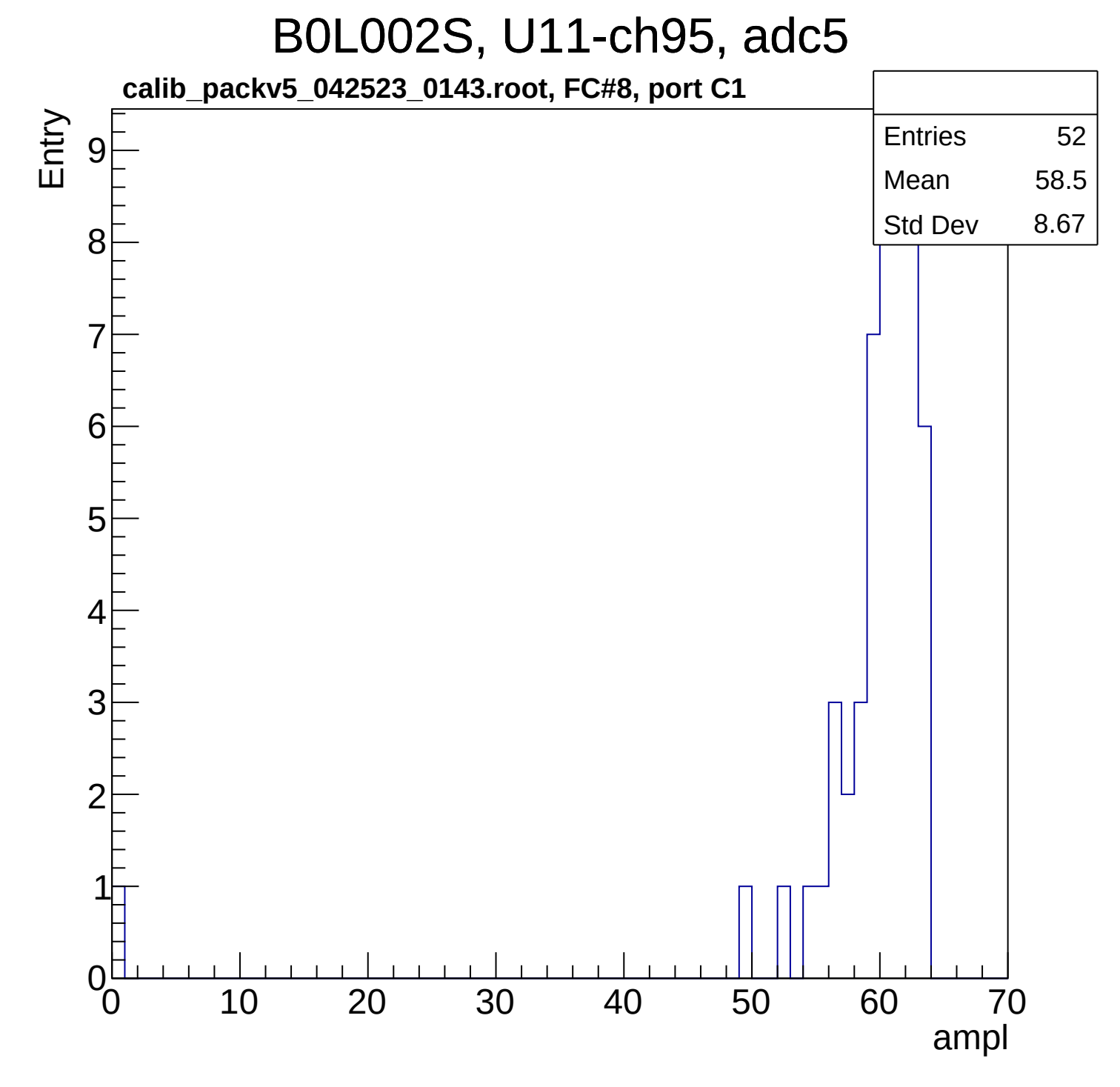
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.5
Std Dev 8.67
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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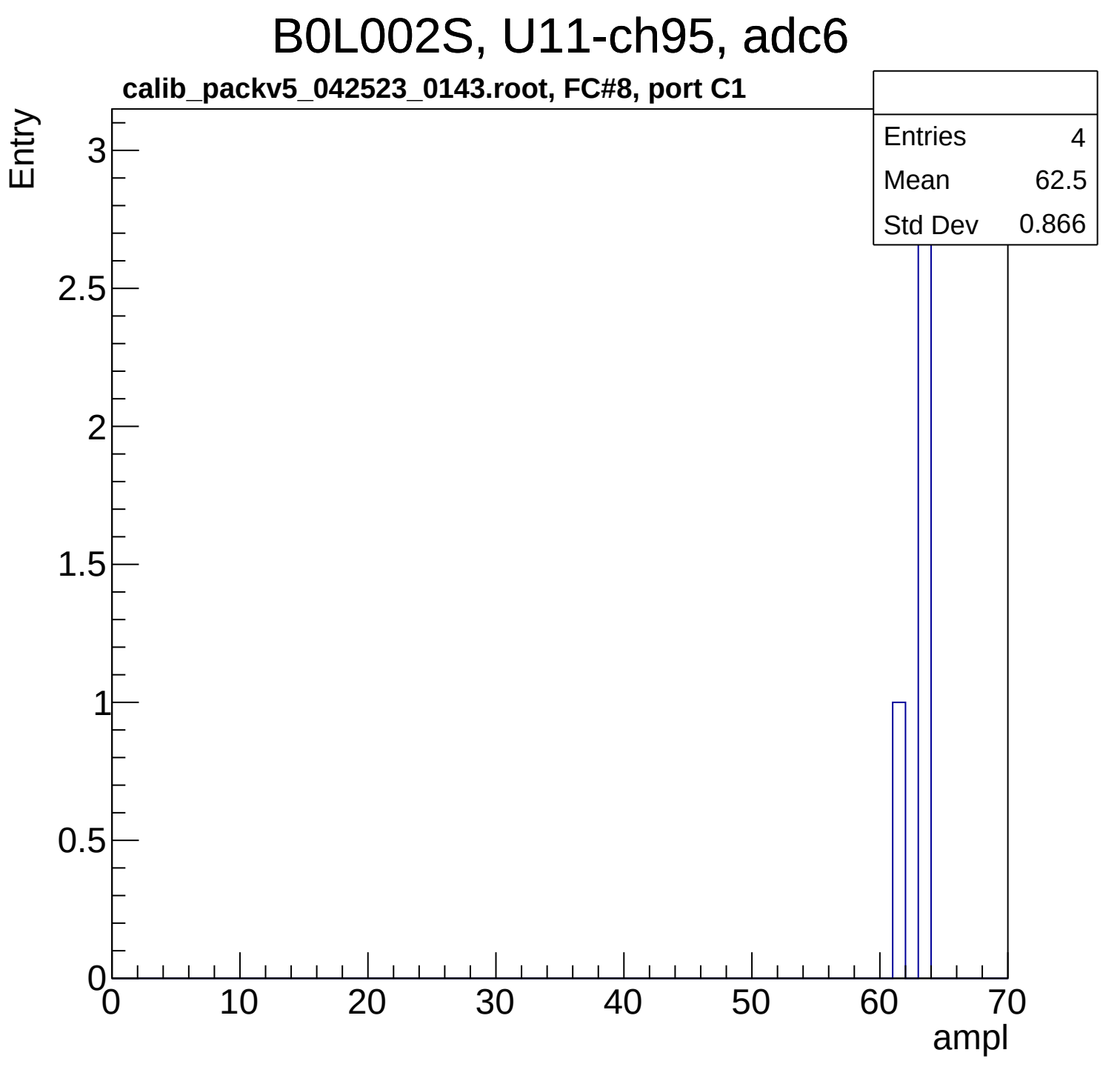
B0L002S, U11-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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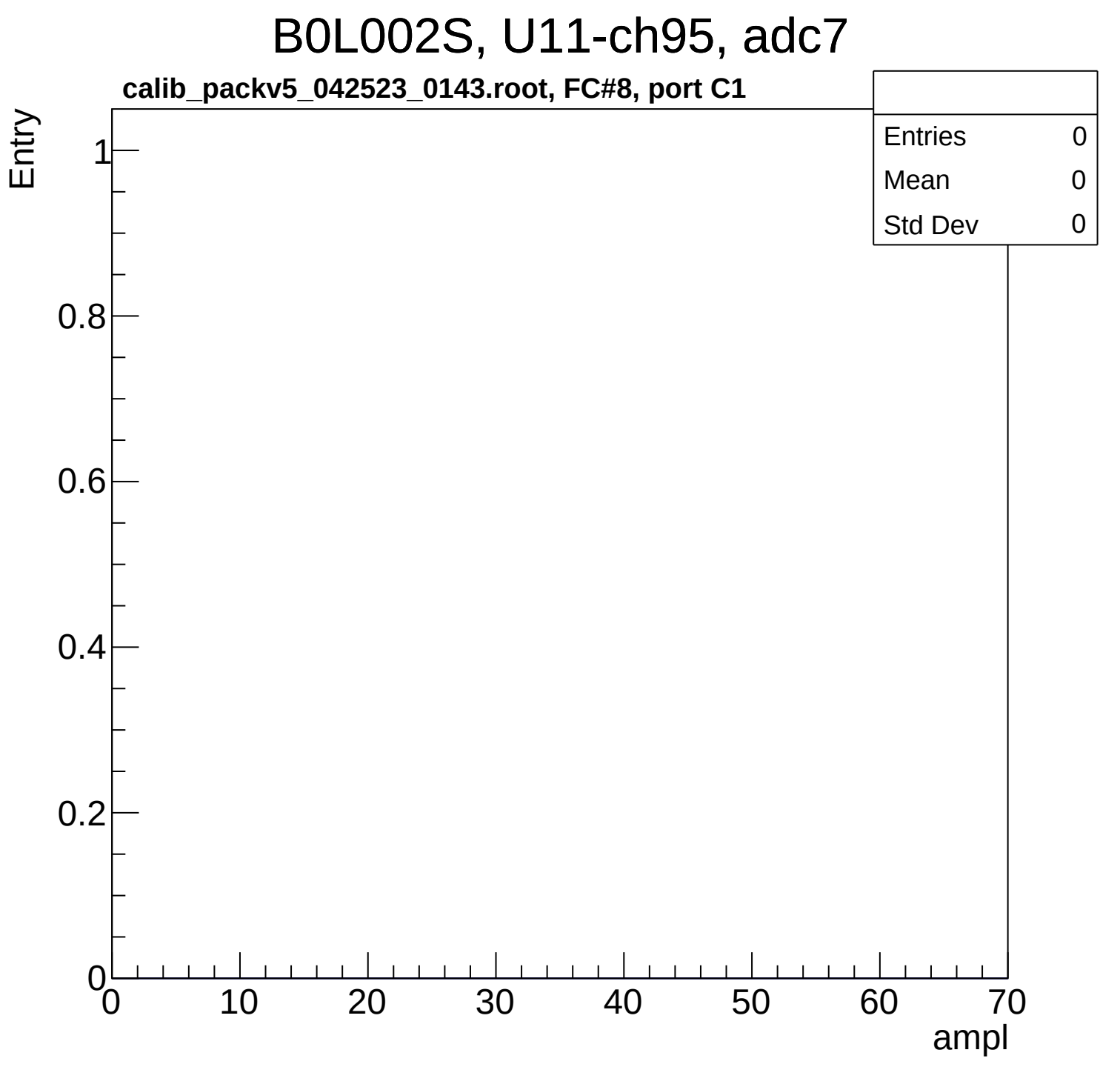
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U11-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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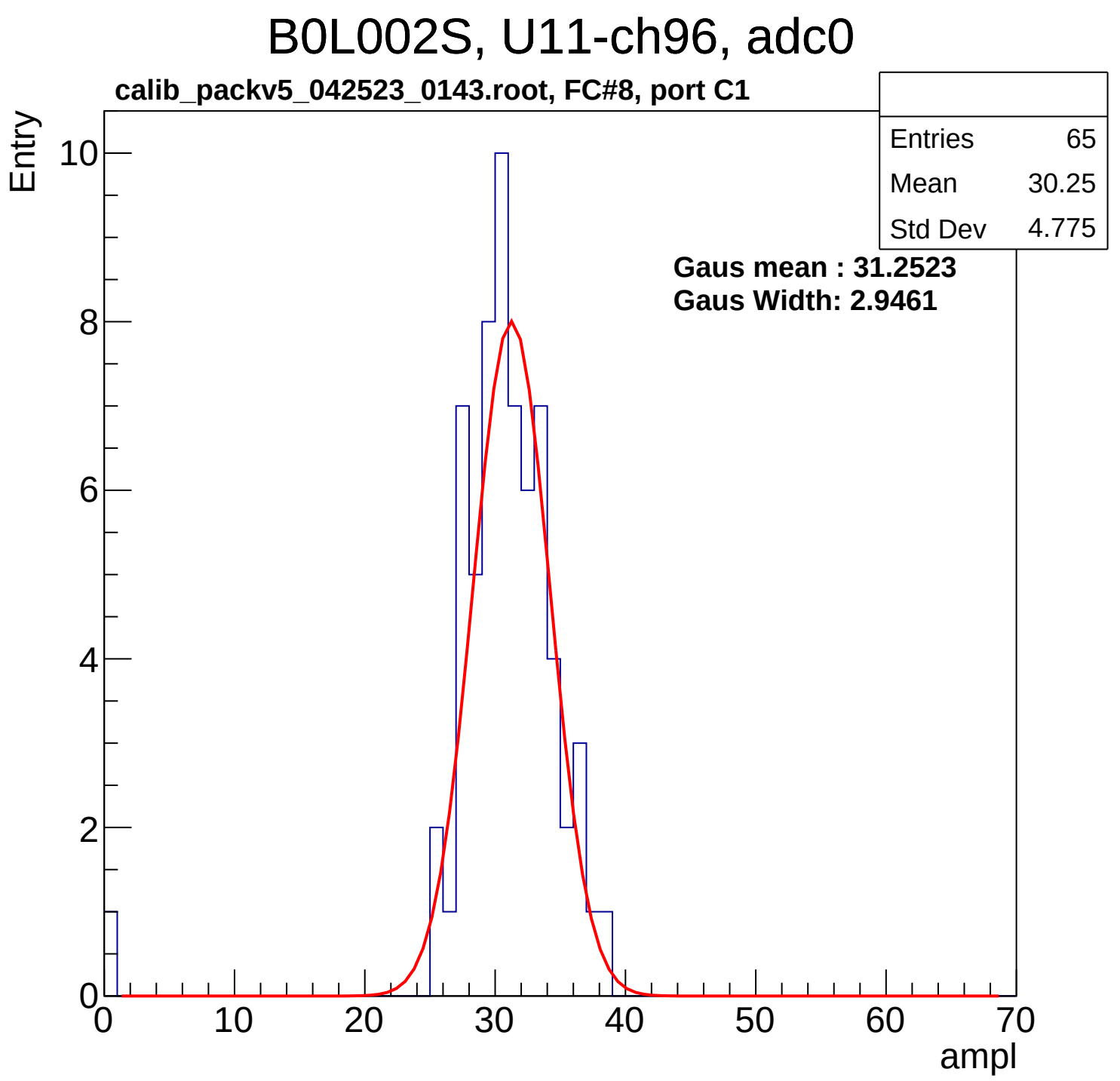
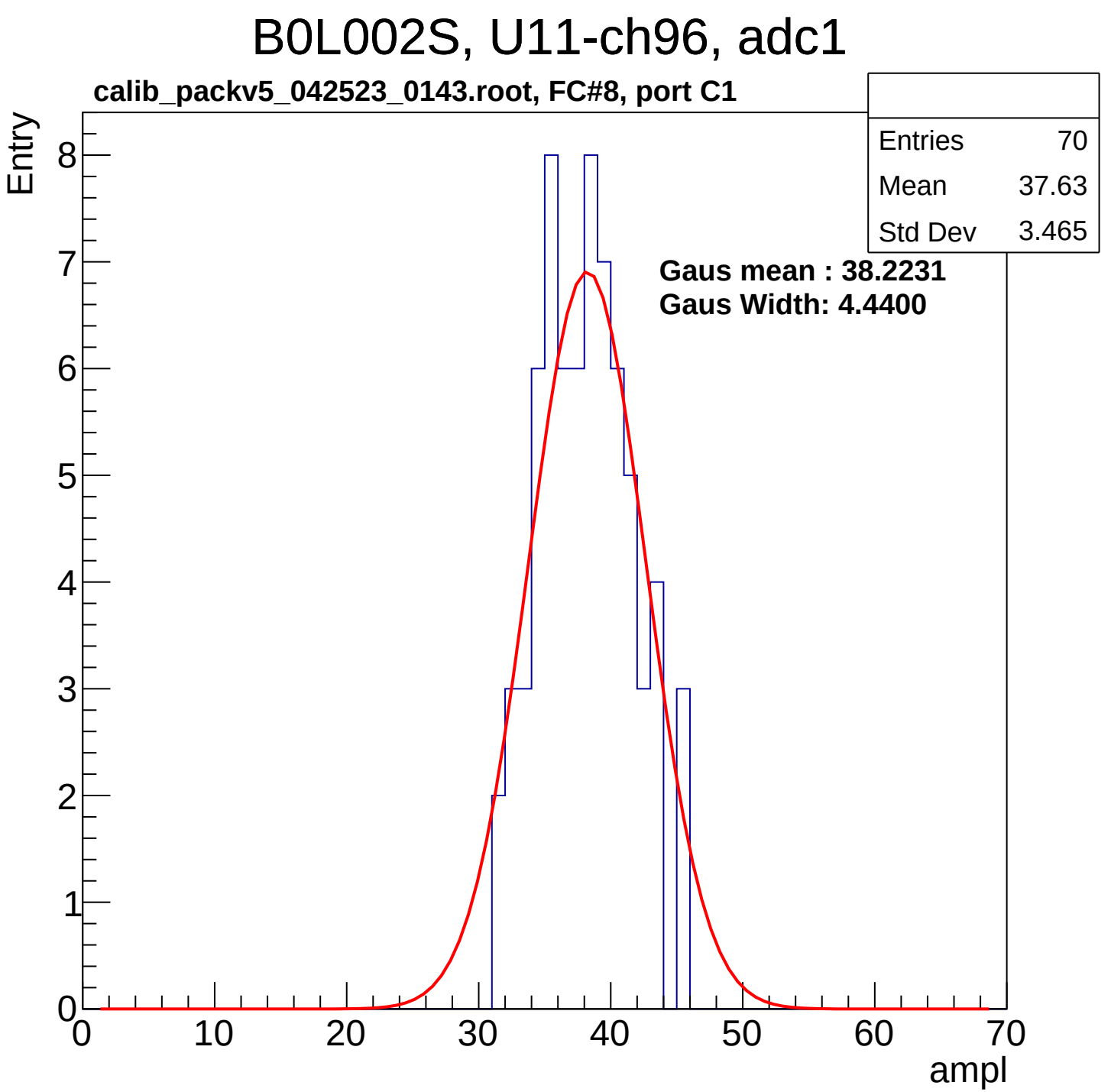
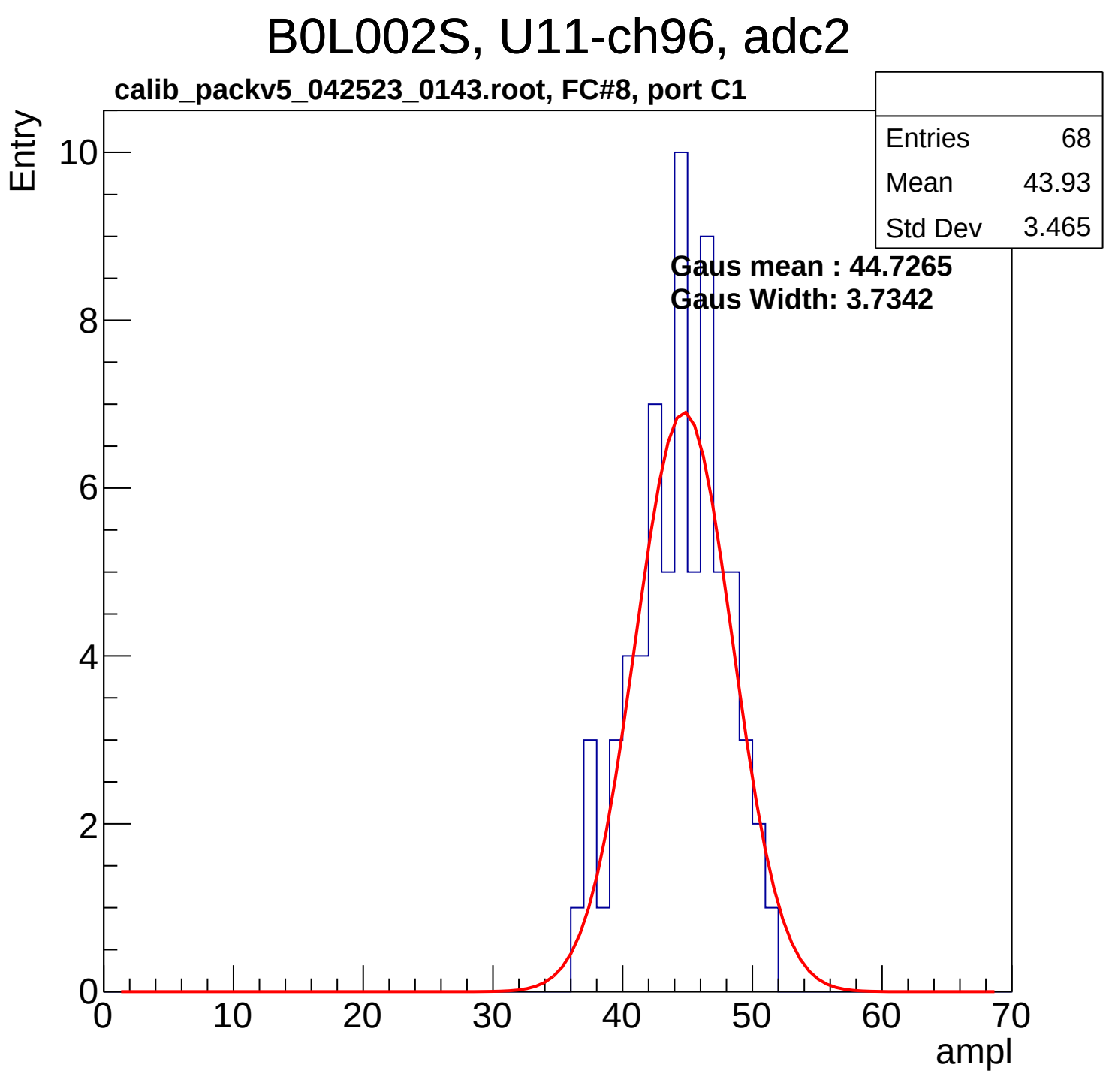
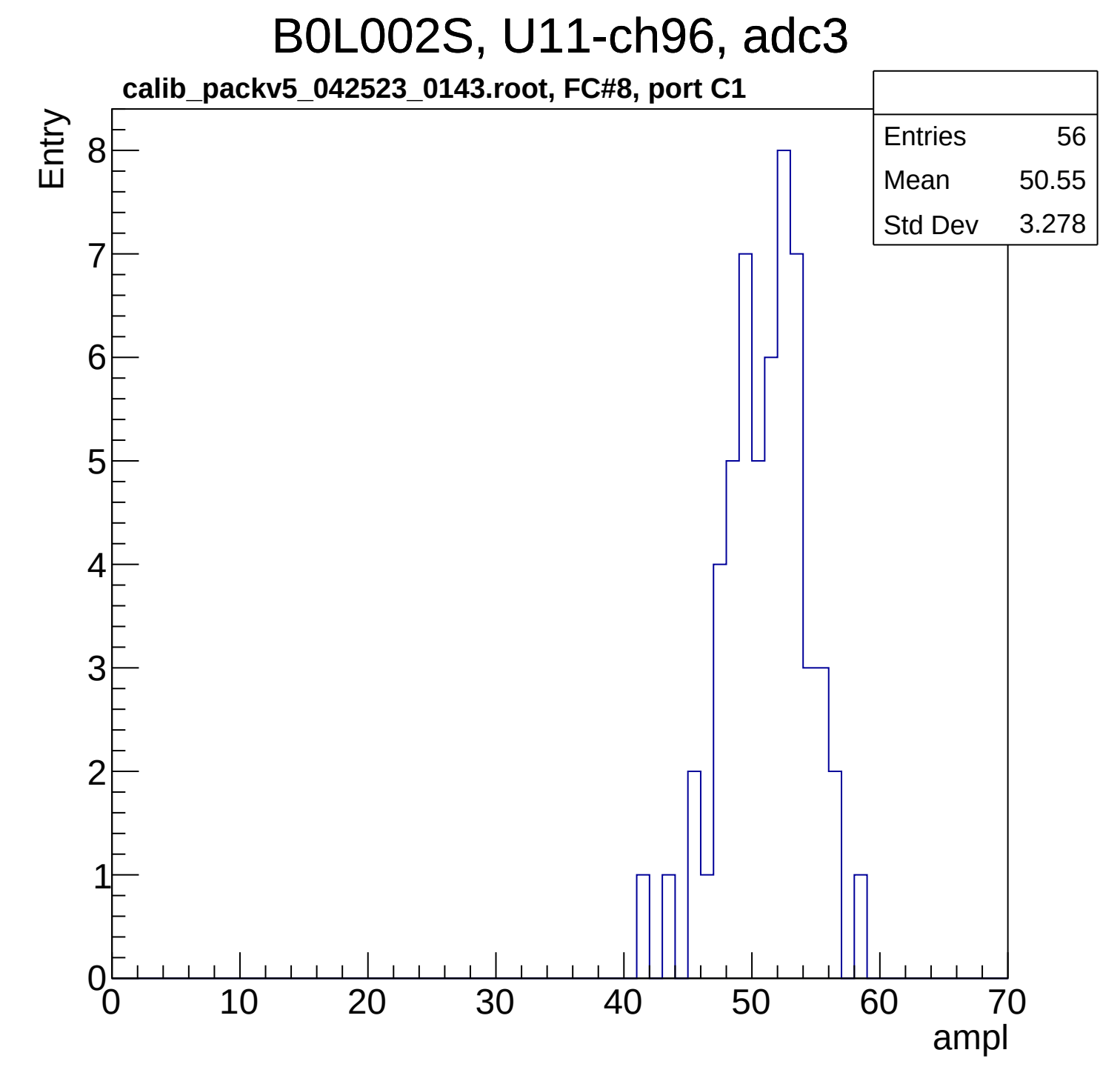
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.25
Std Dev 4.775
Gaus mean : 31.2523
Gaus Width: 2.9461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.63
Std Dev 3.465
Gaus mean : 38.2231
Gaus Width: 4.4400
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.93
Std Dev 3.465
Gaus mean : 44.7265
Gaus Width: 3.7342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 50.55
Std Dev 3.278
Entry
ampl
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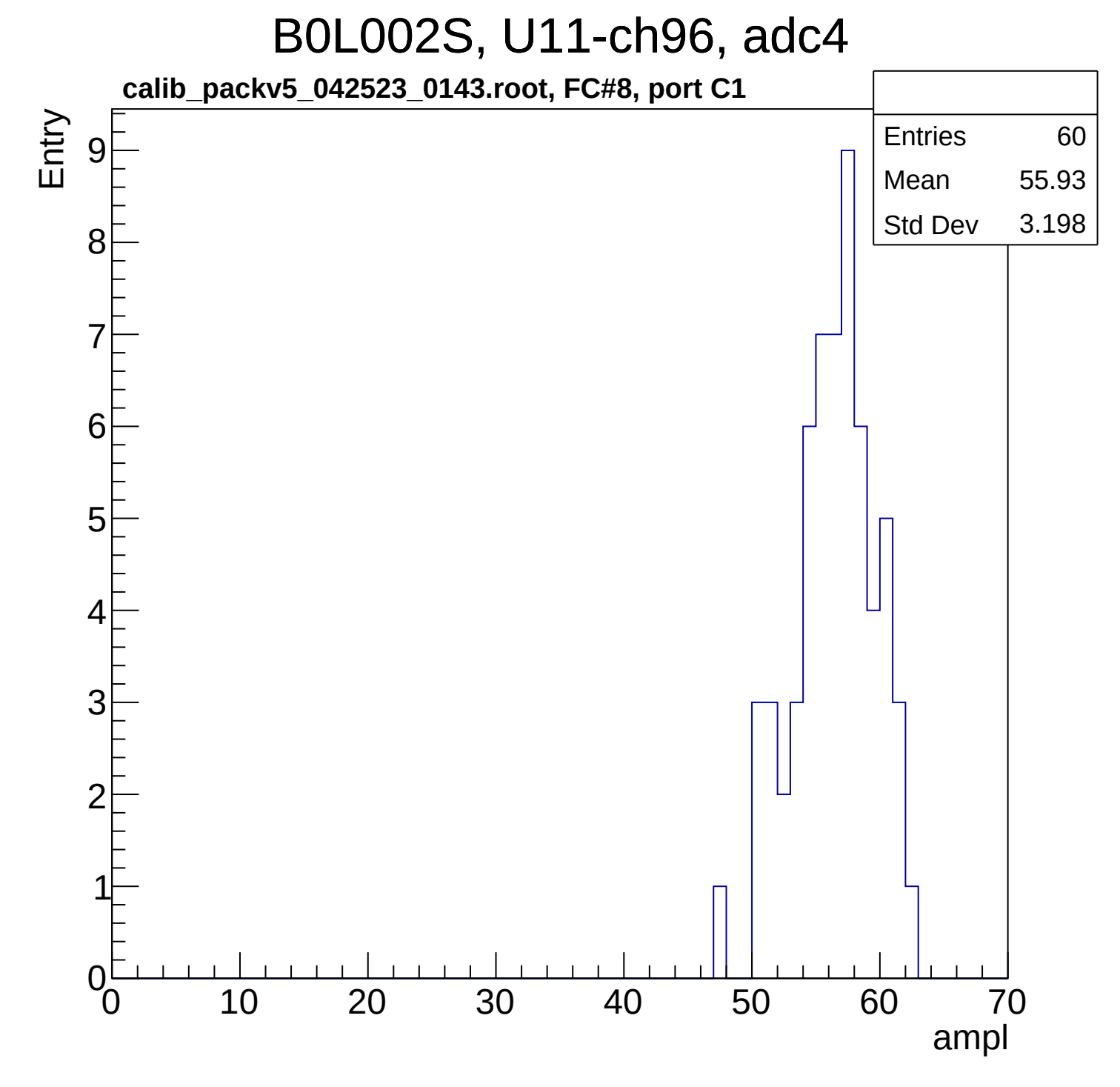
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.93
Std Dev 3.198
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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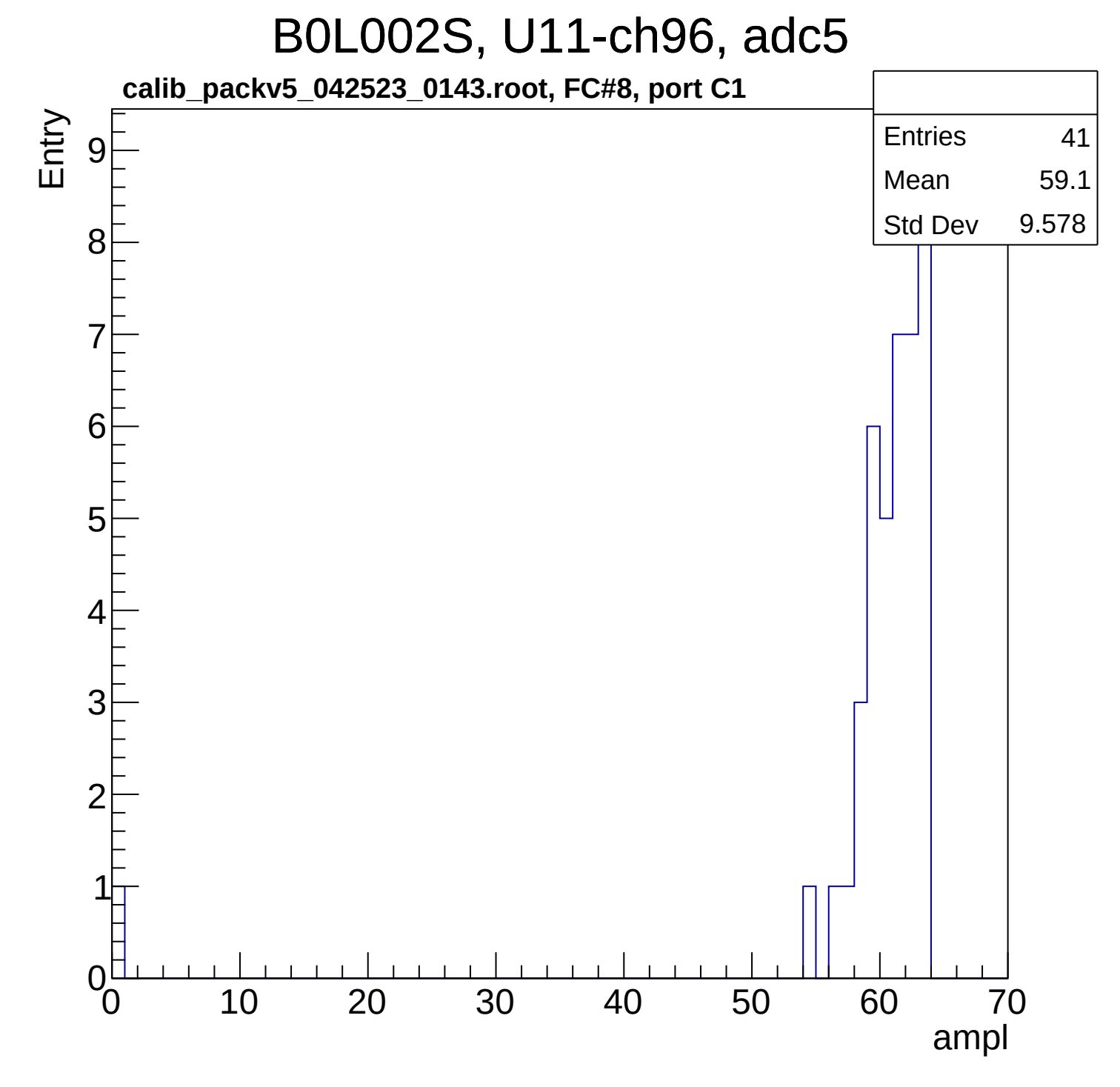
B0L002S, U11-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.1
Std Dev 9.578
Entry
ampl
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2
3
4
5
6
7
8
9
0
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30
40
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60
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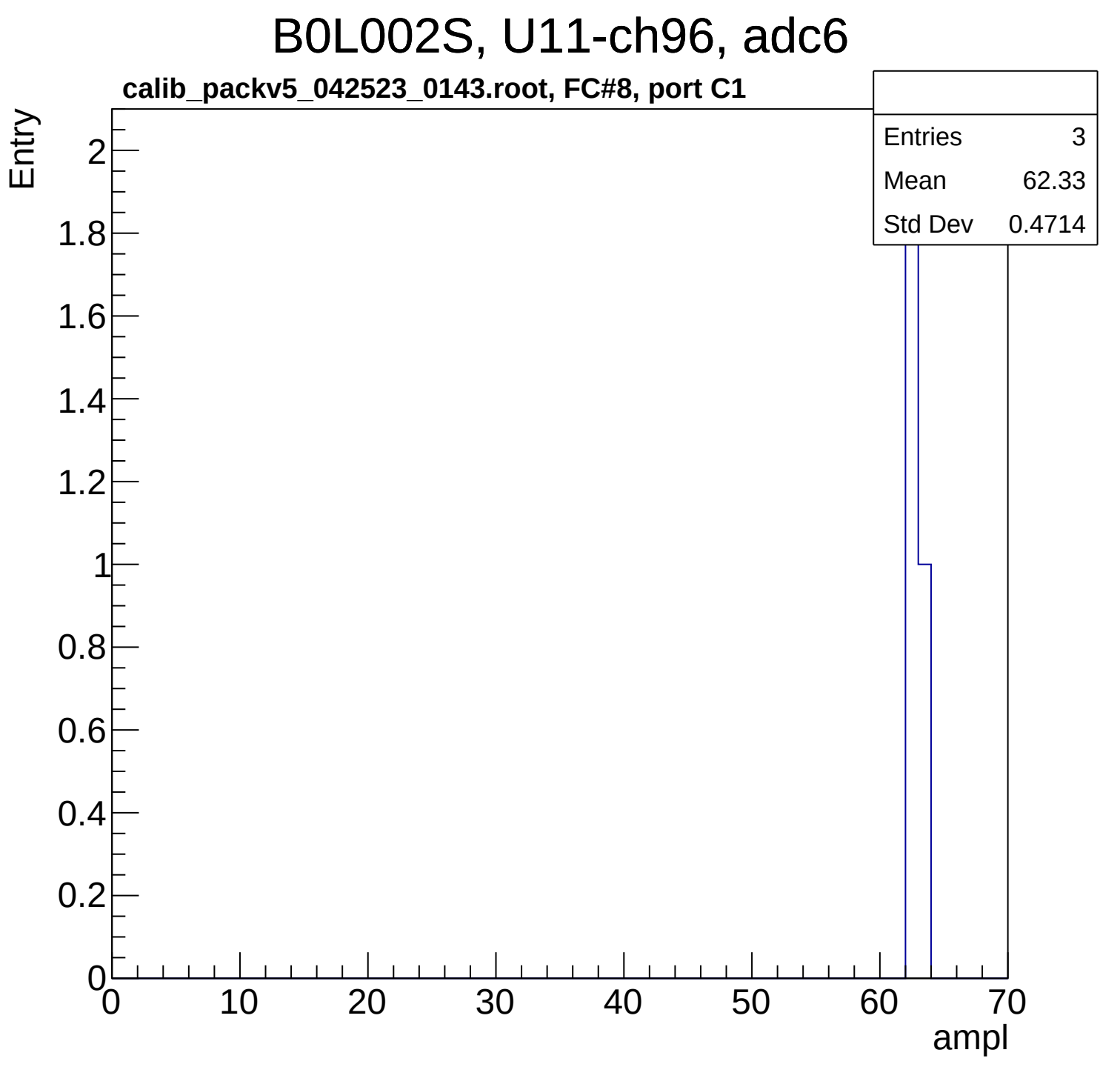
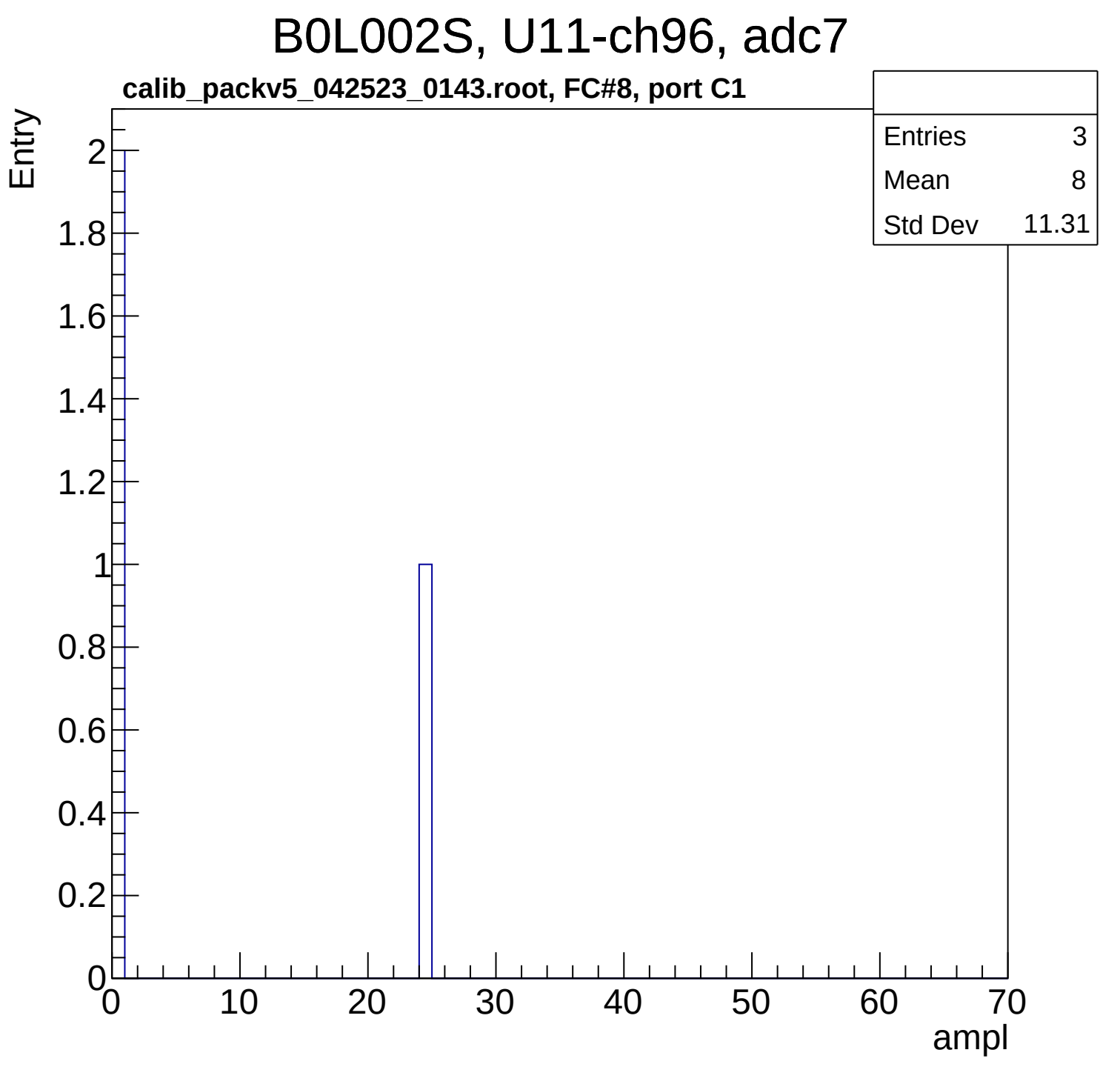

B0L002S, U11-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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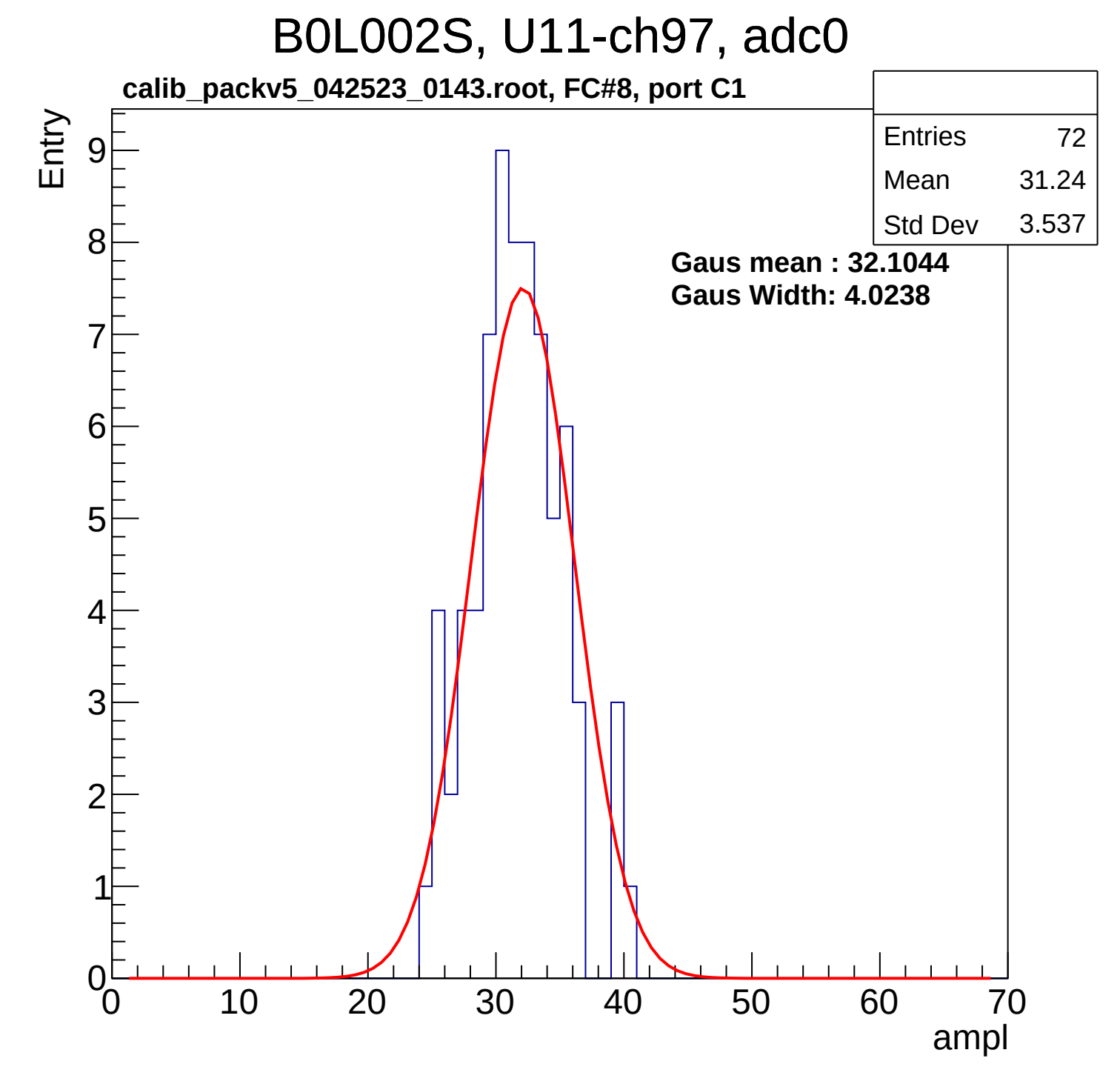
B0L002S, U11-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.24
Std Dev 3.537
Gaus mean : 32.1044
Gaus Width: 4.0238
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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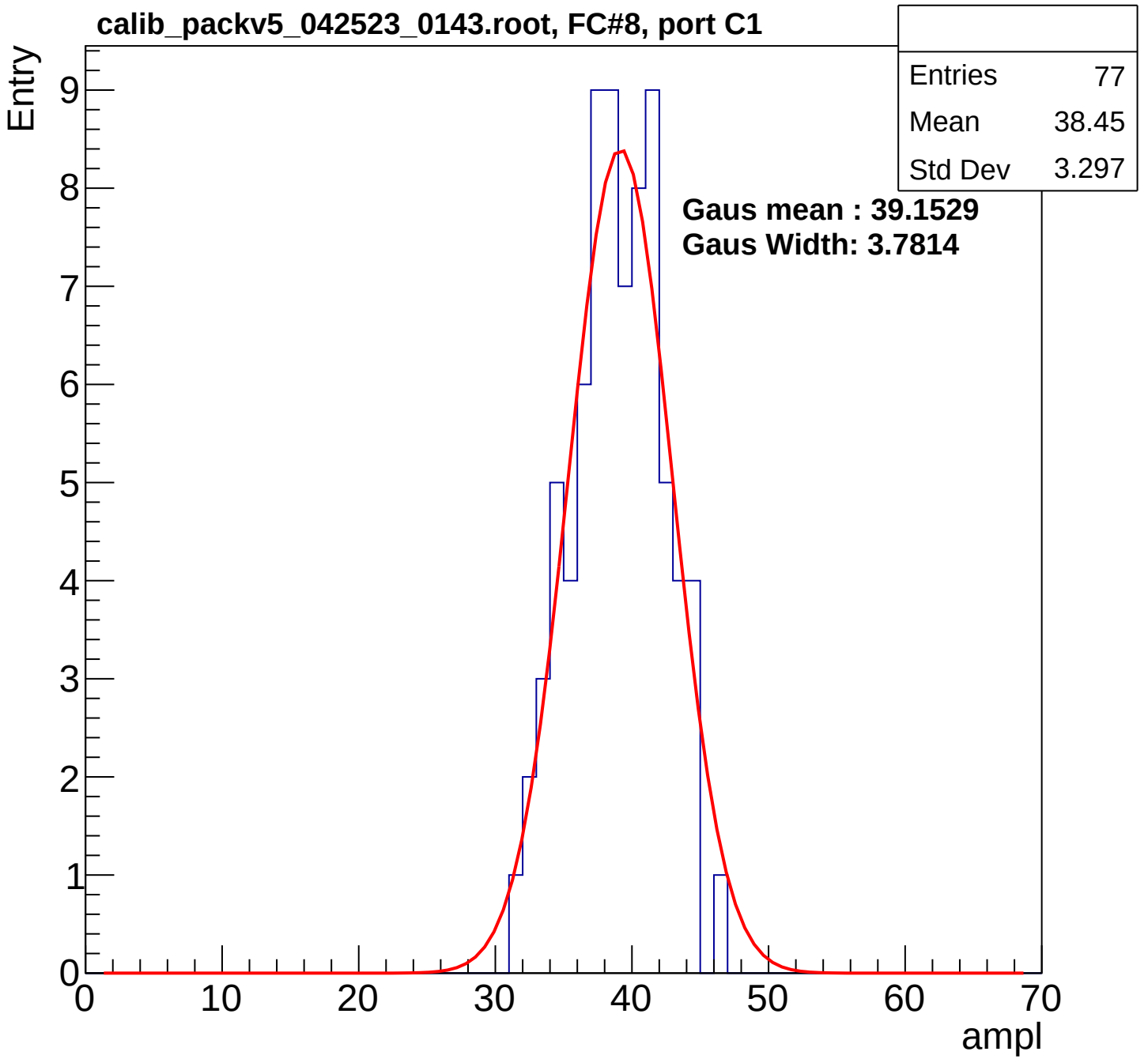
B0L002S, U11-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.45
Std Dev 3.297
Gaus mean : 39.1529
Gaus Width: 3.7814
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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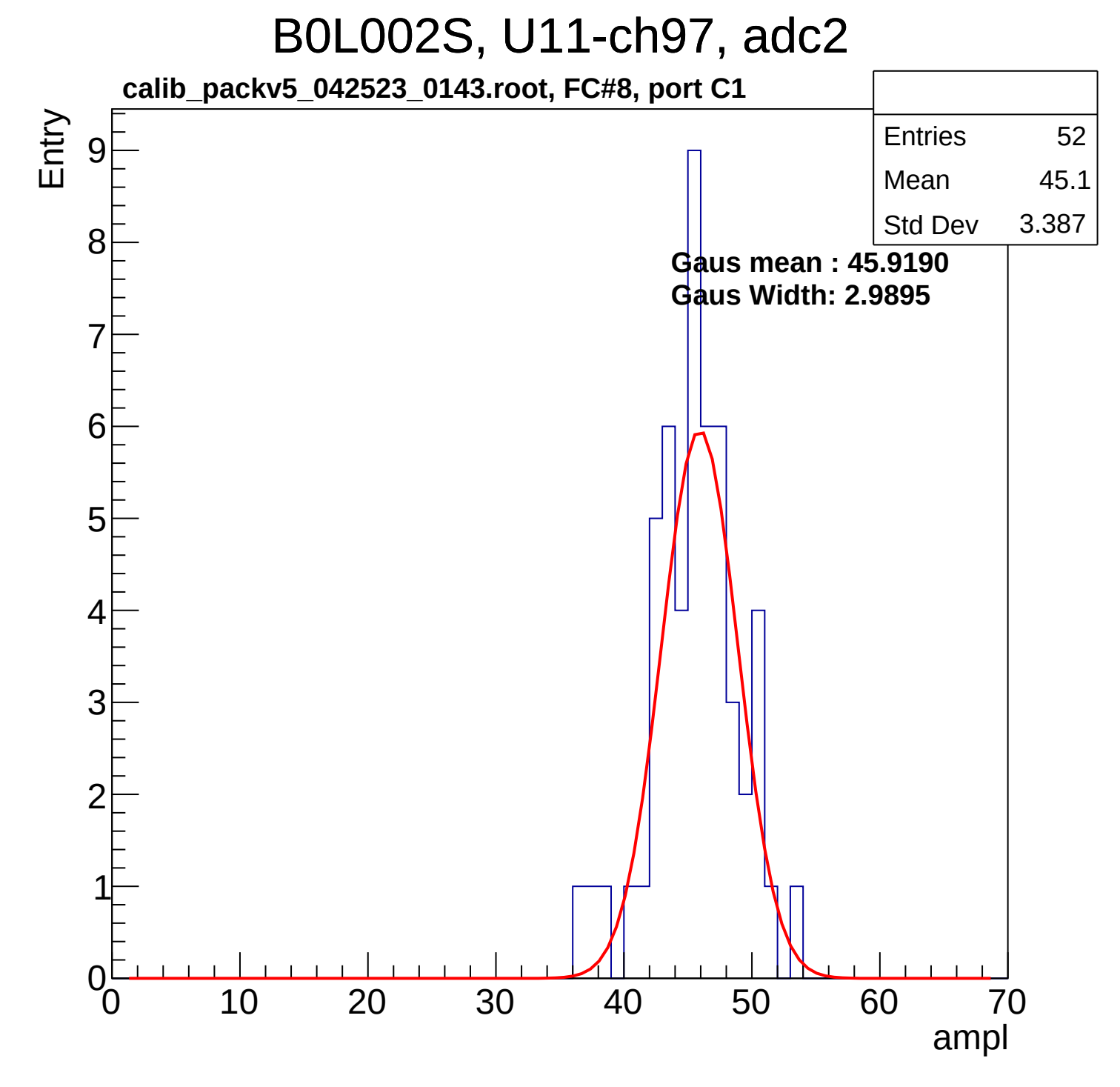
B0L002S, U11-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 45.1
Std Dev 3.387
Gaus mean : 45.9190
Gaus Width: 2.9895
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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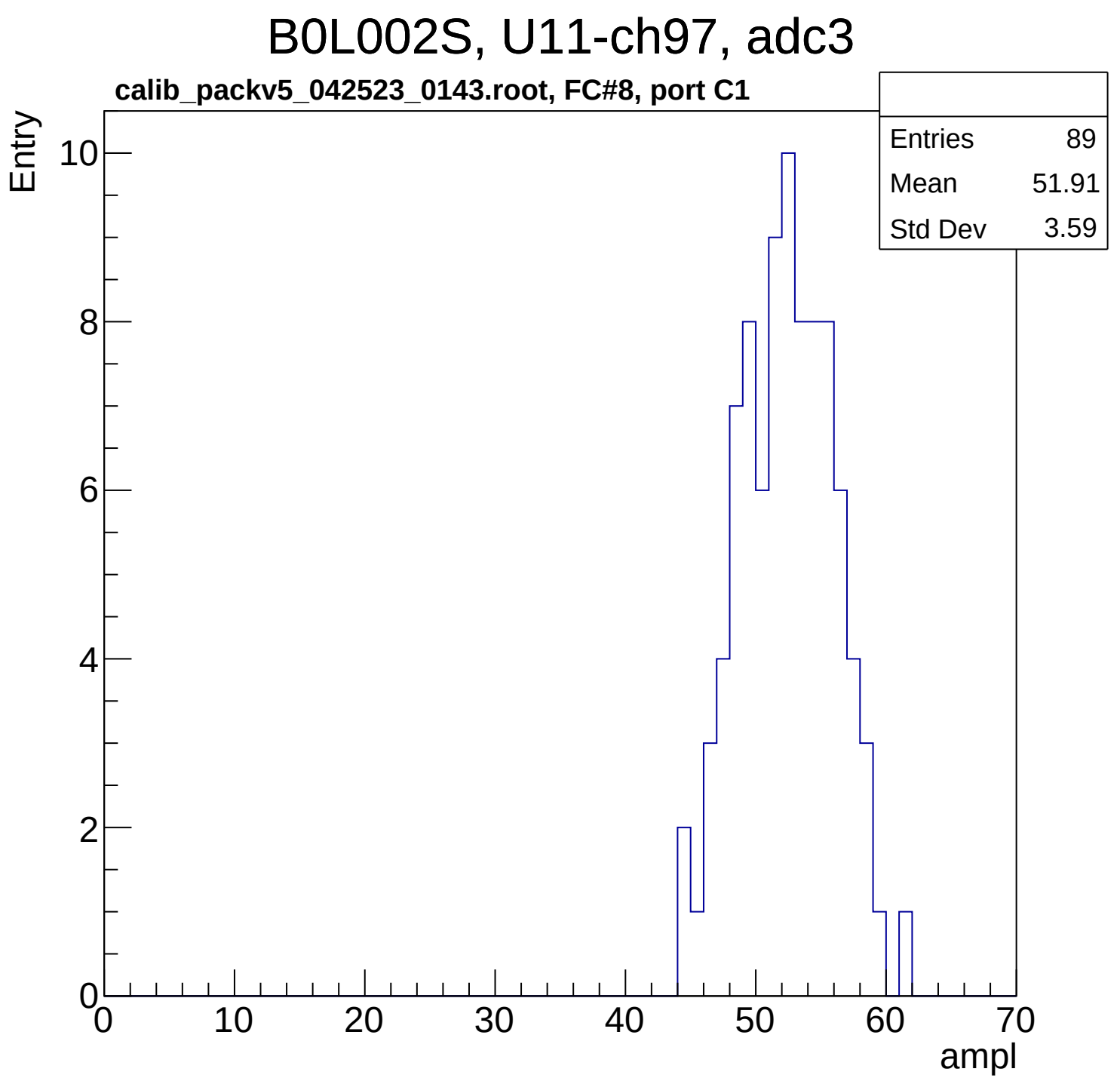
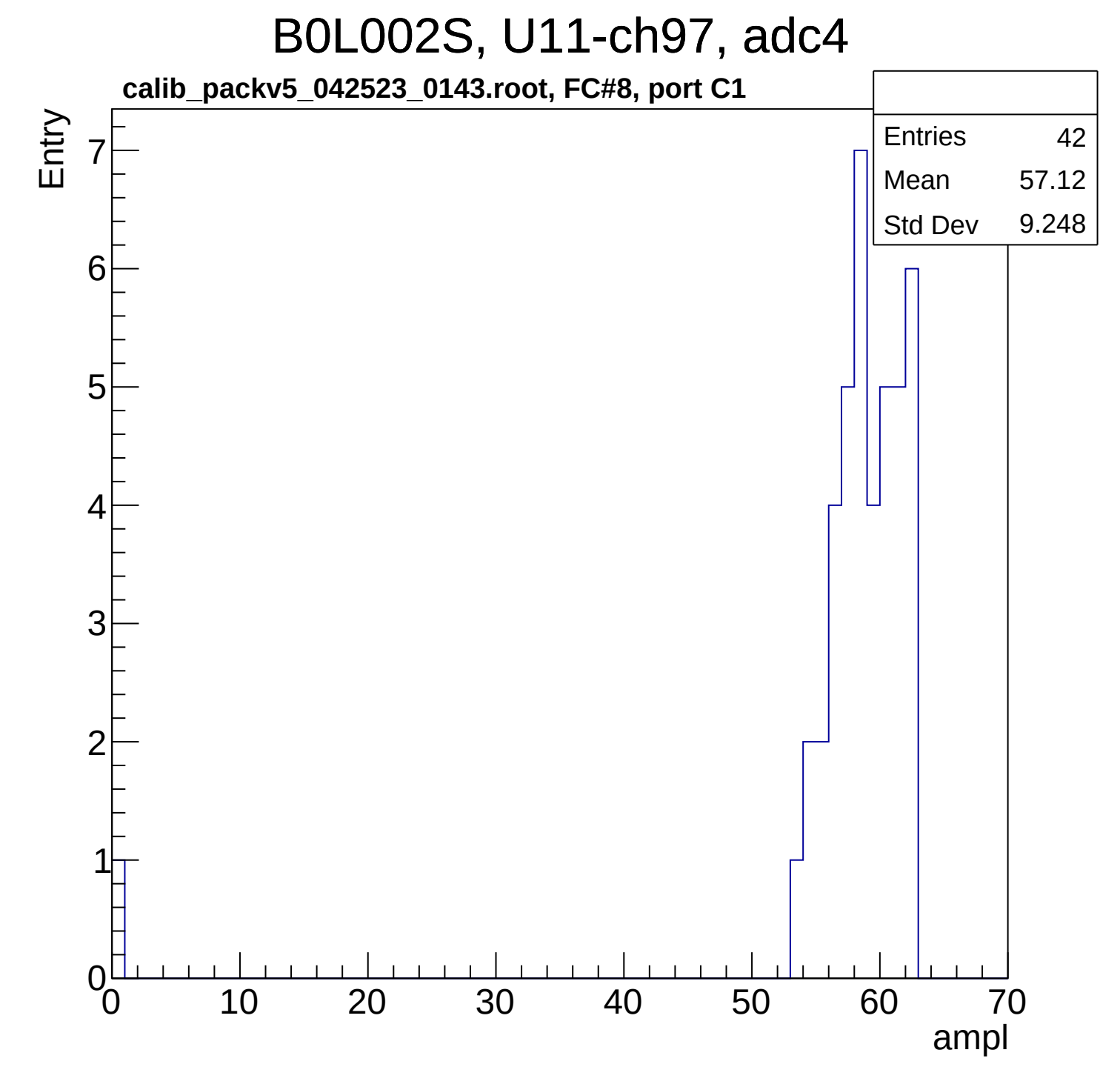

B0L002S, U11-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 51.91
Std Dev 3.59
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 57.12
Std Dev 9.248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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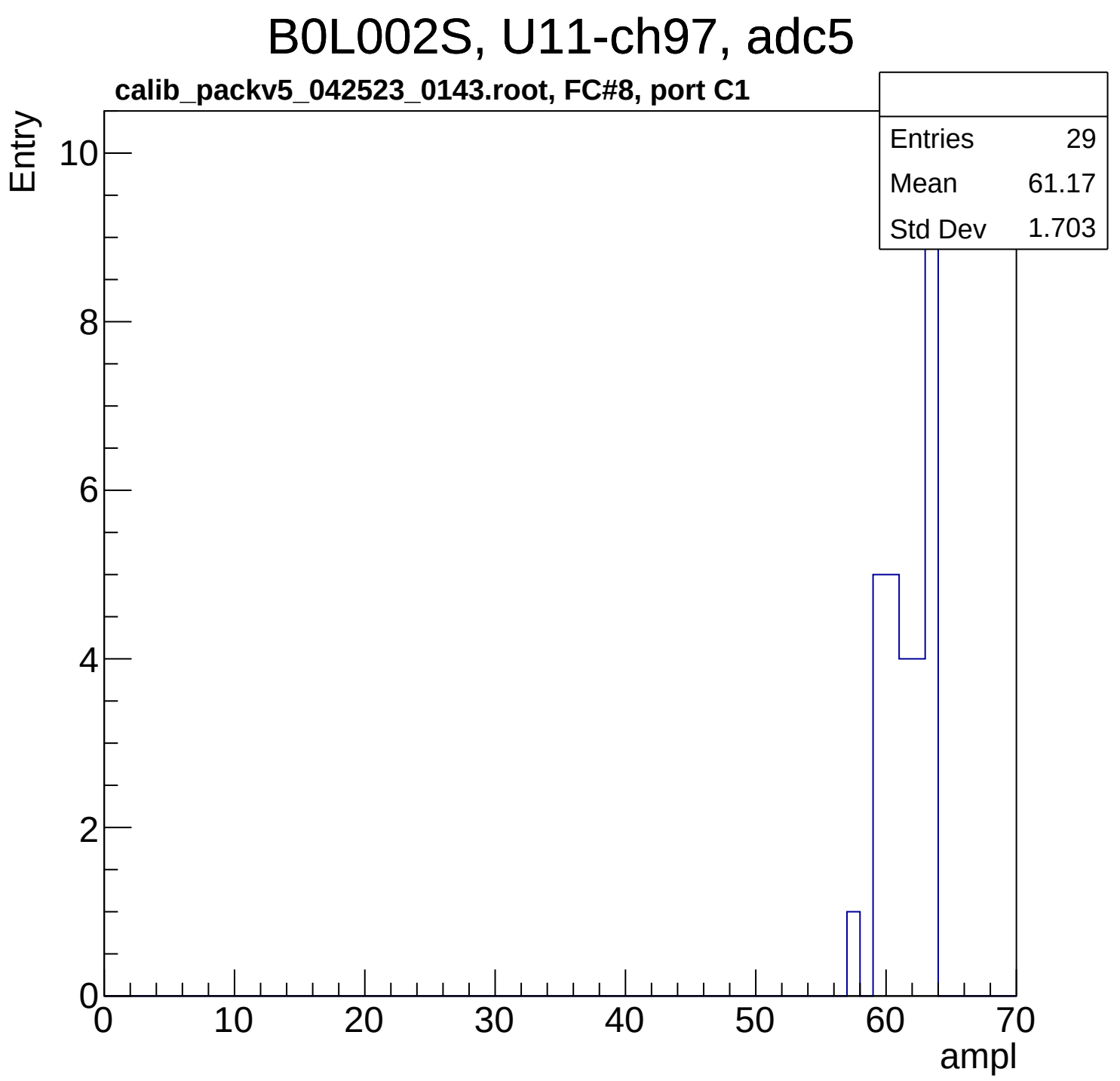
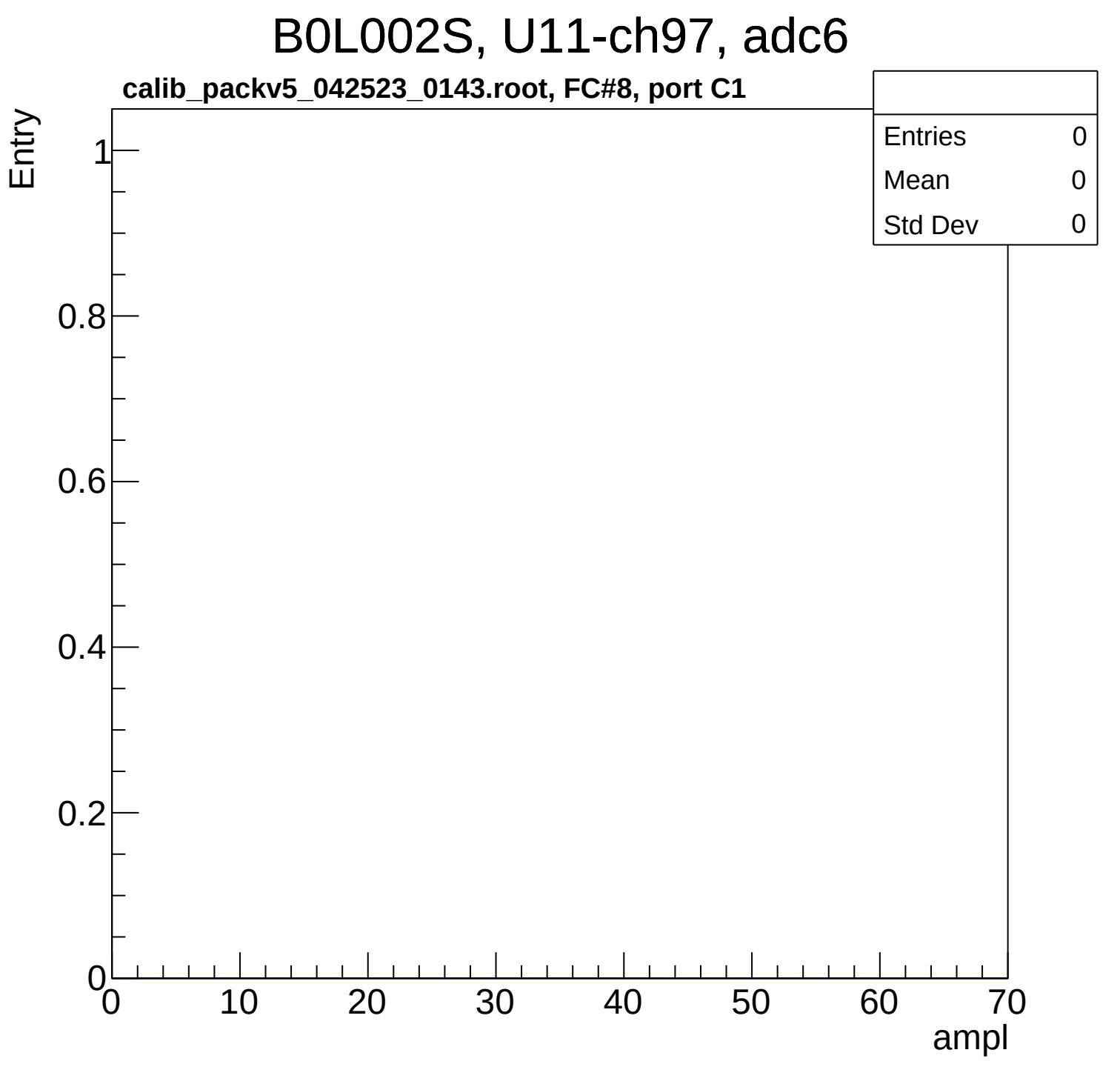

B0L002S, U11-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 61.17
Std Dev 1.703
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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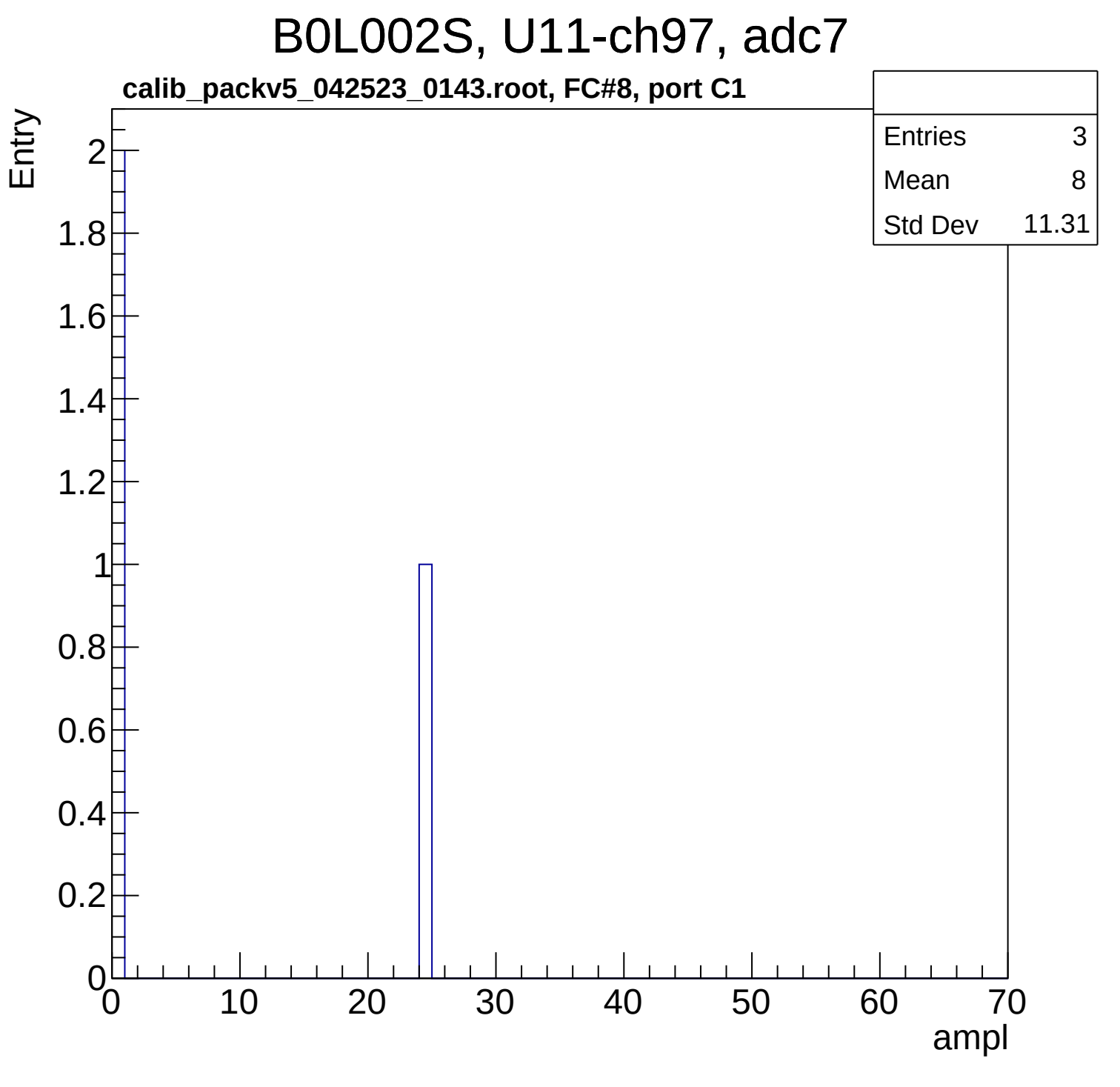
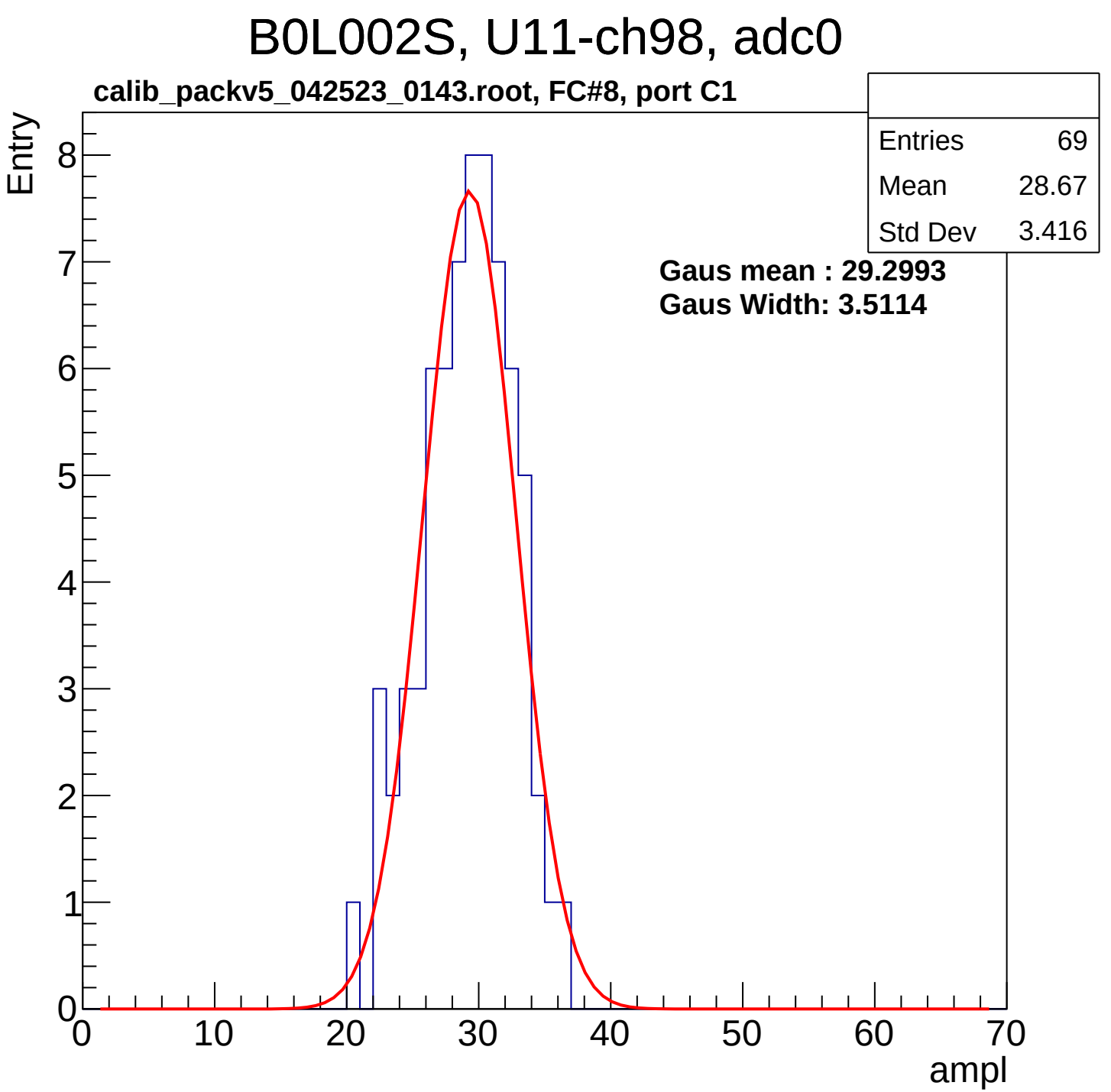
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 28.67
Std Dev 3.416
Gaus mean : 29.2993
Gaus Width: 3.5114
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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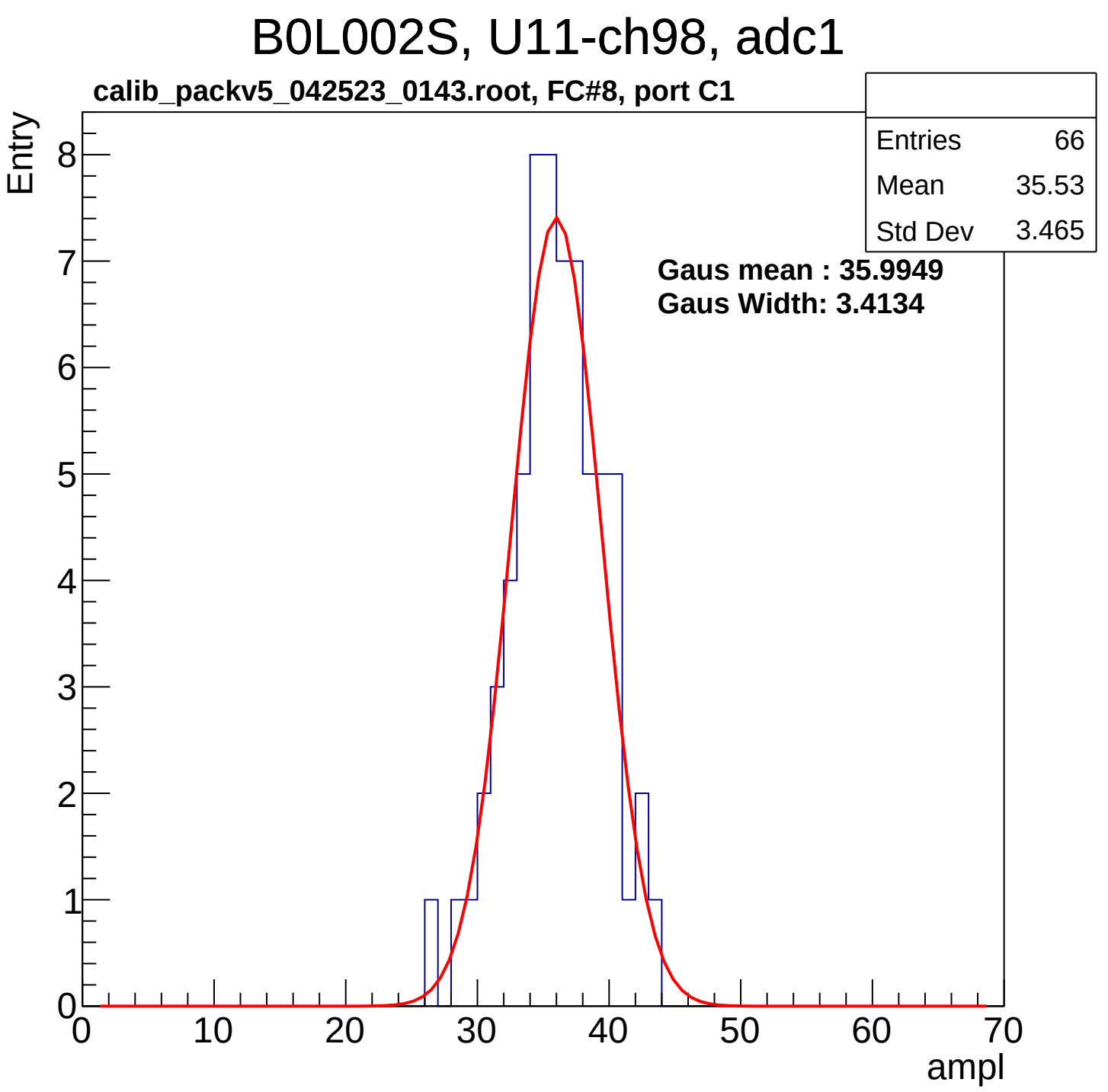
B0L002S, U11-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 35.53
Std Dev 3.465
Gaus mean : 35.9949
Gaus Width: 3.4134
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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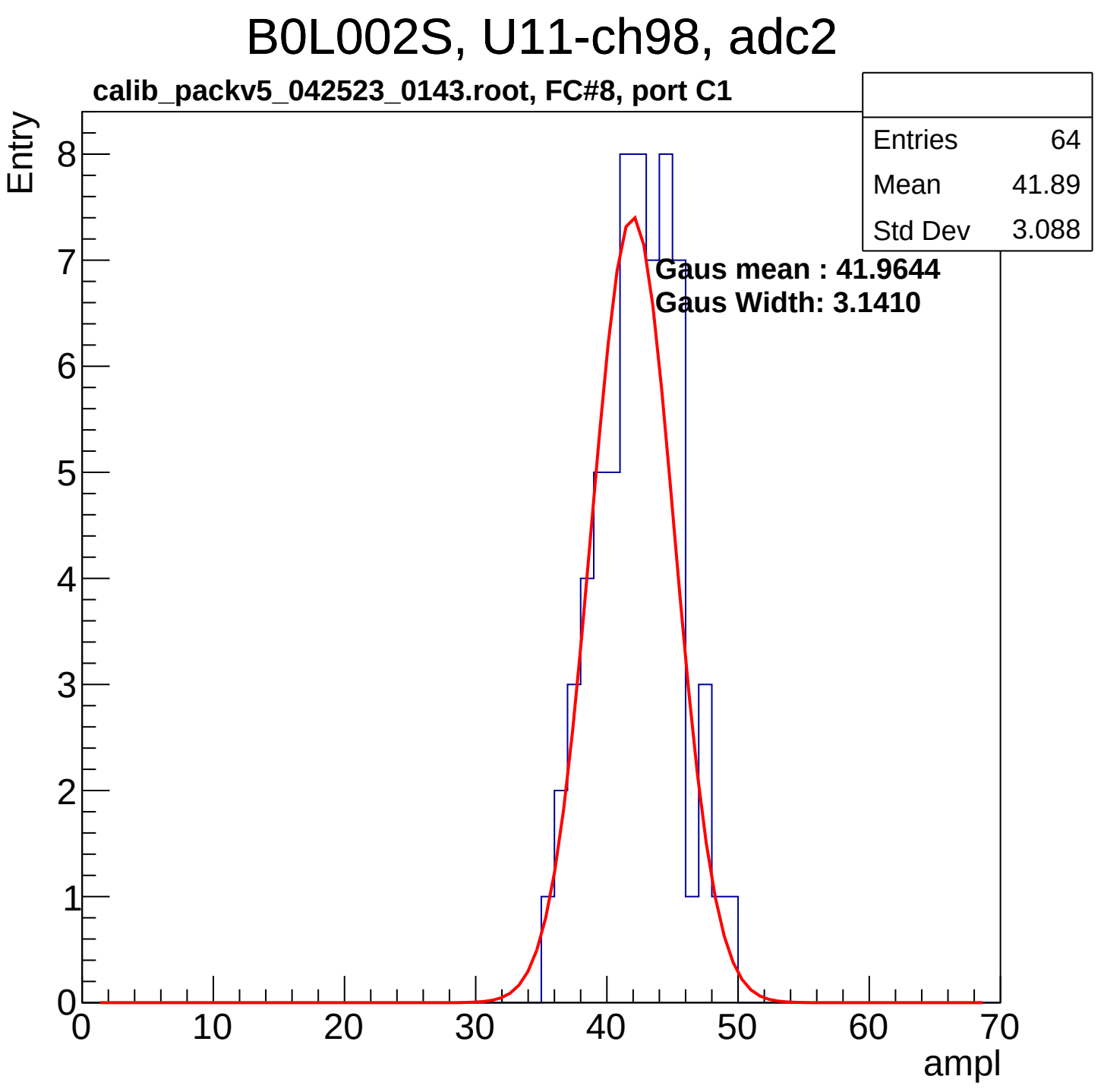
B0L002S, U11-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 41.89
Std Dev 3.088
Gaus mean : 41.9644
Gaus Width: 3.1410
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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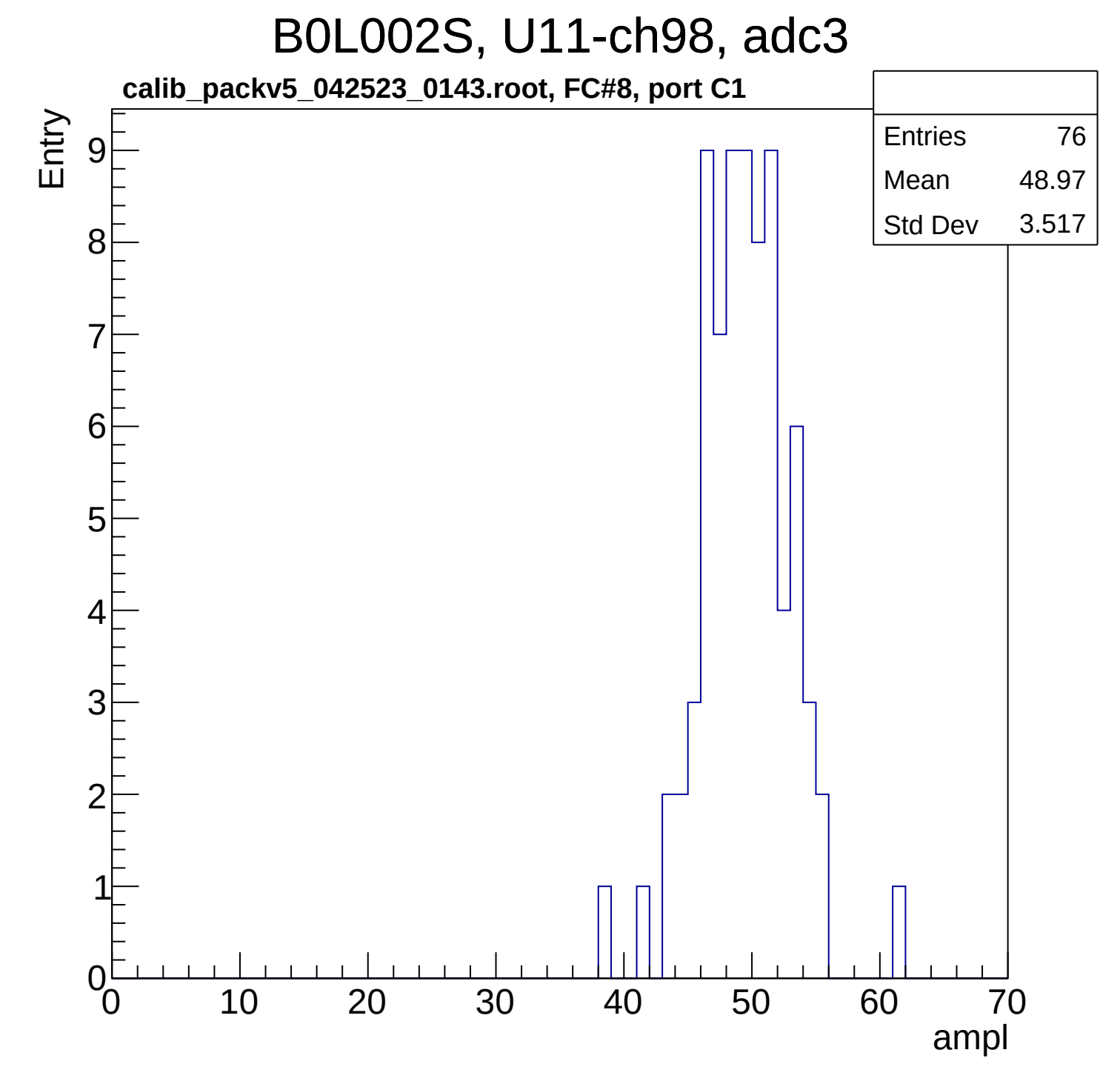
B0L002S, U11-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 48.97
Std Dev 3.517
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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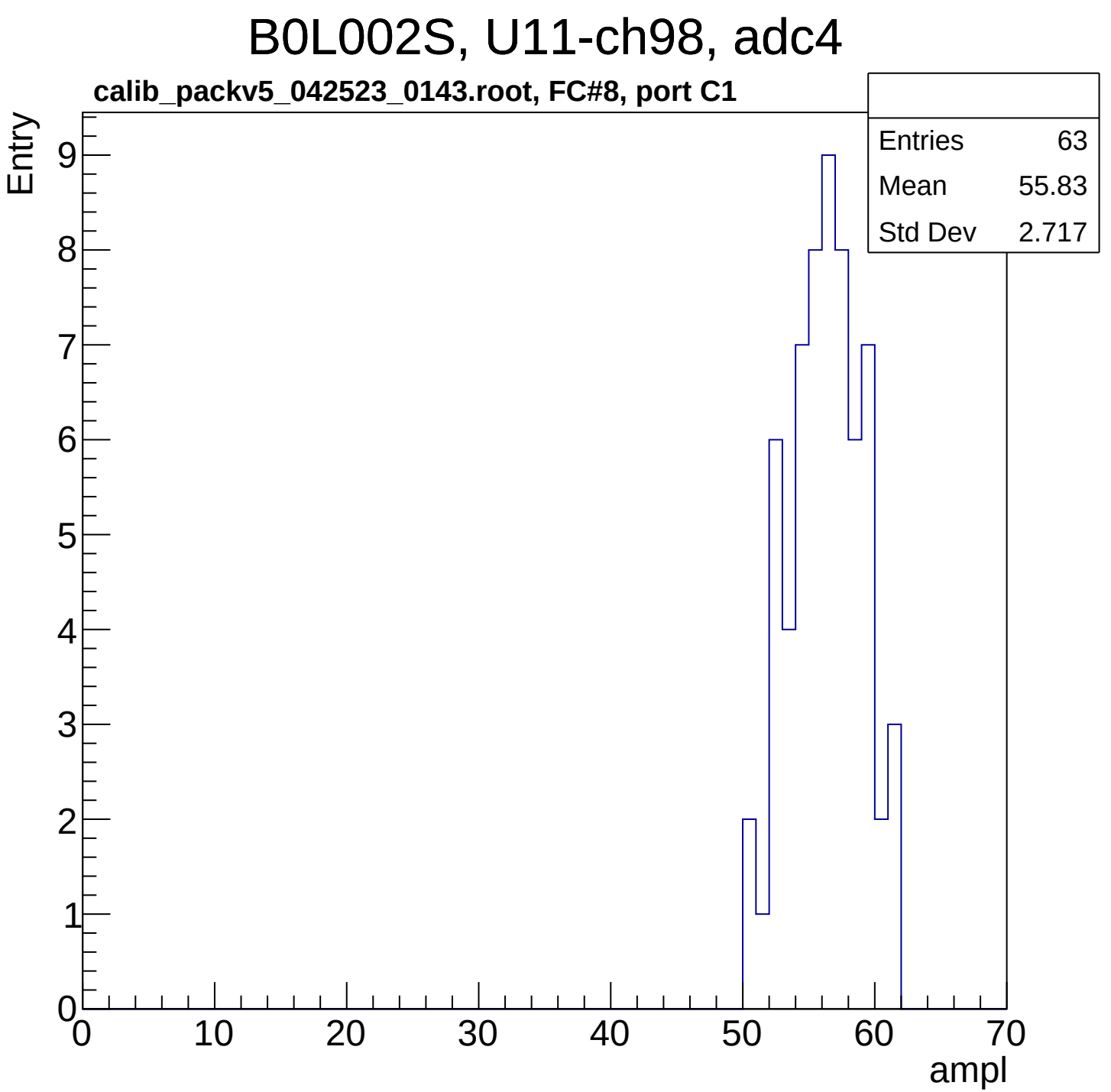
B0L002S, U11-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.83
Std Dev 2.717
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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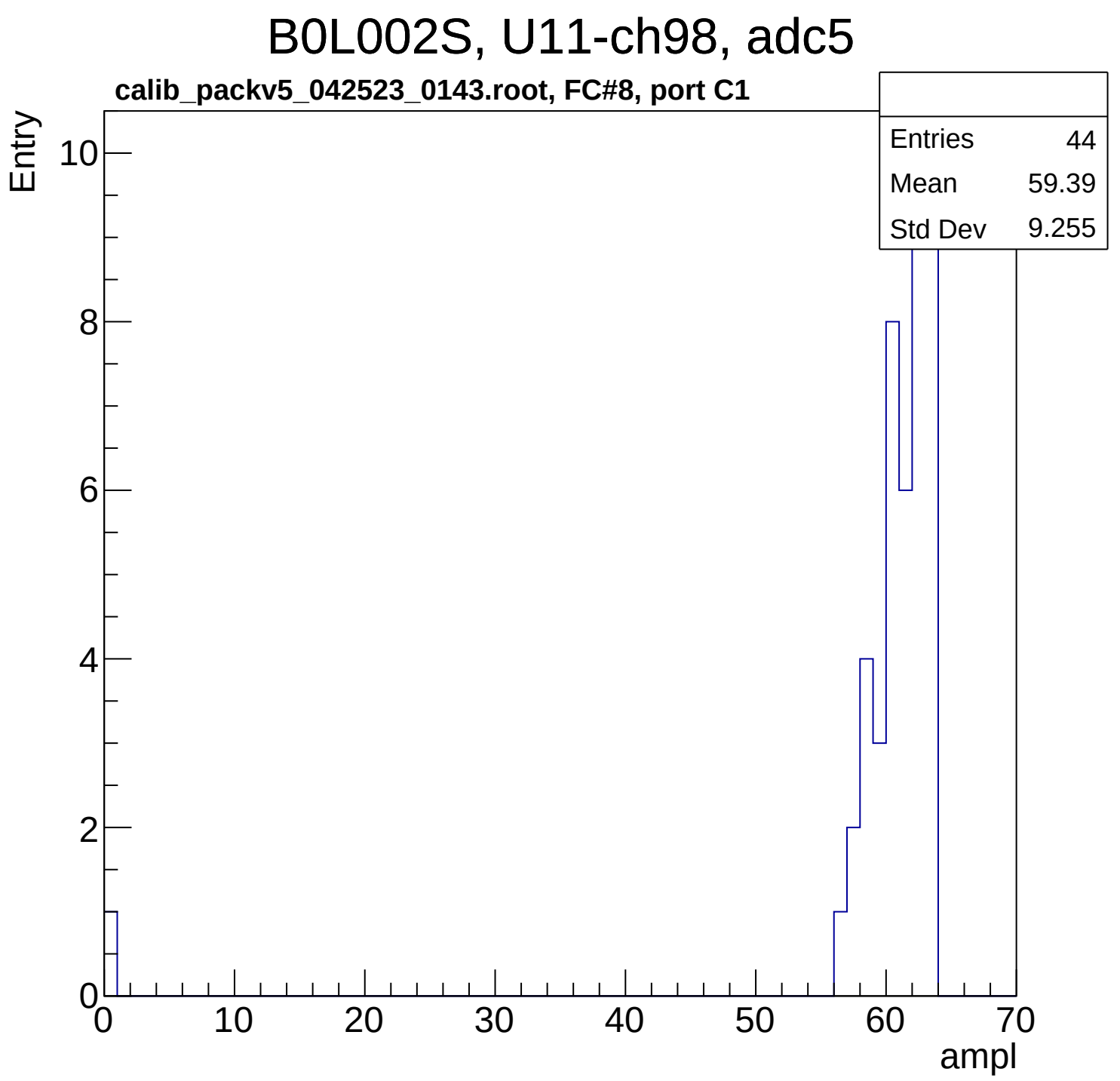
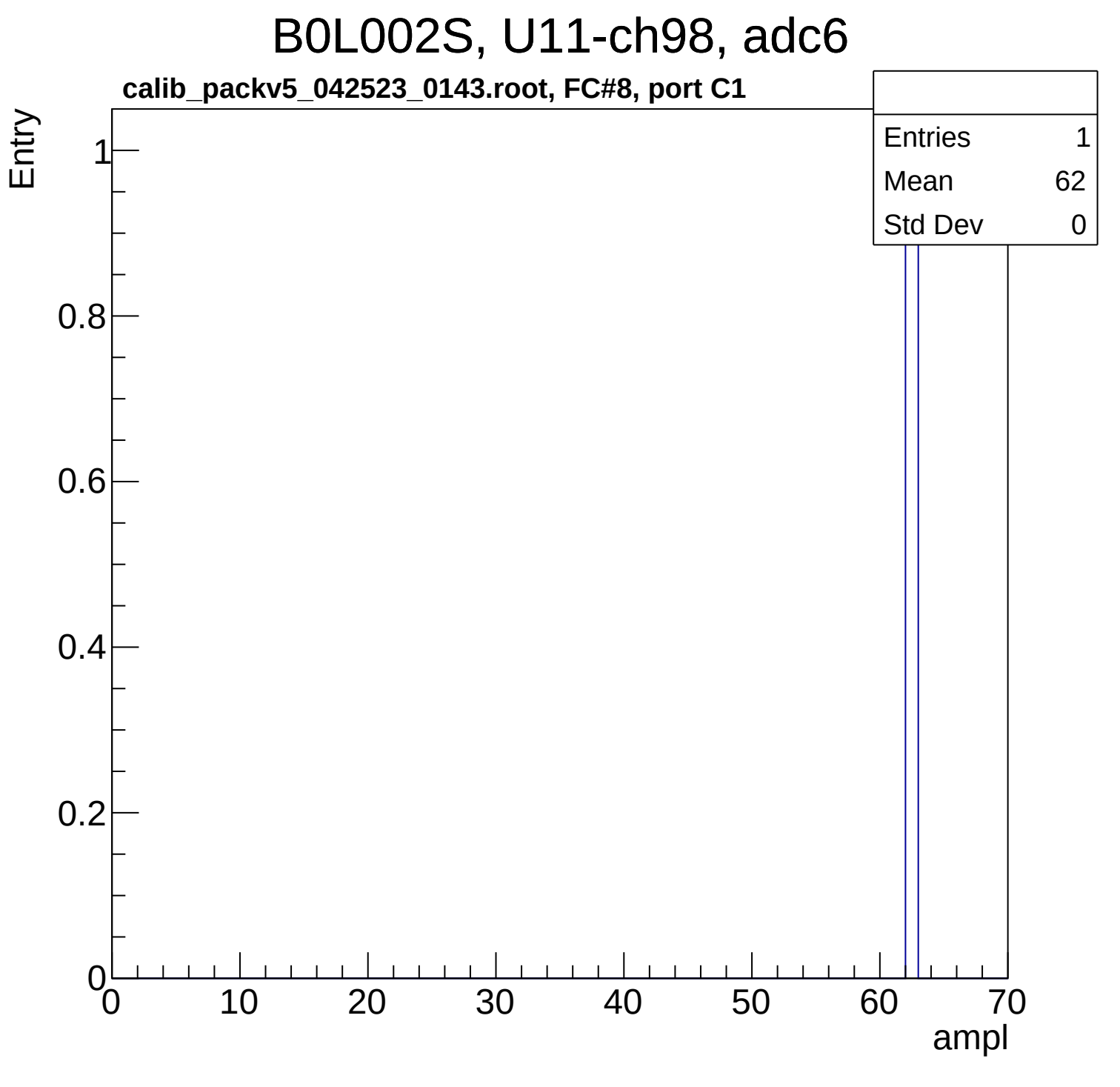

B0L002S, U11-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.39
Std Dev 9.255
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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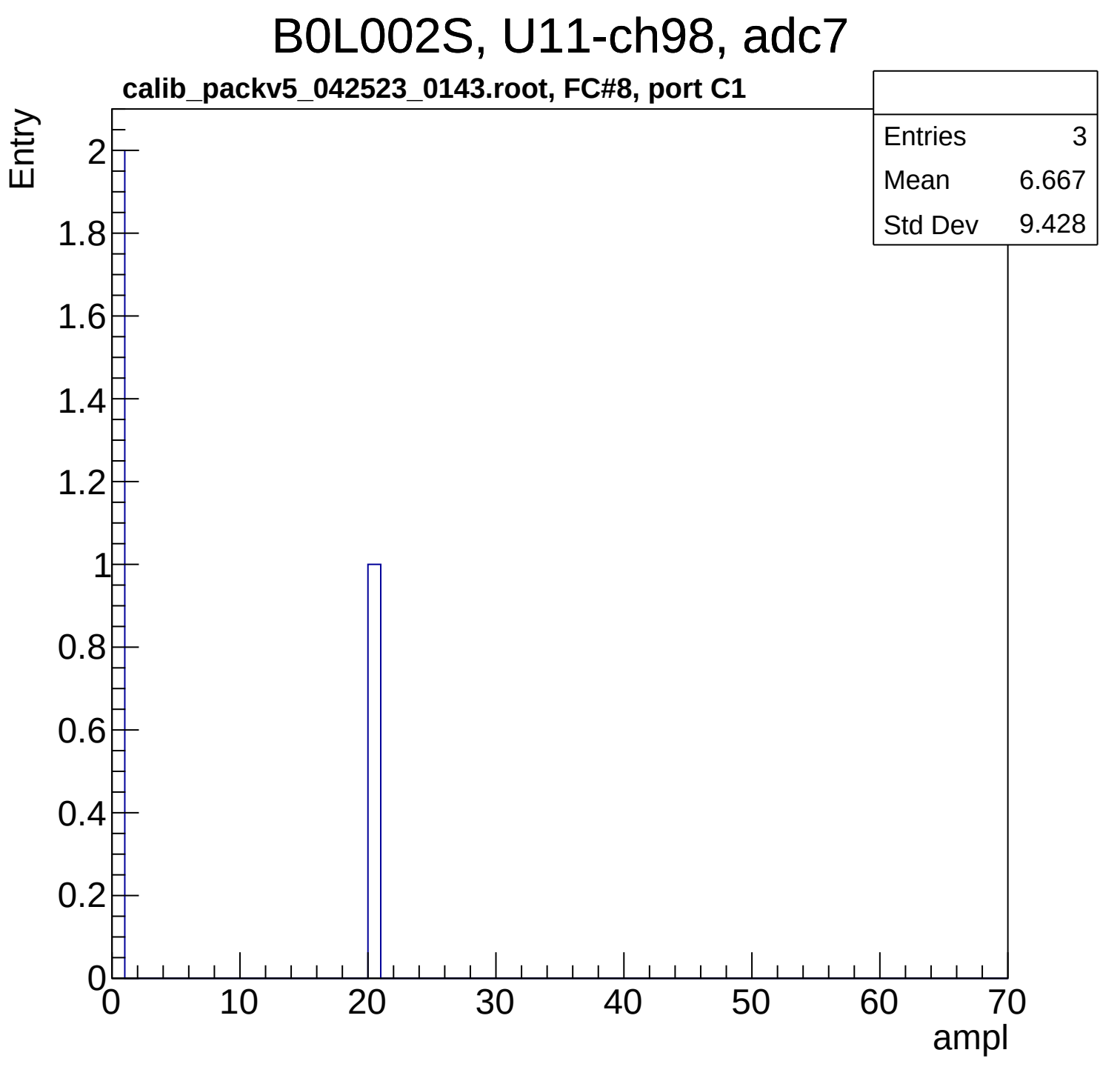
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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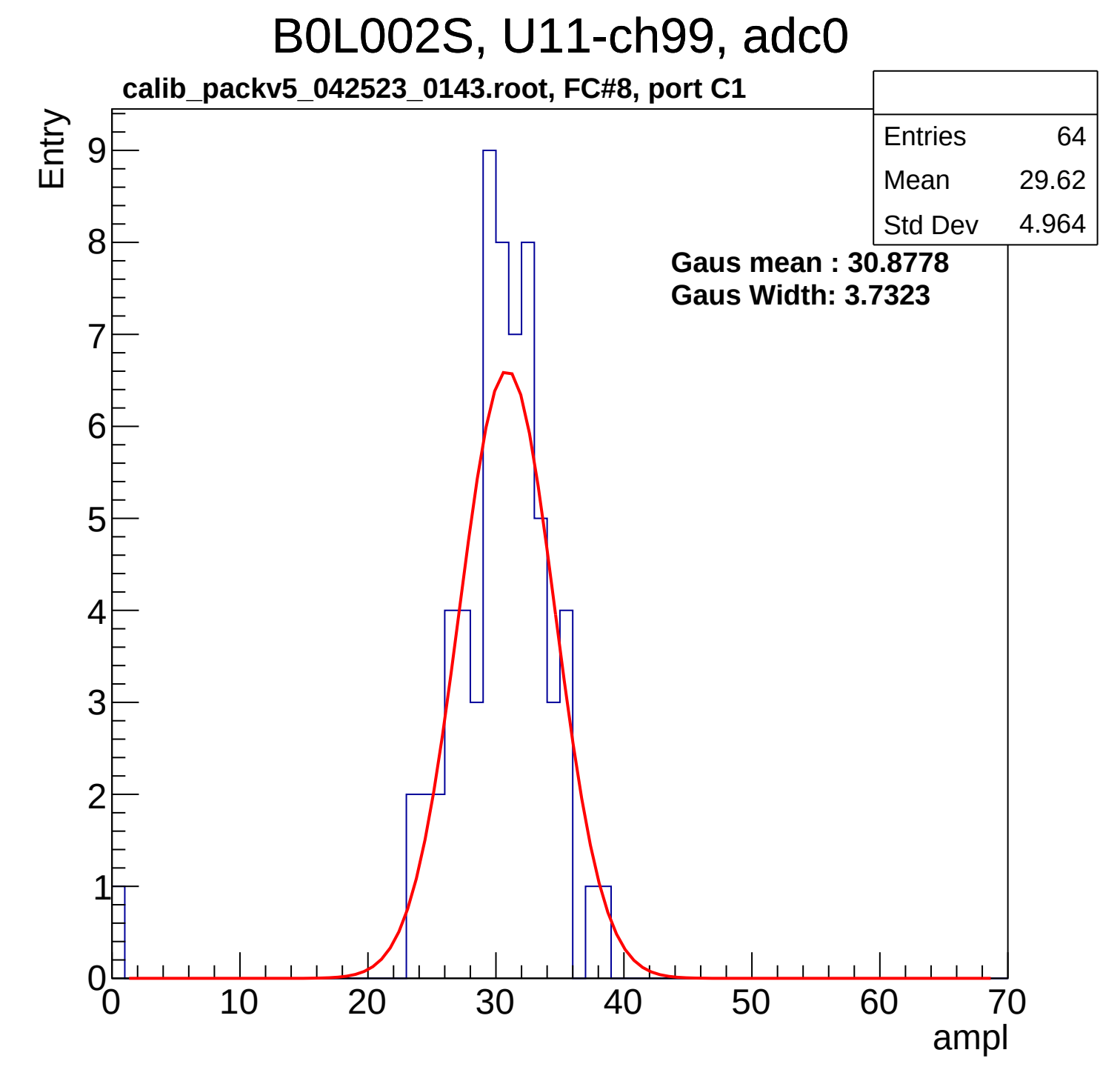
30
40
50
60
70


B0L002S, U11-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.62
Std Dev 4.964
Gaus mean : 30.8778
Gaus Width: 3.7323
Entry
ampl
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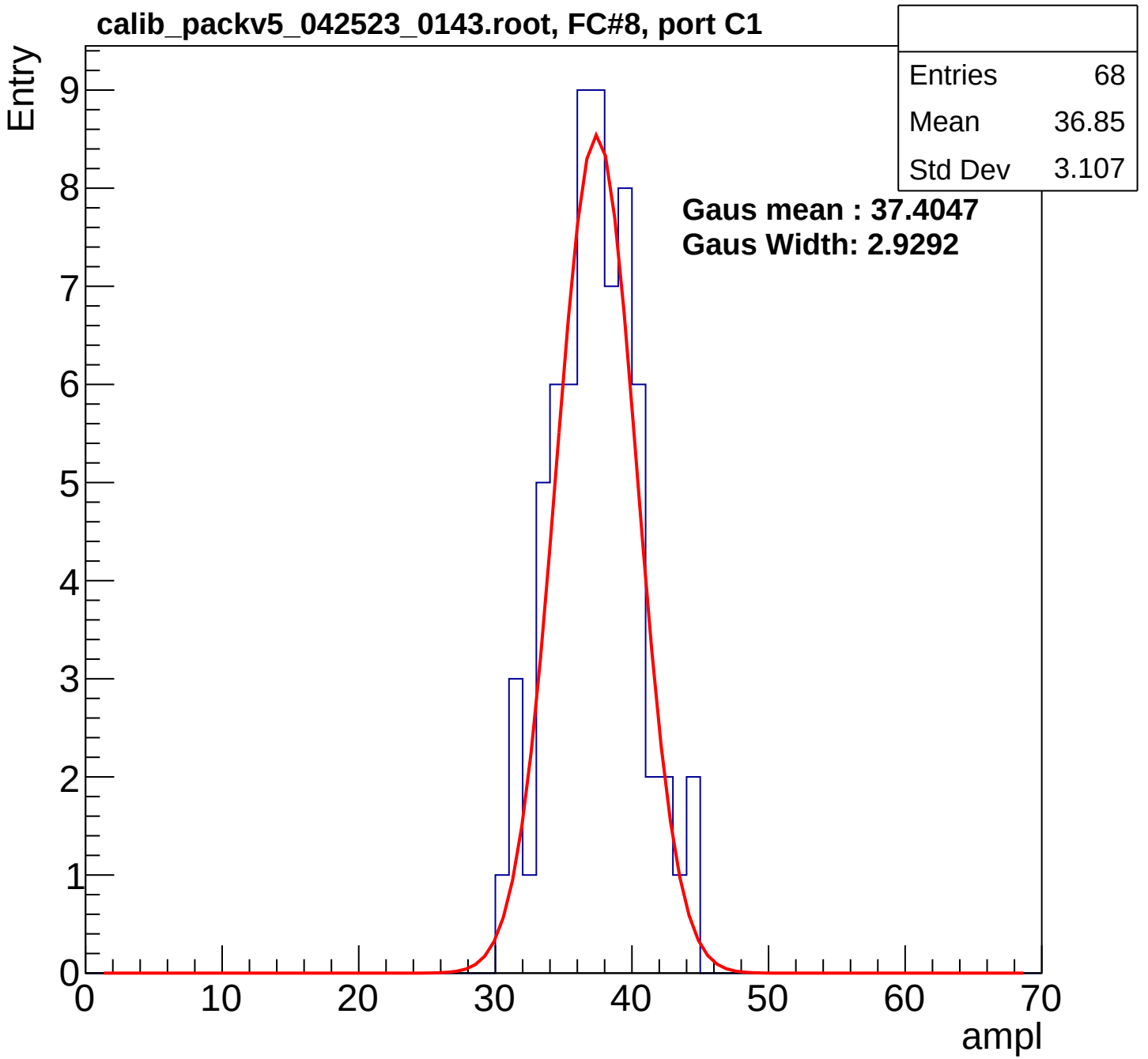
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.85
Std Dev 3.107
Gaus mean : 37.4047
Gaus Width: 2.9292
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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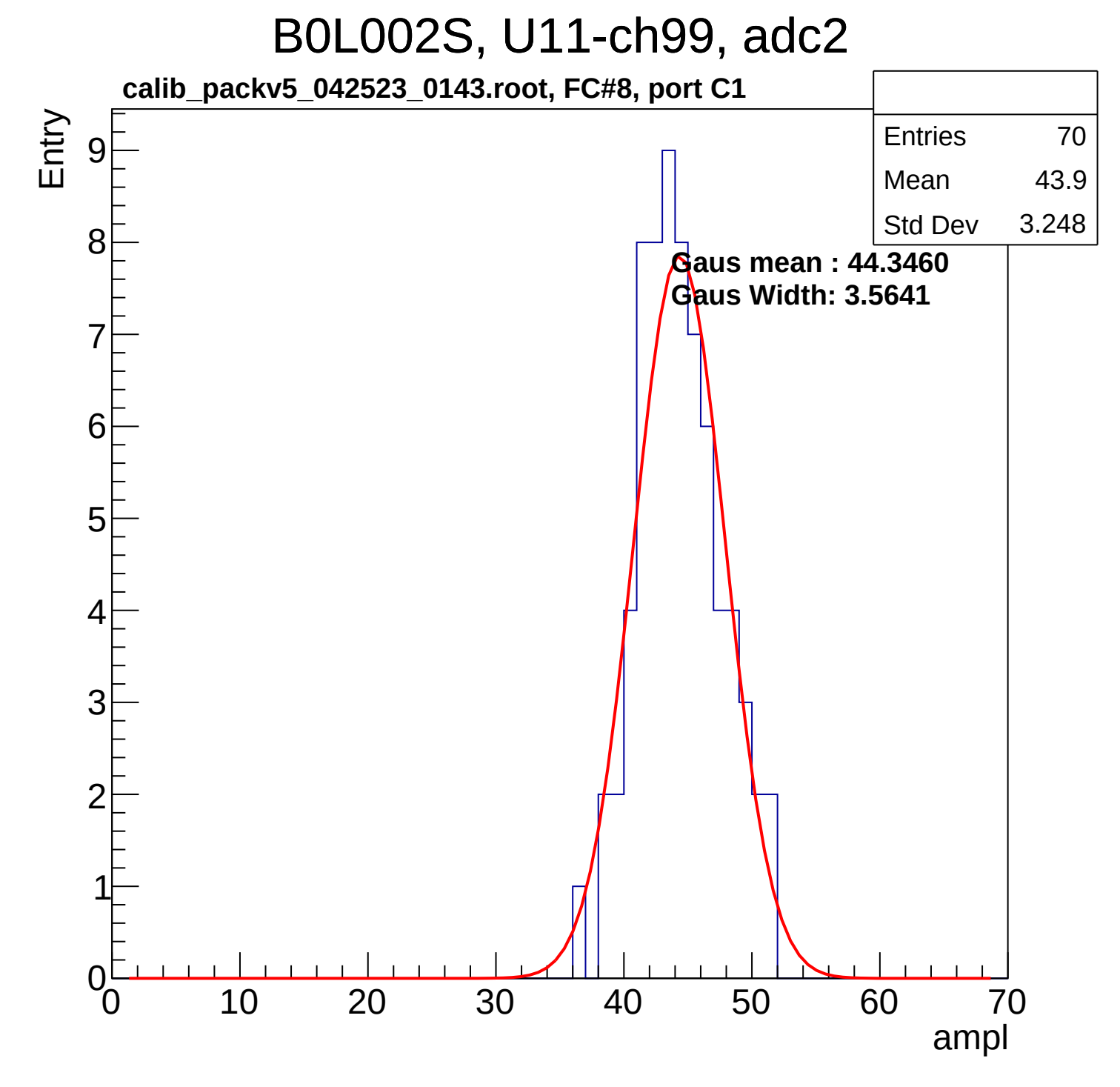
60
70


B0L002S, U11-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.9
Std Dev 3.248
Gaus mean : 44.3460
Gaus Width: 3.5641
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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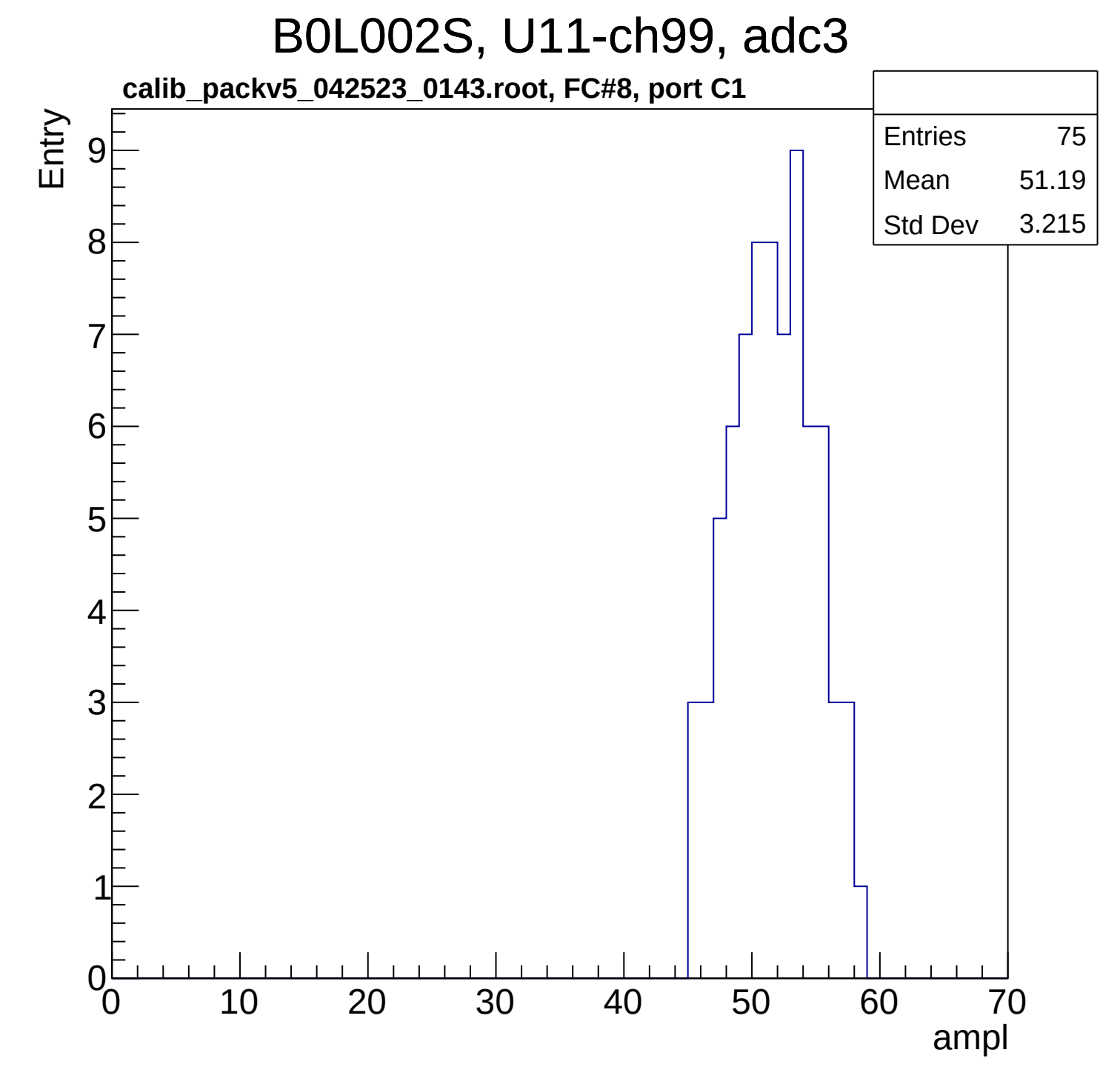
B0L002S, U11-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.19
Std Dev 3.215
Entry
ampl
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3
4
5
6
7
8
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0
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60
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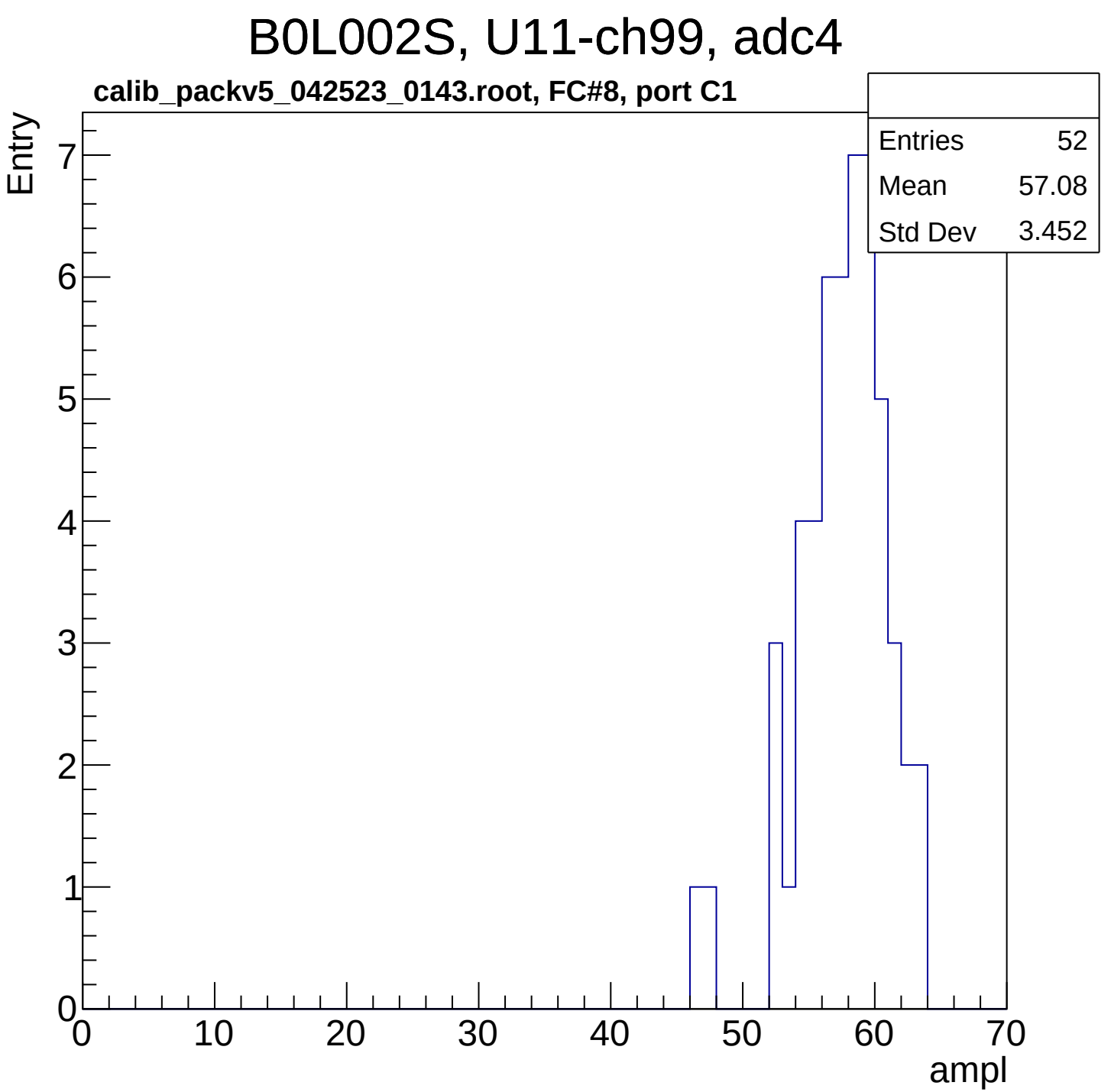
B0L002S, U11-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.08
Std Dev 3.452
Entry
ampl
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1
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3
4
5
6
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0
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20
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40
50
60
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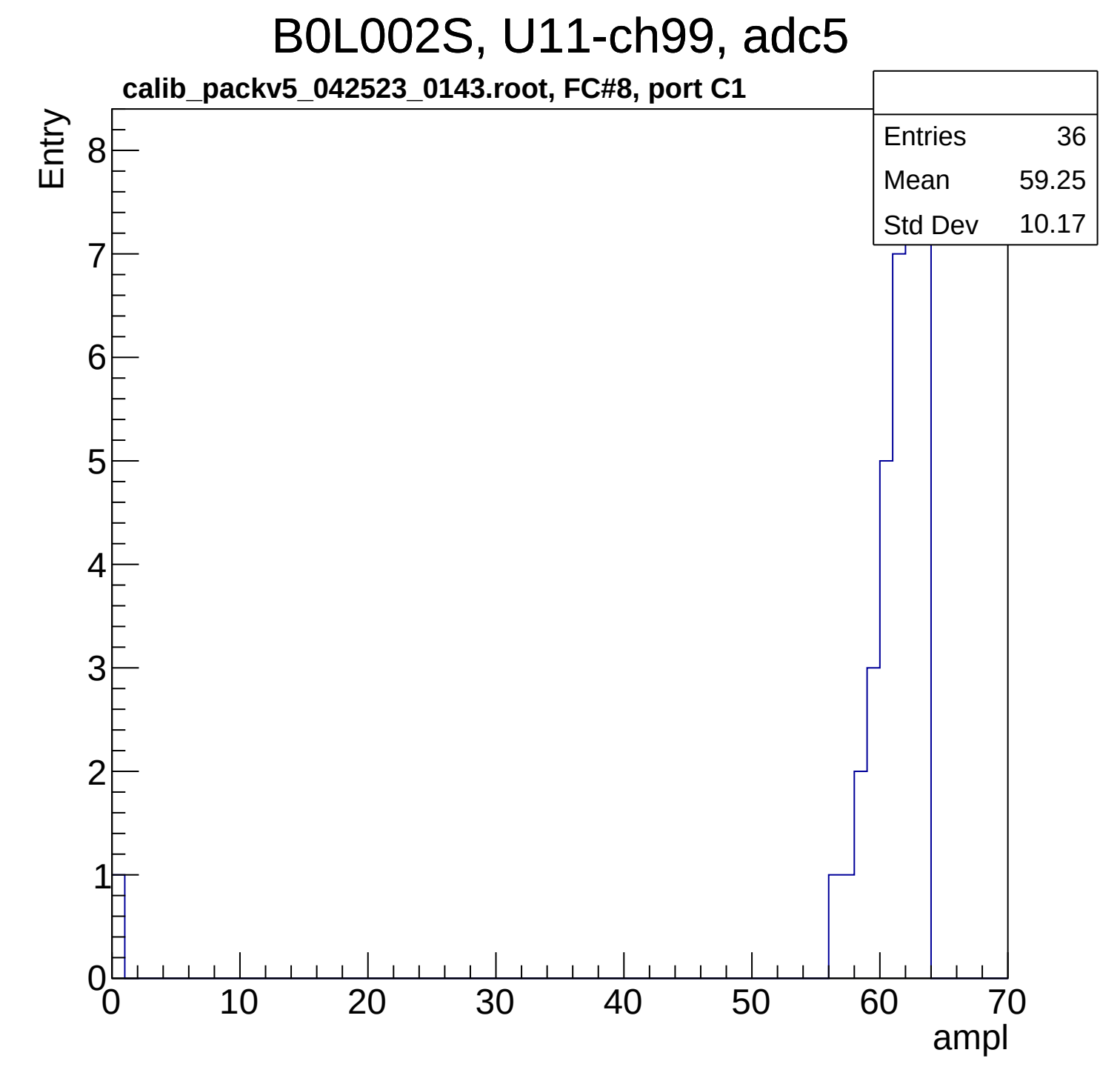
B0L002S, U11-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.25
Std Dev 10.17
Entry
ampl
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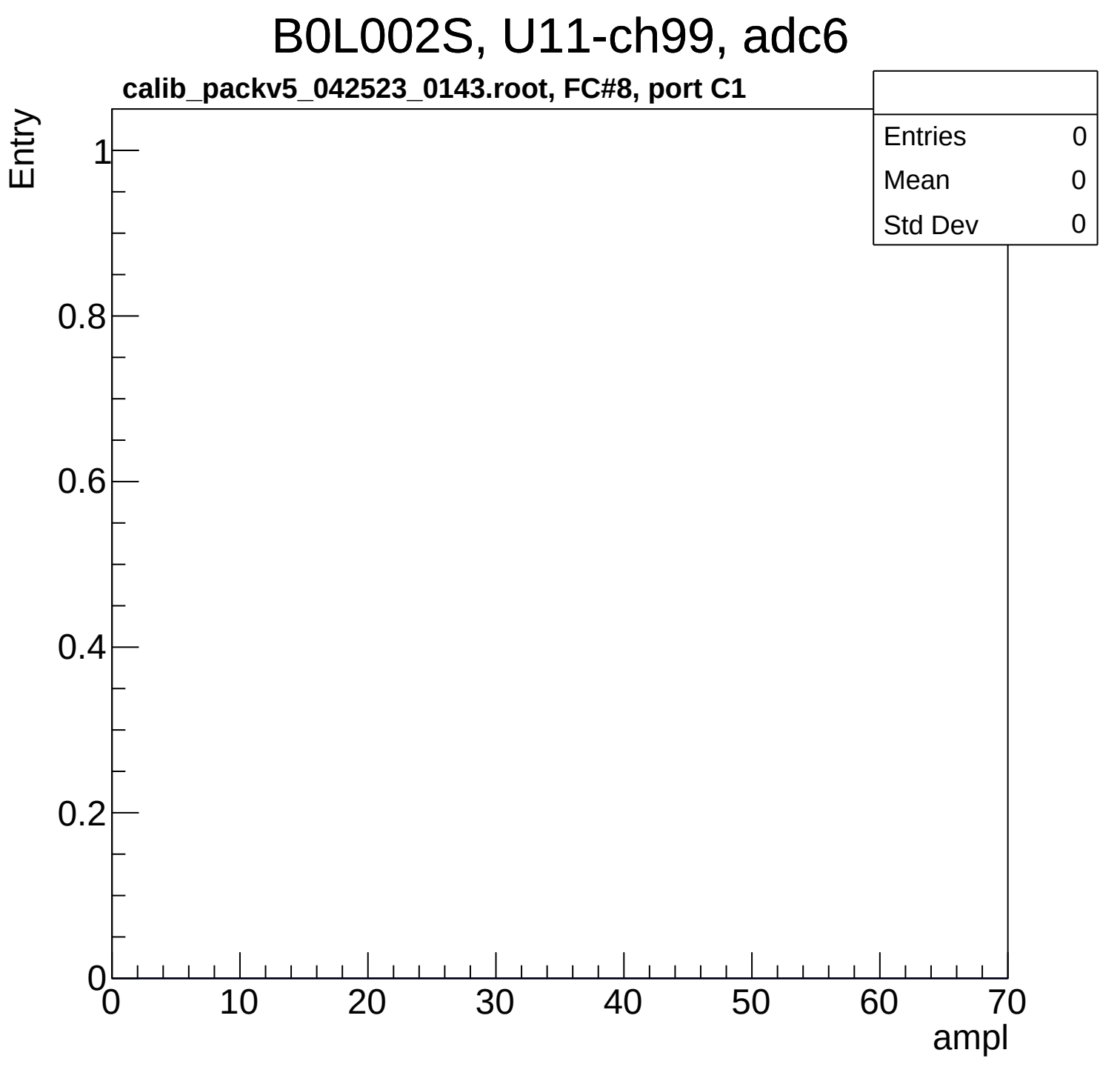
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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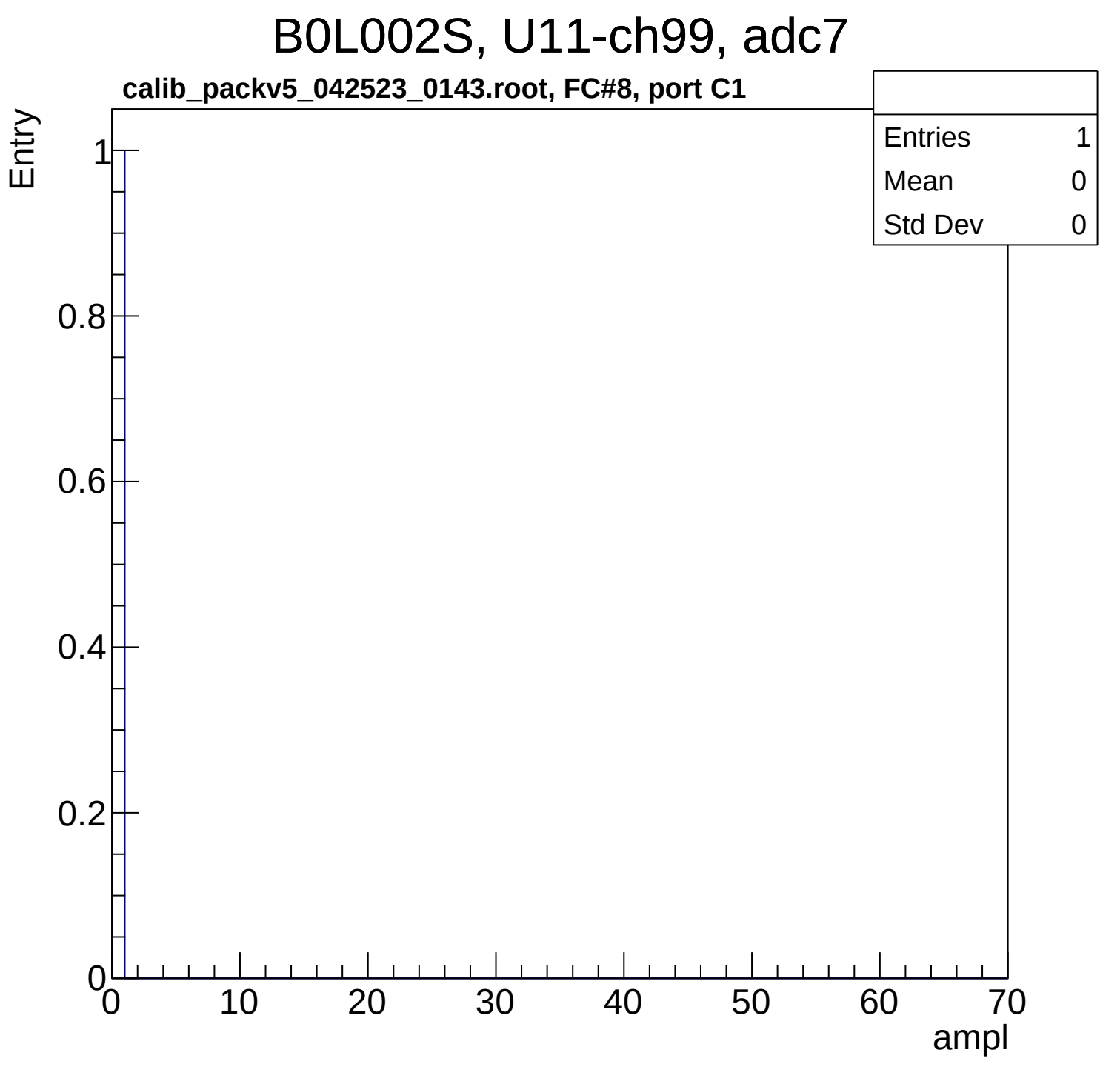
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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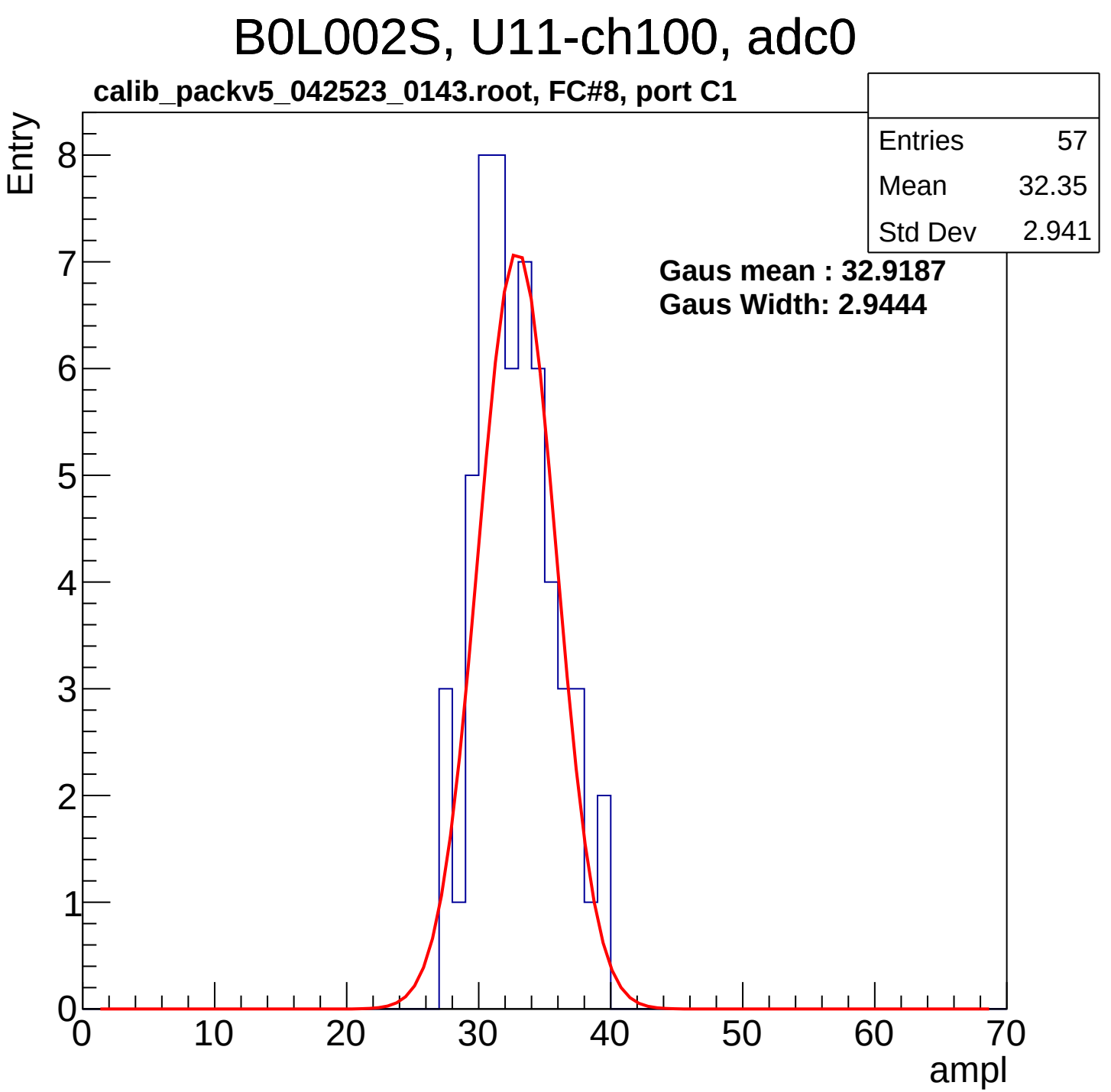
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 32.35
Std Dev 2.941
Gaus mean : 32.9187
Gaus Width: 2.9444
Entry
ampl
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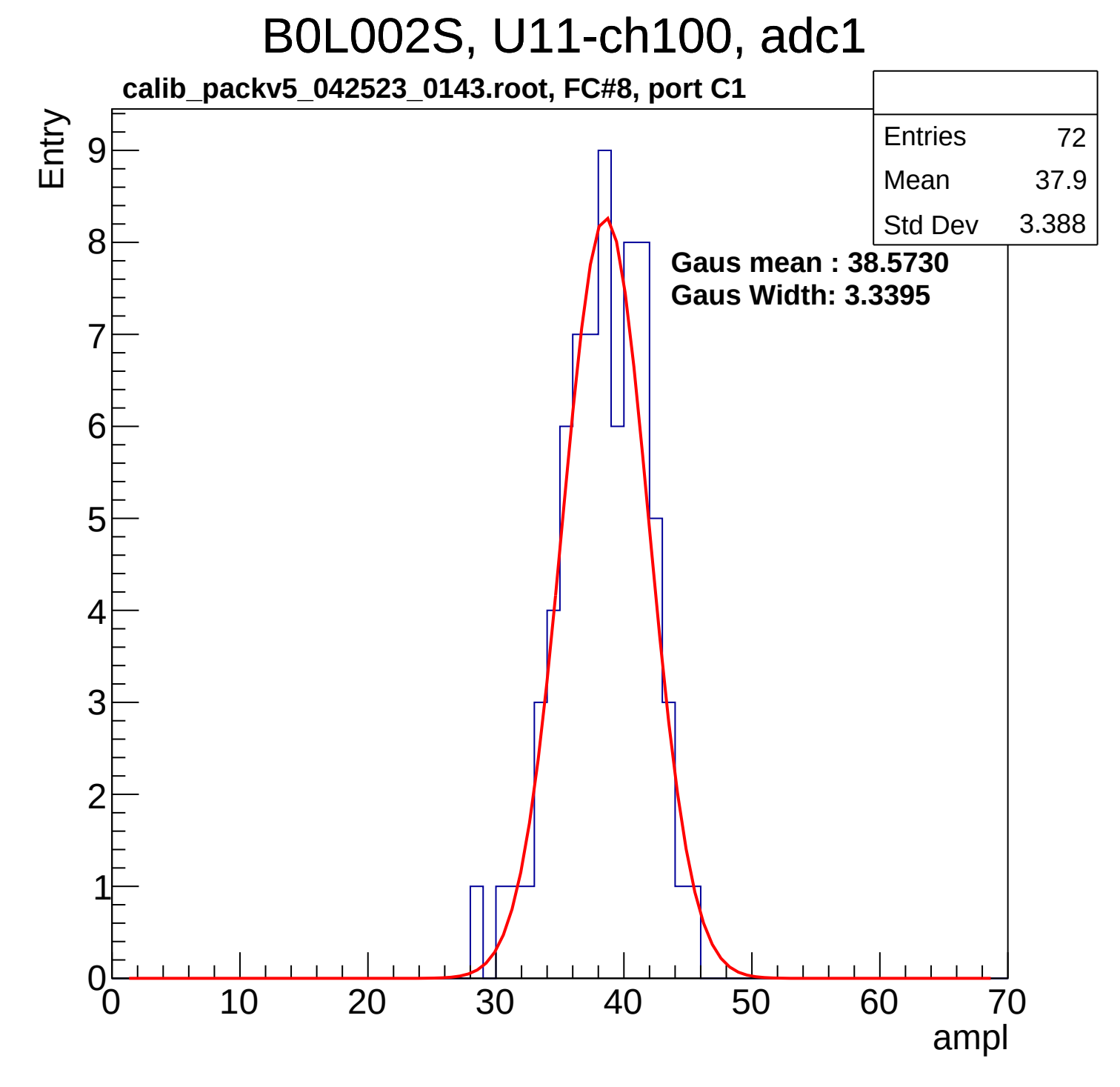
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.9
Std Dev 3.388
Gaus mean : 38.5730
Gaus Width: 3.3395
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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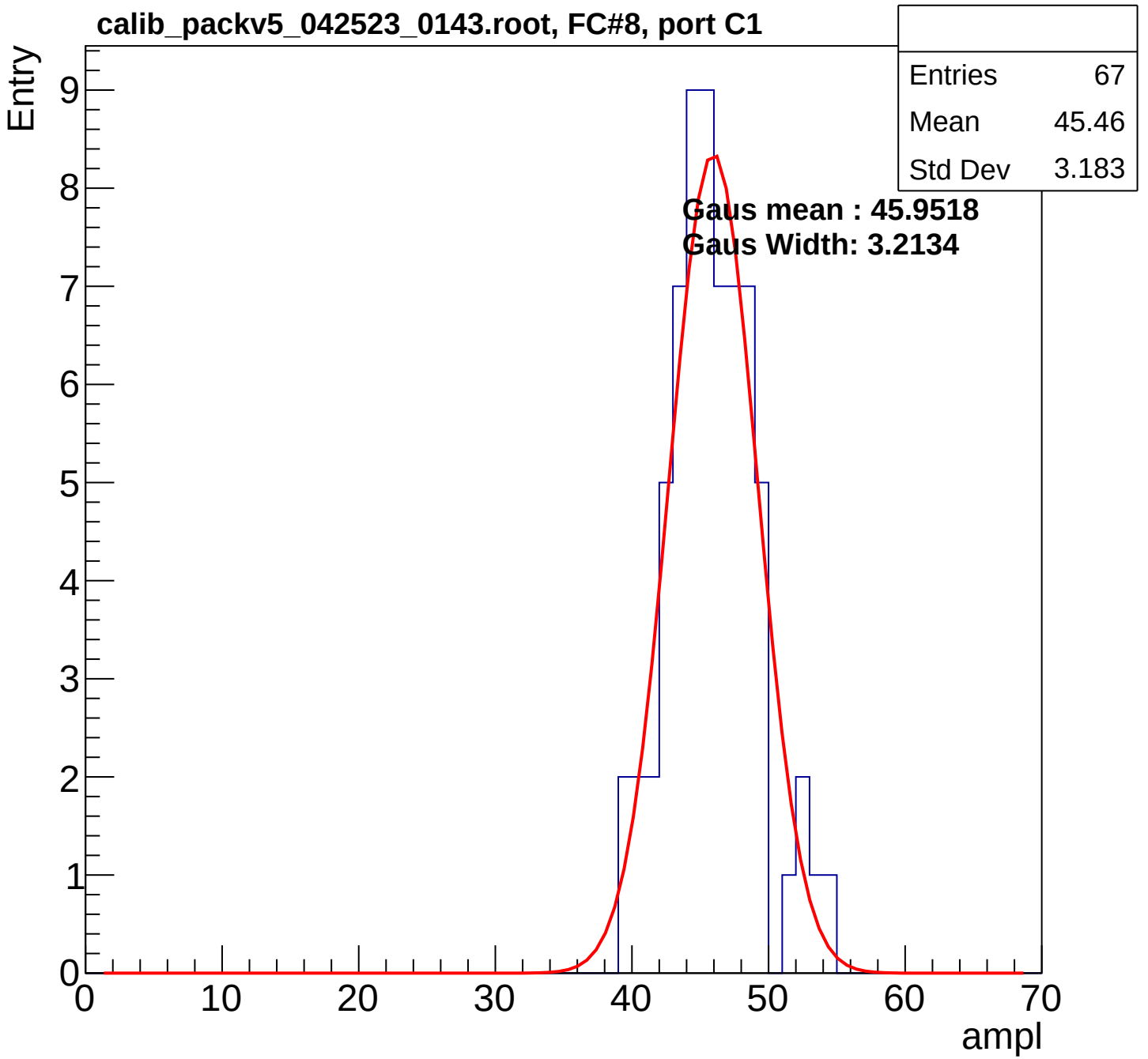
B0L002S, U11-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.46
Std Dev 3.183
Gaus mean : 45.9518
Gaus Width: 3.2134
Entry
ampl
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2
3
4
5
6
7
8
9
0
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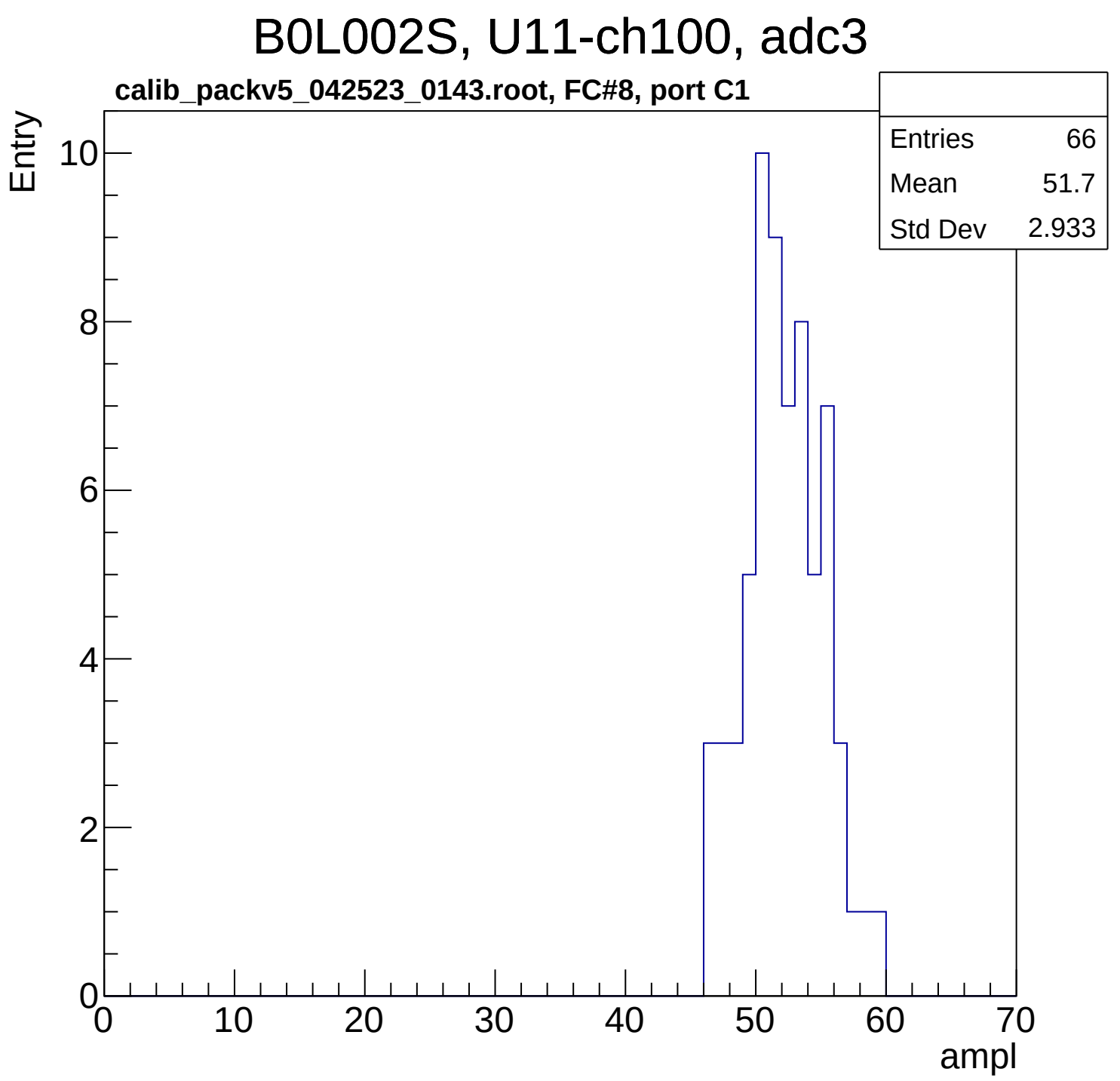
B0L002S, U11-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.7
Std Dev 2.933
Entry
ampl
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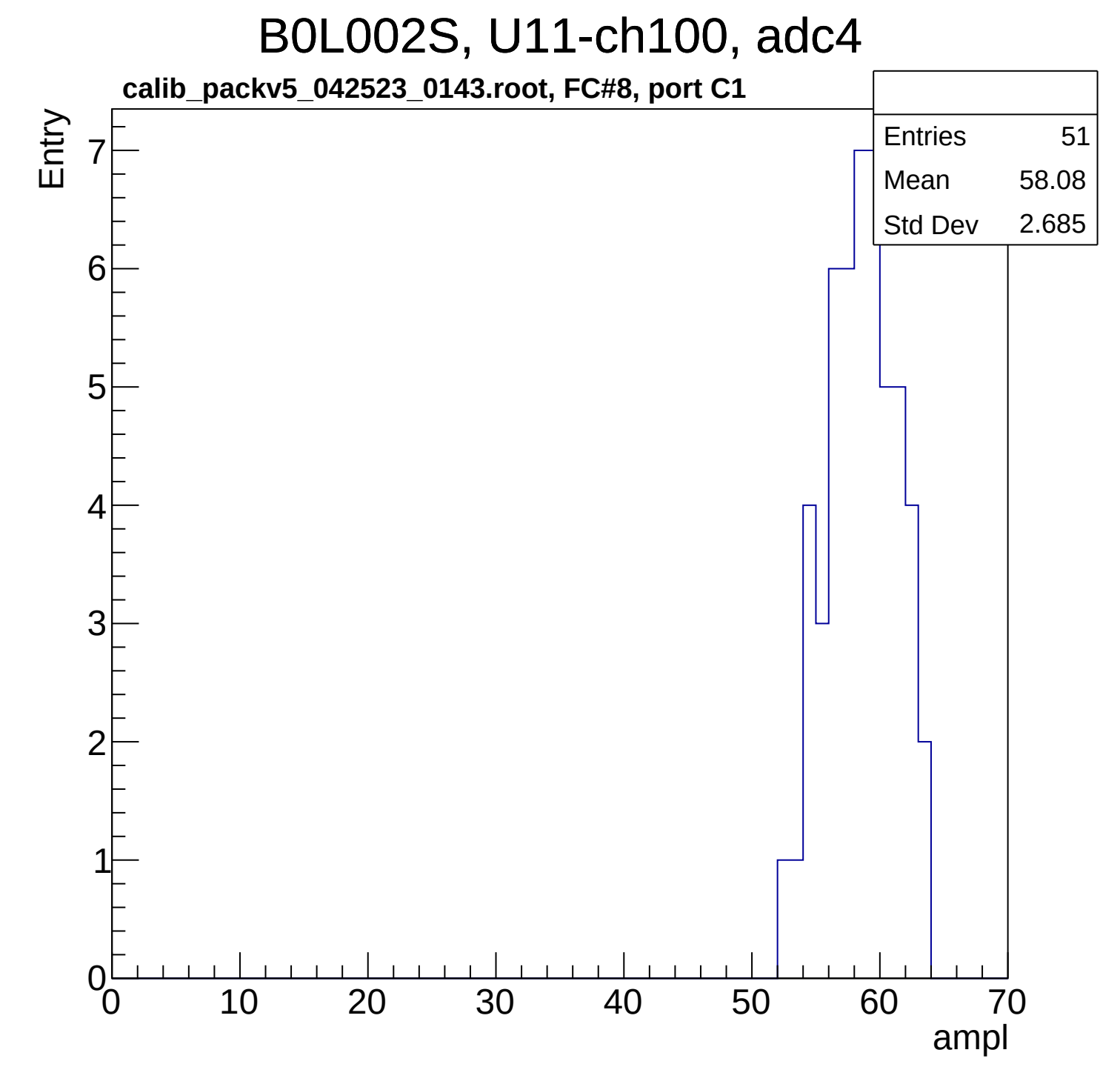
B0L002S, U11-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.08
Std Dev 2.685
Entry
ampl
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4
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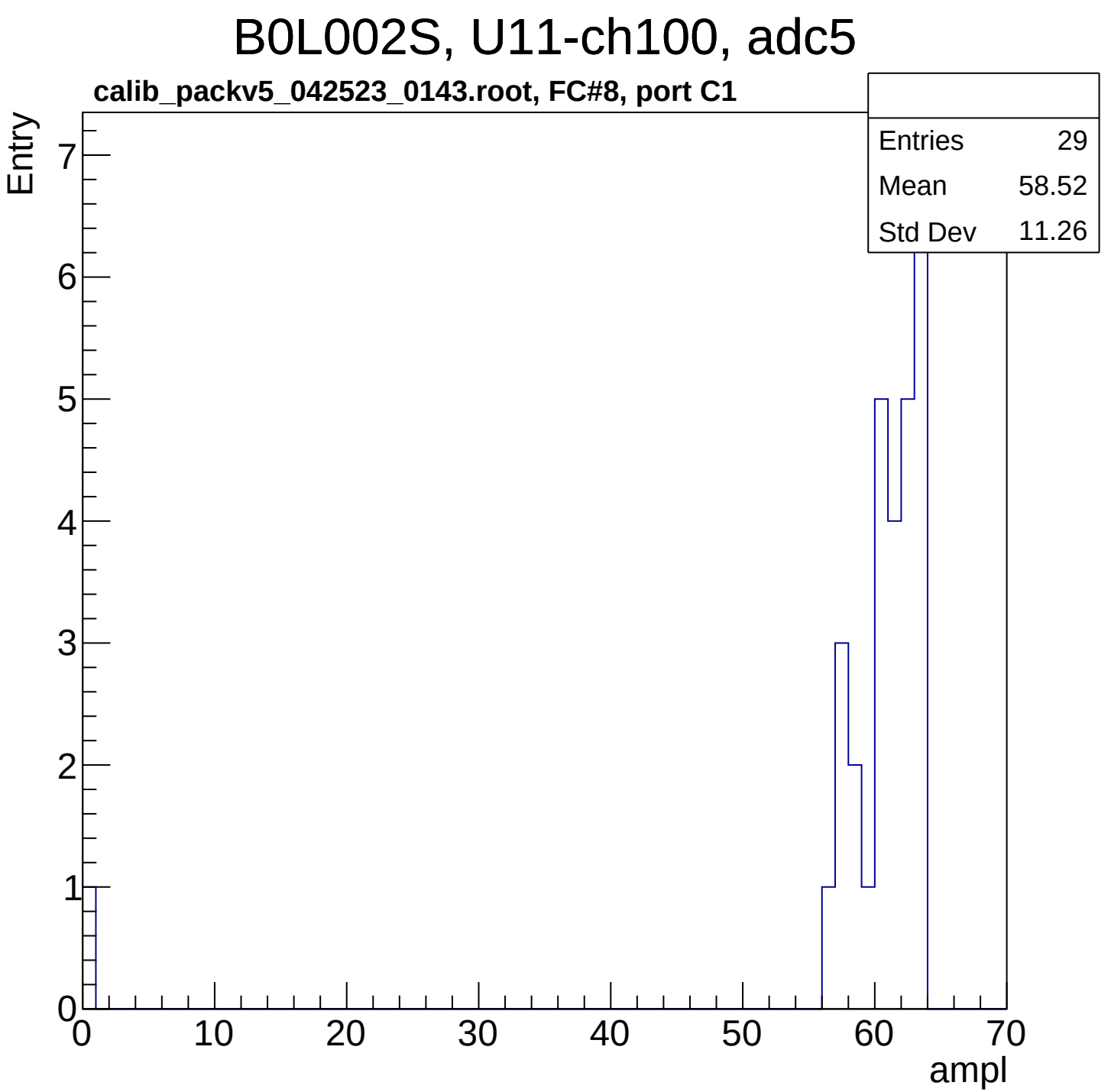
B0L002S, U11-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.52
Std Dev 11.26
Entry
ampl
0
1
2
3
4
5
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60
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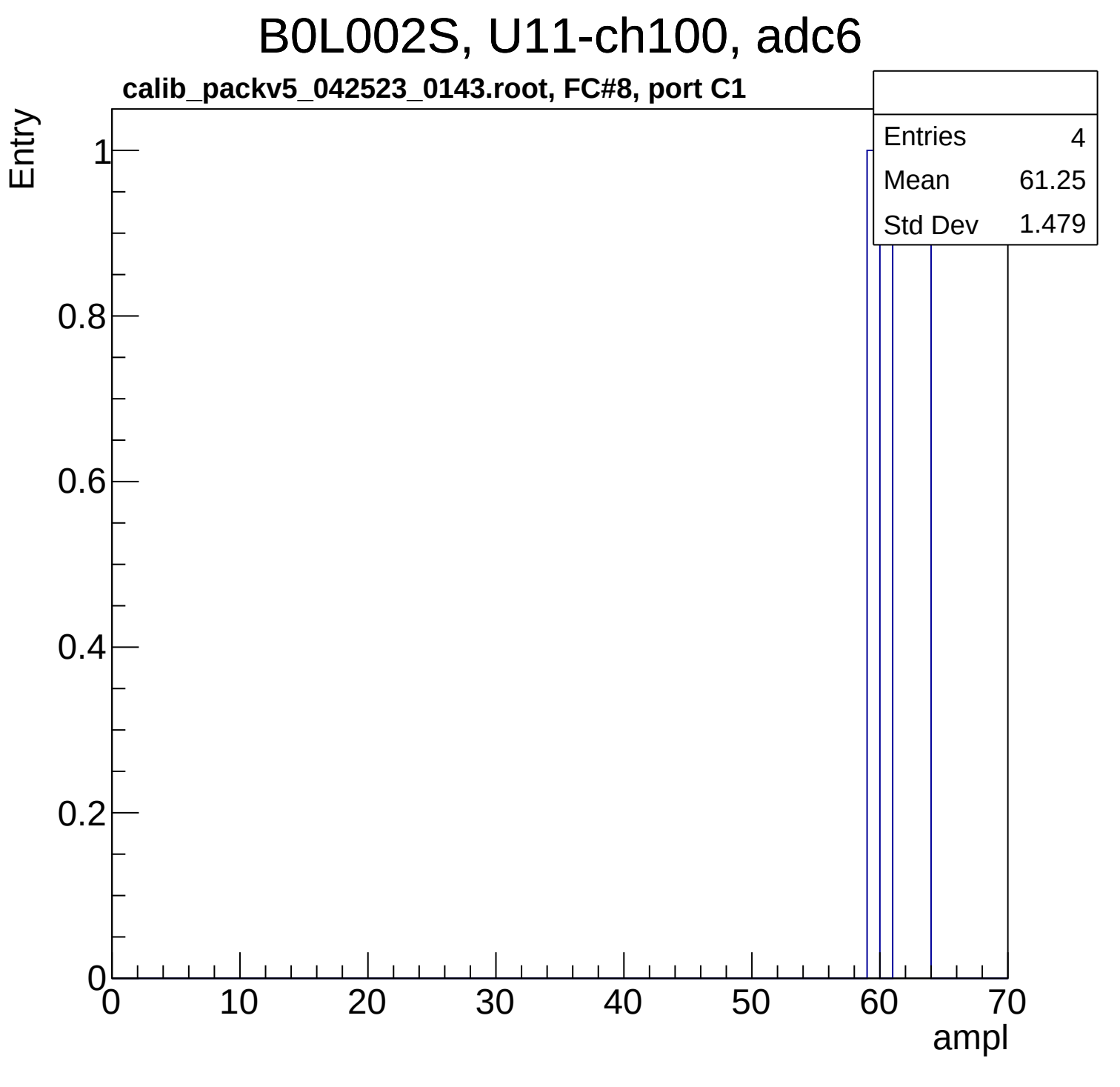
B0L002S, U11-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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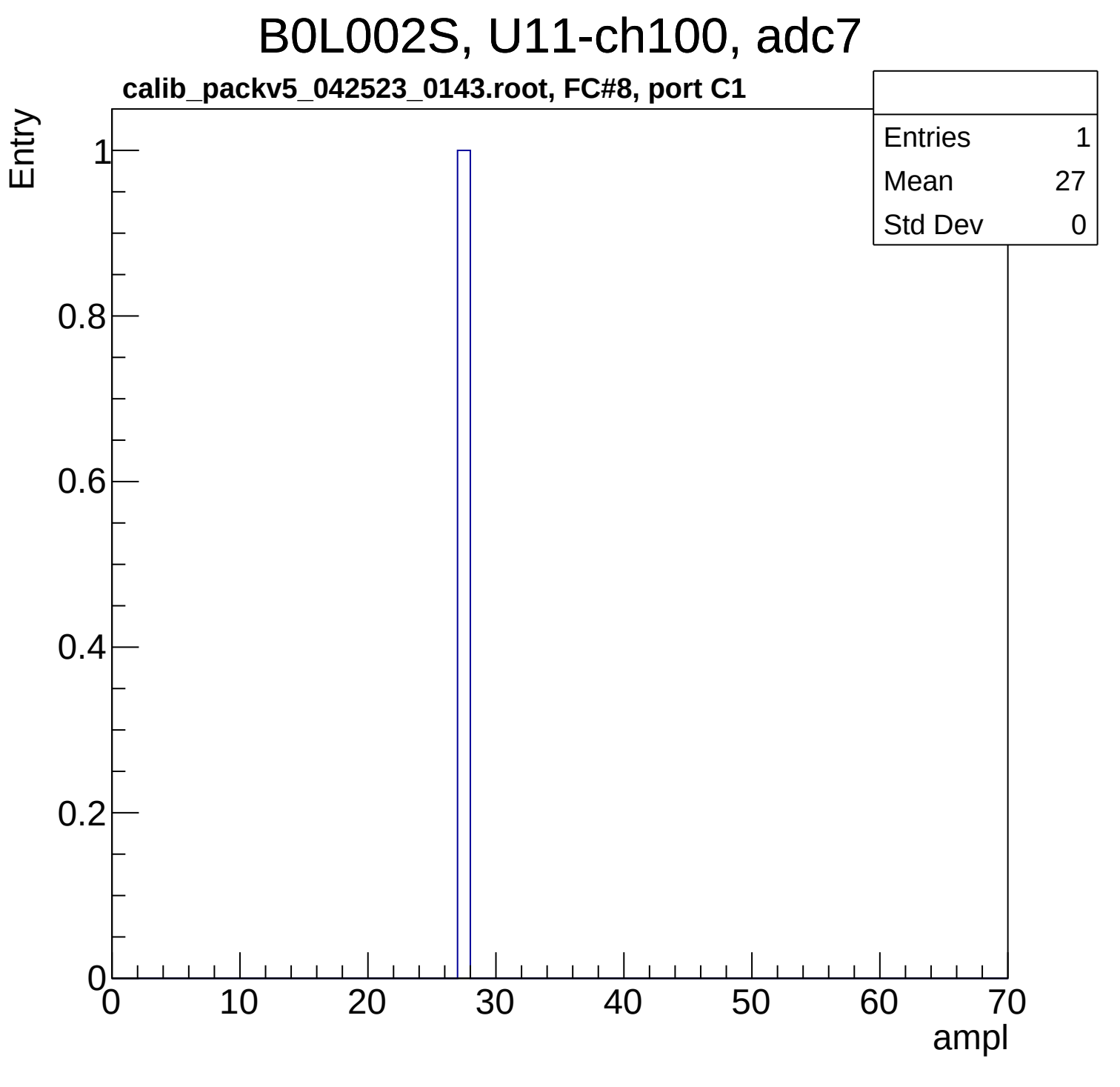
B0L002S, U11-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 27
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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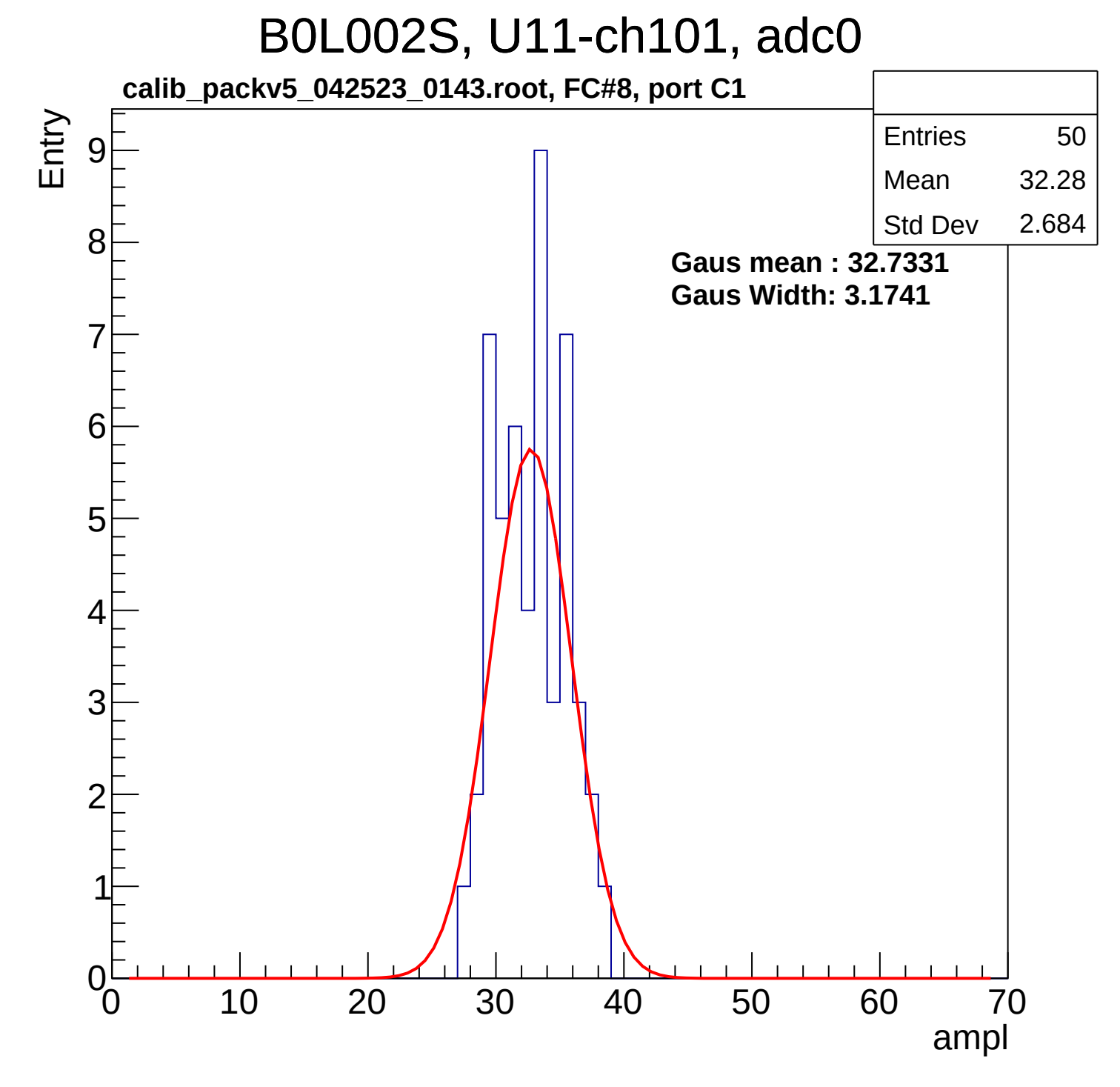
50
60
70
ampl


B0L002S, U11-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 32.28
Std Dev 2.684
Gaus mean : 32.7331
Gaus Width: 3.1741
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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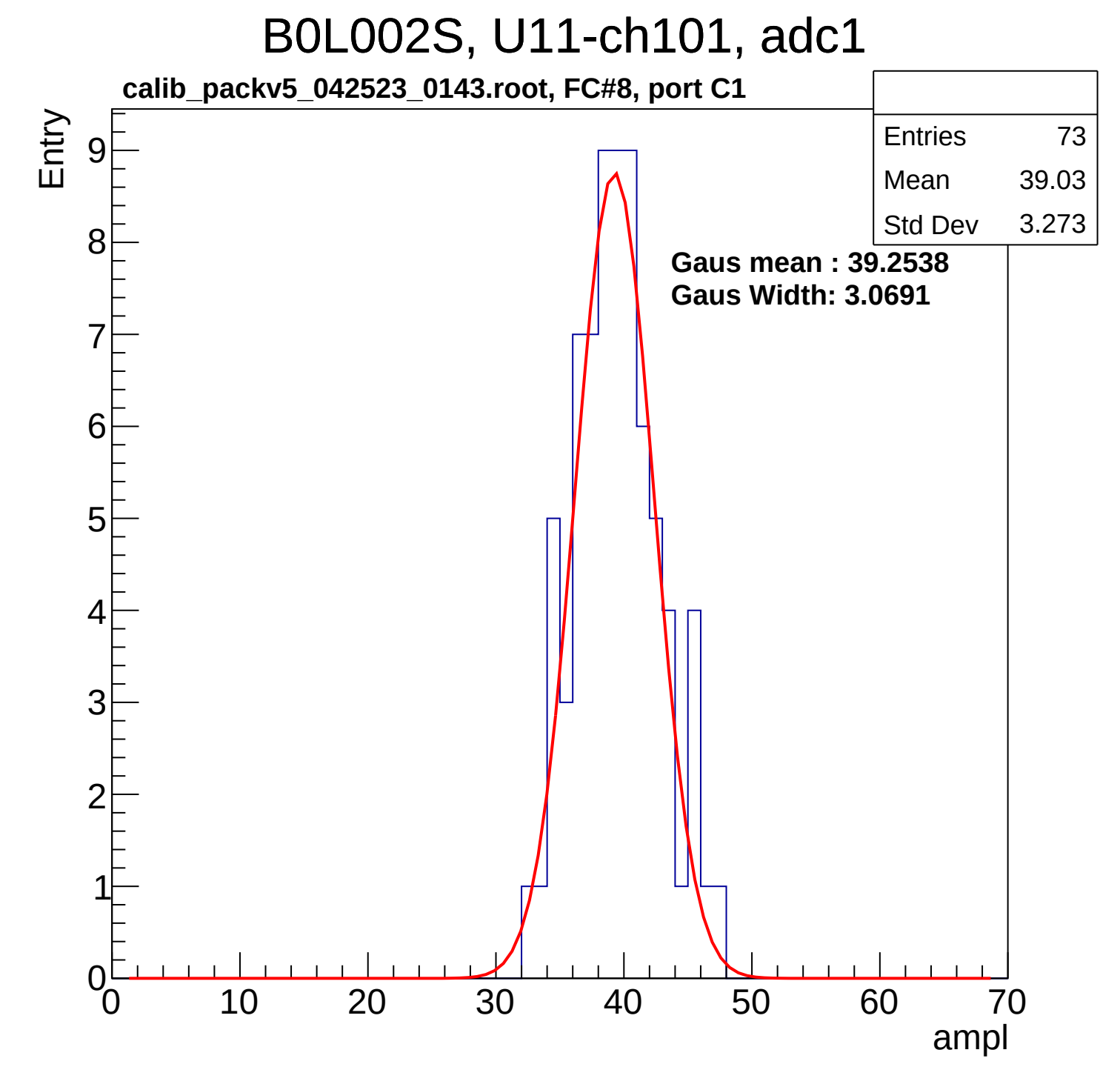
B0L002S, U11-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 39.03
Std Dev 3.273
Gaus mean : 39.2538
Gaus Width: 3.0691
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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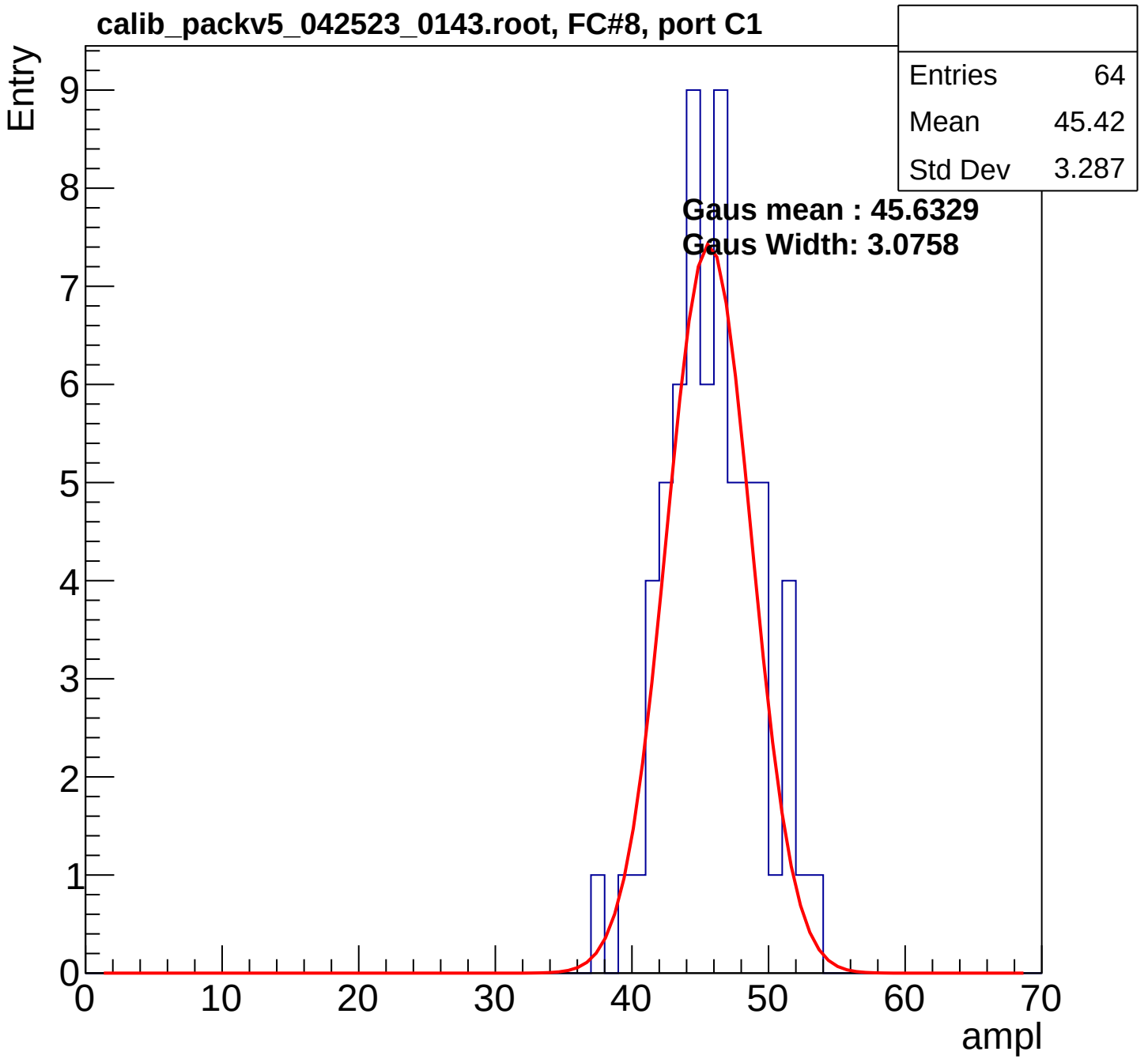
B0L002S, U11-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 45.42
Std Dev 3.287
Gaus mean : 45.6329
Gaus Width: 3.0758
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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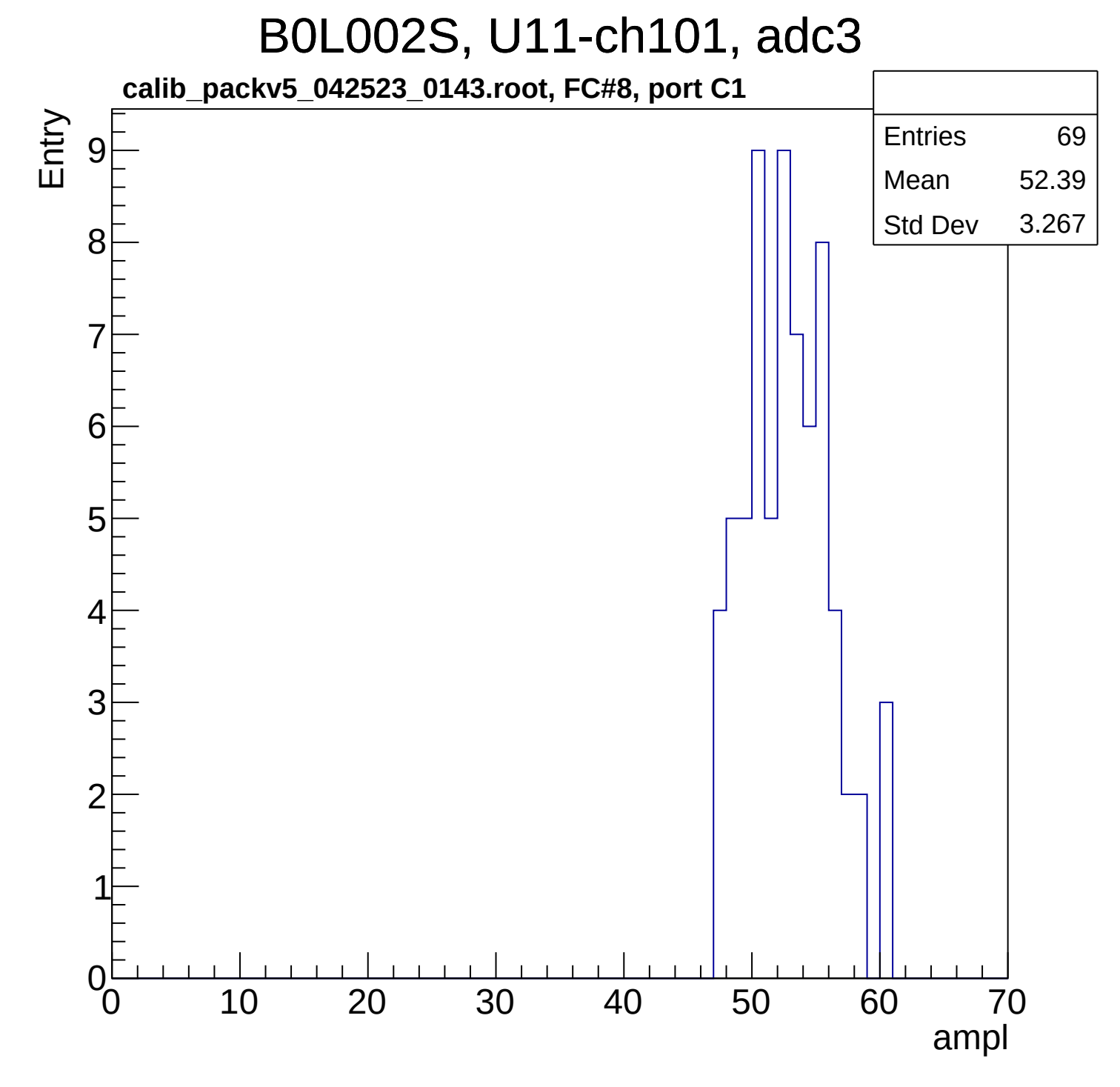
B0L002S, U11-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.39
Std Dev 3.267
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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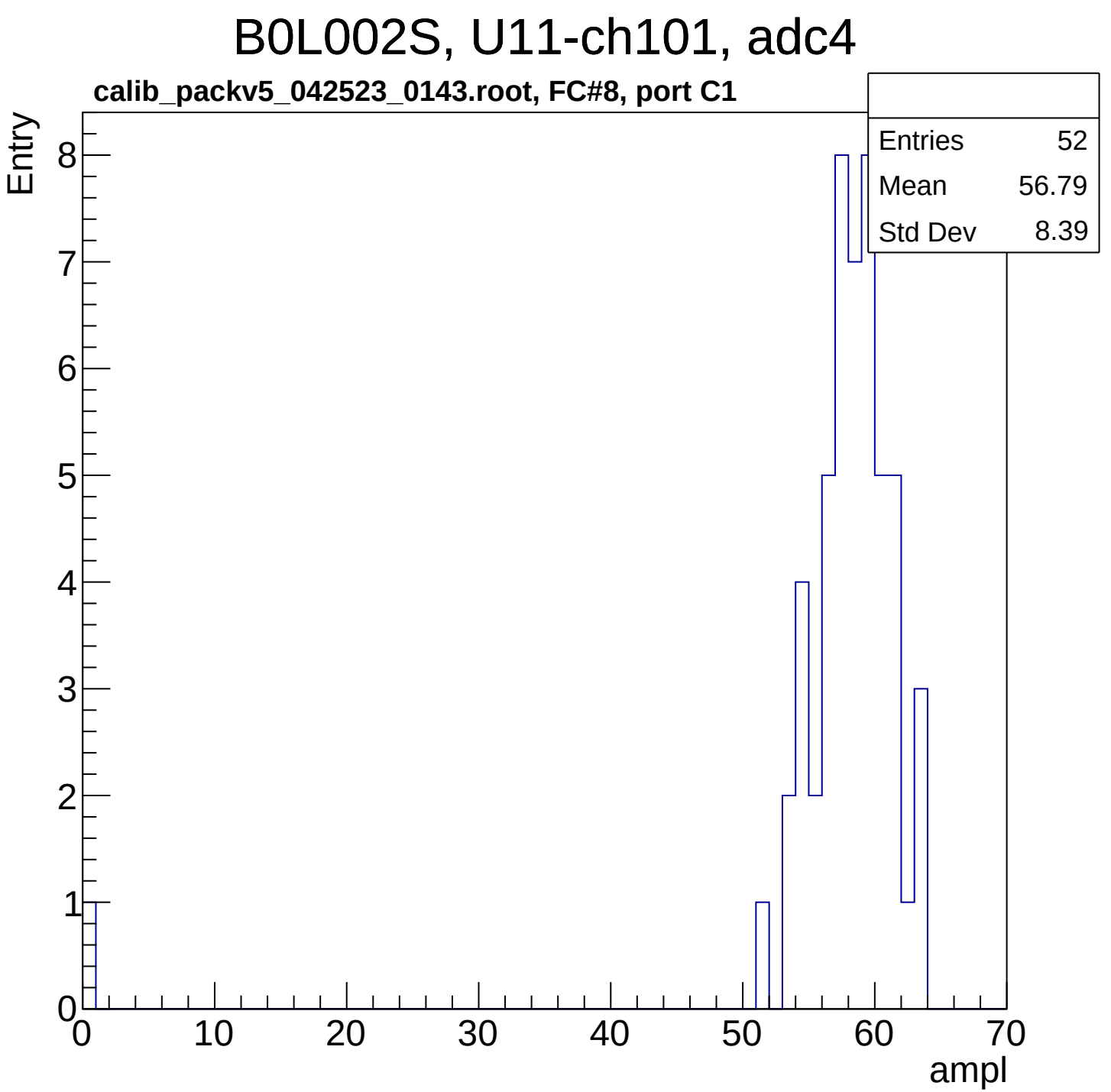
B0L002S, U11-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.79
Std Dev 8.39
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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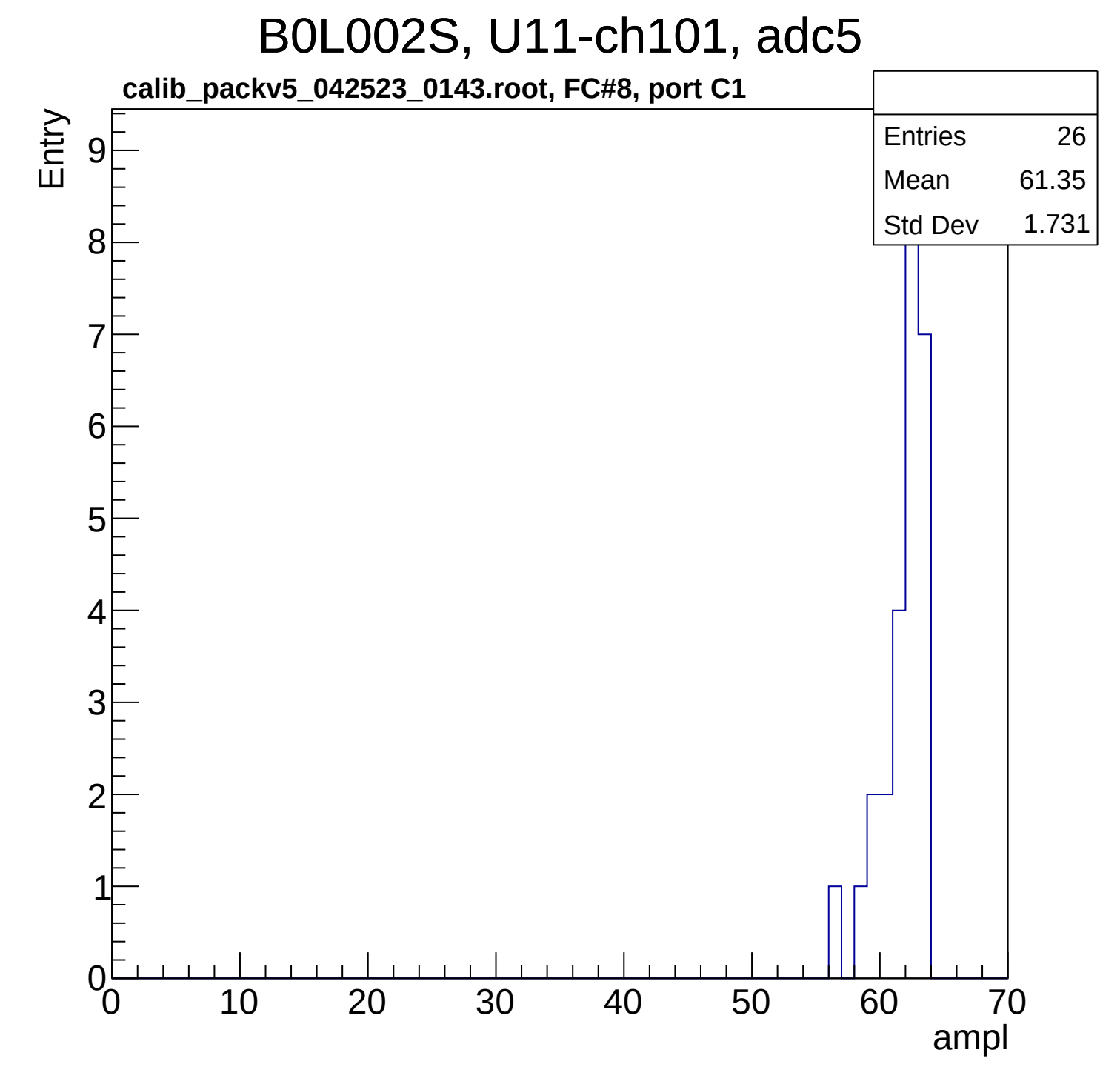
B0L002S, U11-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 61.35
Std Dev 1.731
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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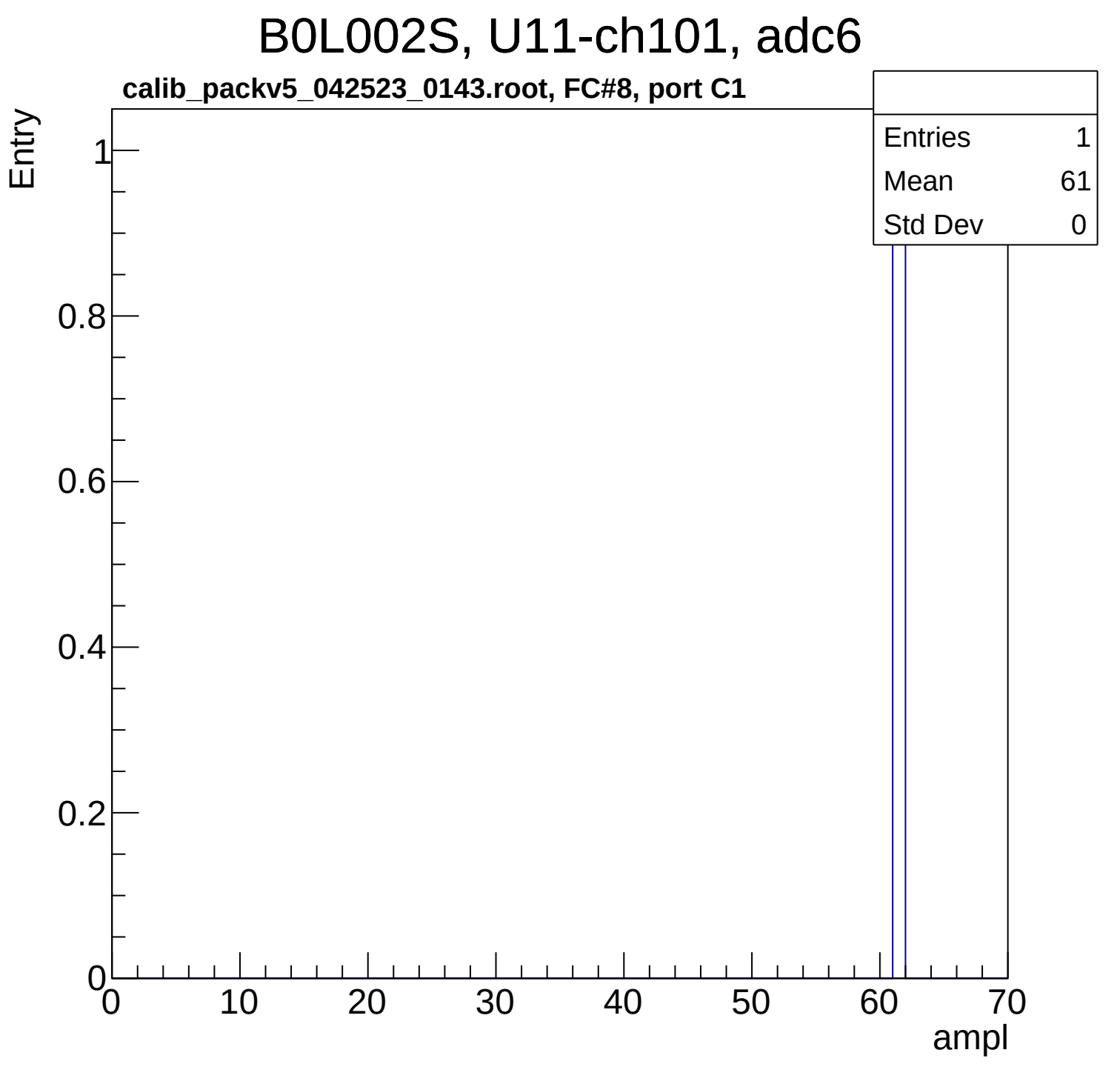
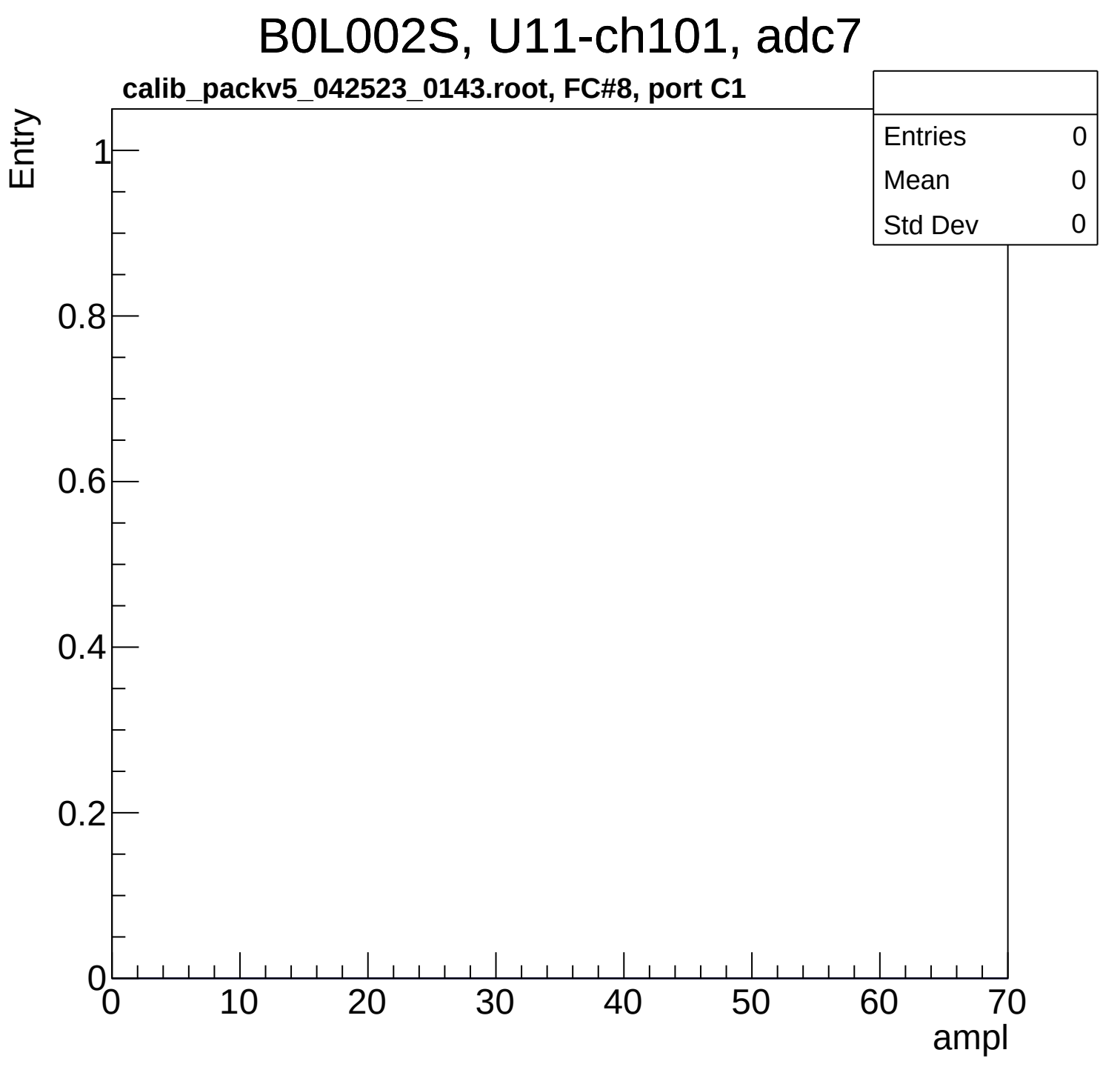

B0L002S, U11-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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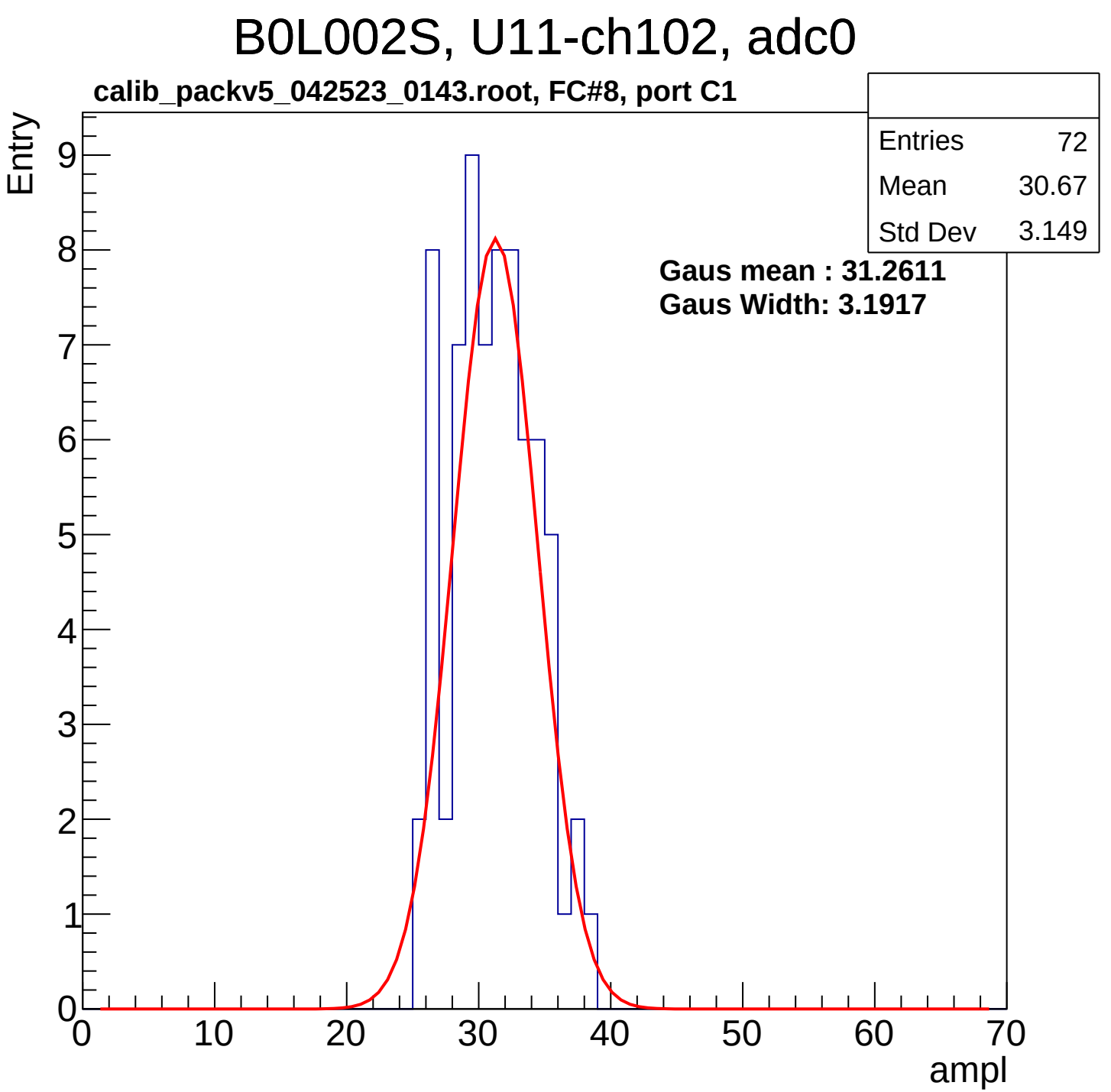
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.67
Std Dev 3.149
Gaus mean : 31.2611
Gaus Width: 3.1917
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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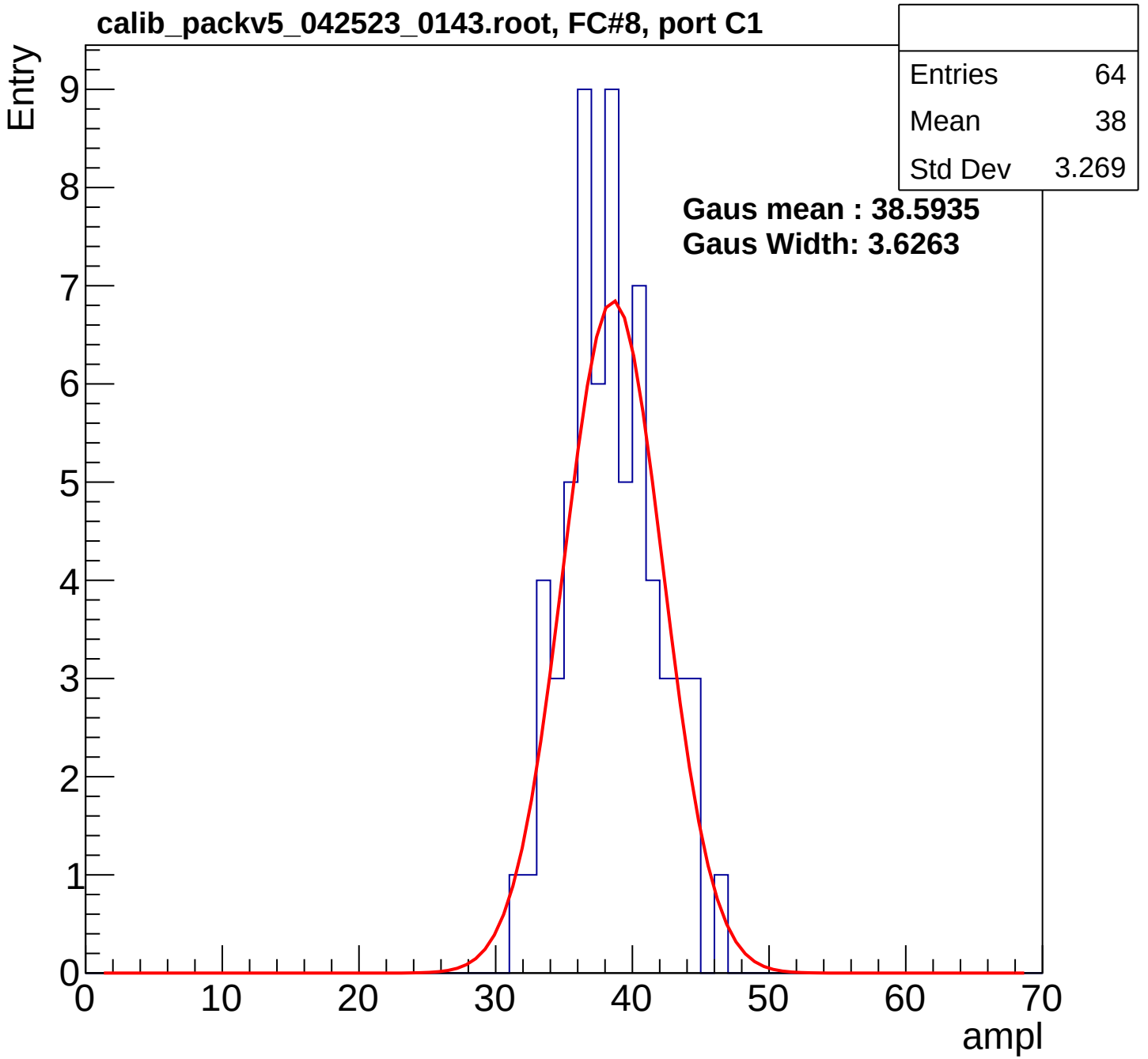
B0L002S, U11-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38
Std Dev 3.269
Gaus mean : 38.5935
Gaus Width: 3.6263
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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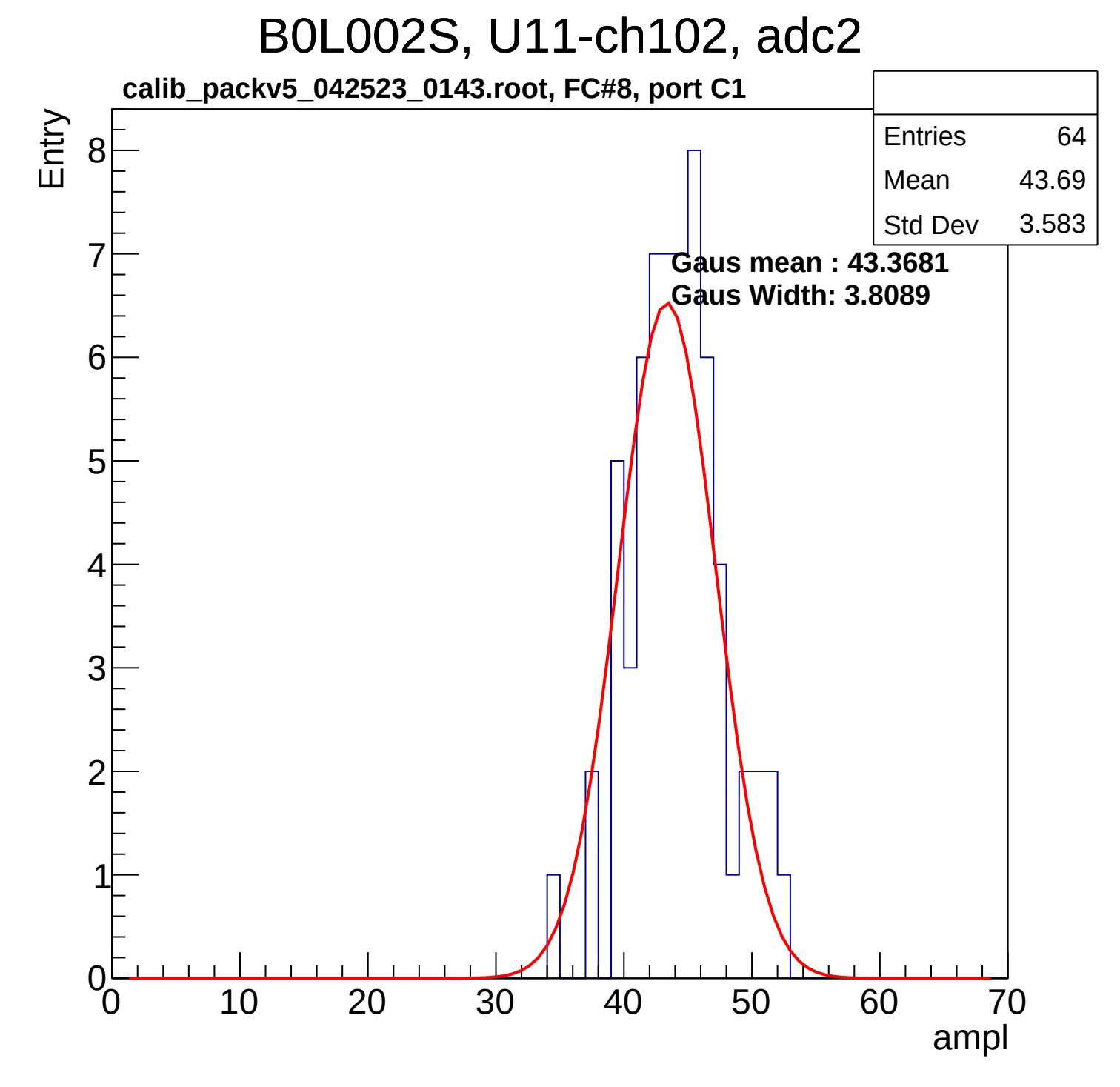
B0L002S, U11-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.69
Std Dev 3.583
Gaus mean : 43.3681
Gaus Width: 3.8089
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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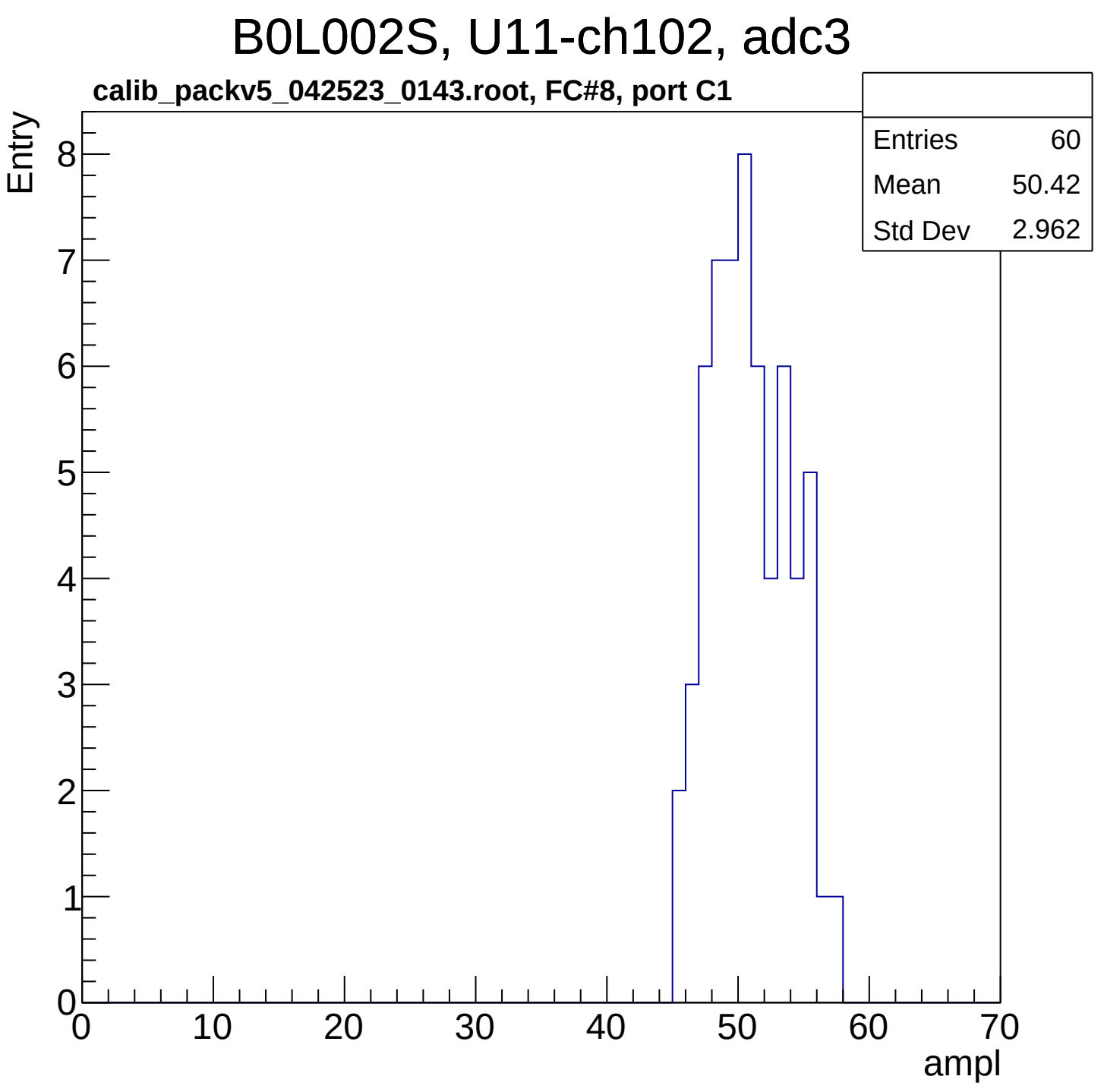
B0L002S, U11-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.42
Std Dev 2.962
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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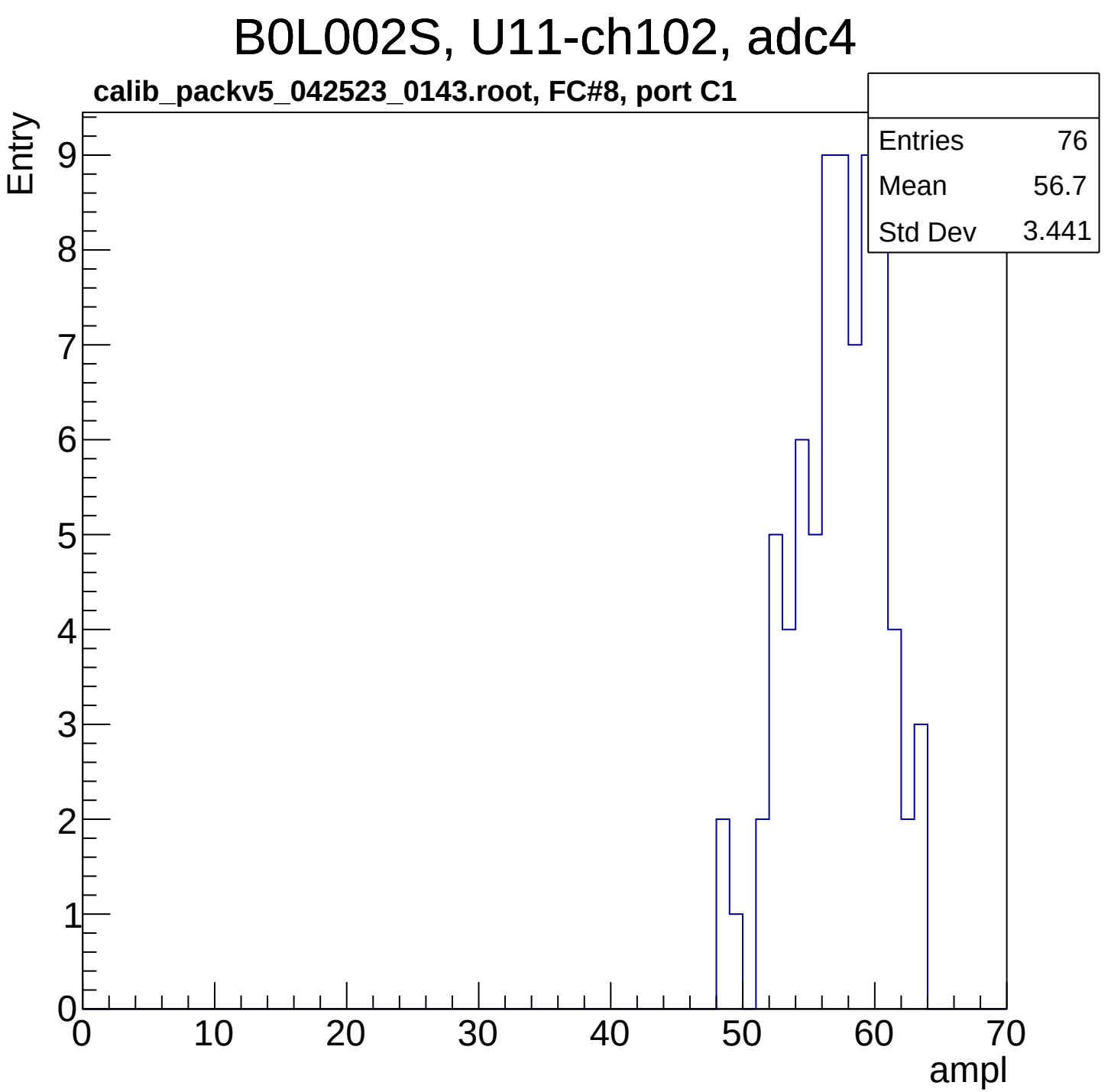
B0L002S, U11-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 56.7
Std Dev 3.441
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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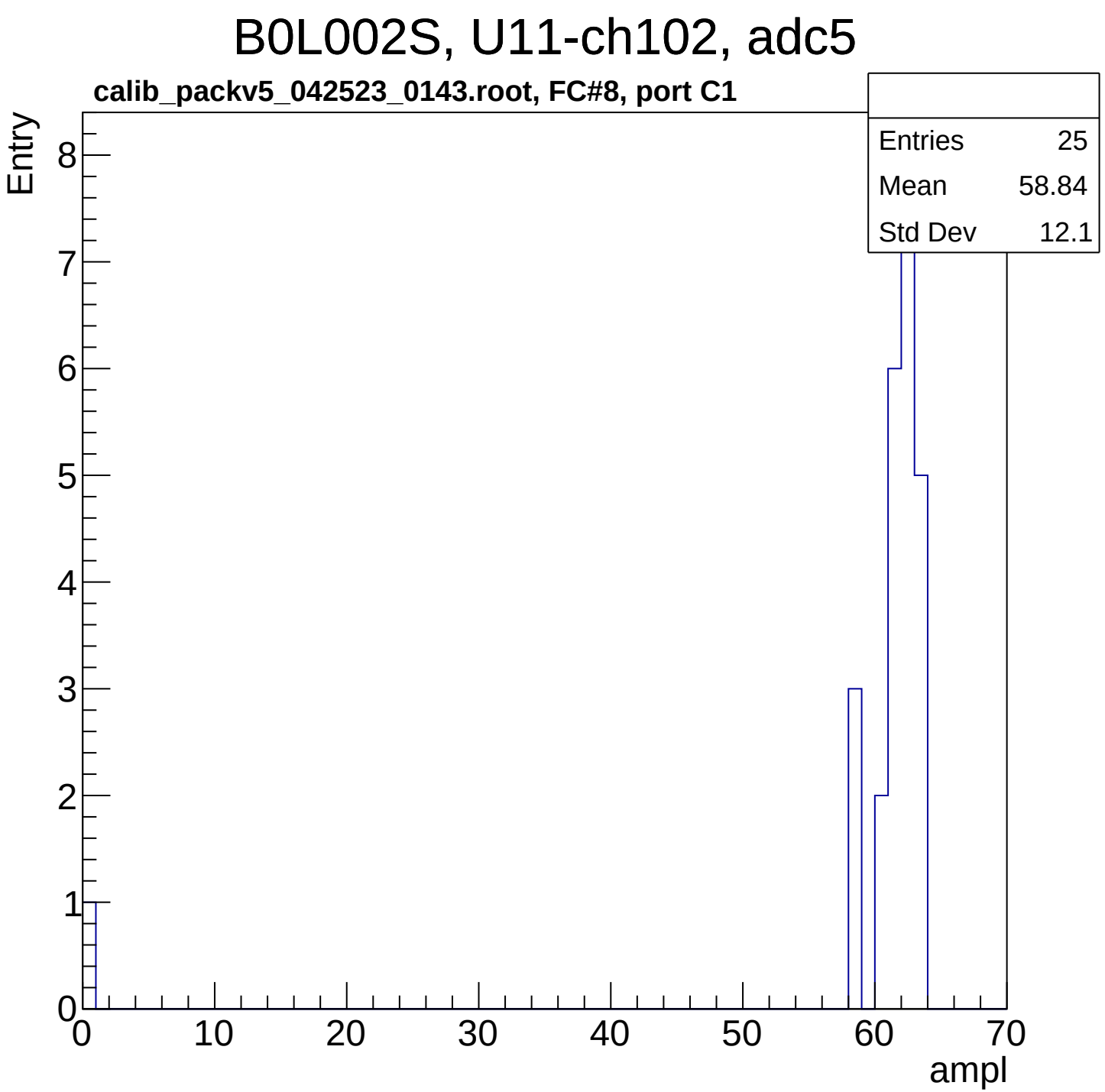
B0L002S, U11-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.84
Std Dev 12.1
Entry
ampl
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1
2
3
4
5
6
7
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0
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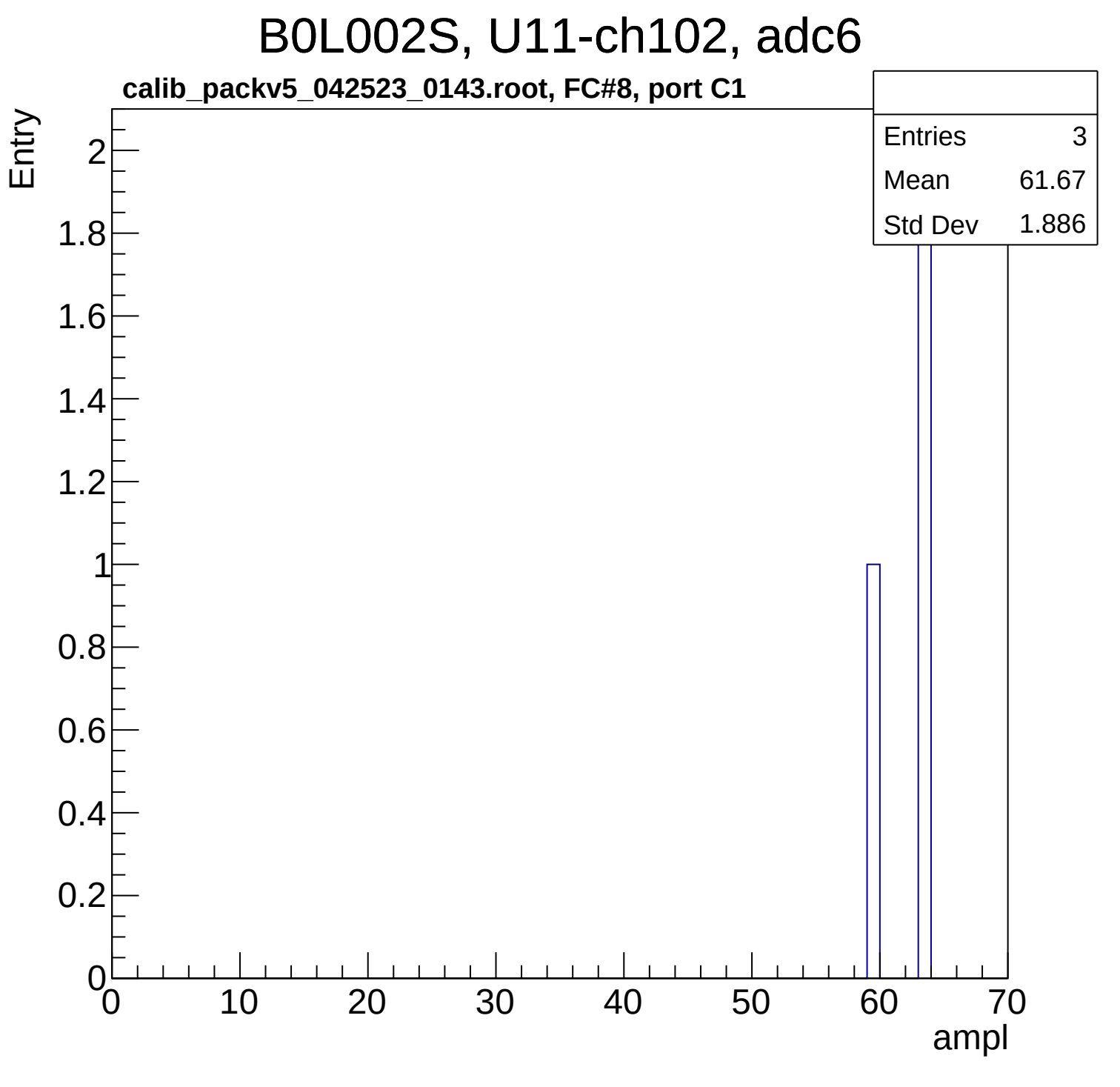
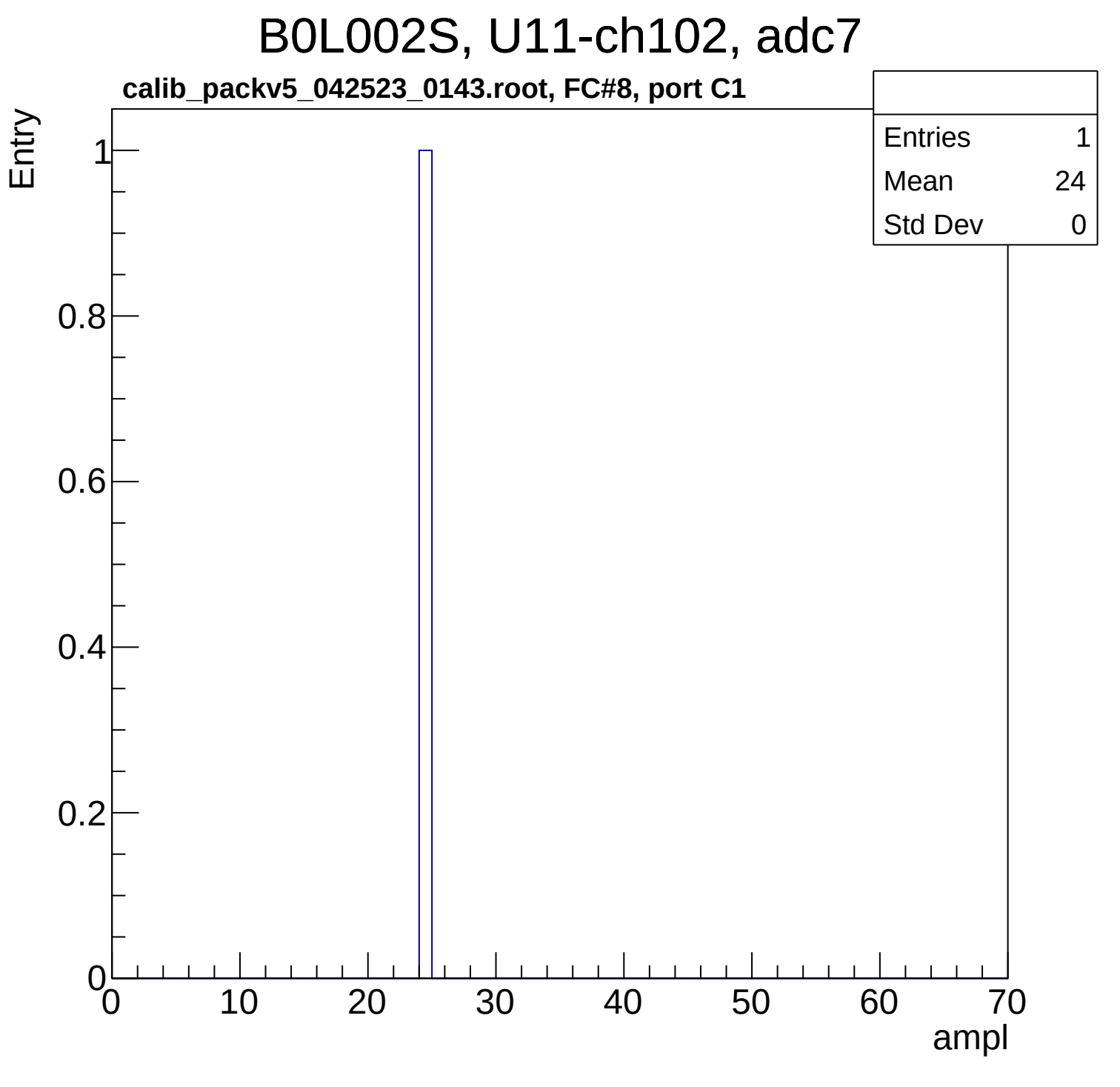

B0L002S, U11-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
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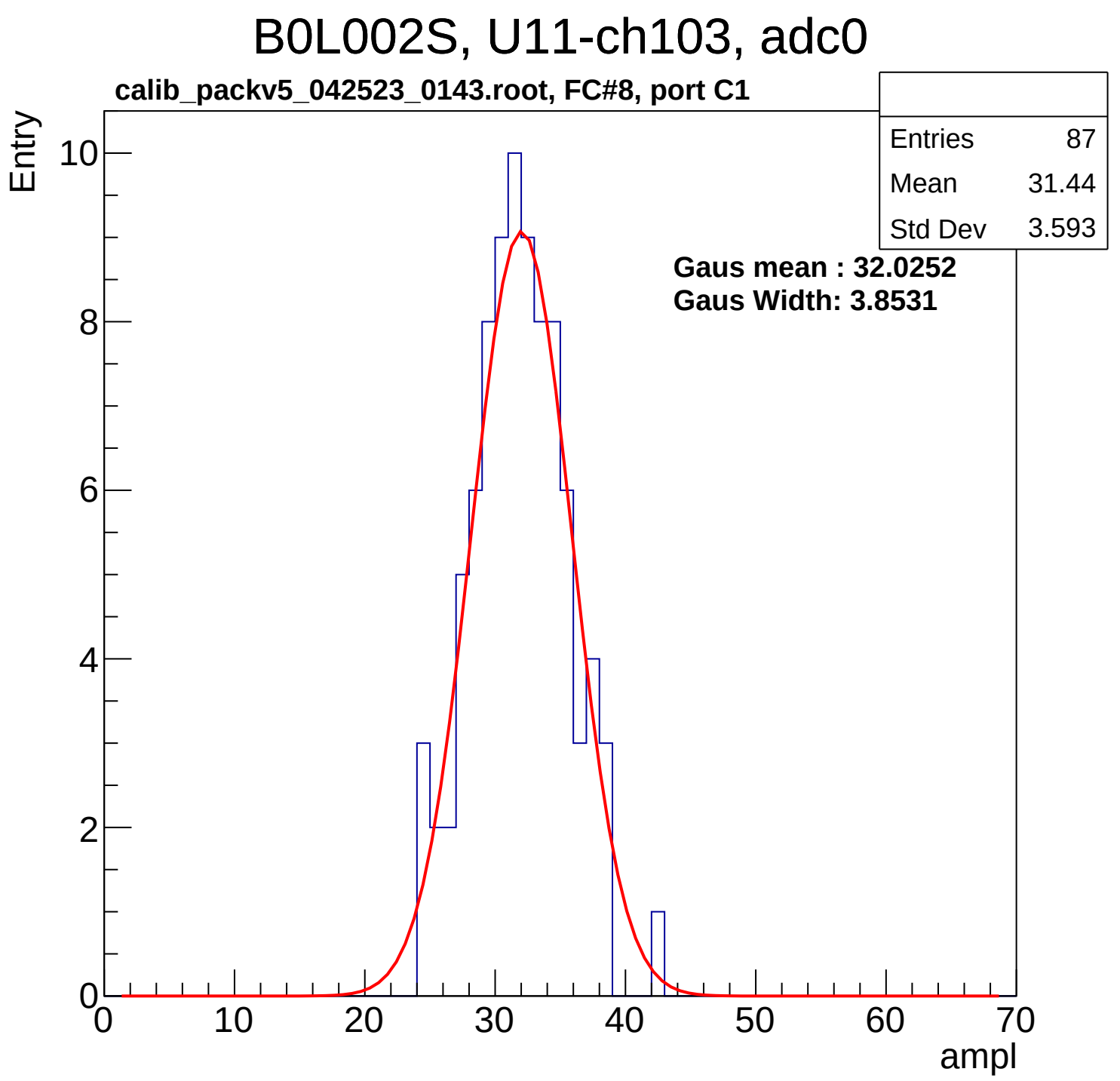
60
70
ampl


B0L002S, U11-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 31.44
Std Dev 3.593
Gaus mean : 32.0252
Gaus Width: 3.8531
Entry
ampl
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2
4
6
8
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0
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20
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40
50
60
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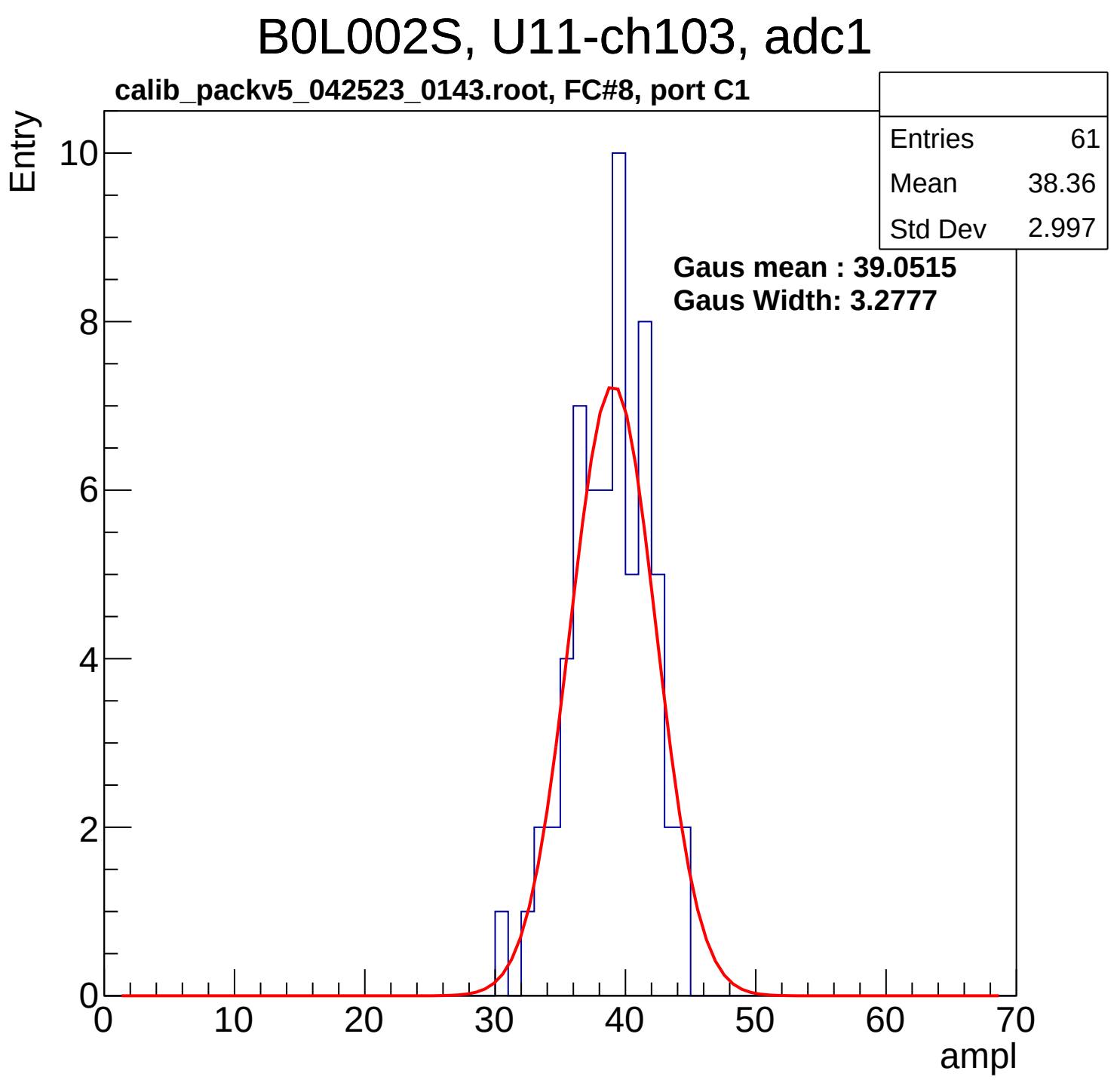
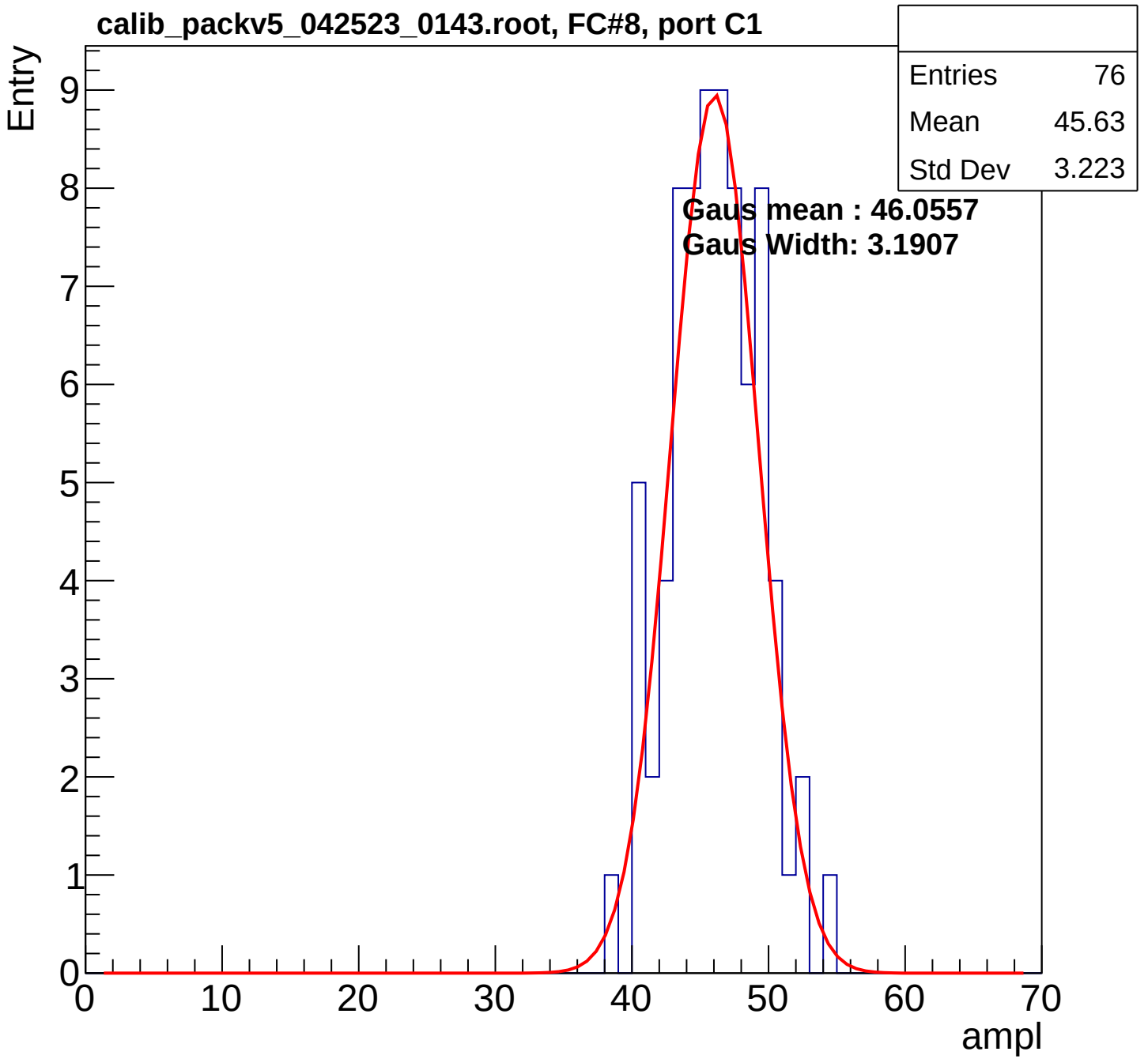

B0L002S, U11-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.36
Std Dev 2.997
Gaus mean : 39.0515
Gaus Width: 3.2777
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 45.63
Std Dev 3.223
Gaus mean : 46.0557
Gaus Width: 3.1907
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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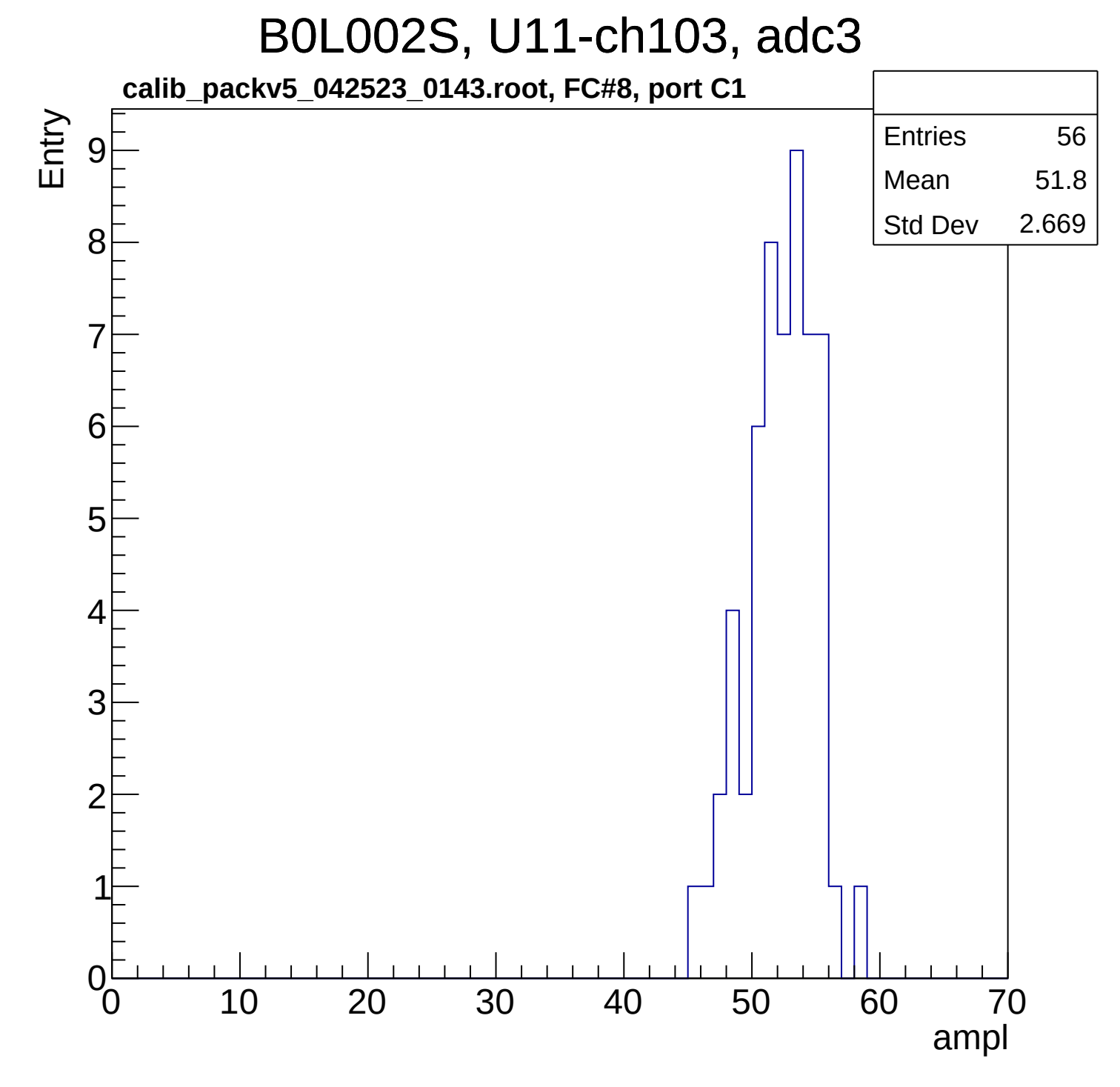
B0L002S, U11-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 51.8
Std Dev 2.669
Entry
ampl
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2
3
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5
6
7
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0
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60
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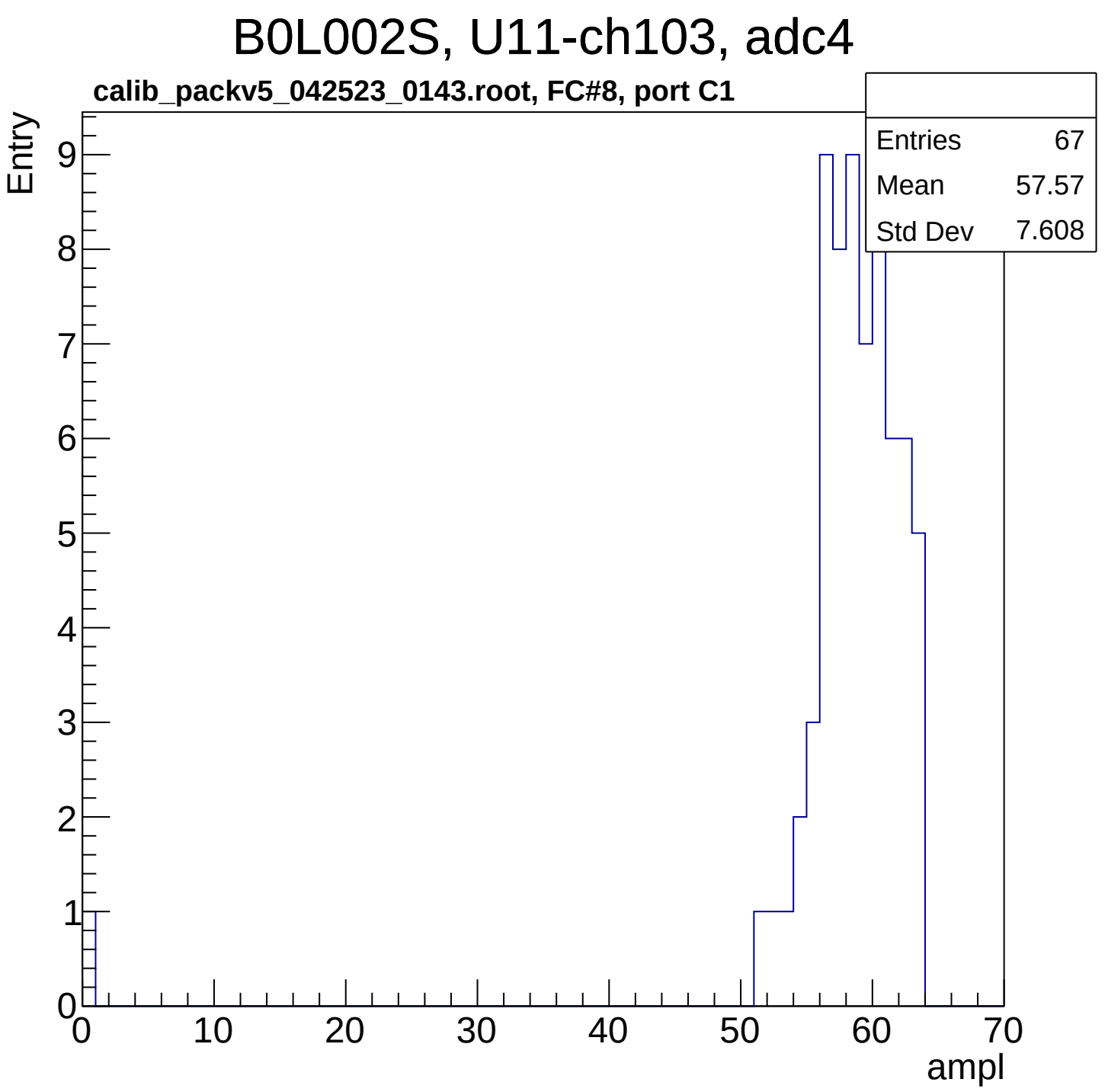
B0L002S, U11-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 57.57
Std Dev 7.608
Entry
ampl
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3
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5
6
7
8
9
0
10
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60
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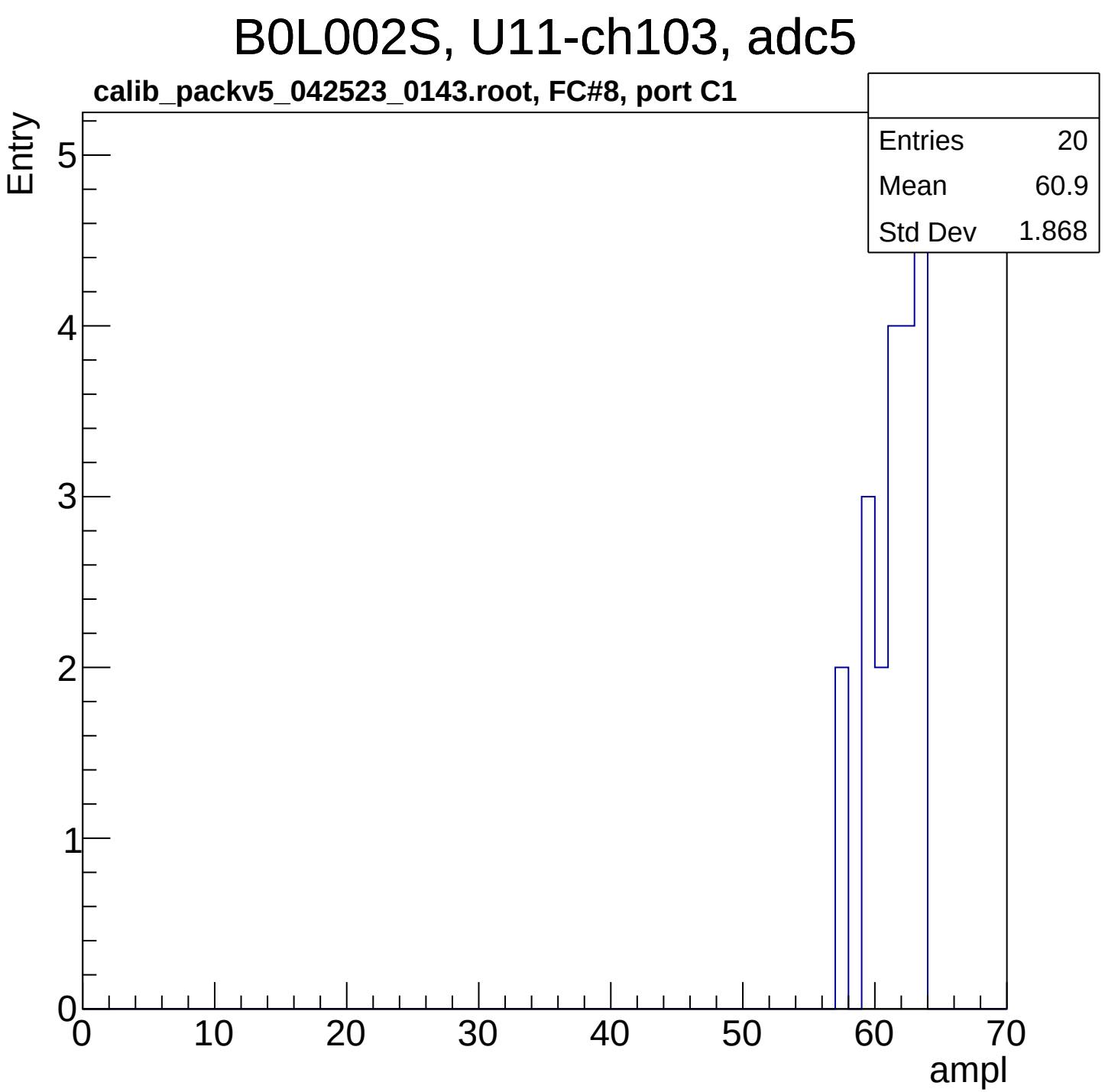
B0L002S, U11-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 60.9
Std Dev 1.868
Entry
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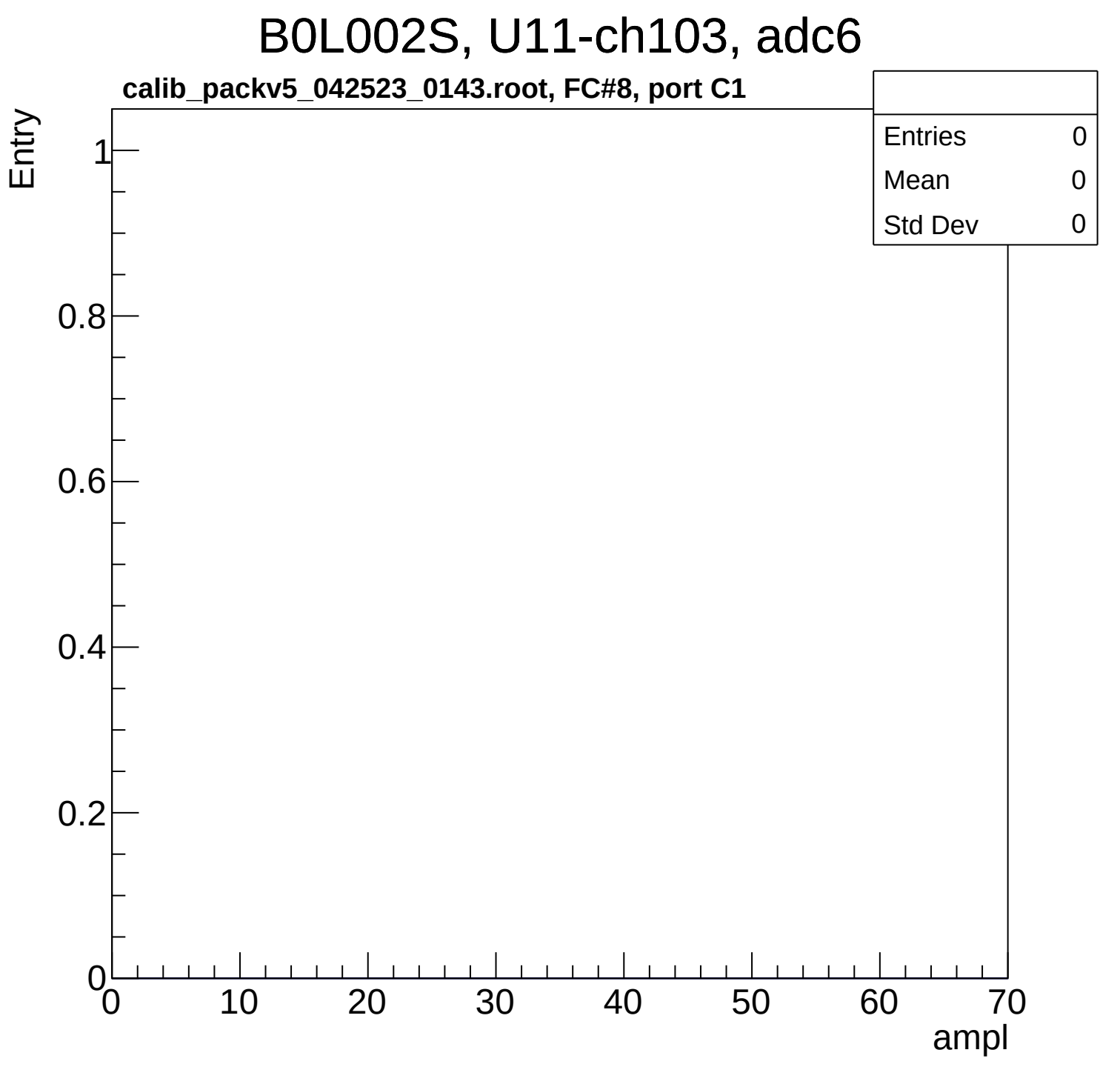
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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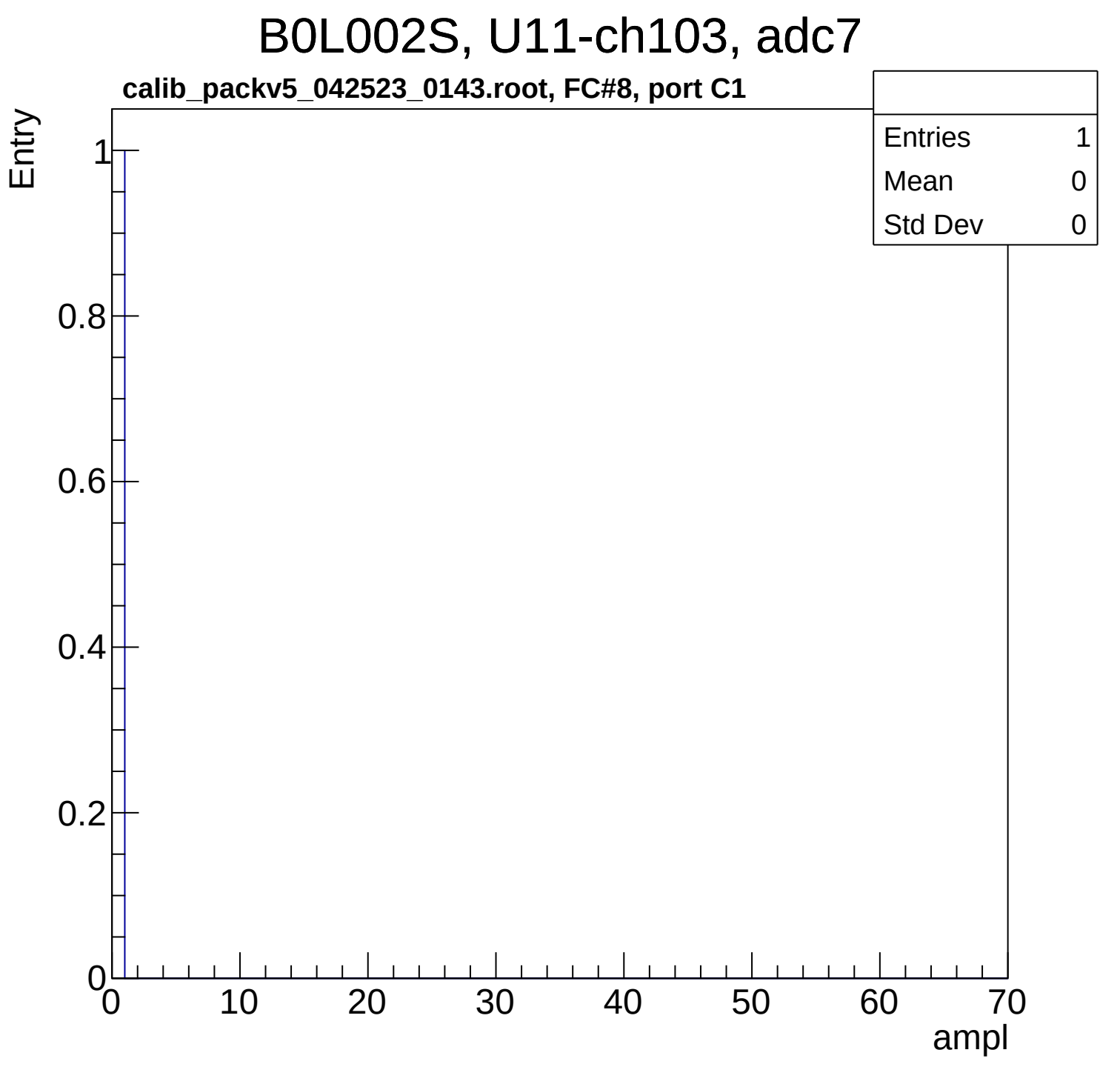
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
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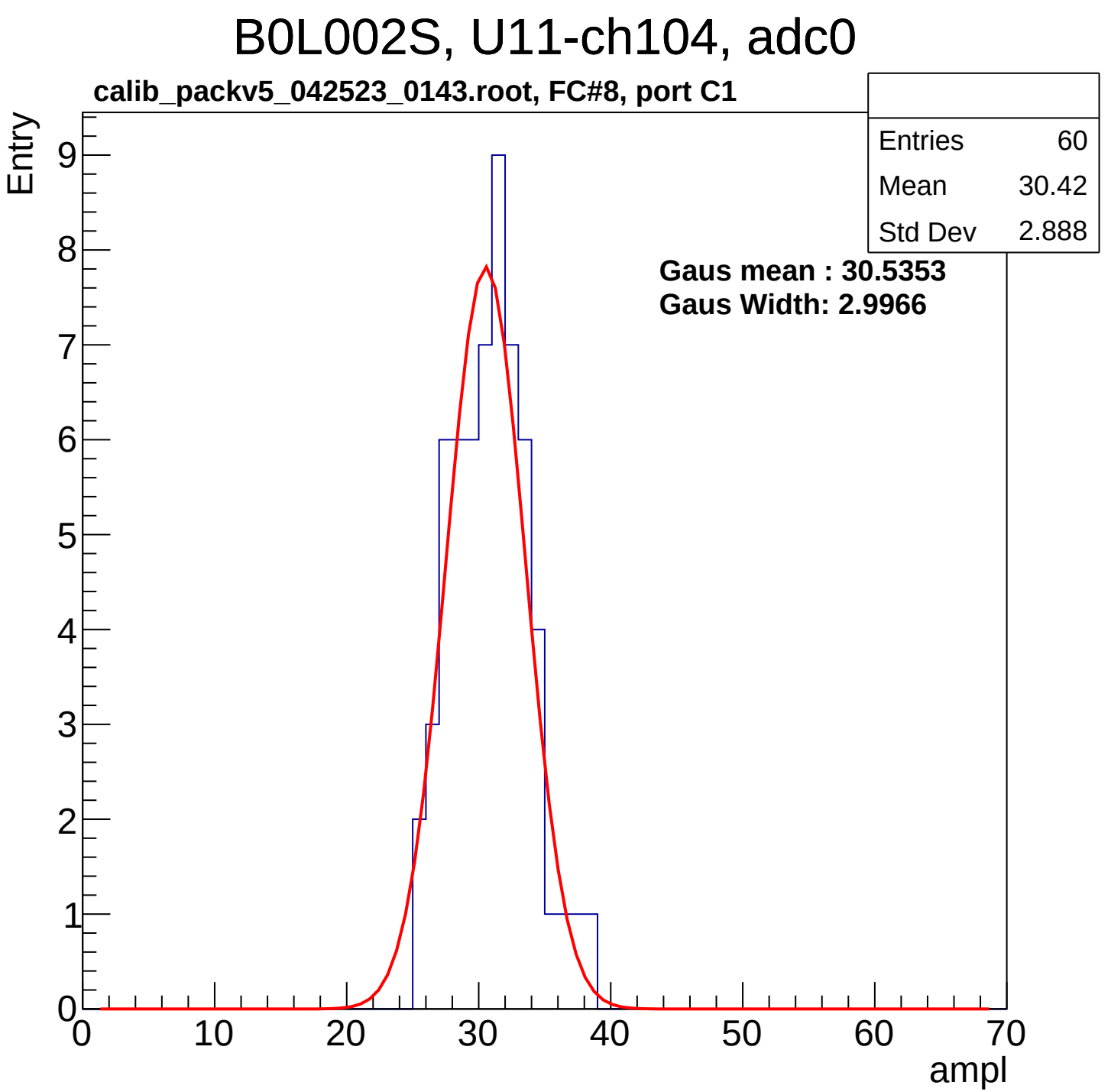
40
50
60
70
ampl


B0L002S, U11-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 30.42
Std Dev 2.888
Gaus mean : 30.5353
Gaus Width: 2.9966
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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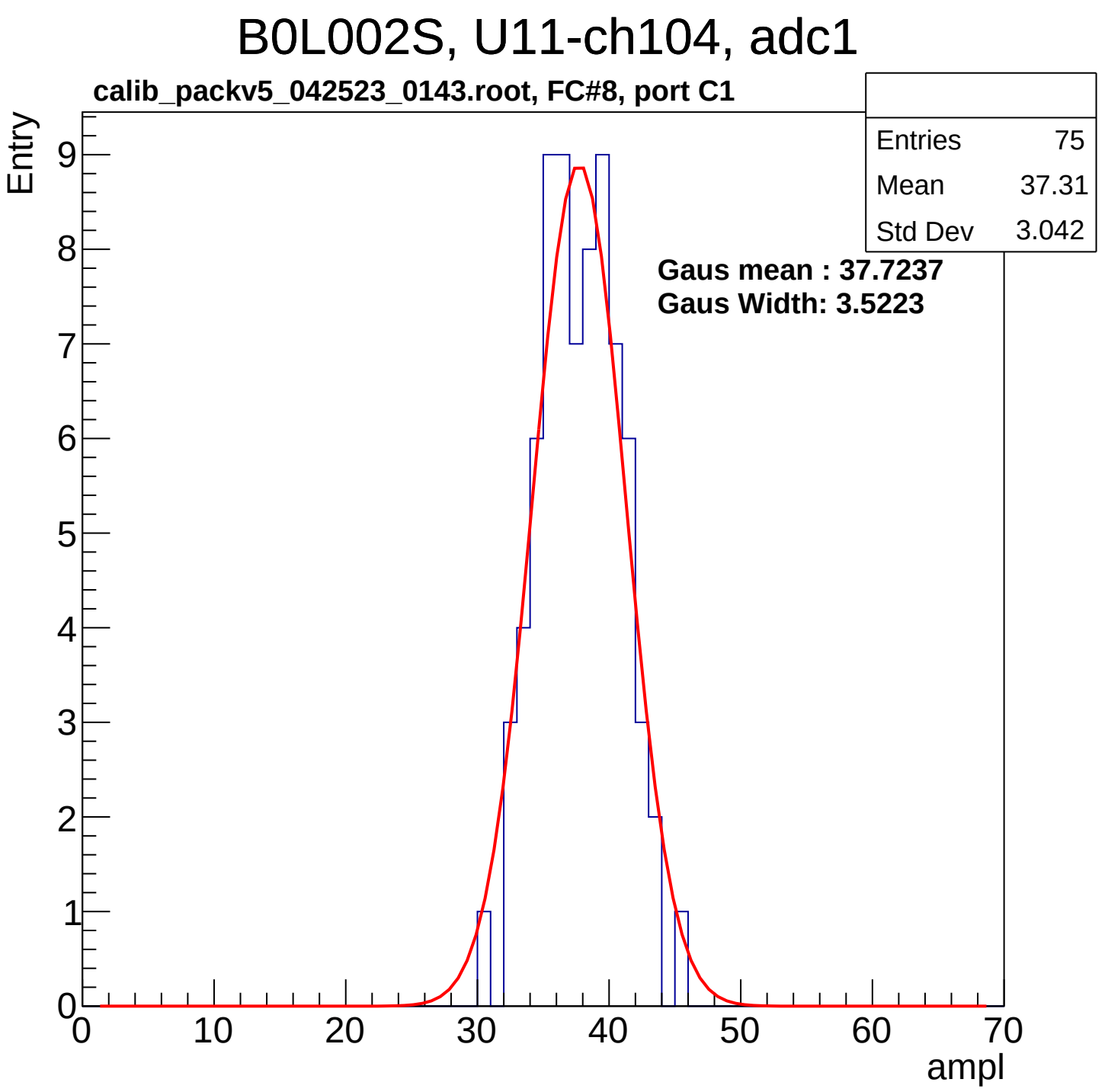
B0L002S, U11-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.31
Std Dev 3.042
Gaus mean : 37.7237
Gaus Width: 3.5223
Entry
ampl
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2
3
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5
6
7
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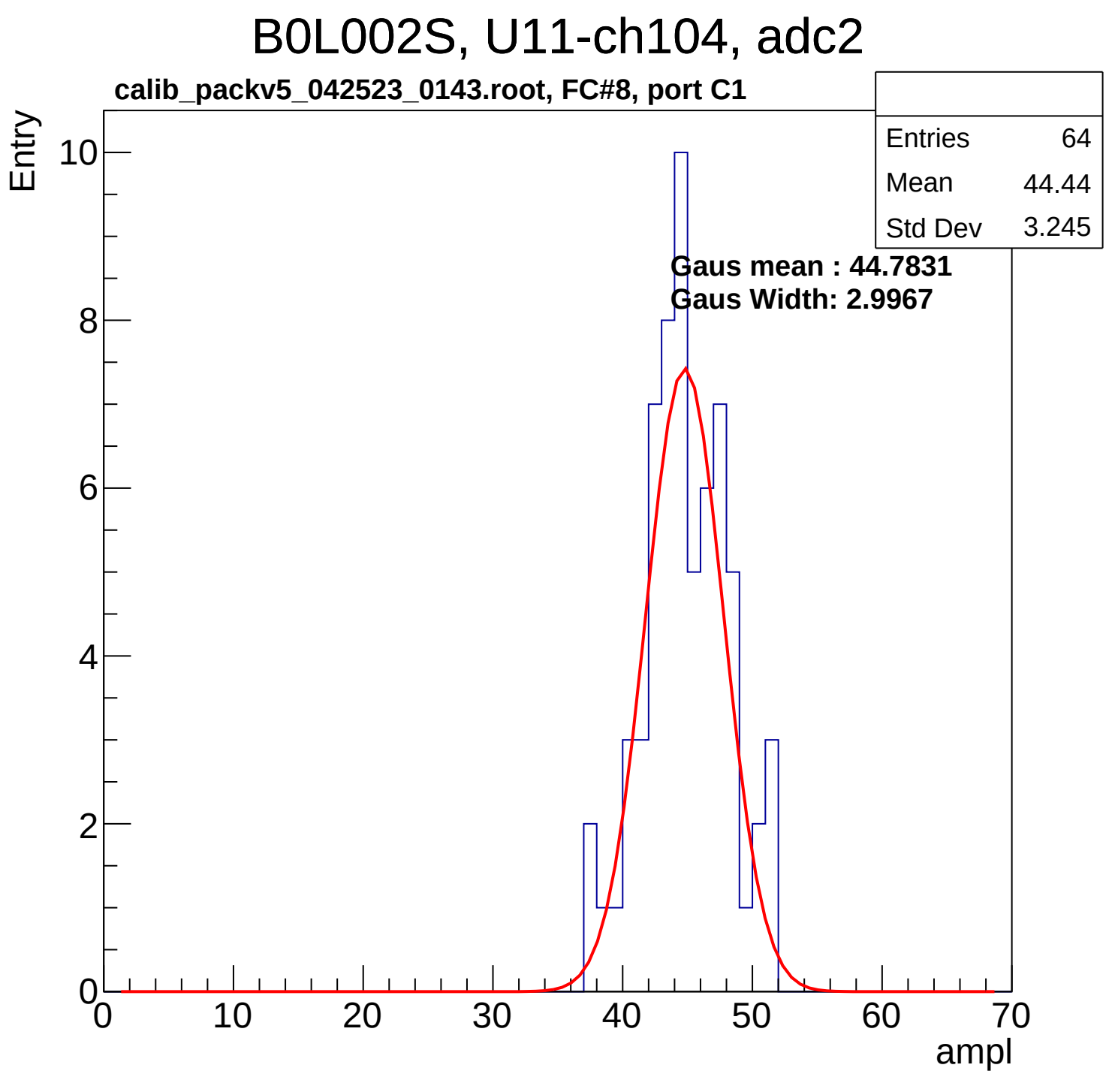
B0L002S, U11-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.44
Std Dev 3.245
Gaus mean : 44.7831
Gaus Width: 2.9967
Entry
ampl
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4
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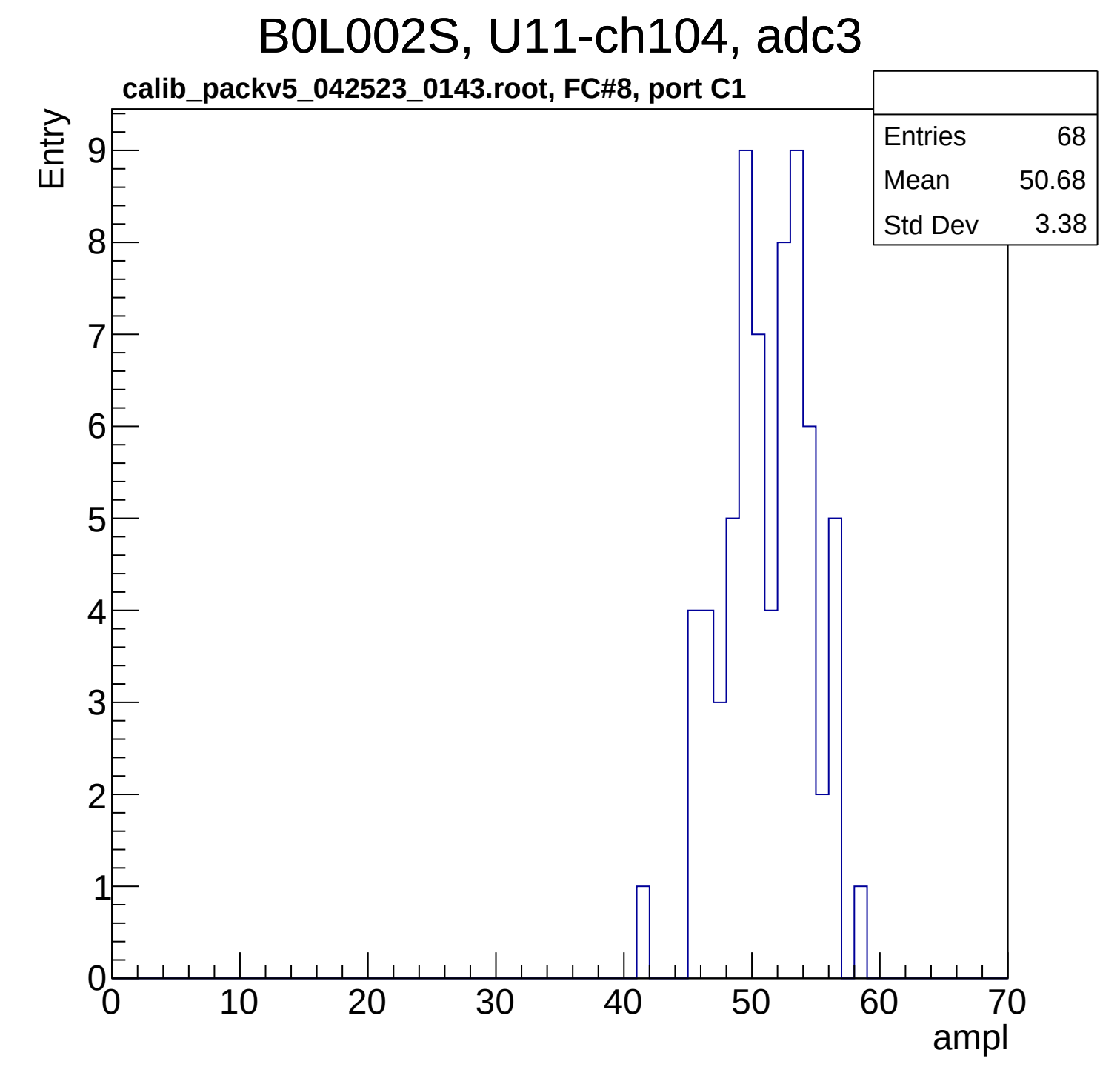
B0L002S, U11-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.68
Std Dev 3.38
Entry
ampl
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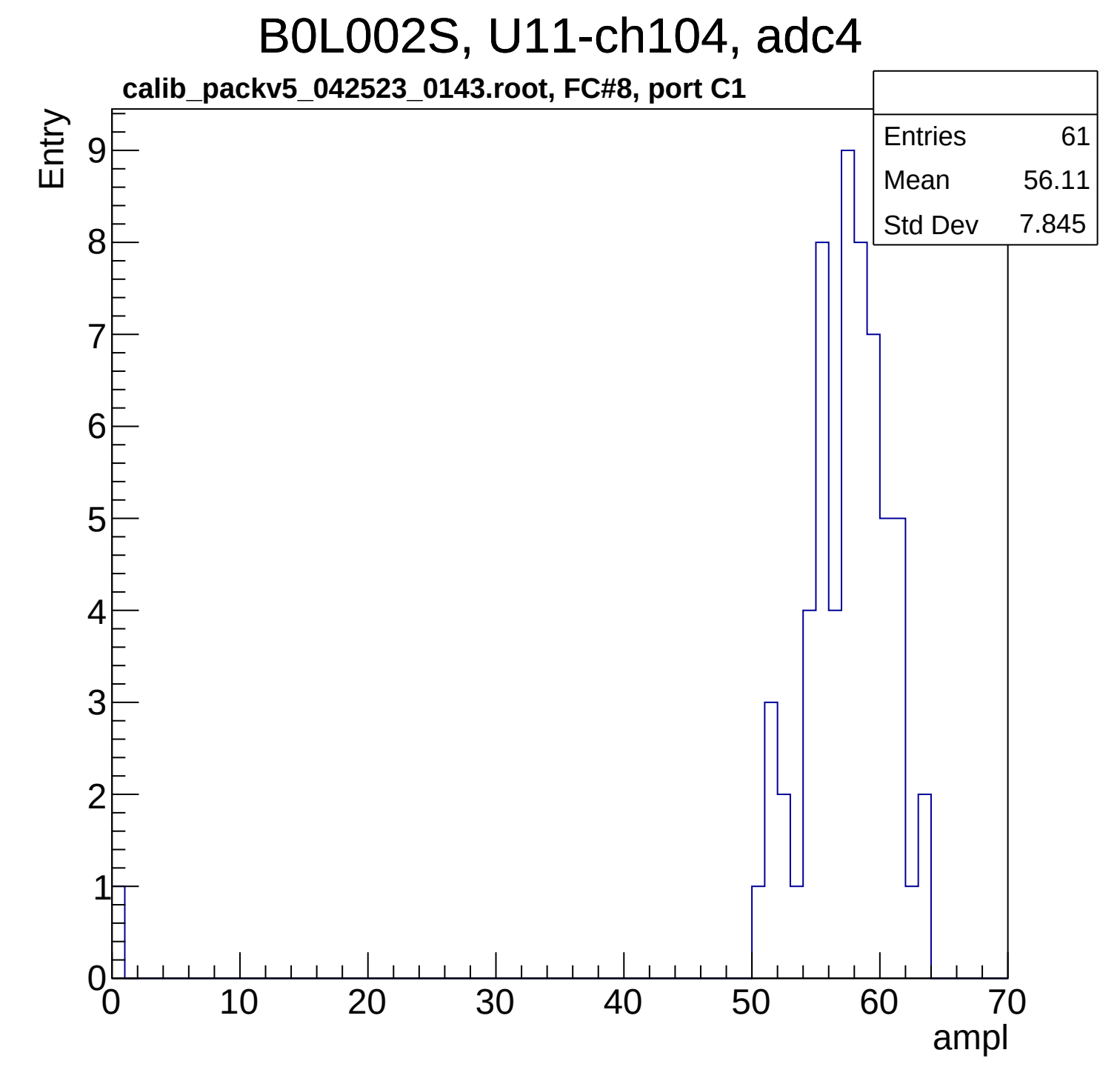
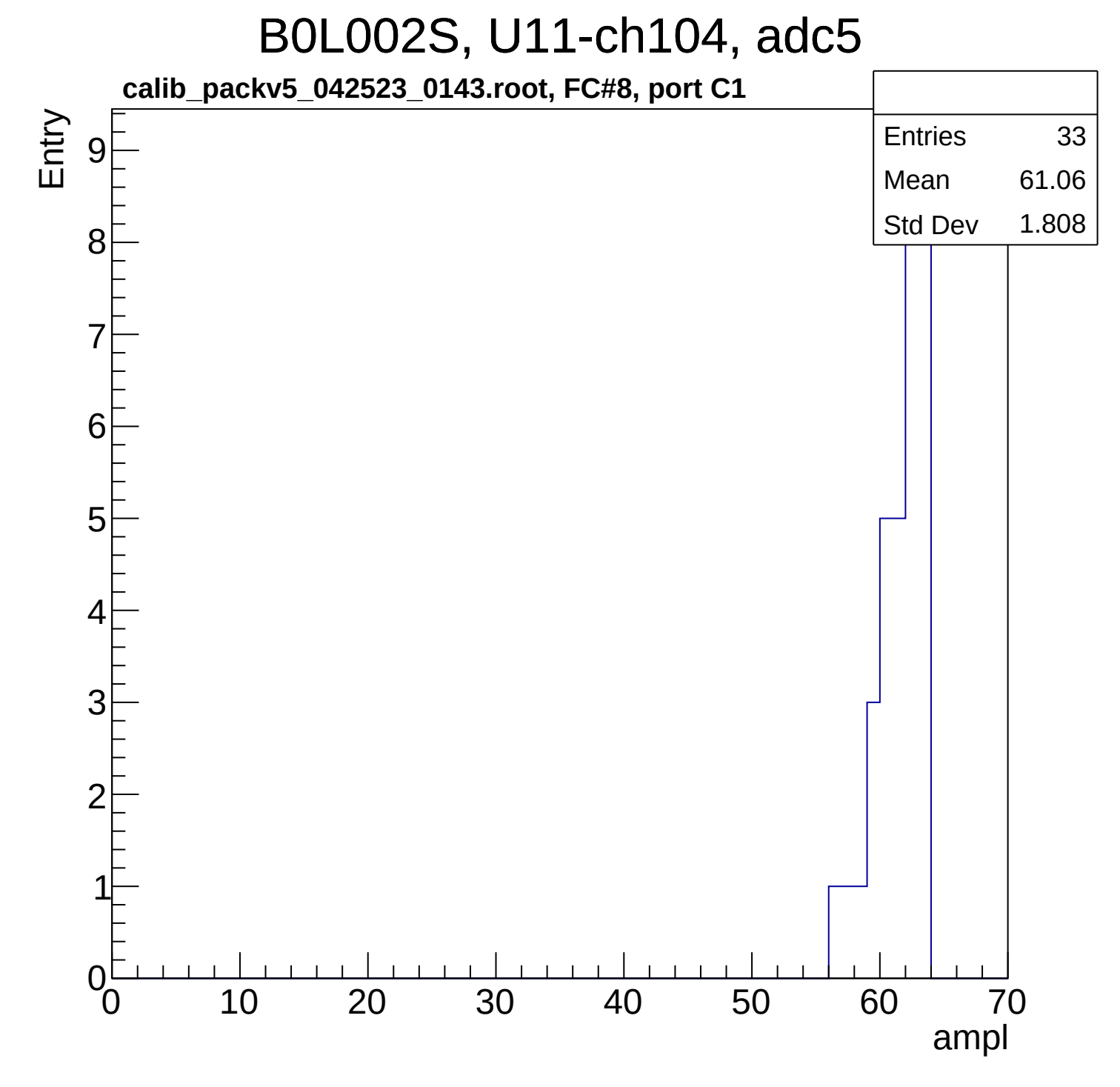

B0L002S, U11-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.11
Std Dev 7.845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 61.06
Std Dev 1.808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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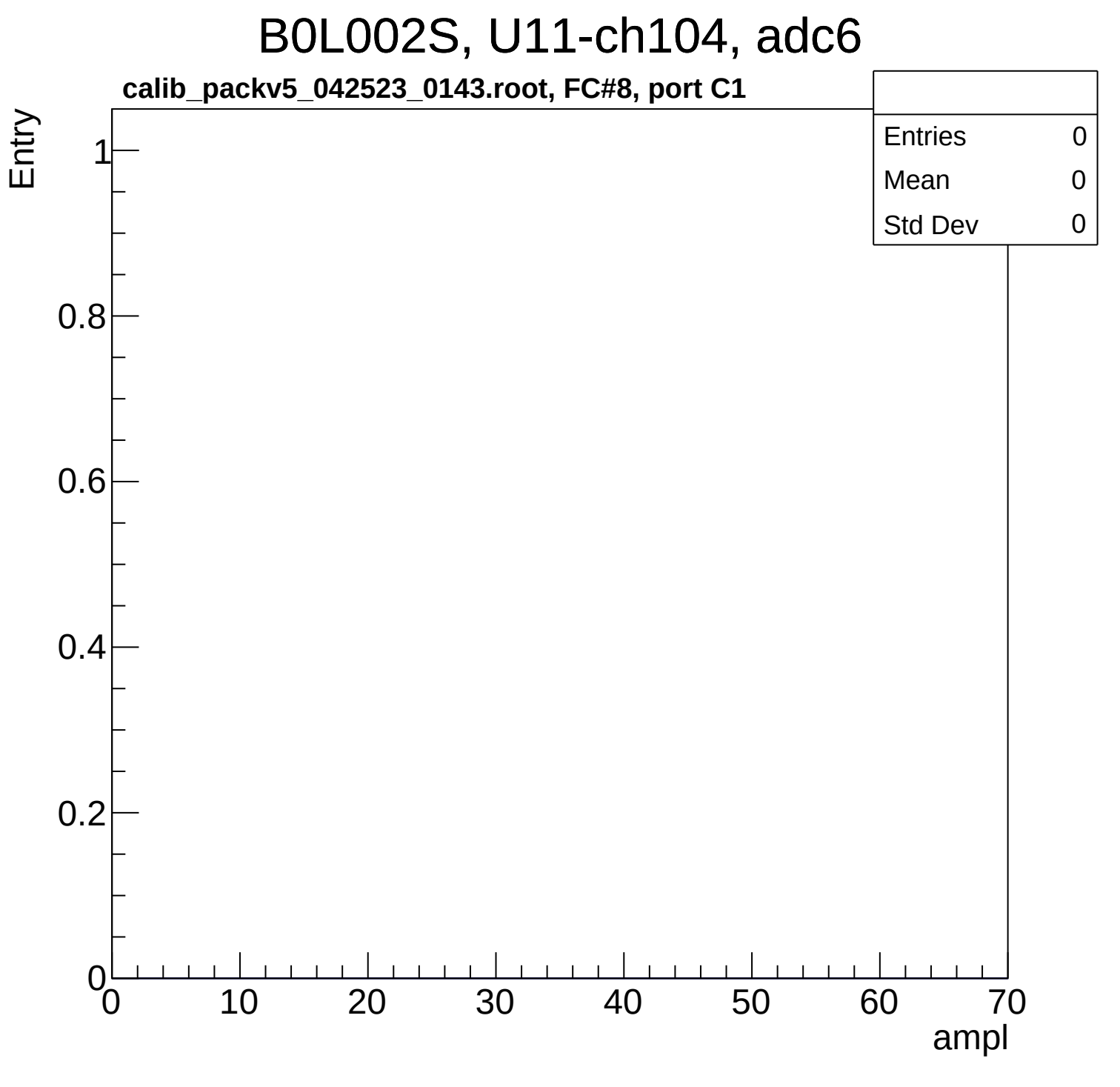
B0L002S, U11-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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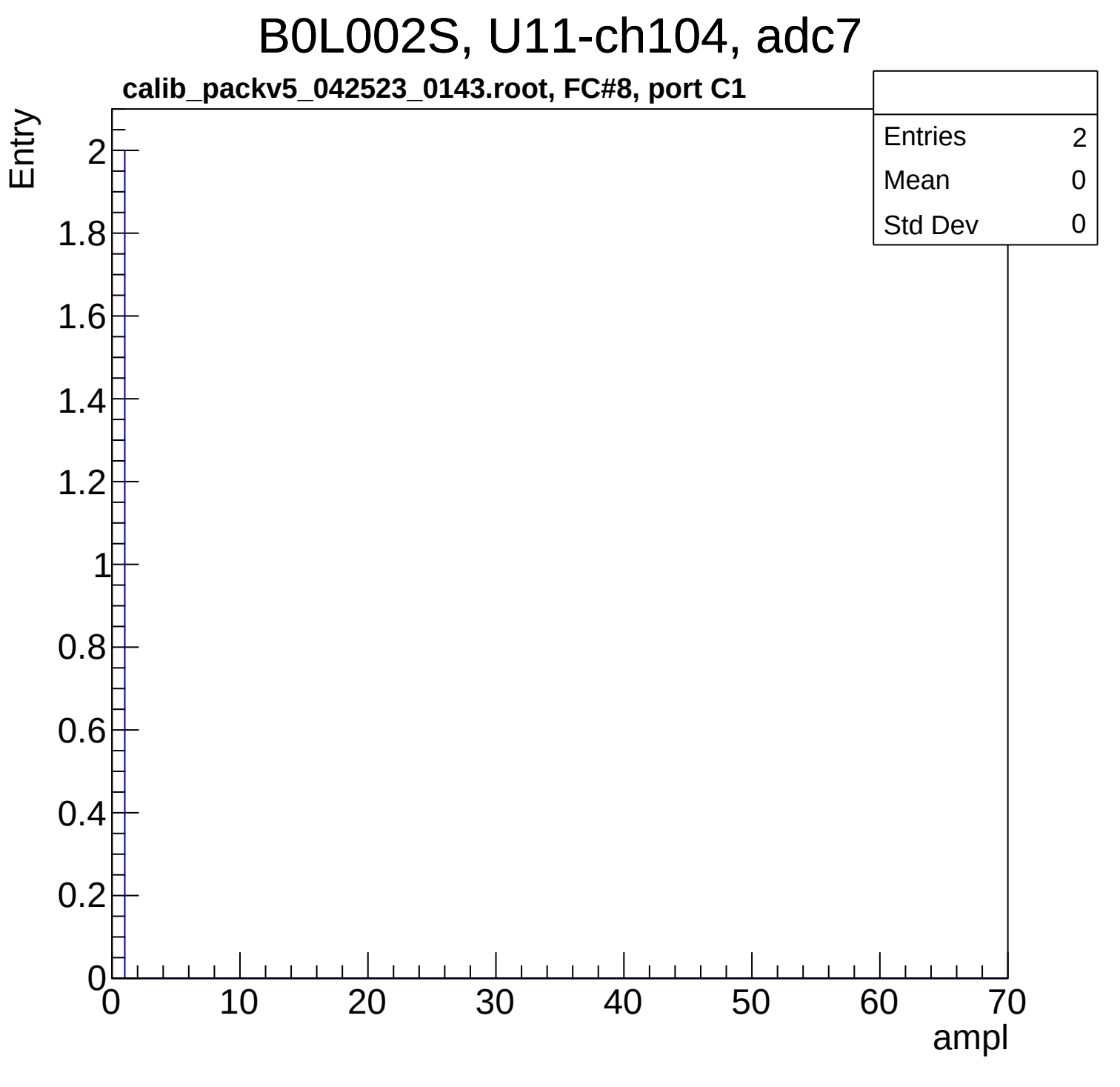
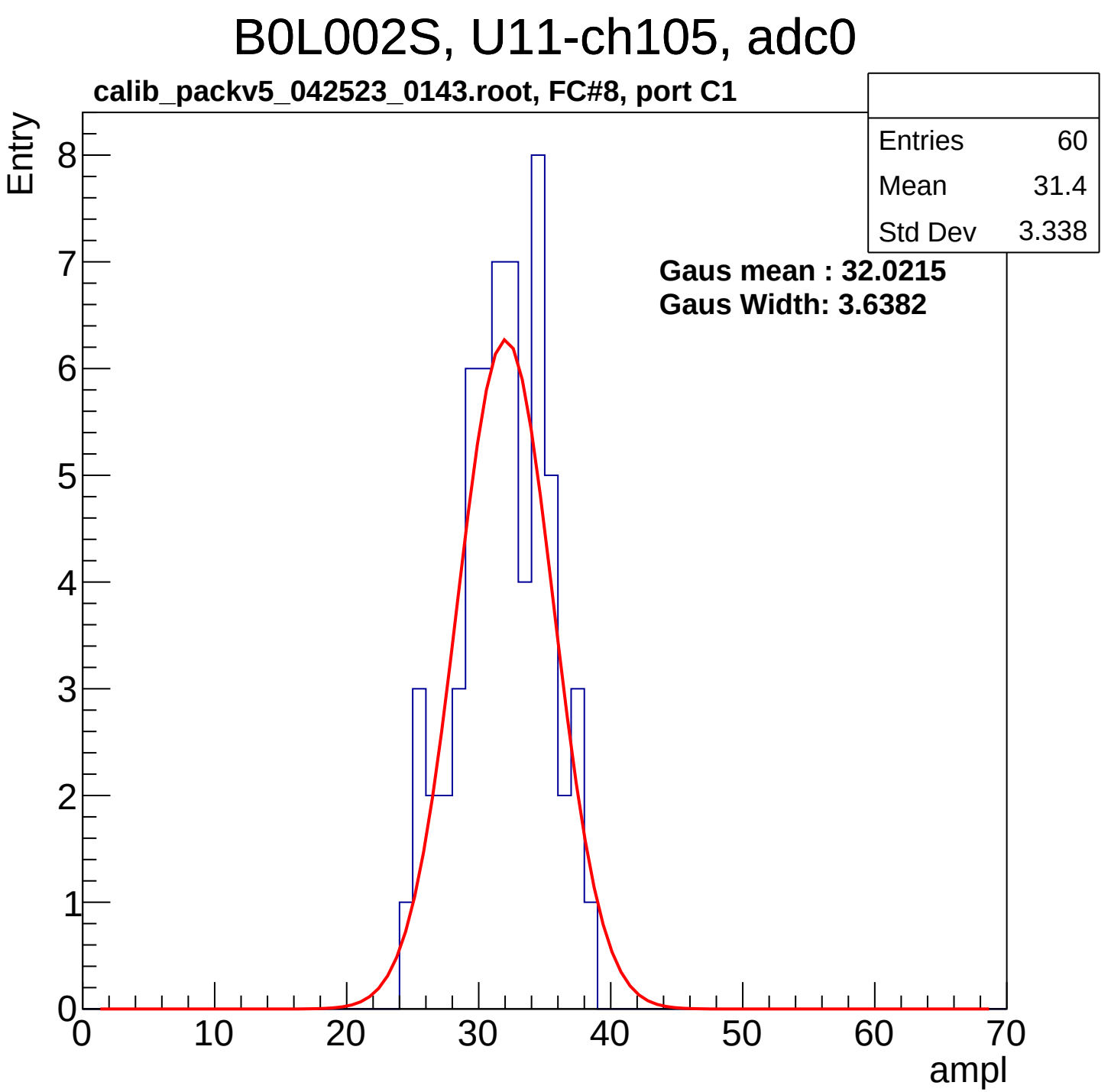
40
50
60
70
ampl


B0L002S, U11-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.4
Std Dev 3.338
Gaus mean : 32.0215
Gaus Width: 3.6382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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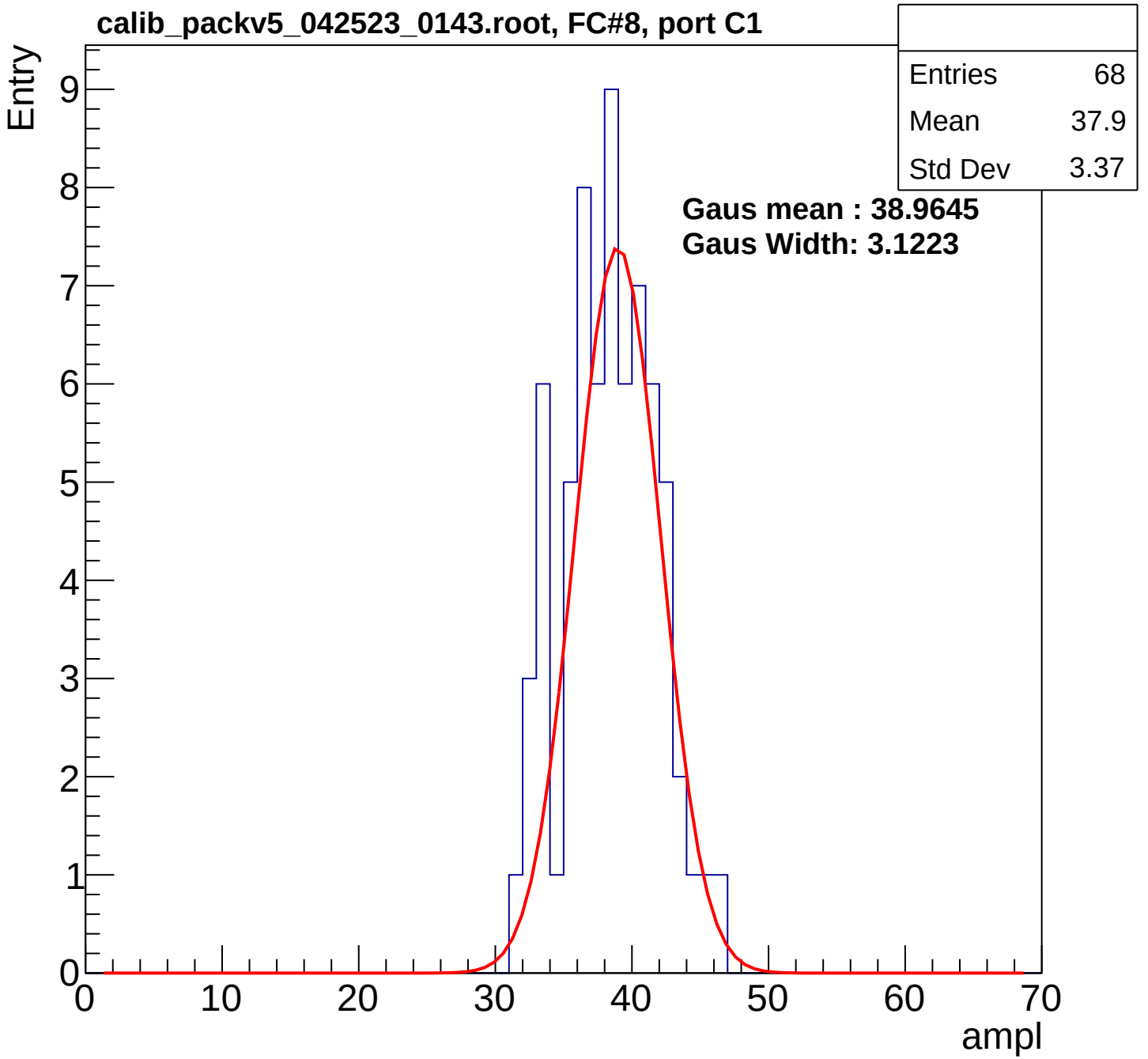
B0L002S, U11-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.9
Std Dev 3.37
Gaus mean : 38.9645
Gaus Width: 3.1223
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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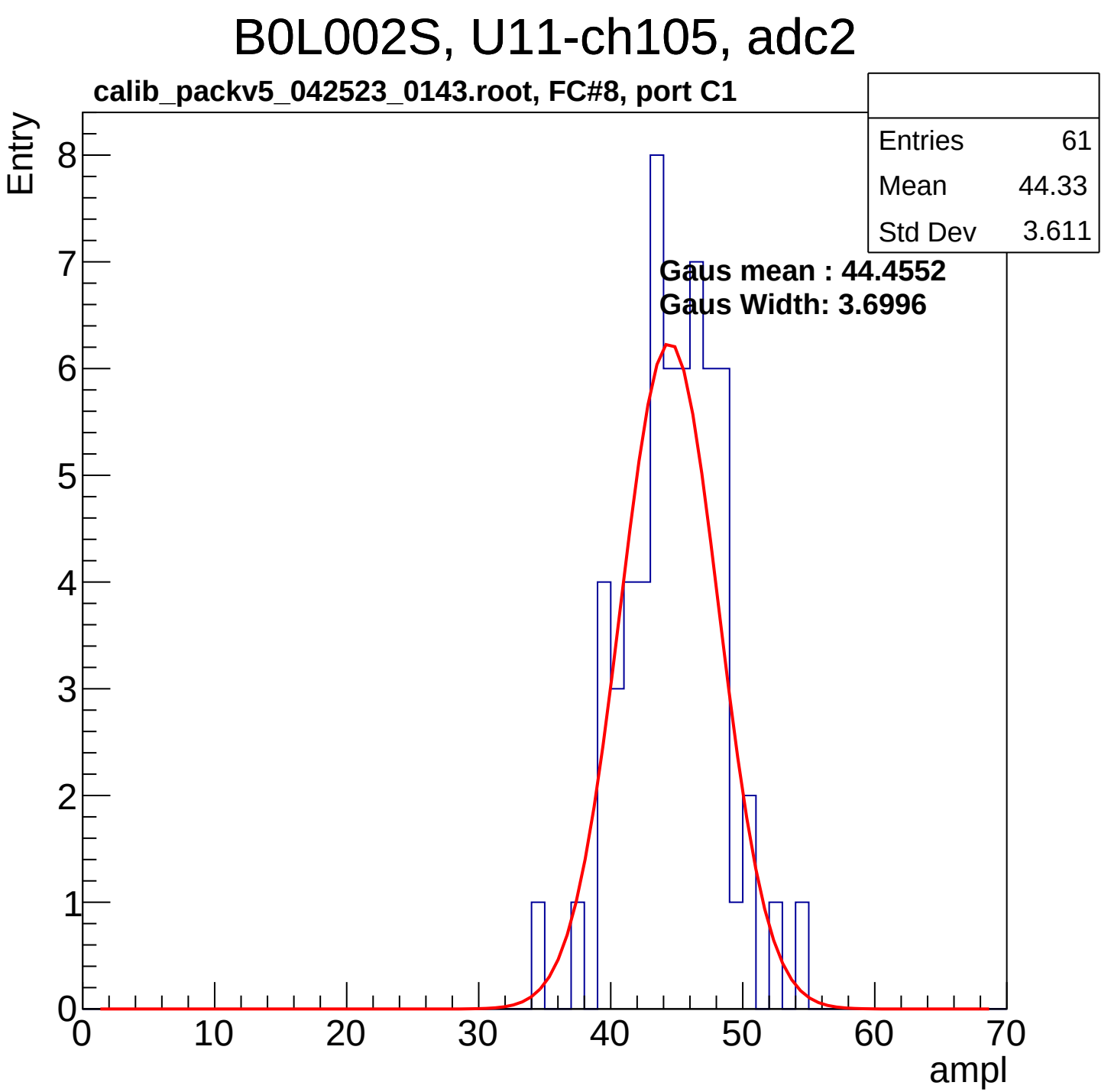
B0L002S, U11-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.33
Std Dev 3.611
Gaus mean : 44.4552
Gaus Width: 3.6996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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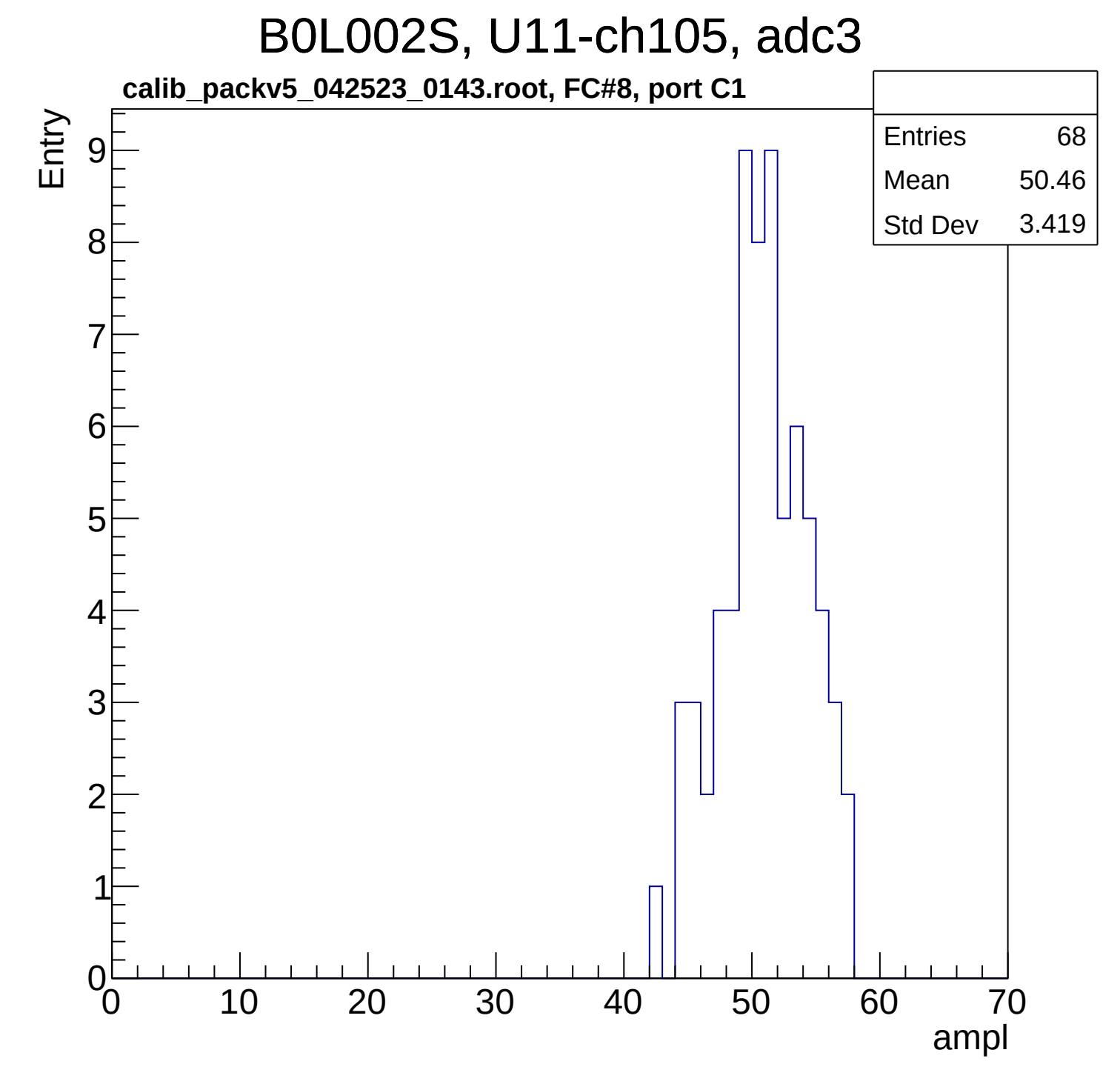
B0L002S, U11-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.46
Std Dev 3.419
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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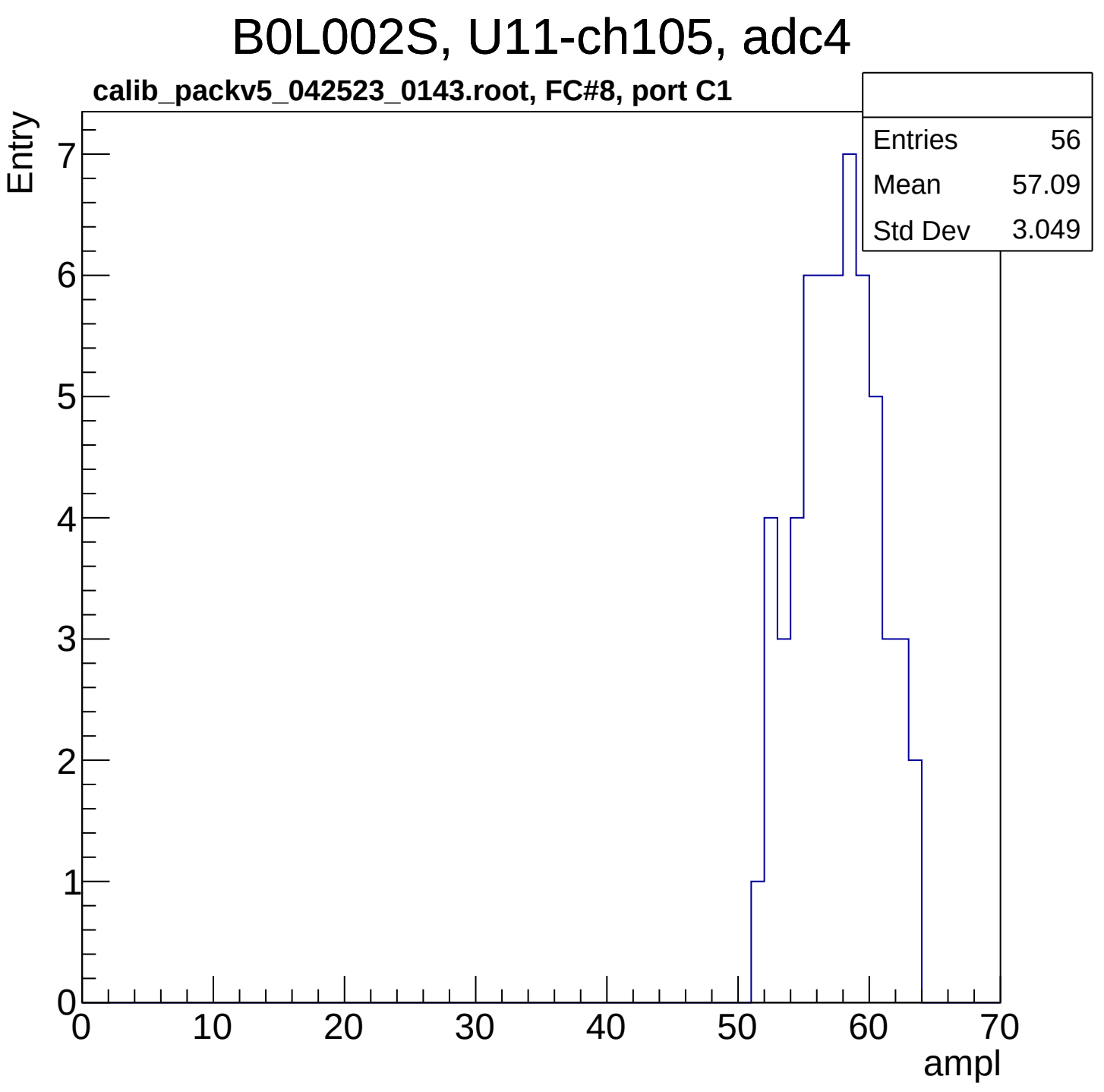
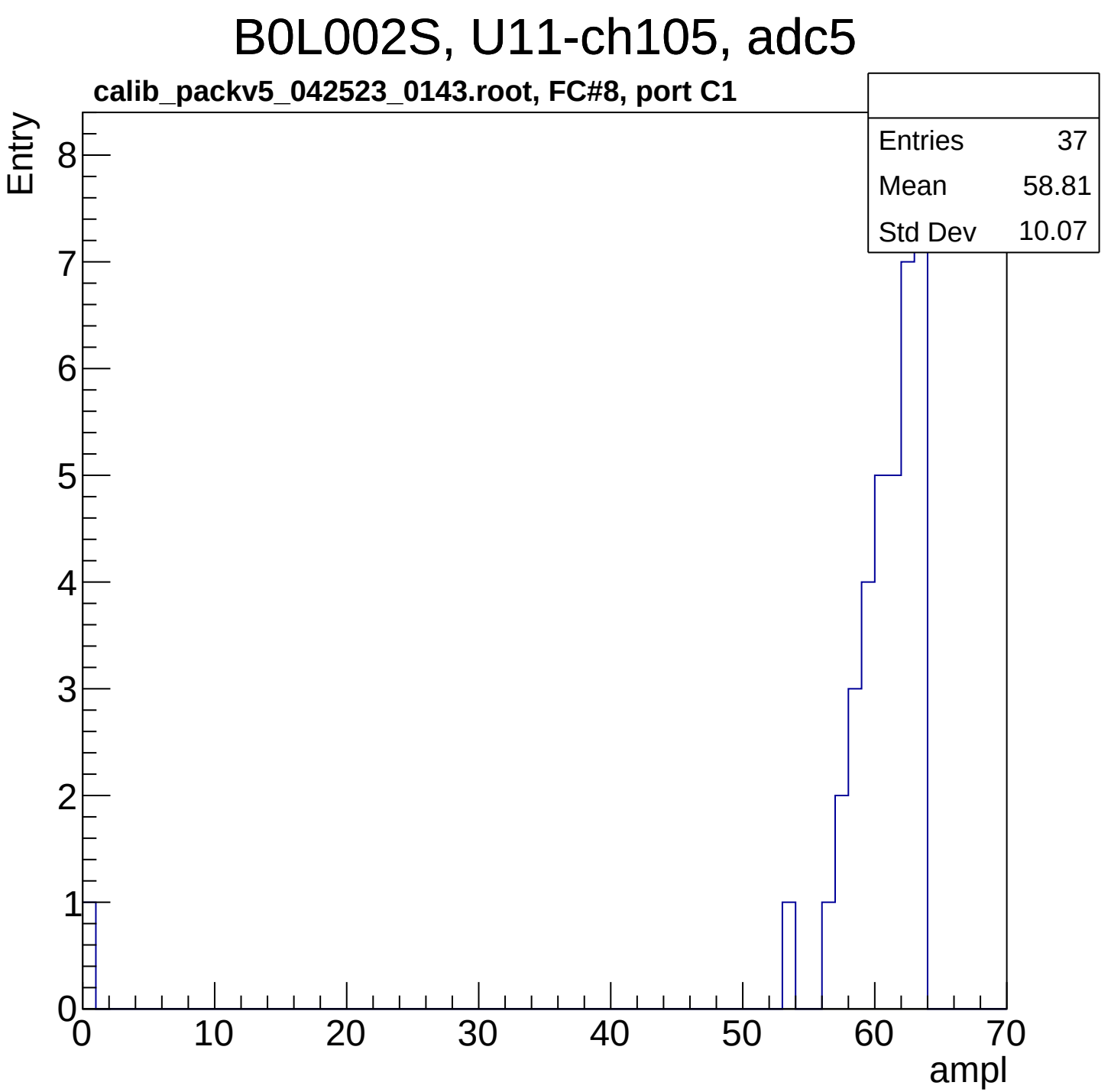
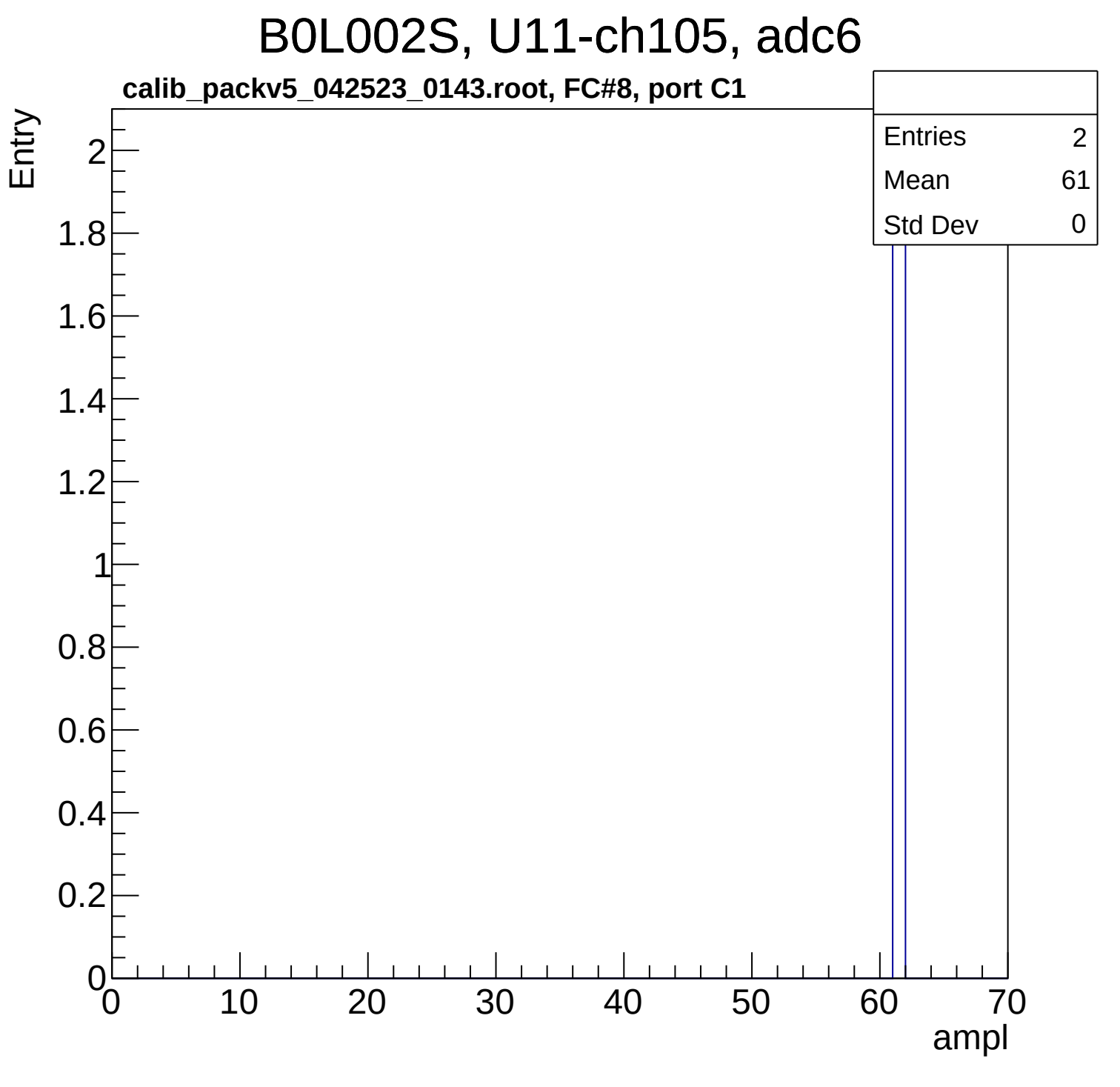
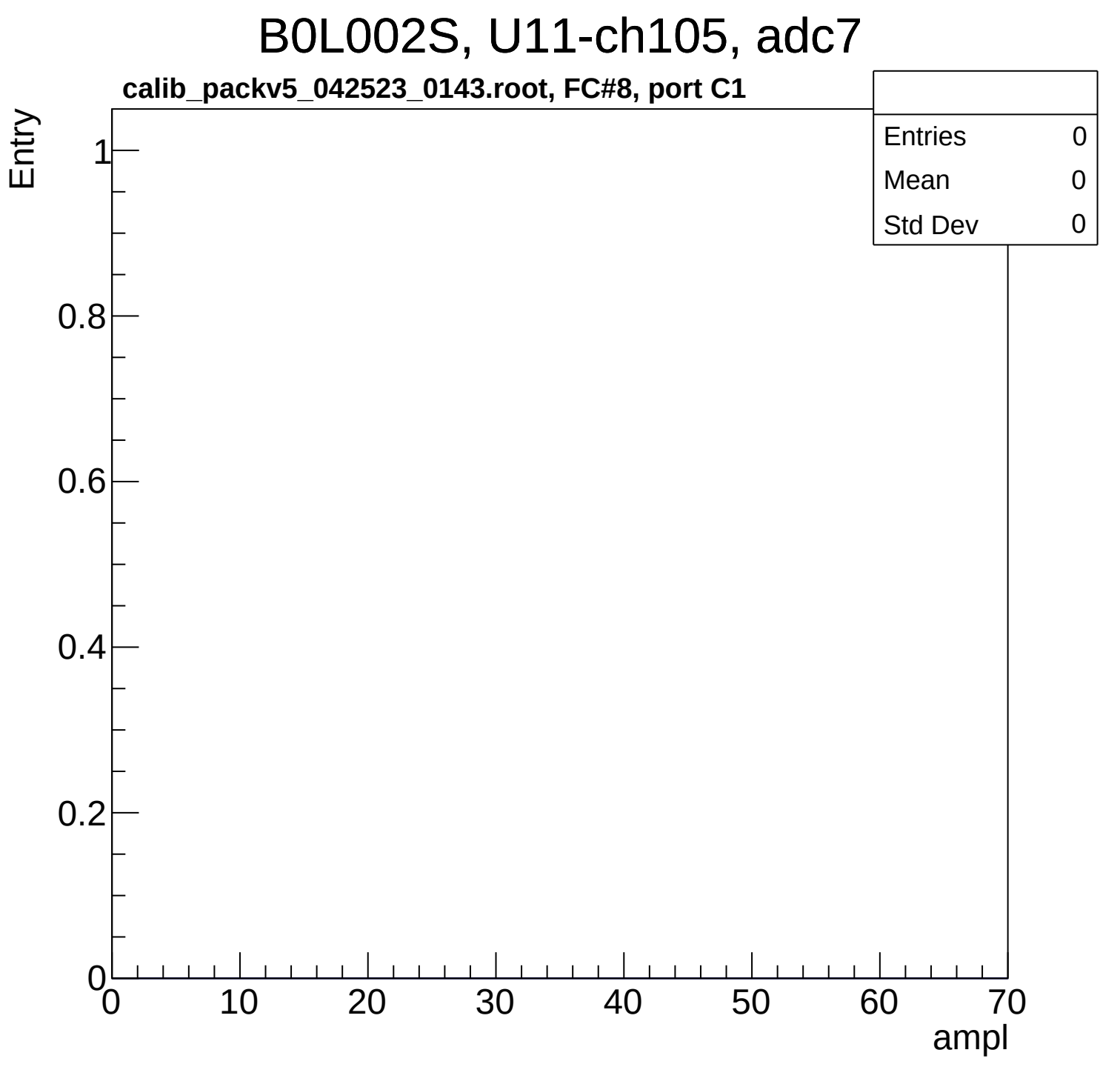
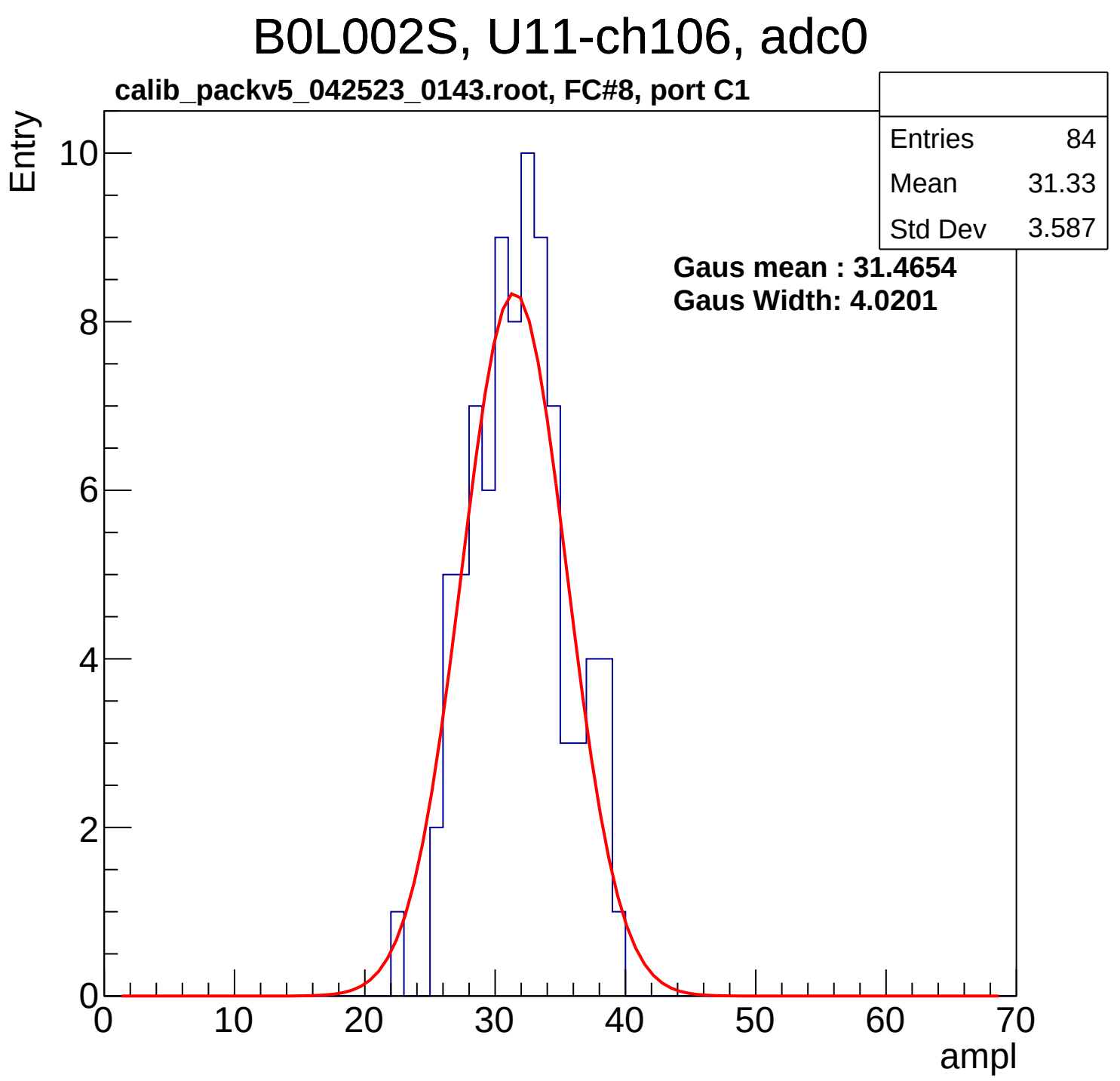

B0L002S, U11-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.09
Std Dev 3.049
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.81
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 31.33
Std Dev 3.587
Gaus mean : 31.4654
Gaus Width: 4.0201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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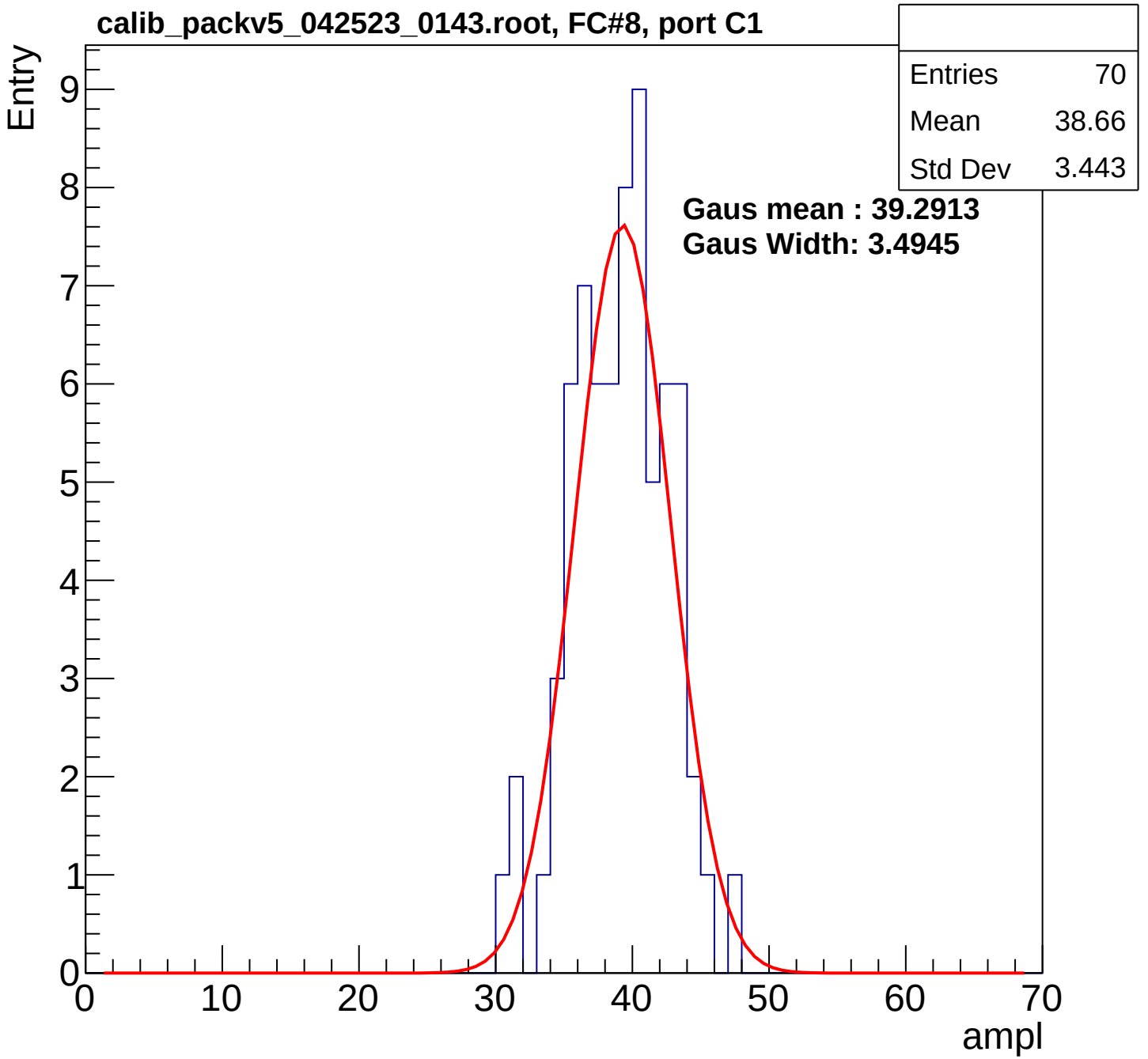
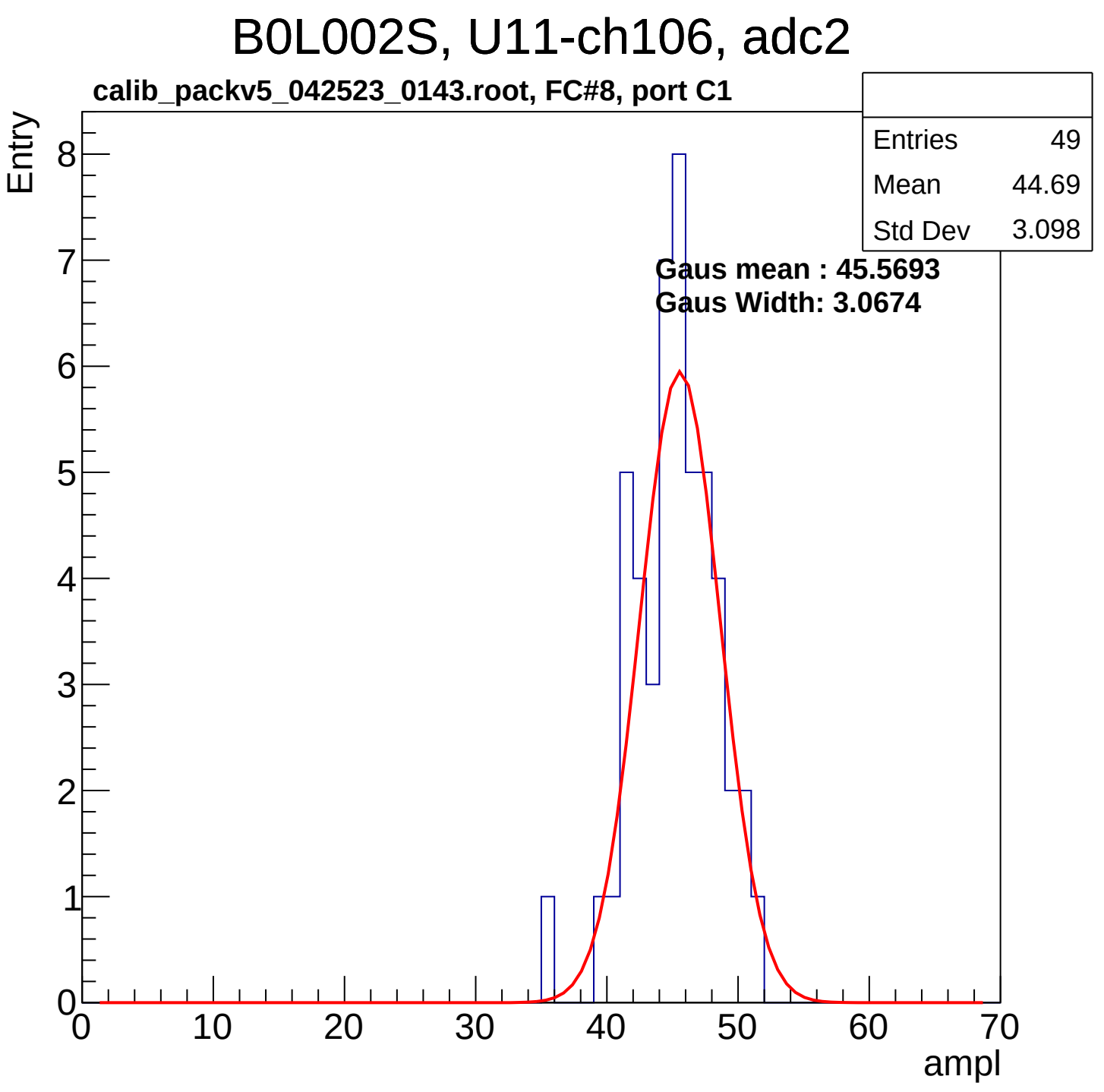

B0L002S, U11-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.66
Std Dev 3.443
Gaus mean : 39.2913
Gaus Width: 3.4945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 44.69
Std Dev 3.098
Gaus mean : 45.5693
Gaus Width: 3.0674
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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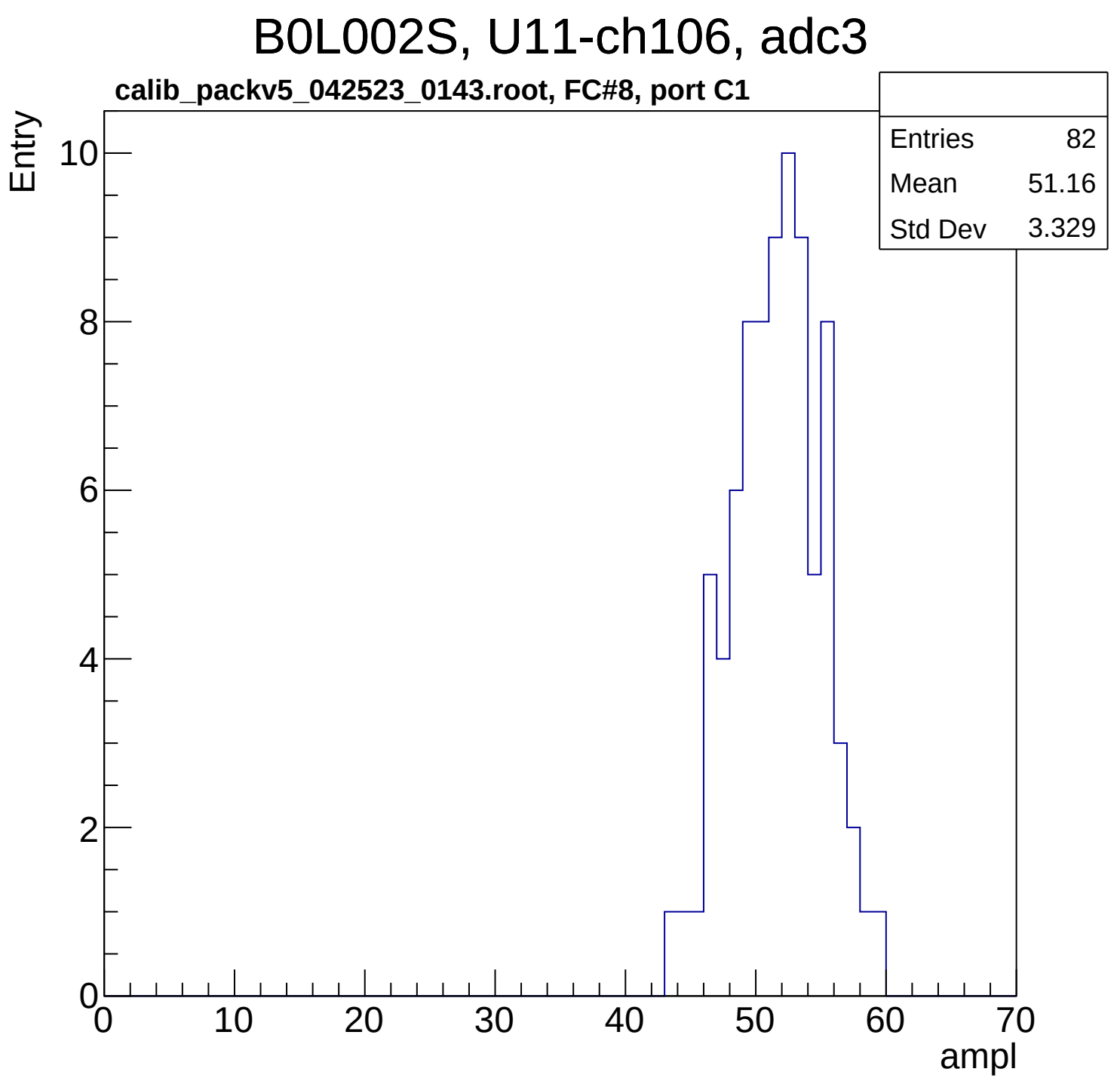
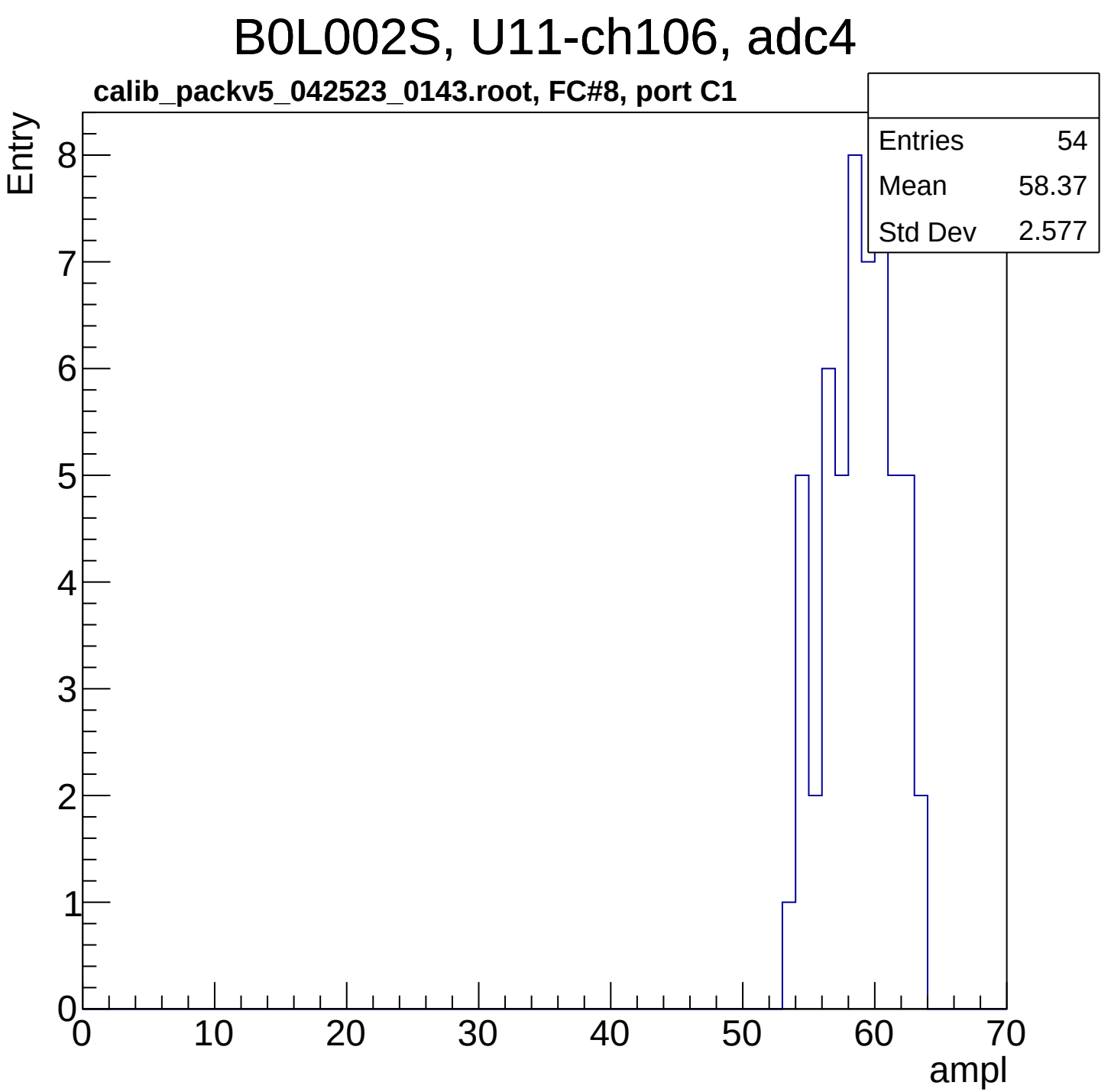

B0L002S, U11-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 51.16
Std Dev 3.329
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 58.37
Std Dev 2.577
Entry
ampl
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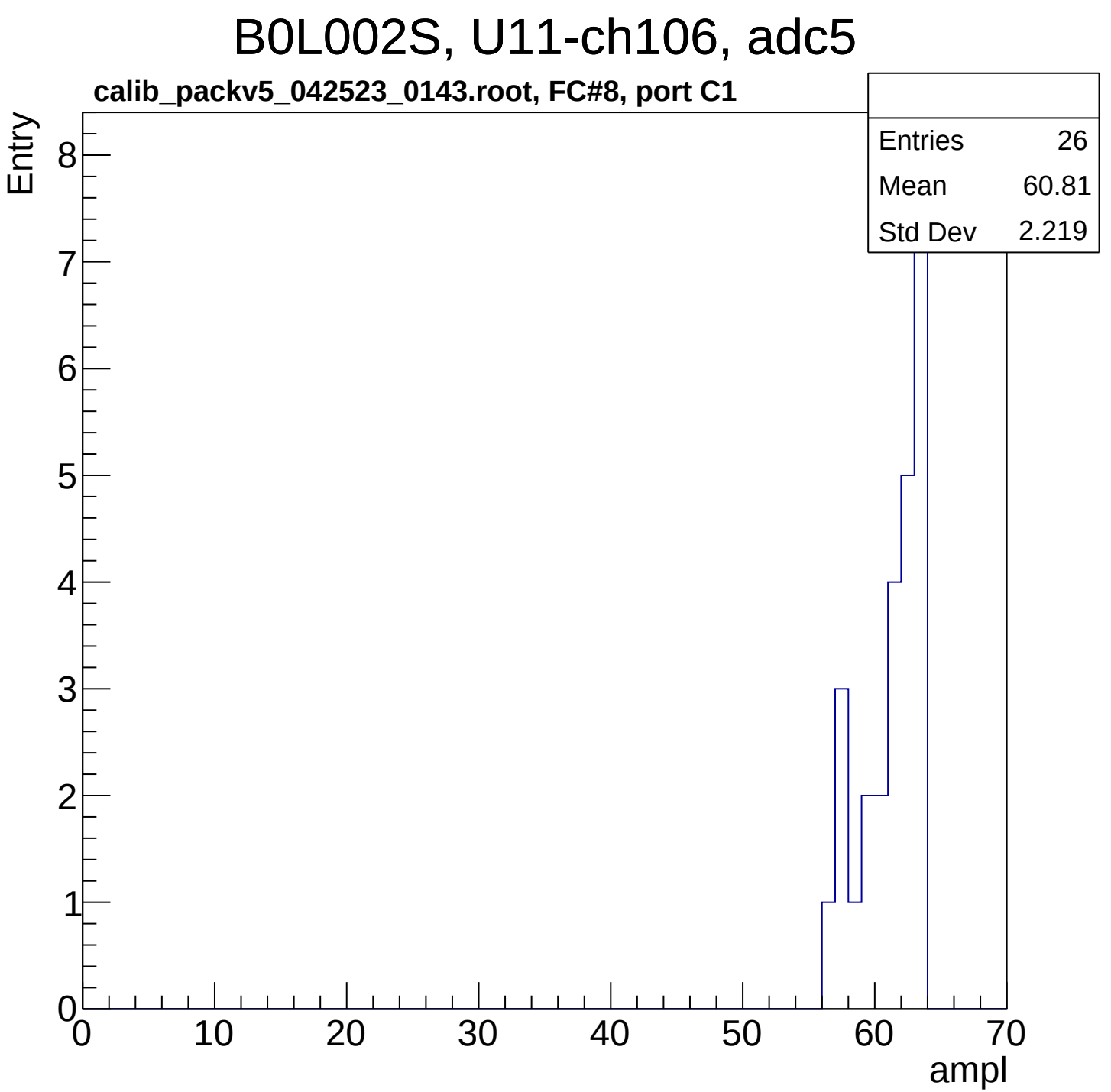
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 60.81
Std Dev 2.219
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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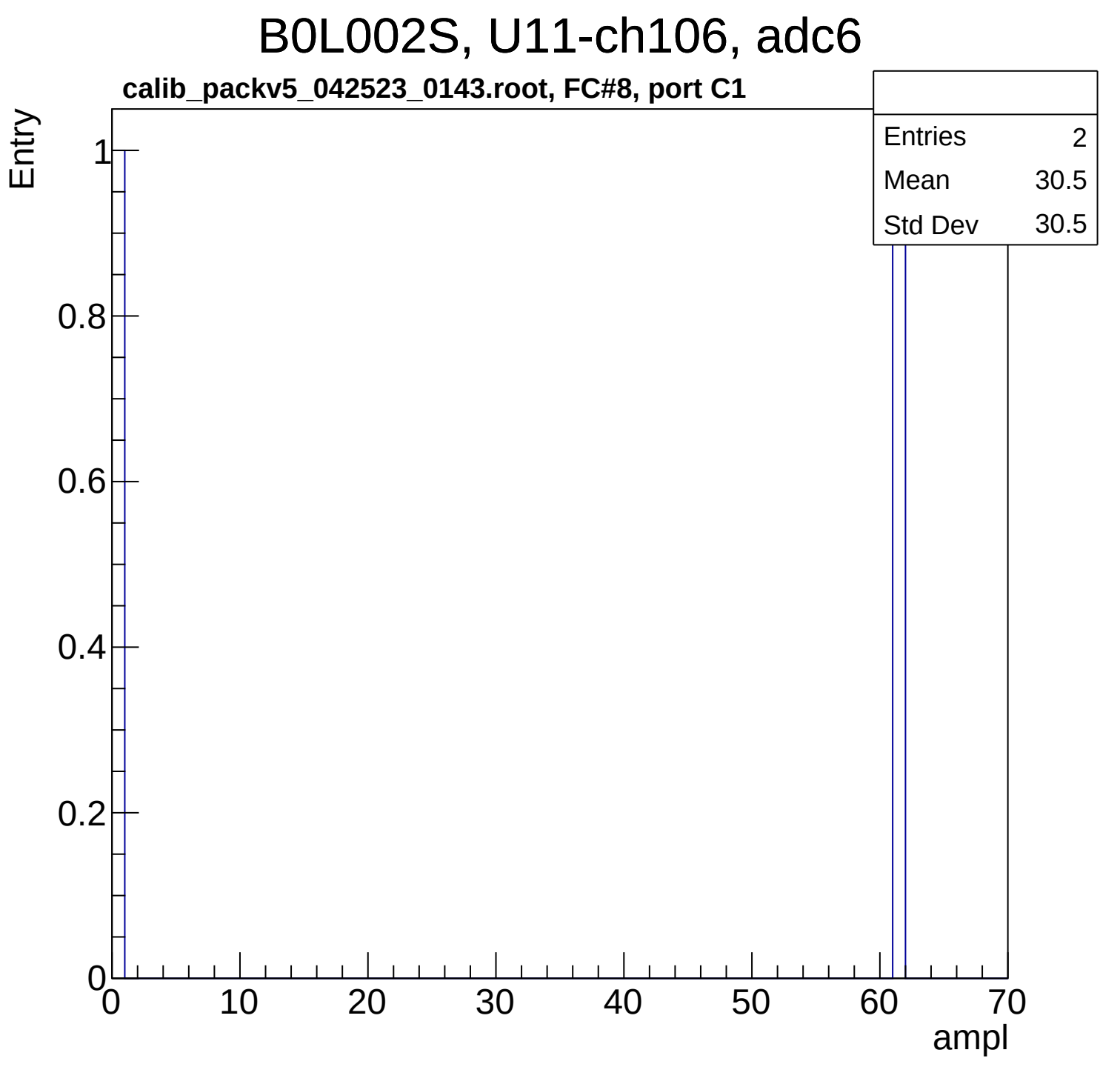
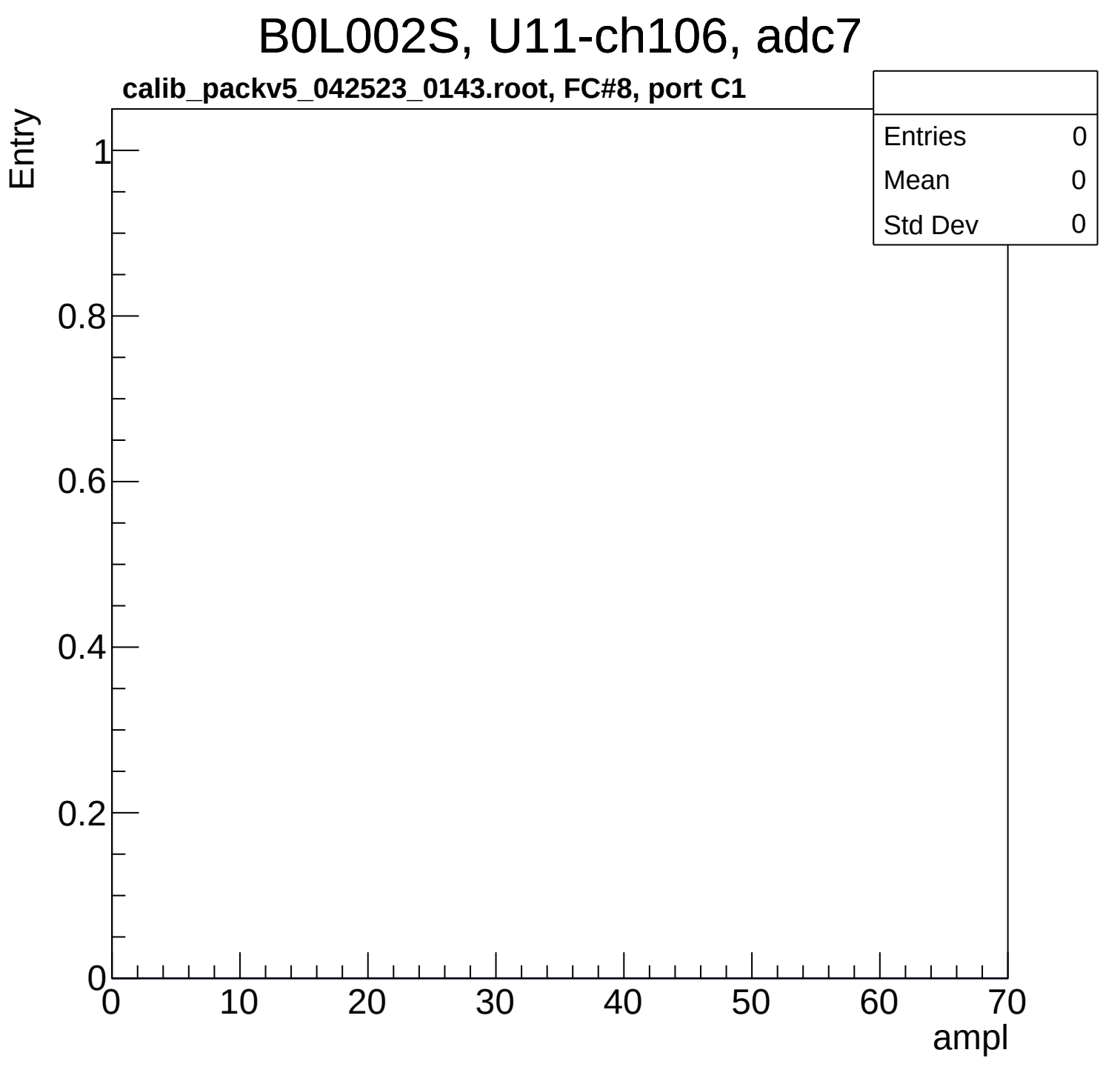

B0L002S, U11-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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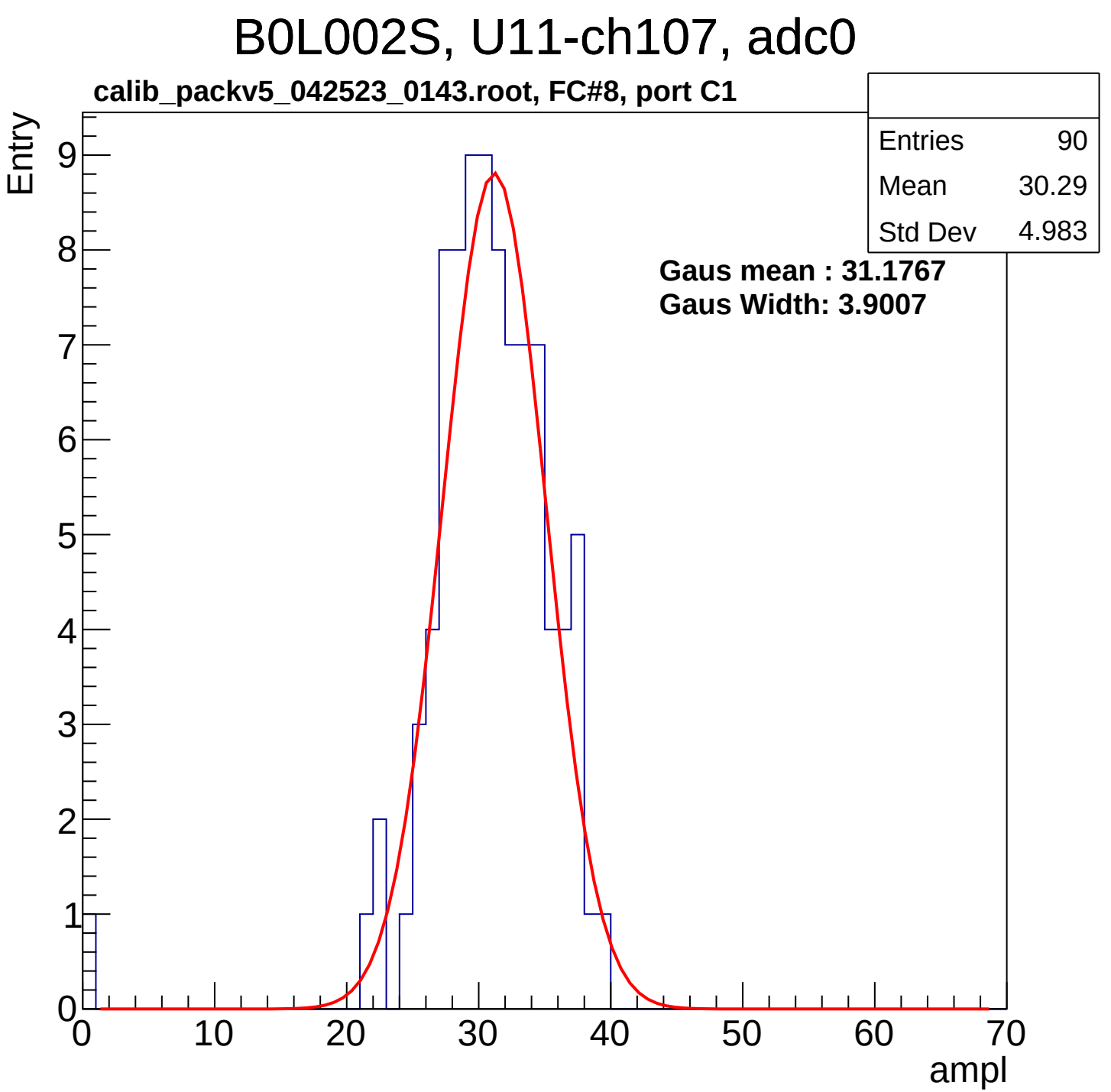
30
40
50
60
70
ampl


B0L002S, U11-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 30.29
Std Dev 4.983
Gaus mean : 31.1767
Gaus Width: 3.9007
Entry
ampl
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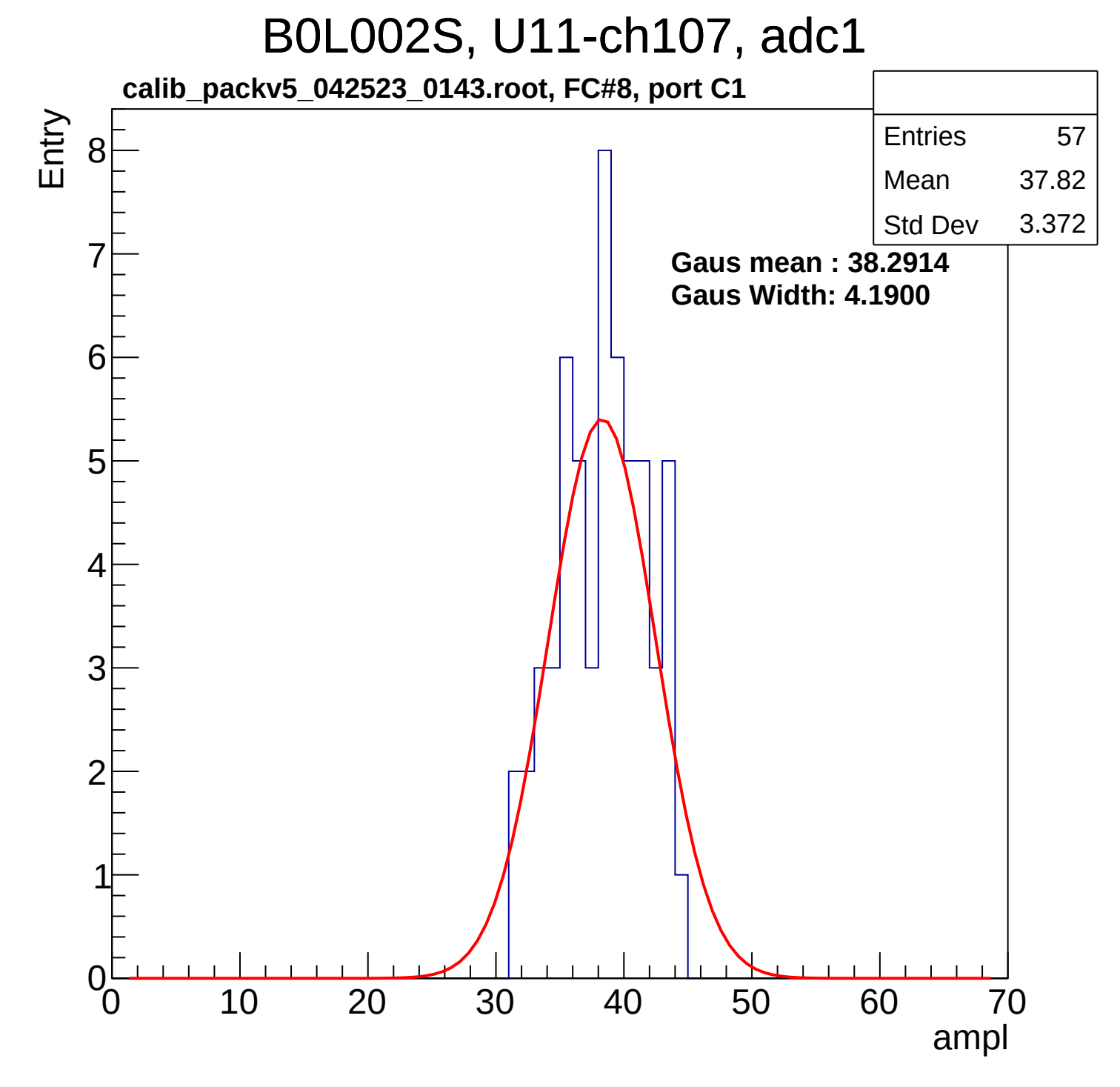
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.82
Std Dev 3.372
Gaus mean : 38.2914
Gaus Width: 4.1900
Entry
ampl
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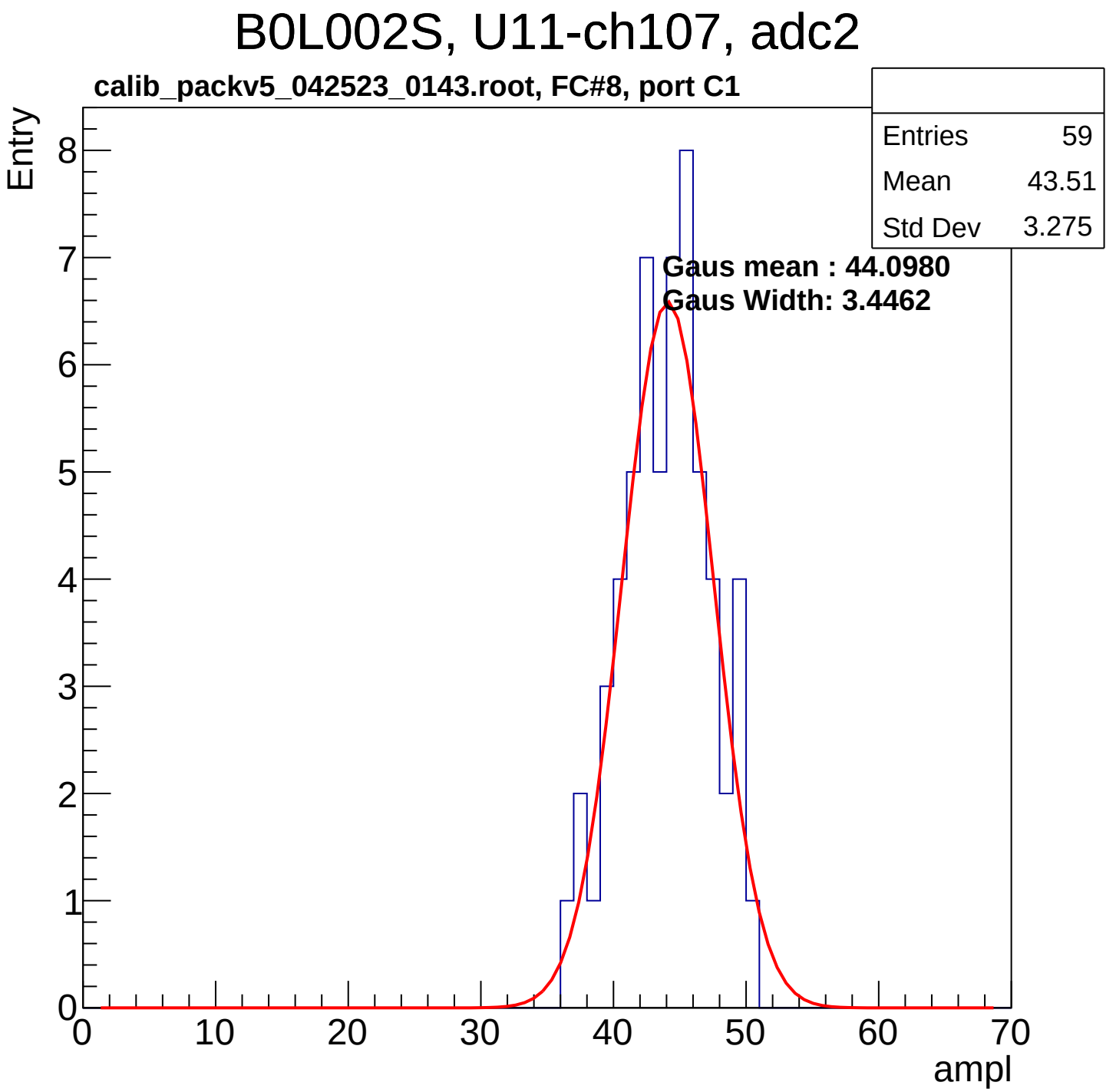
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.51
Std Dev 3.275
Gaus mean : 44.0980
Gaus Width: 3.4462
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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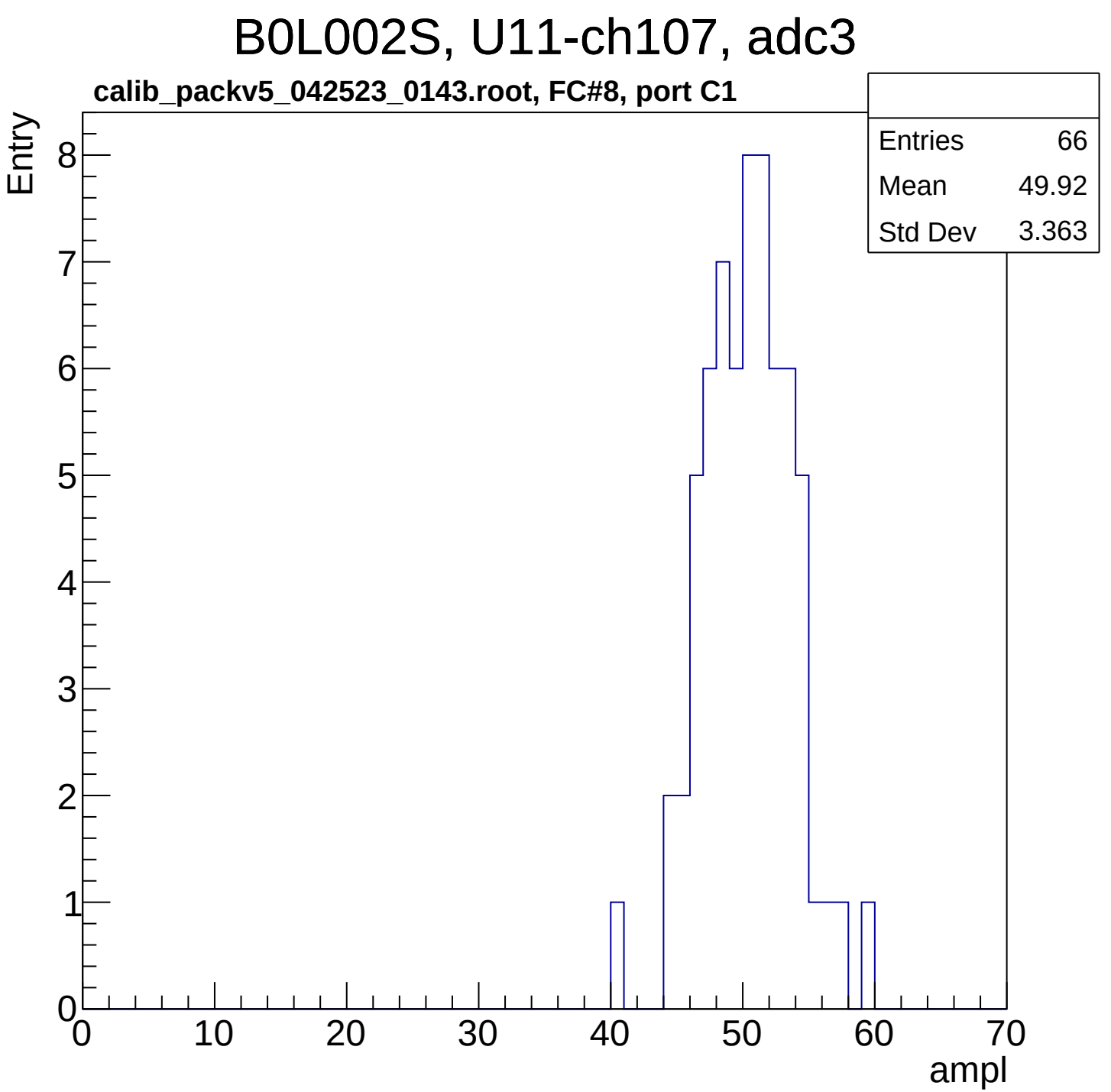
B0L002S, U11-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.92
Std Dev 3.363
Entry
ampl
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4
5
6
7
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0
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40
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60
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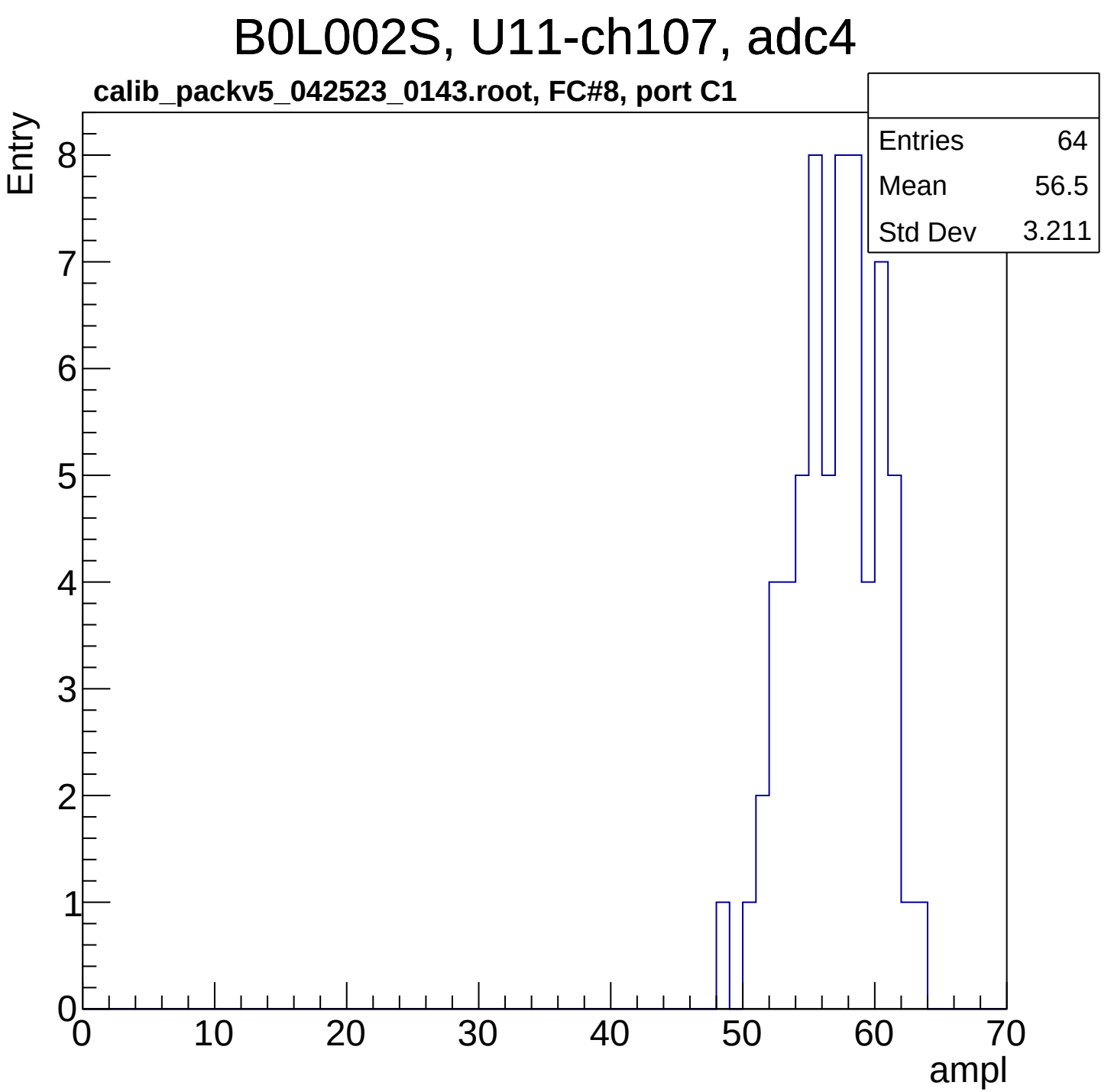
B0L002S, U11-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.5
Std Dev 3.211
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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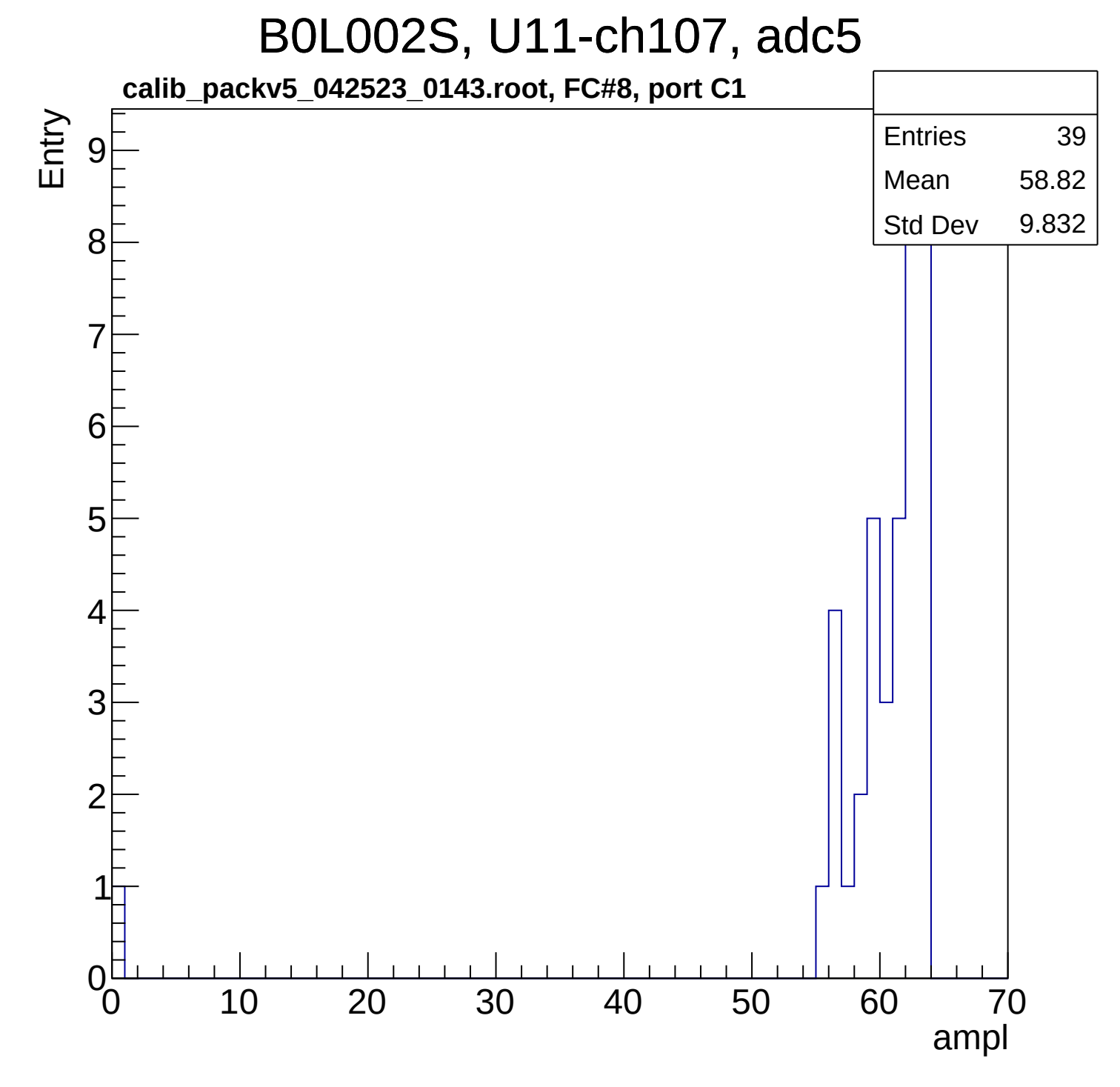
B0L002S, U11-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.82
Std Dev 9.832
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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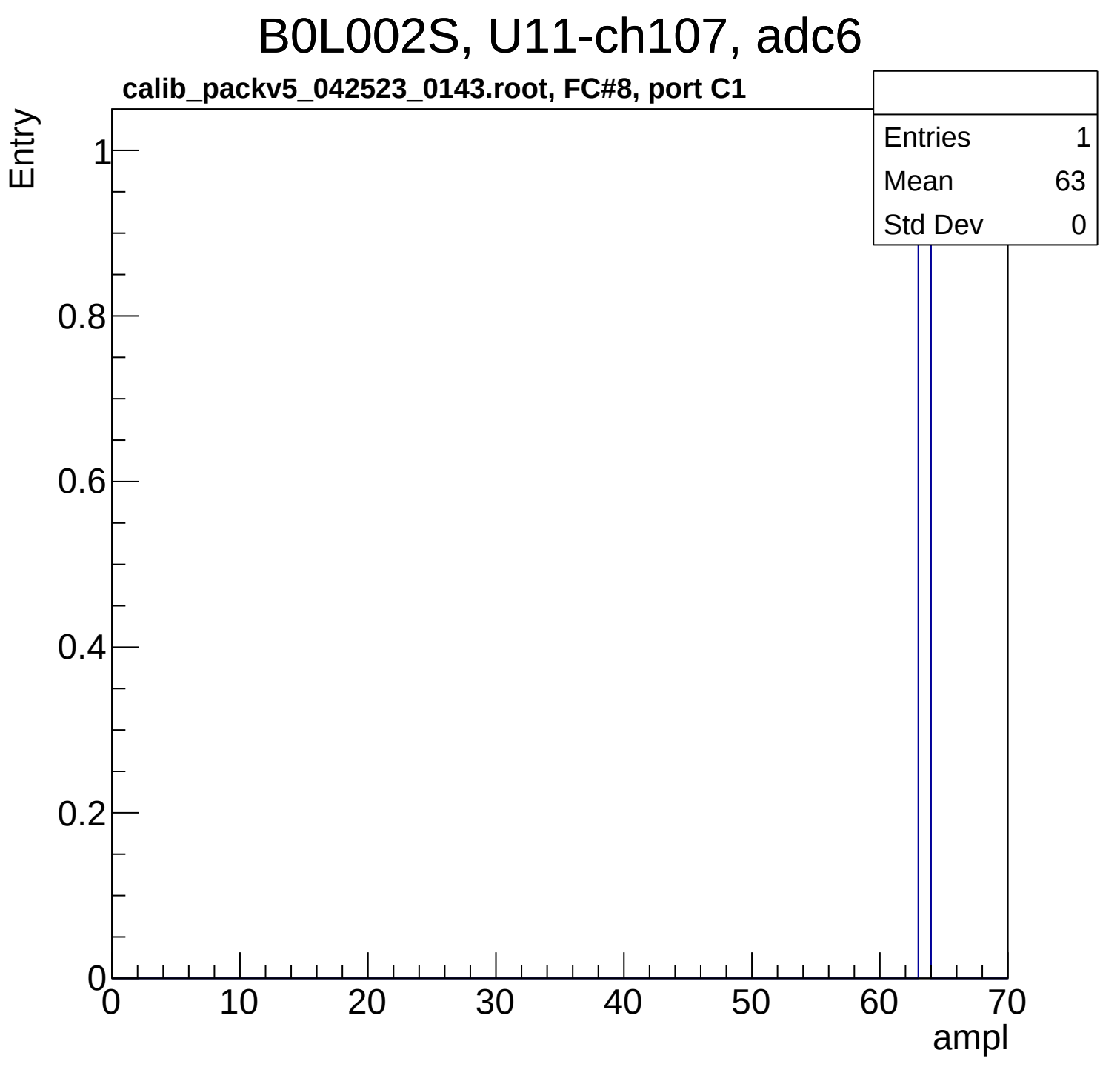
B0L002S, U11-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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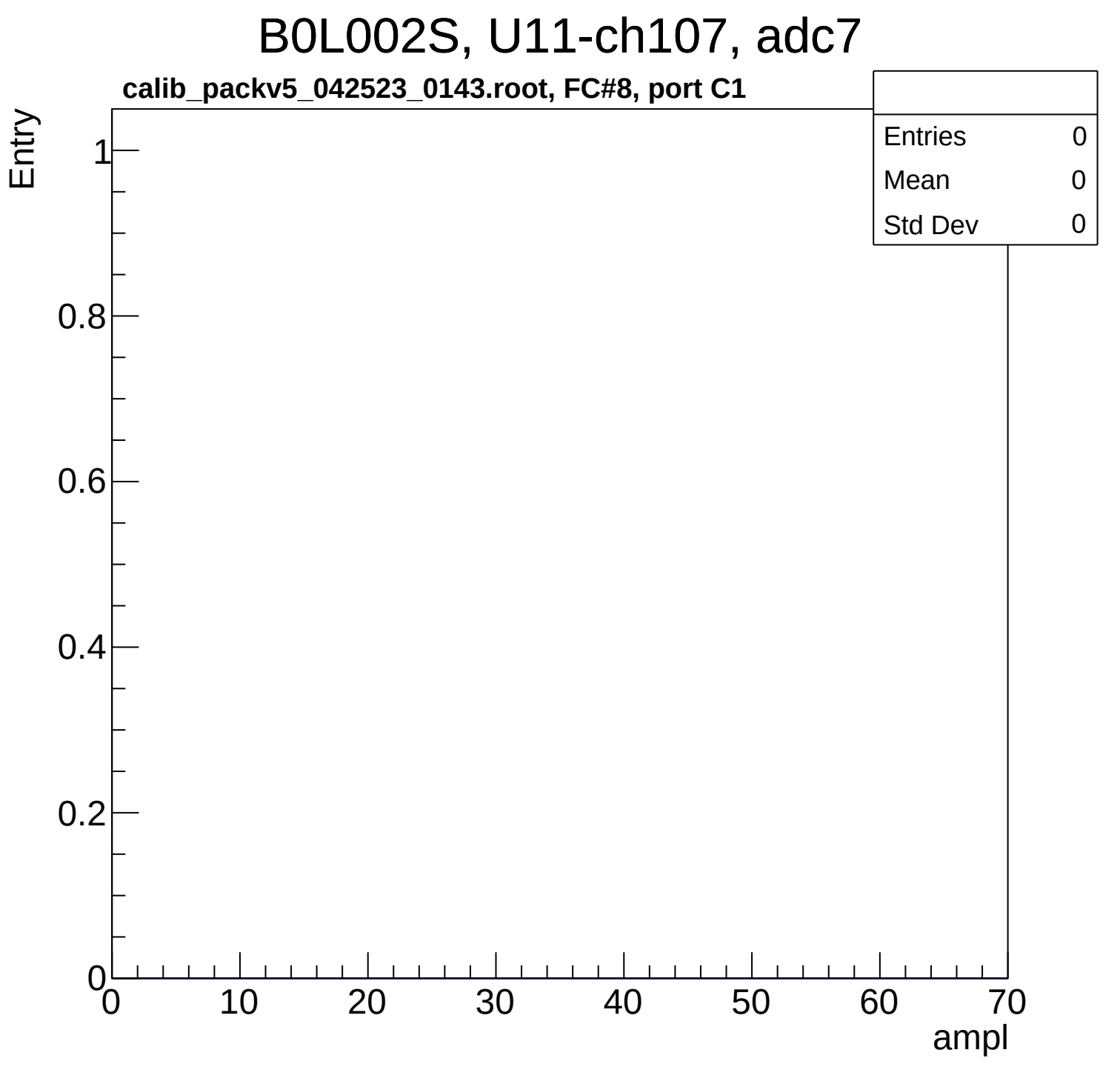
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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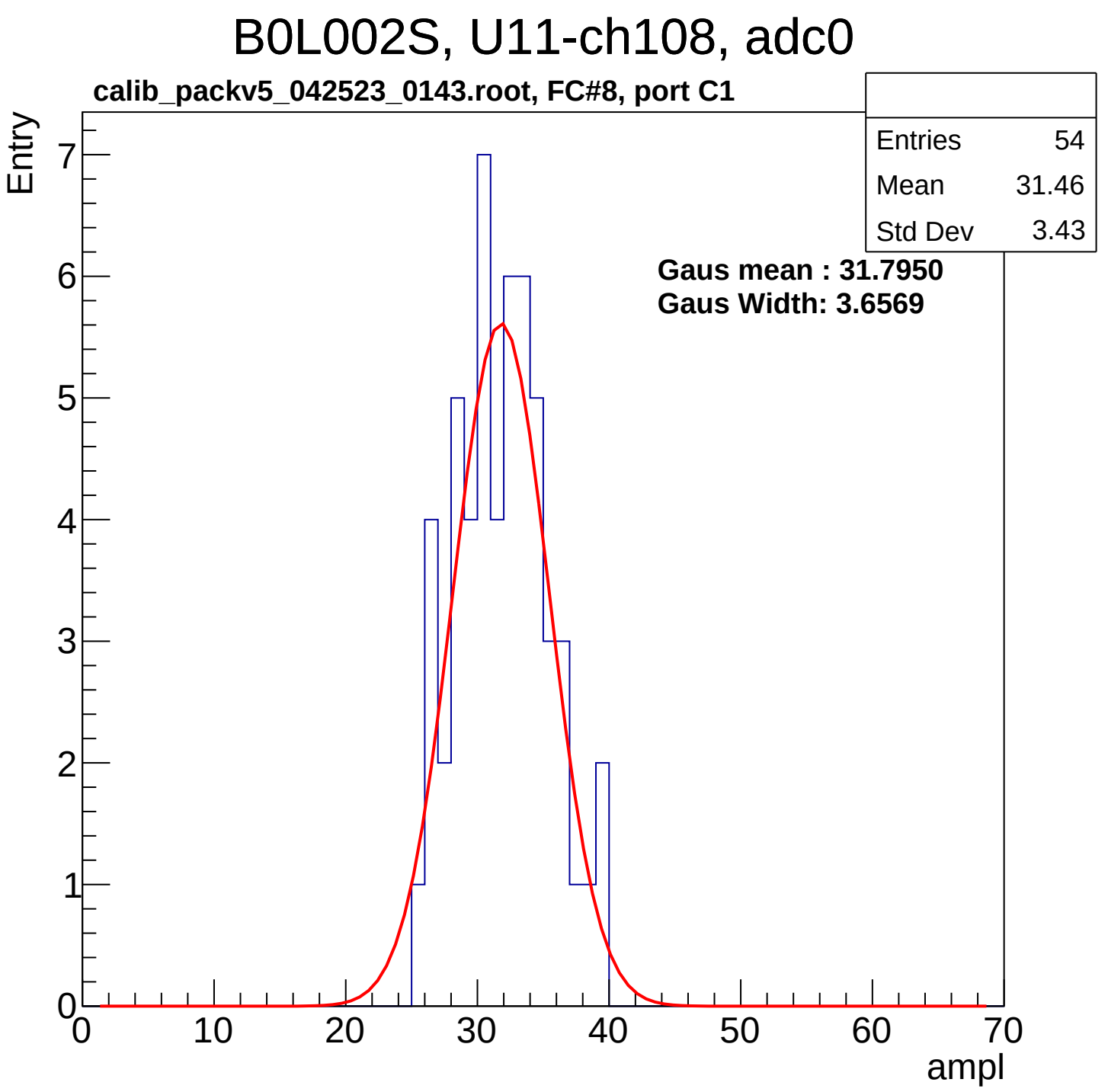
50
60
70
ampl


B0L002S, U11-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 31.46
Std Dev 3.43
Gaus mean : 31.7950
Gaus Width: 3.6569
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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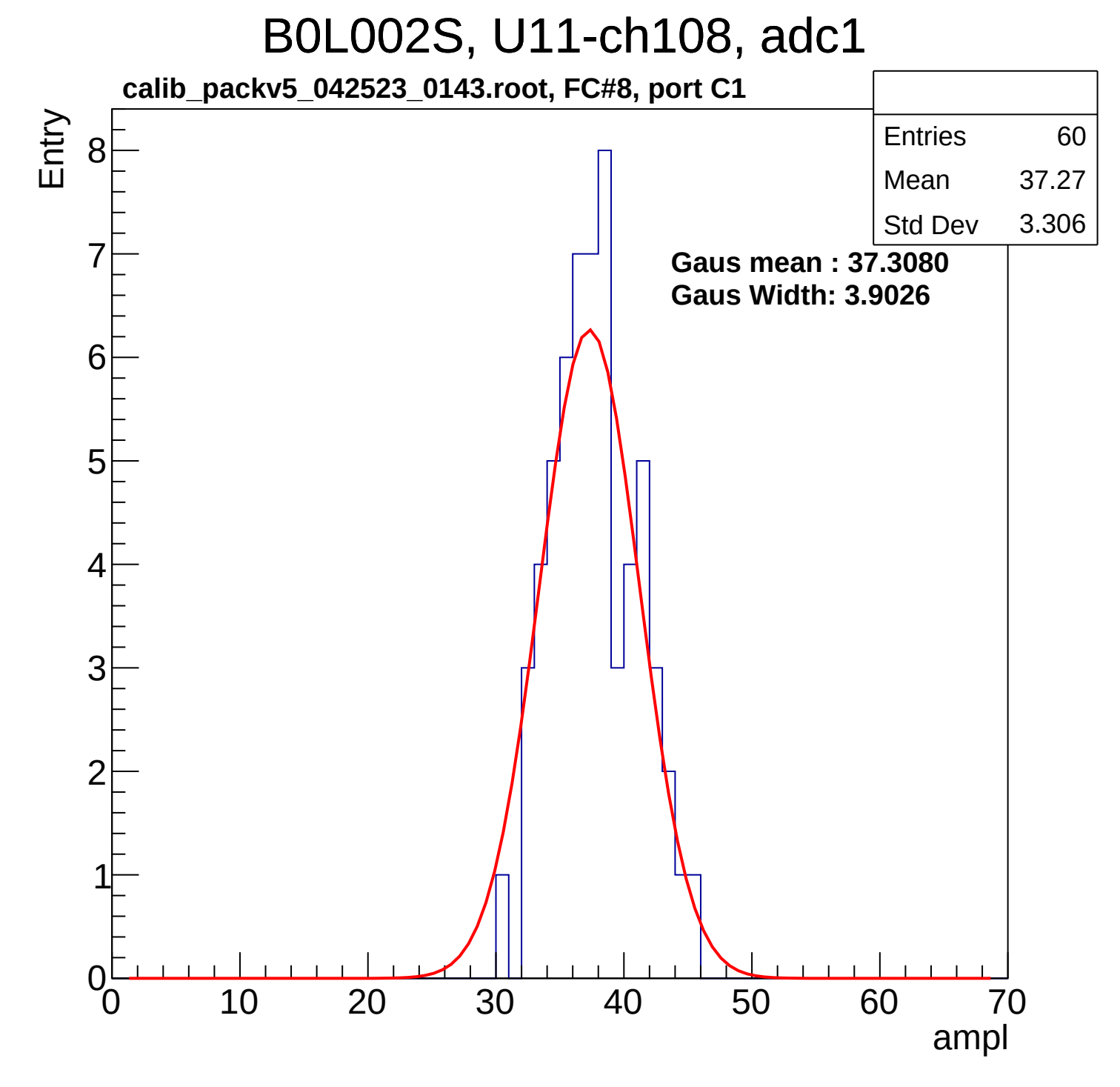
B0L002S, U11-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.27
Std Dev 3.306
Gaus mean : 37.3080
Gaus Width: 3.9026
Entry
ampl
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2
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4
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6
7
8
0
10
20
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60
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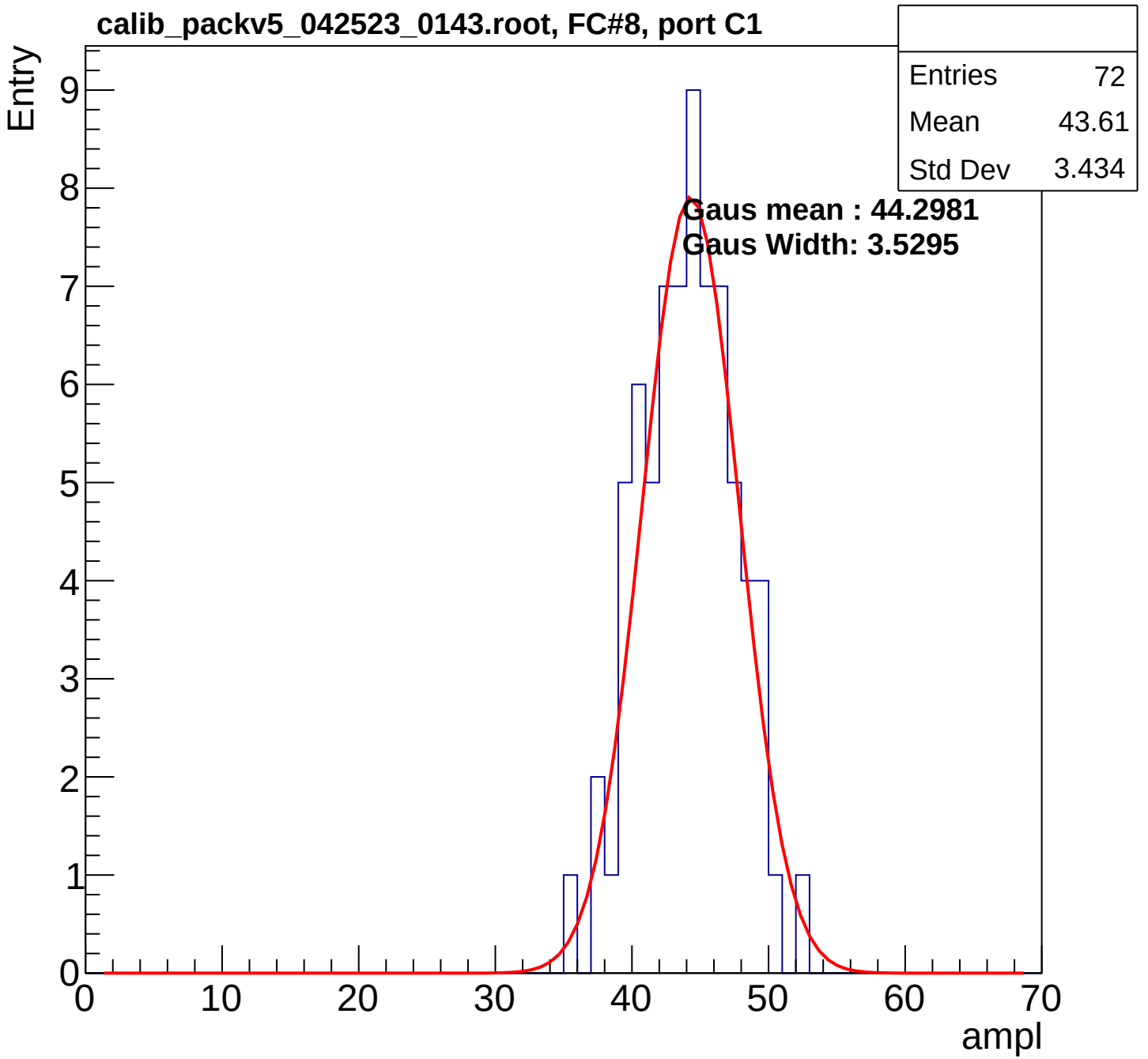
B0L002S, U11-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.61
Std Dev 3.434
Gaus mean : 44.2981
Gaus Width: 3.5295
Entry
ampl
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2
3
4
5
6
7
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0
10
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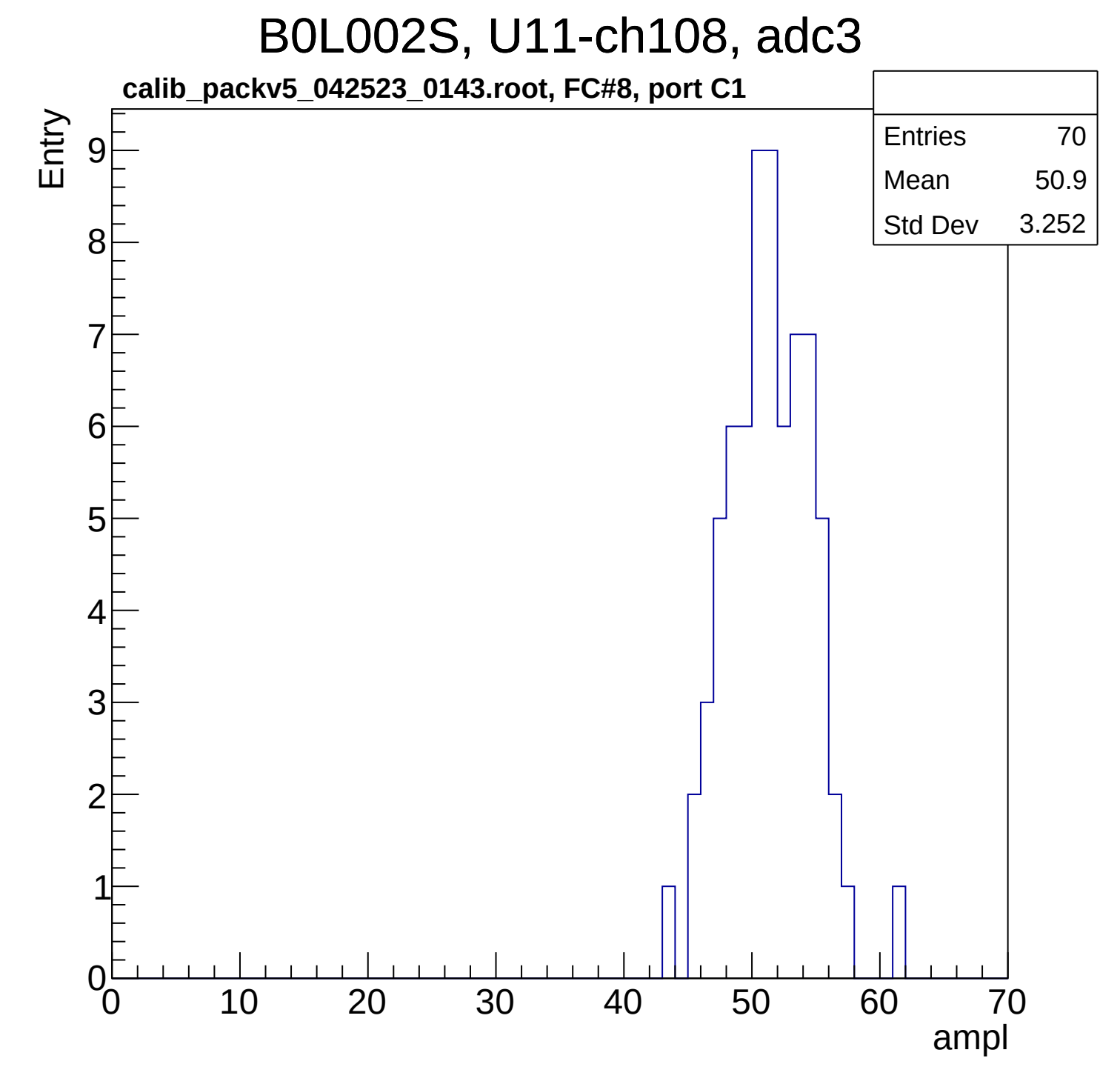
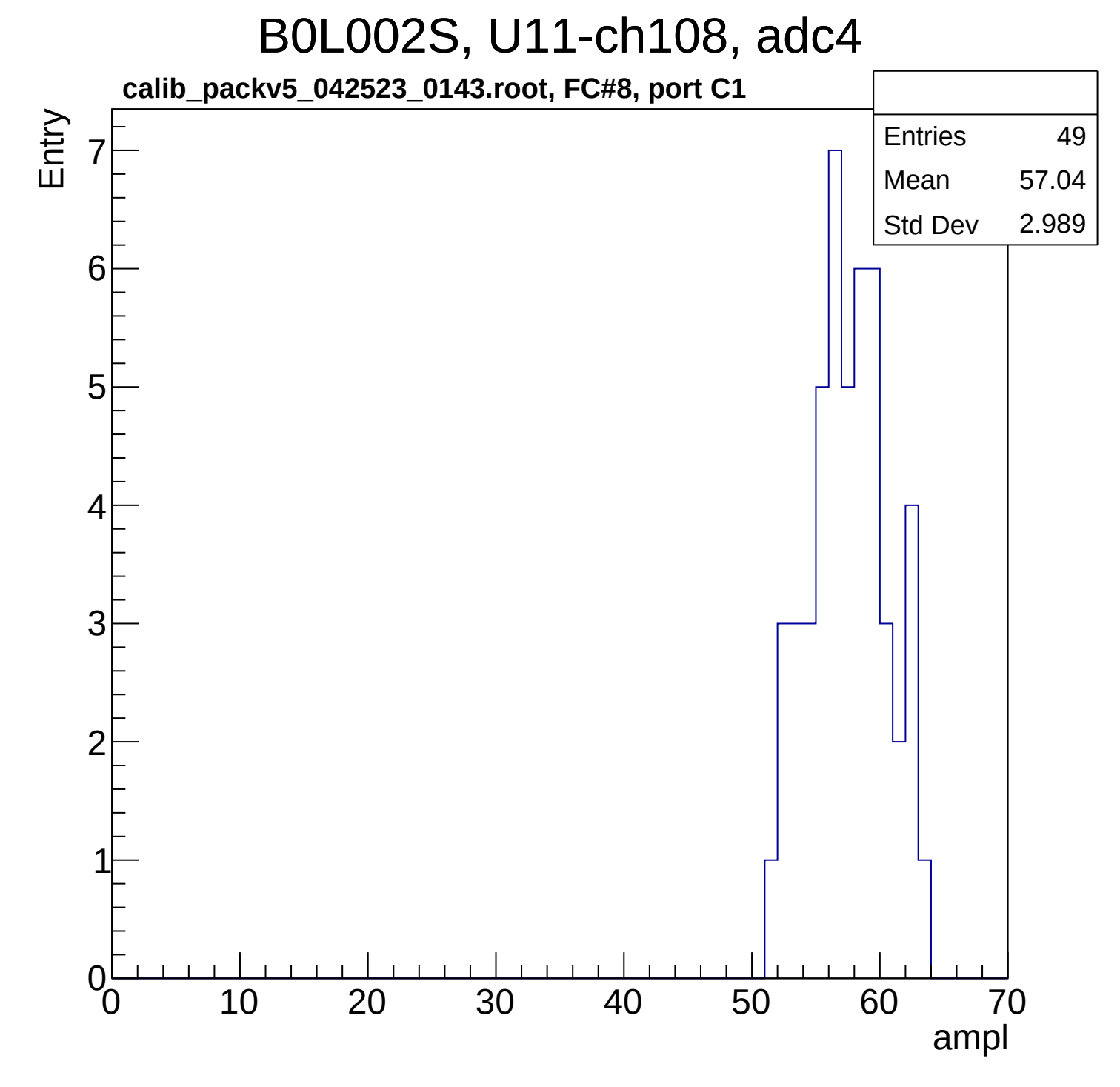
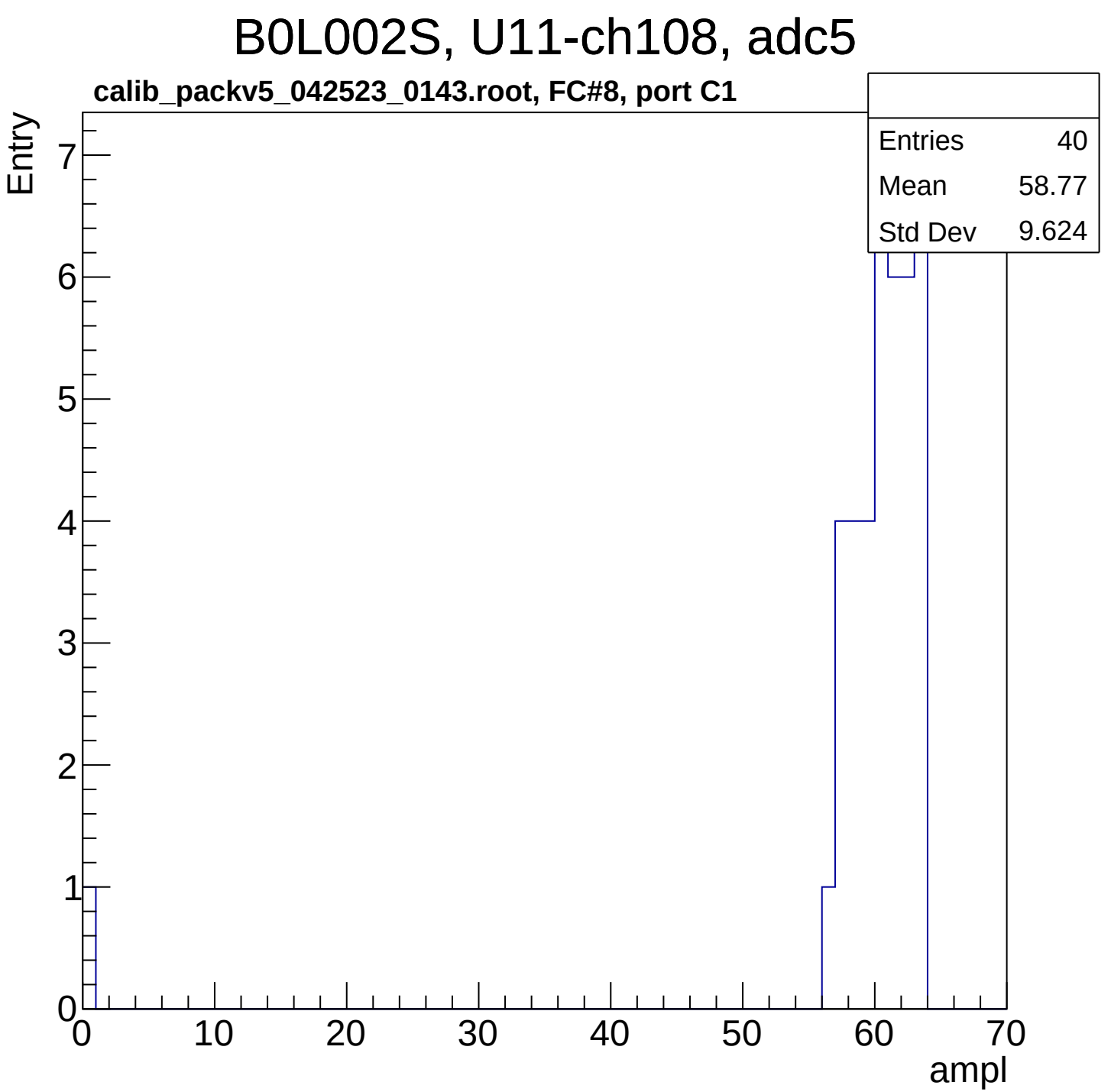
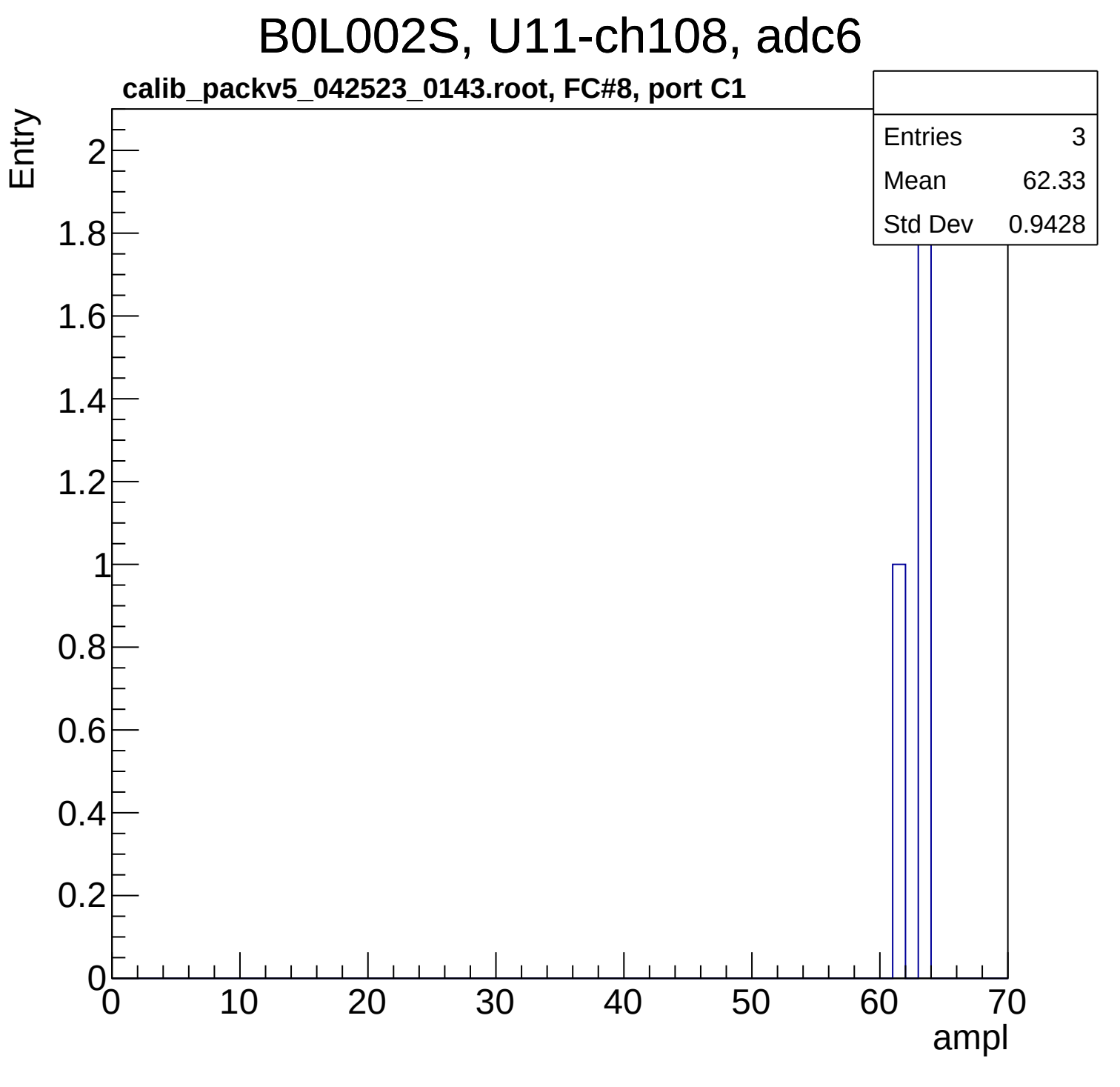
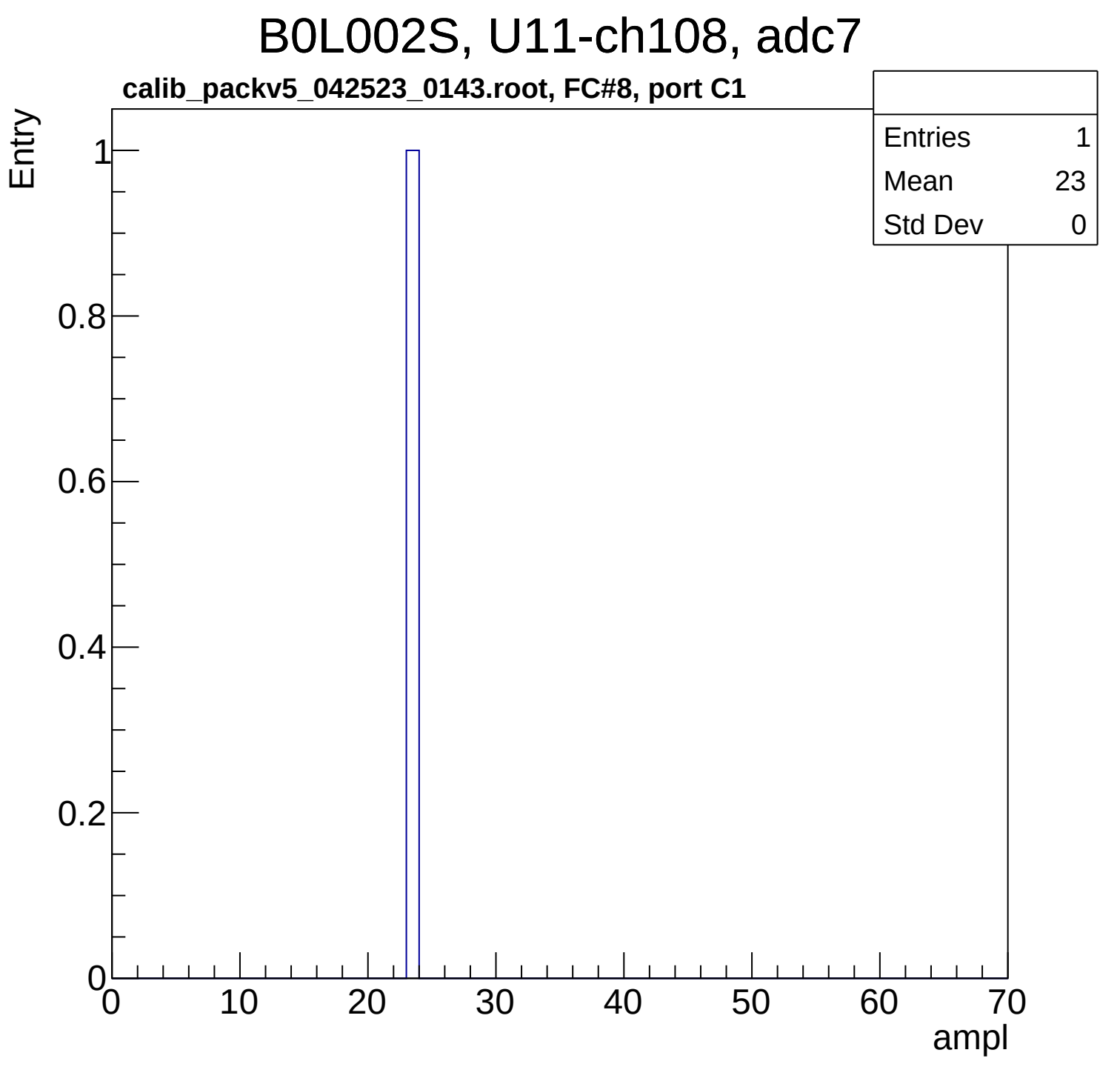
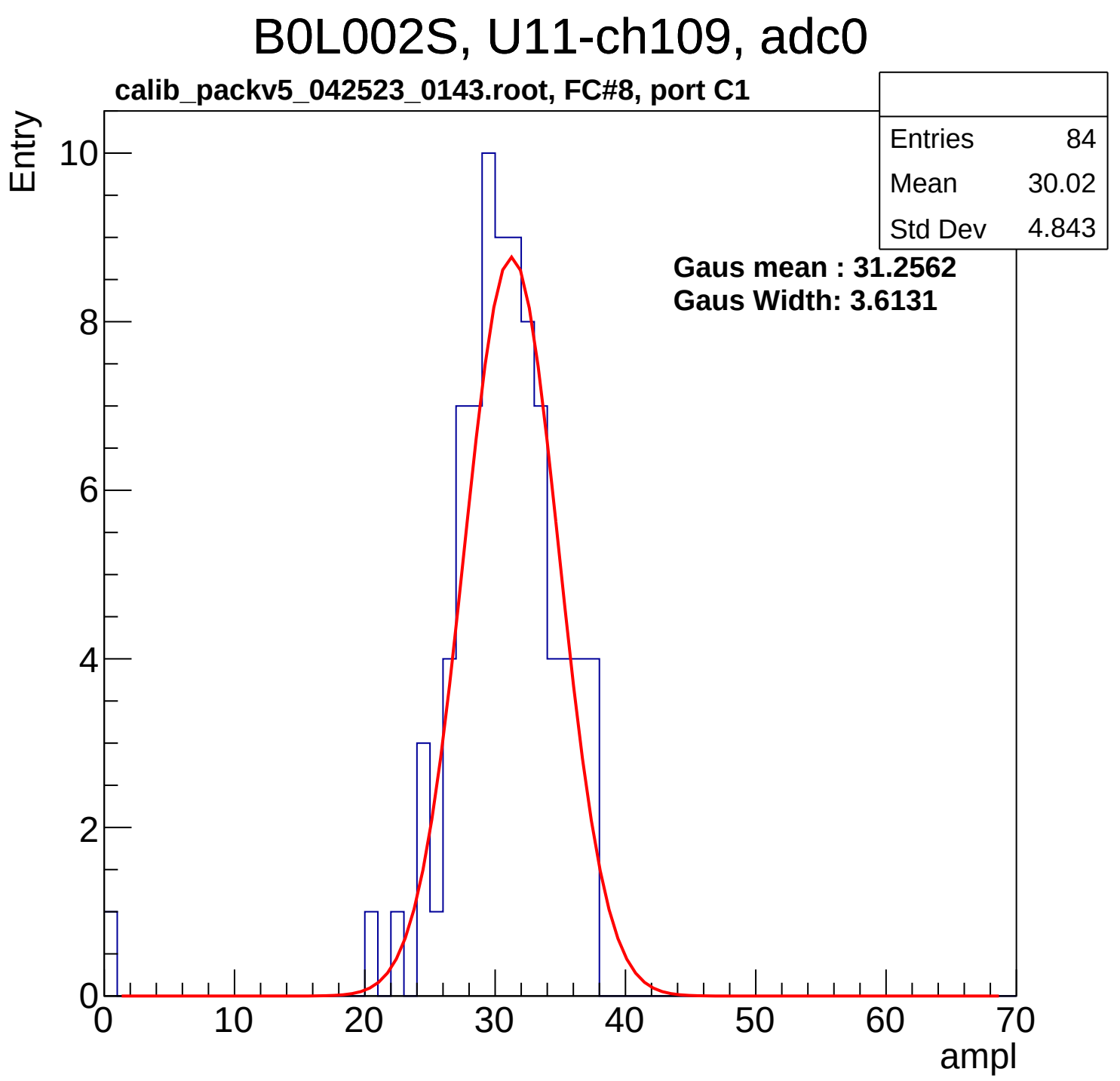

B0L002S, U11-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 50.9
Std Dev 3.252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

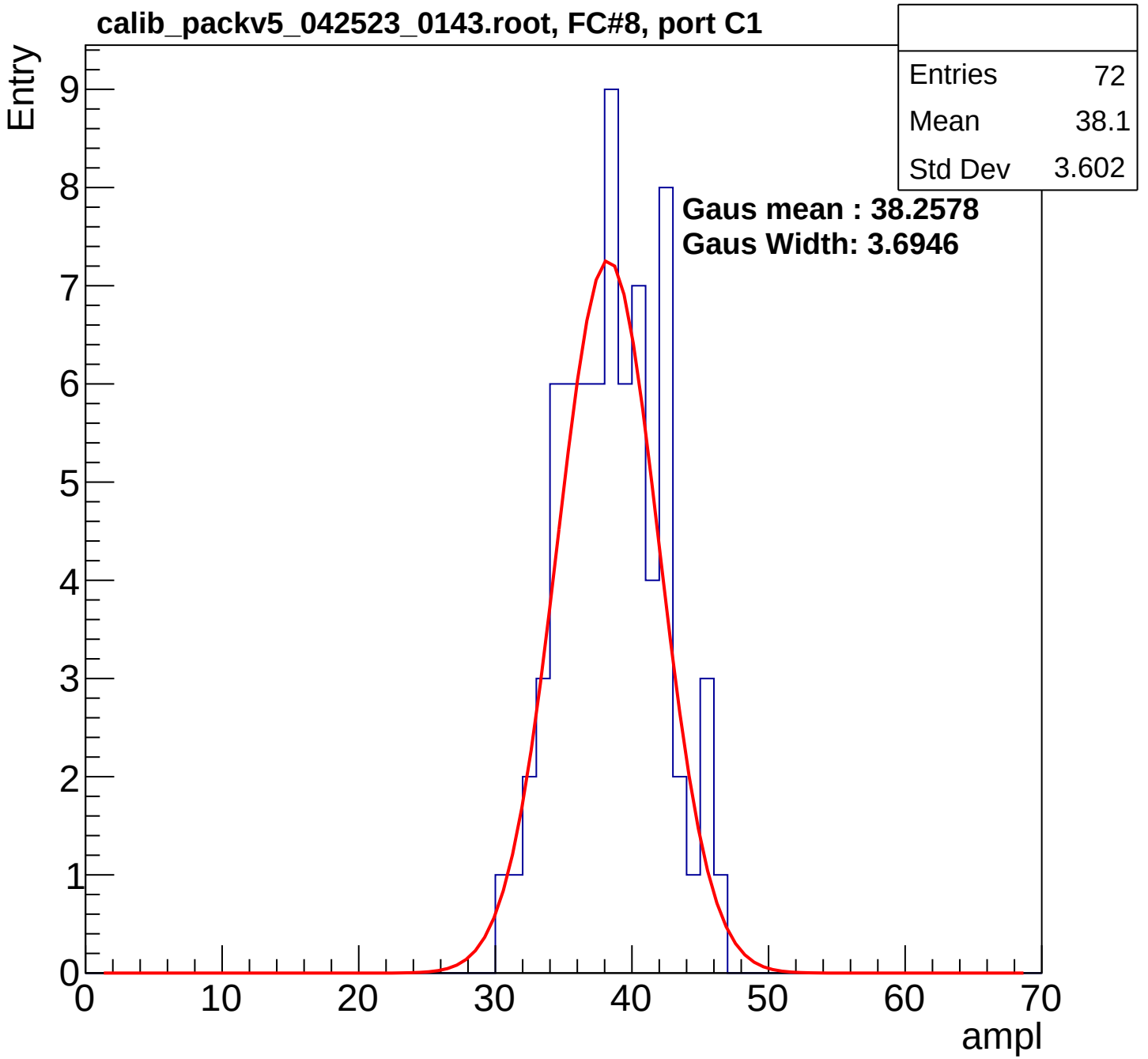

B0L002S, U11-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.04
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.77
Std Dev 9.624
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.02
Std Dev 4.843
Gaus mean : 31.2562
Gaus Width: 3.6131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.1
Std Dev 3.602
Gaus mean : 38.2578
Gaus Width: 3.6946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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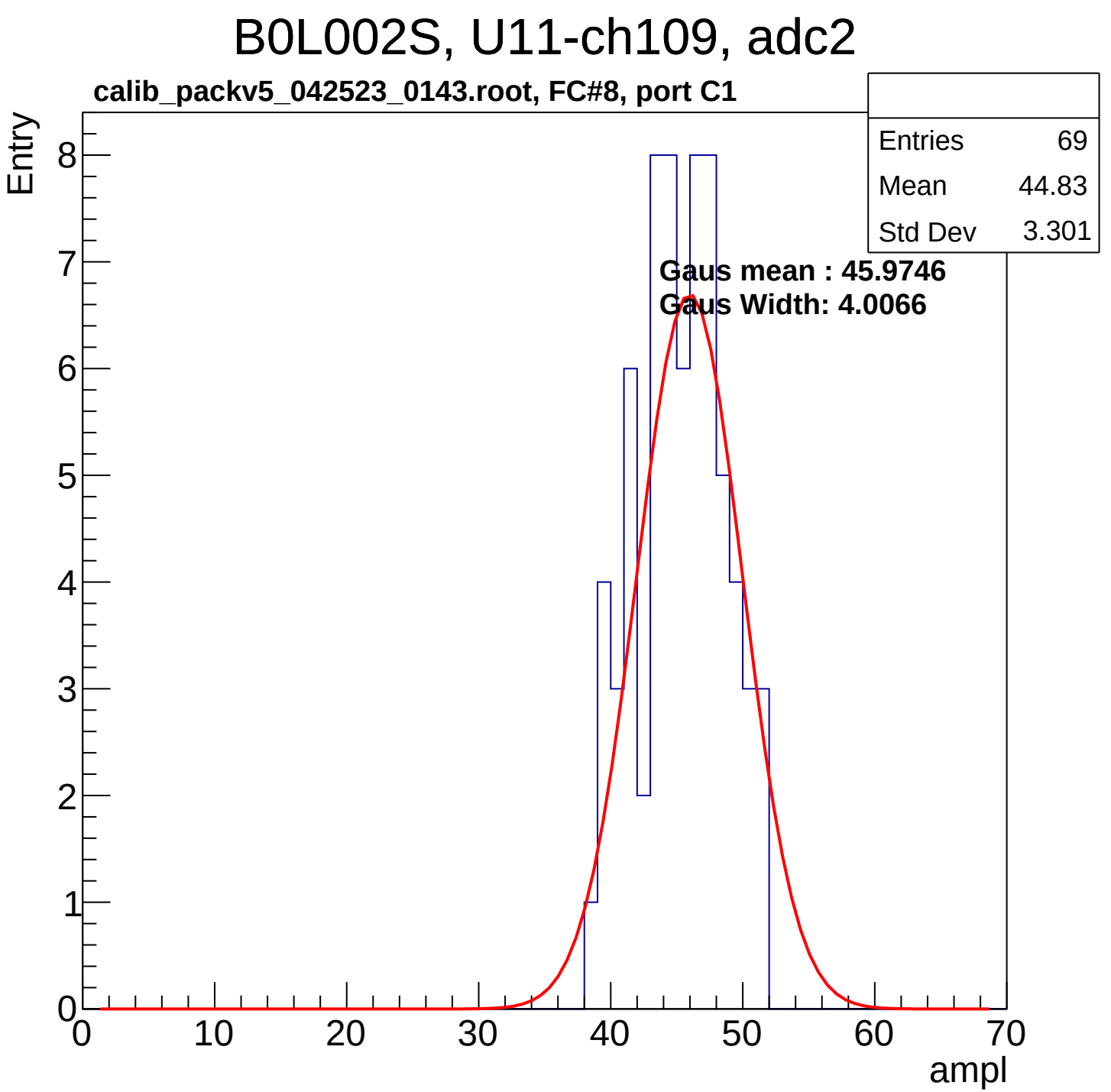
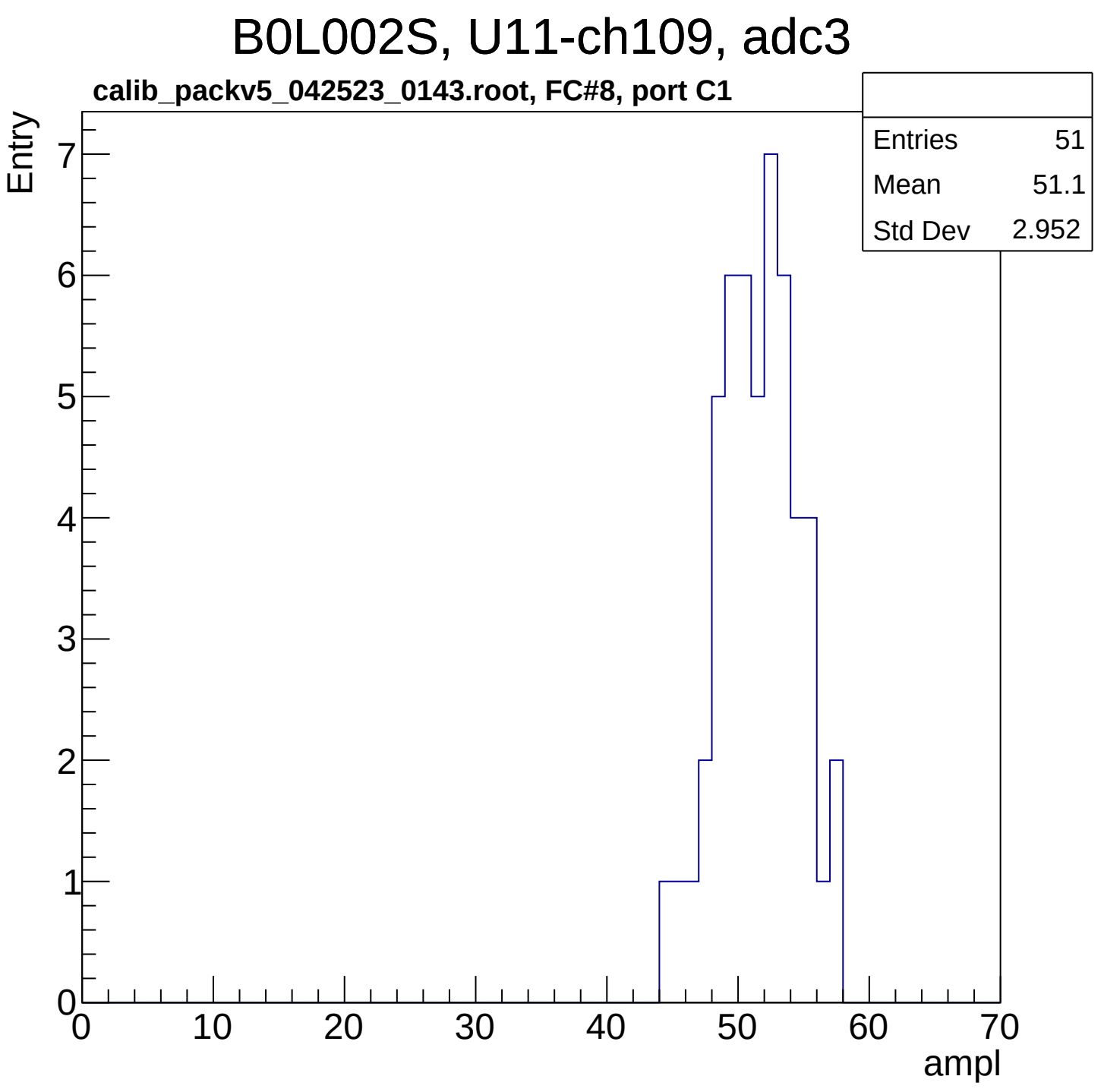
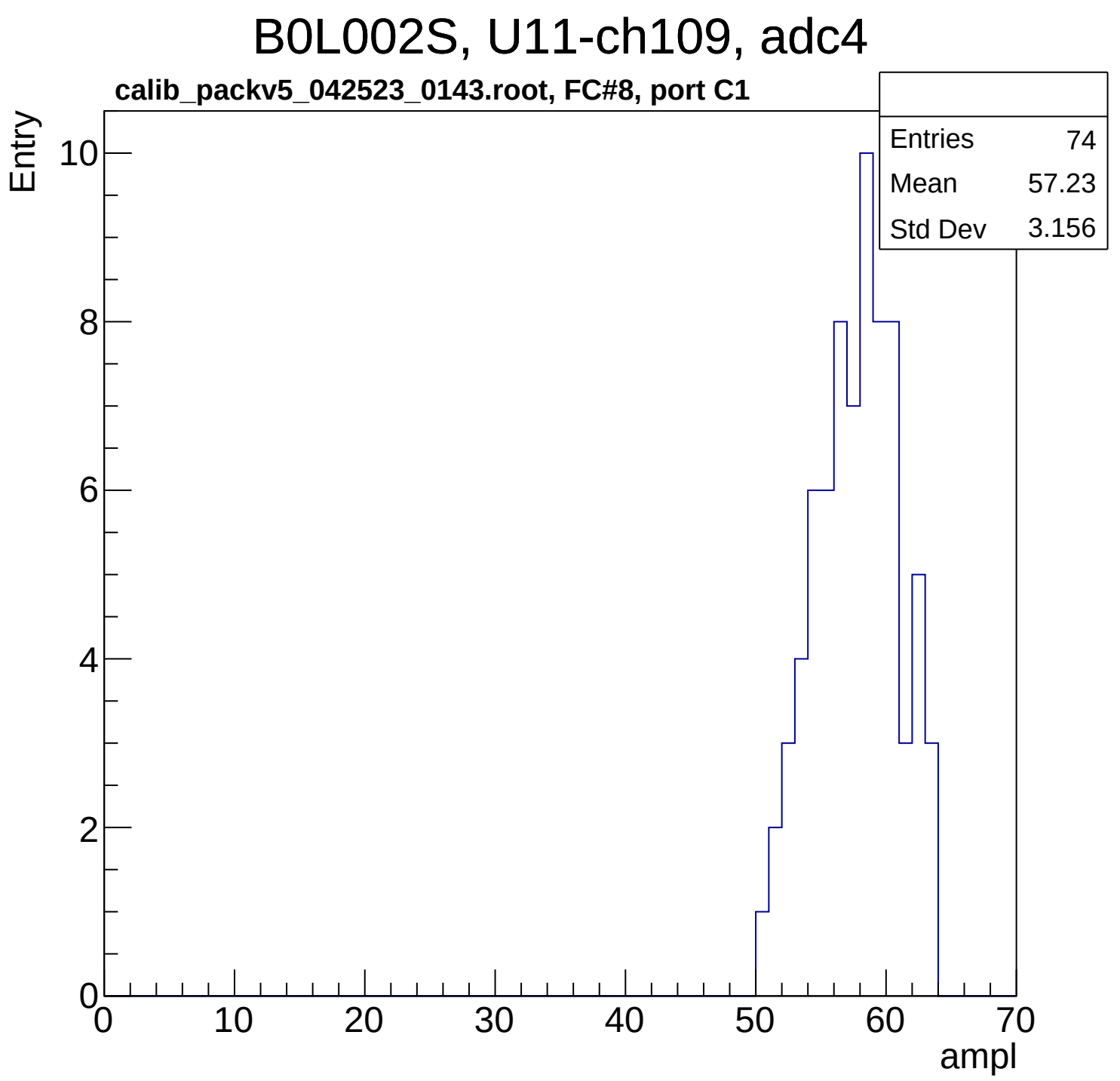
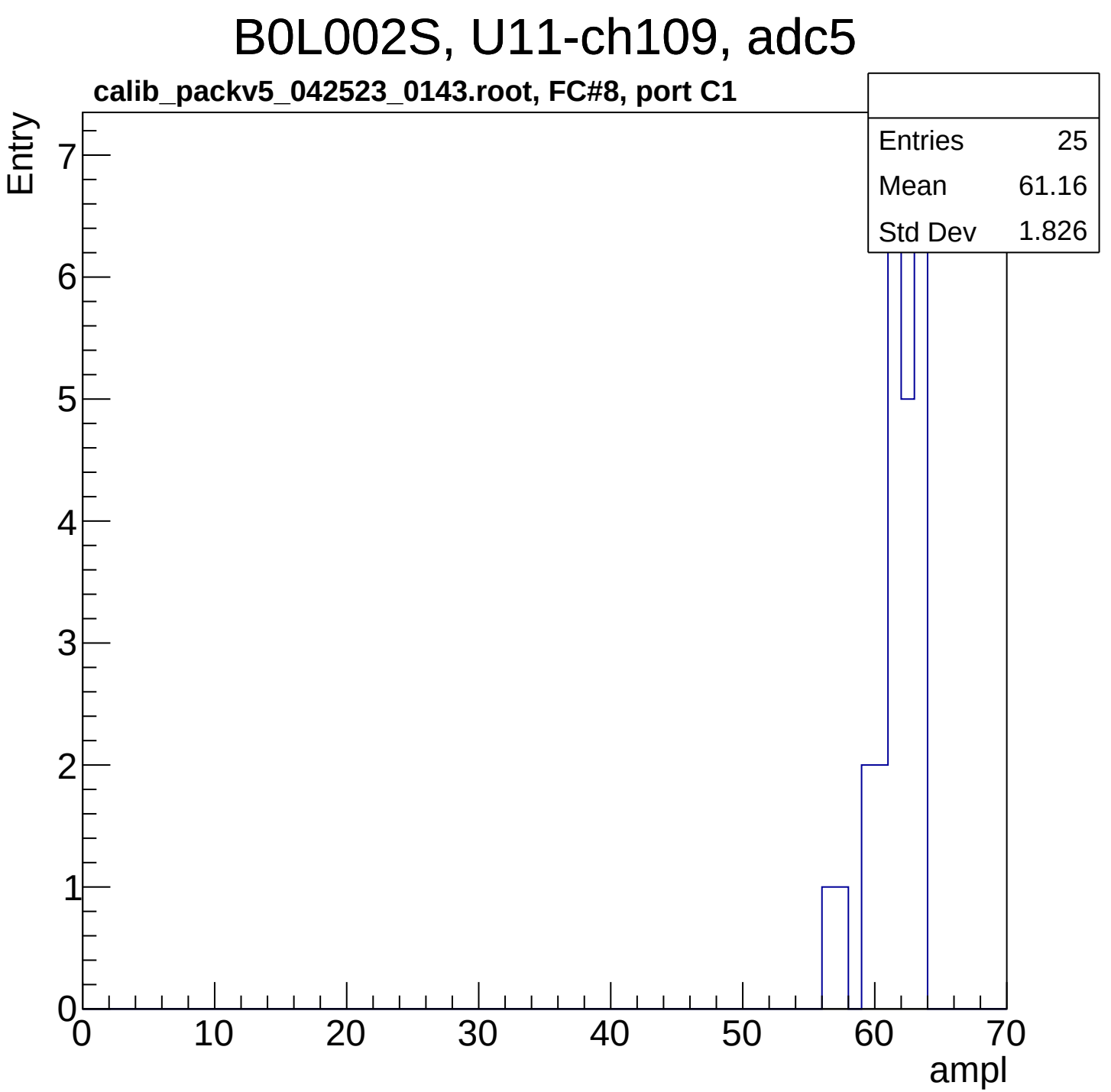
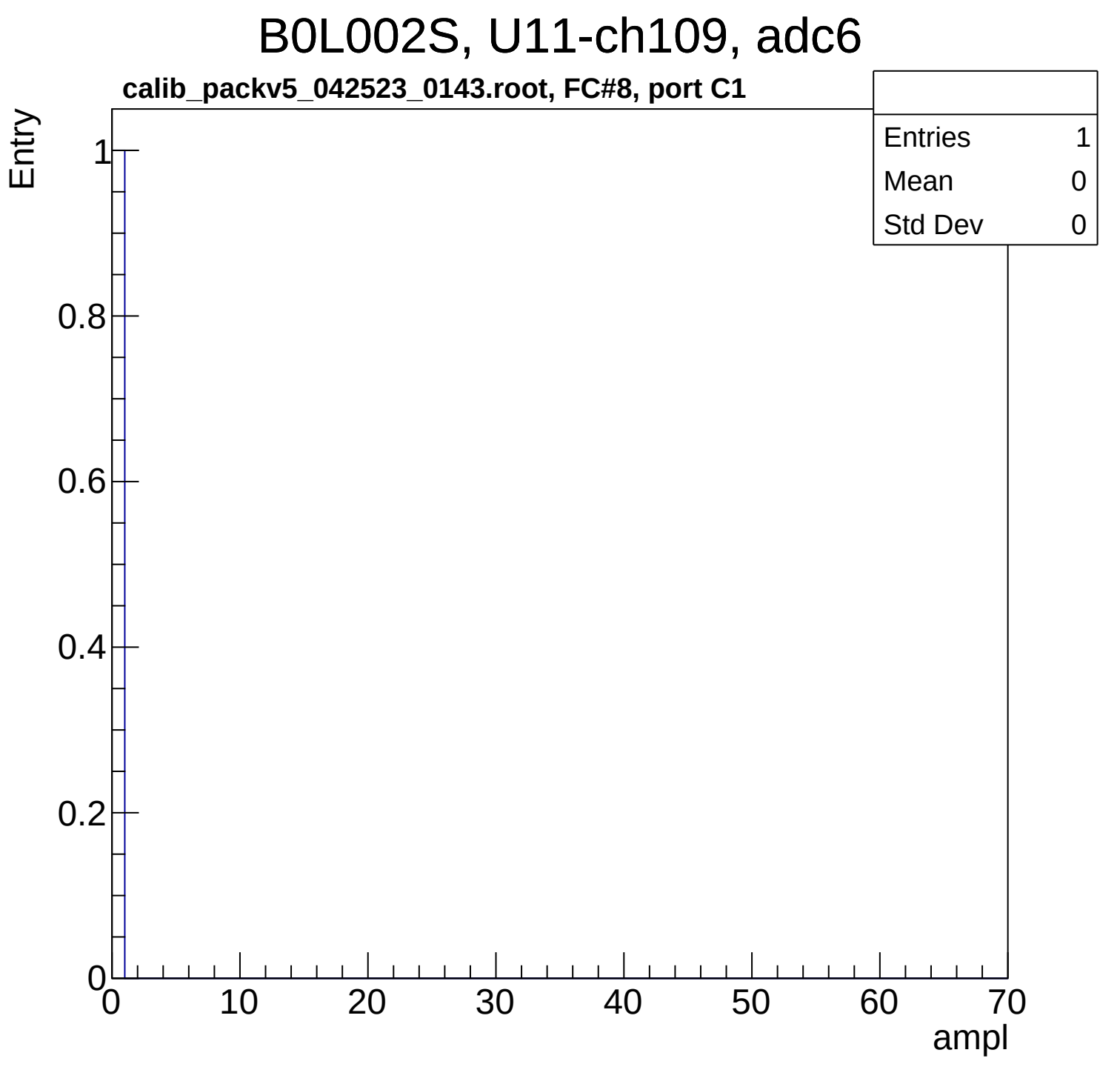
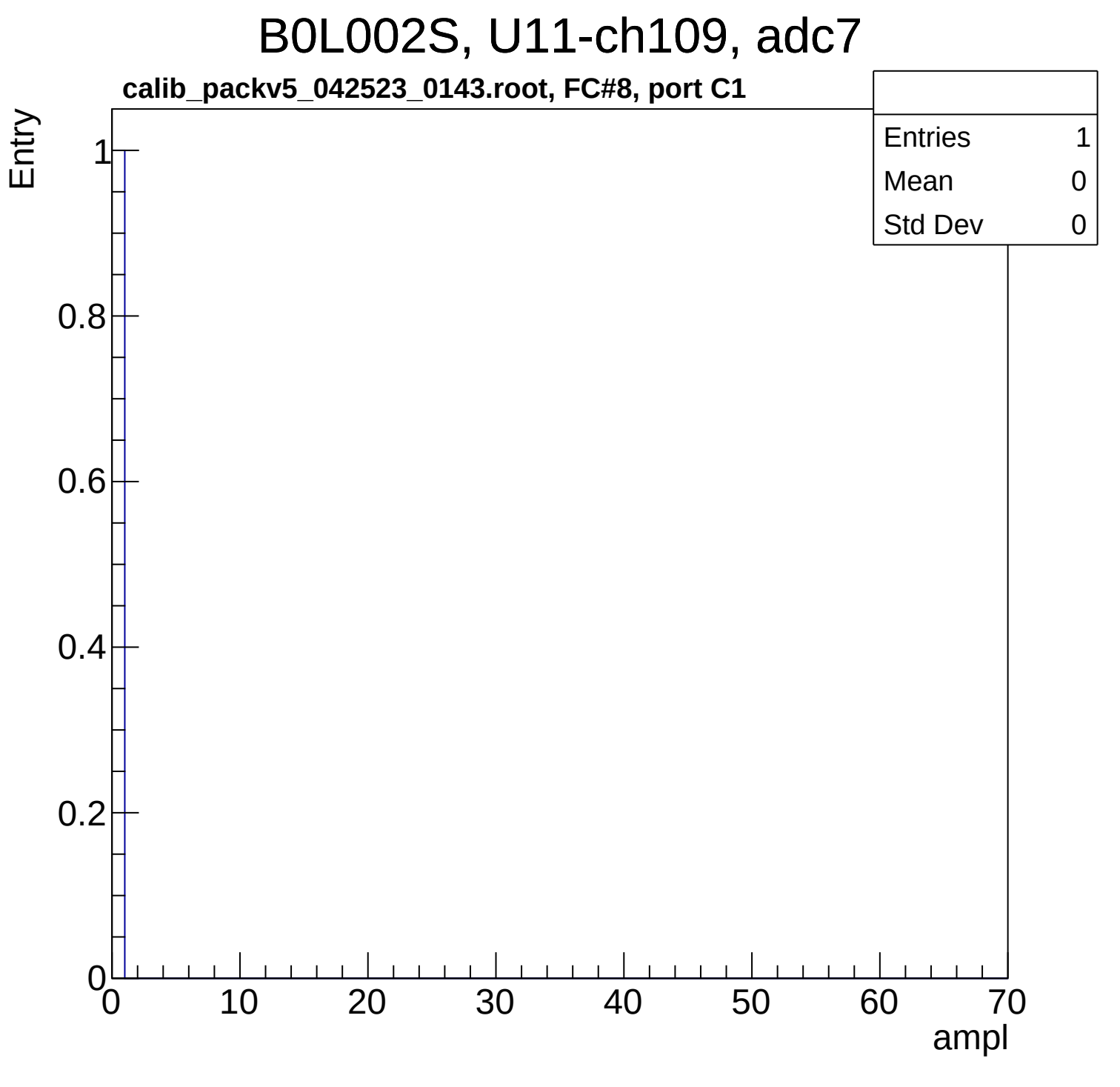

B0L002S, U11-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.83
Std Dev 3.301
Gaus mean : 45.9746
Gaus Width: 4.0066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

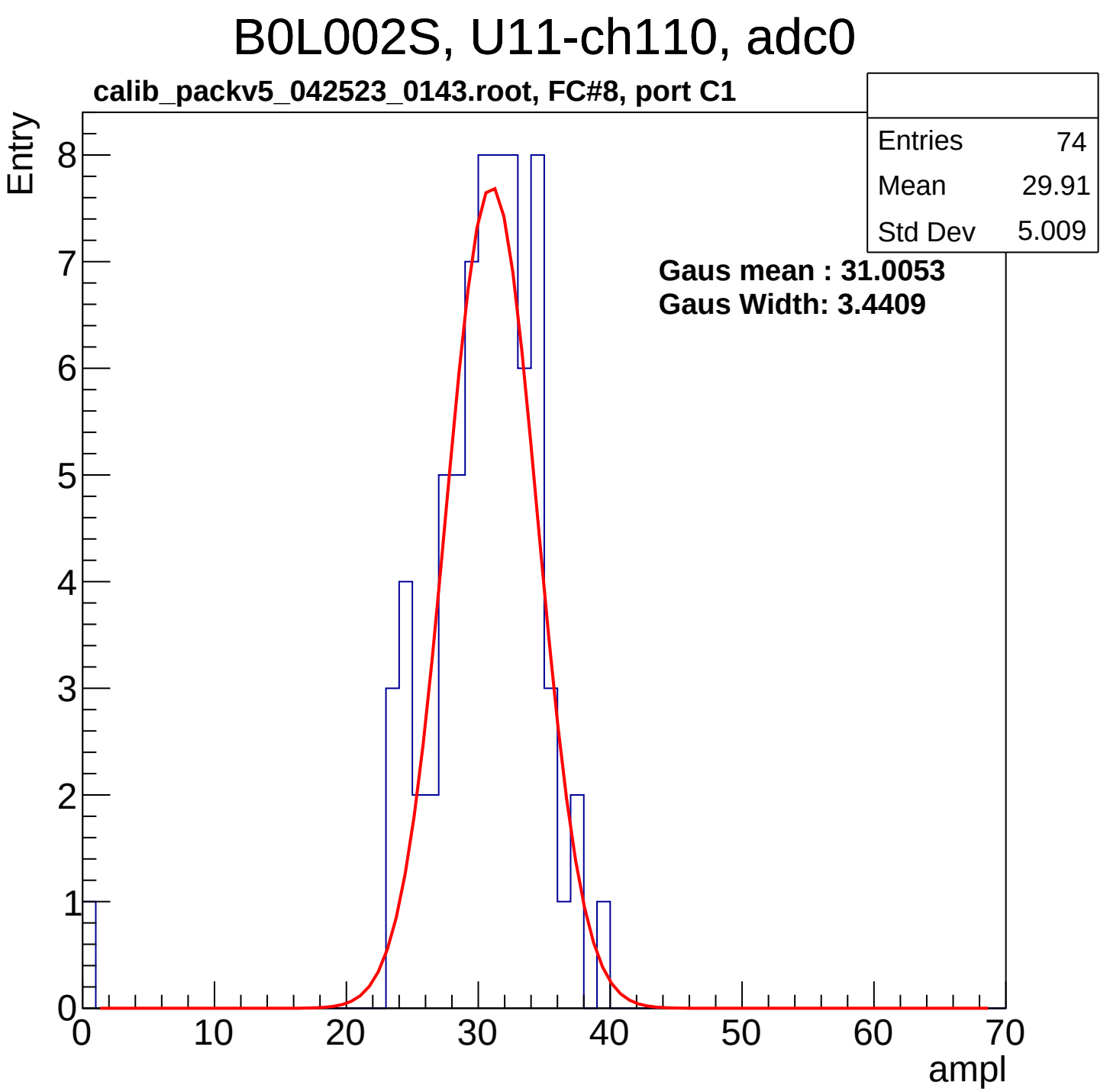

B0L002S, U11-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 51.1
Std Dev 2.952
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

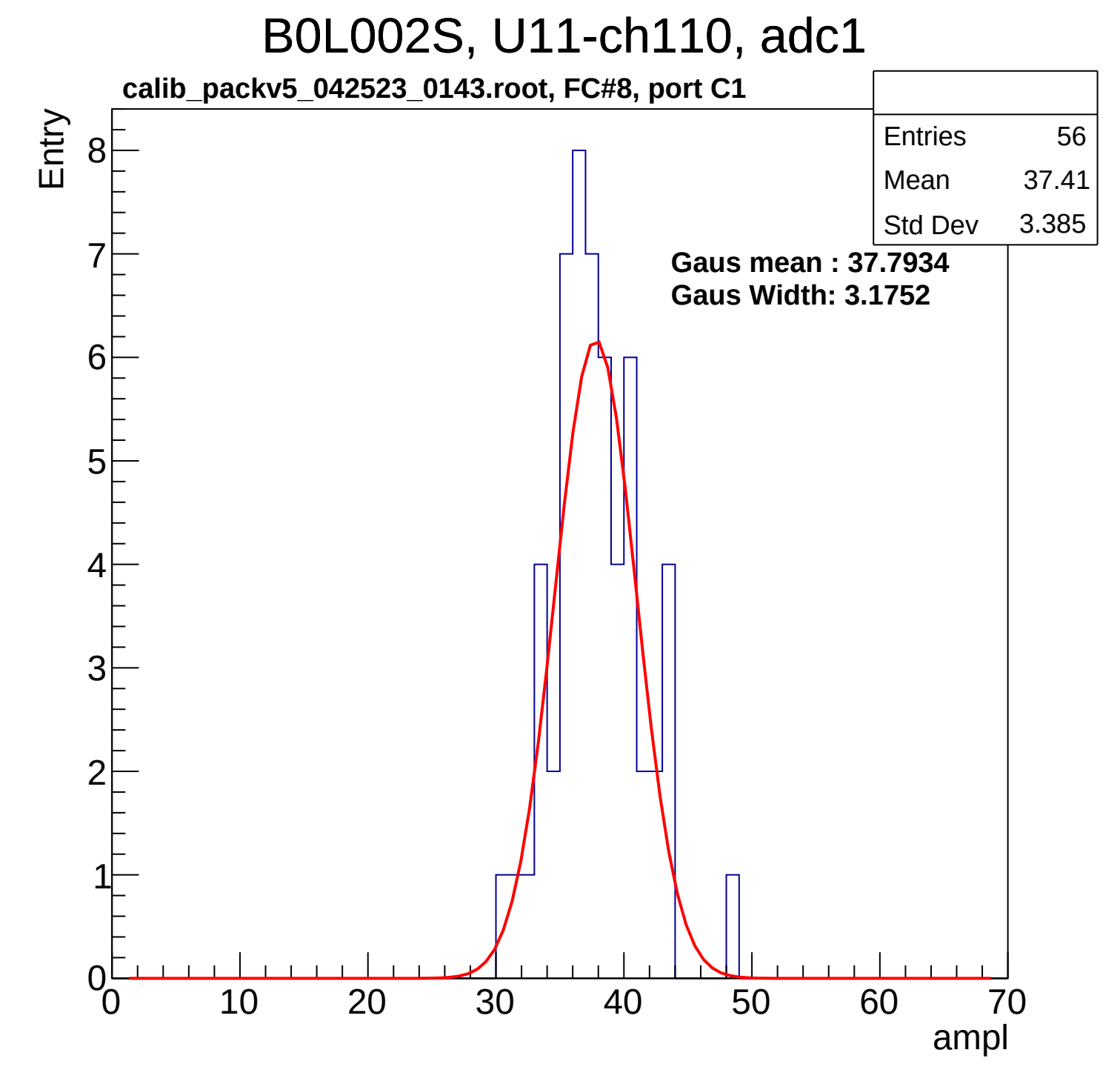

B0L002S, U11-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 57.23
Std Dev 3.156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

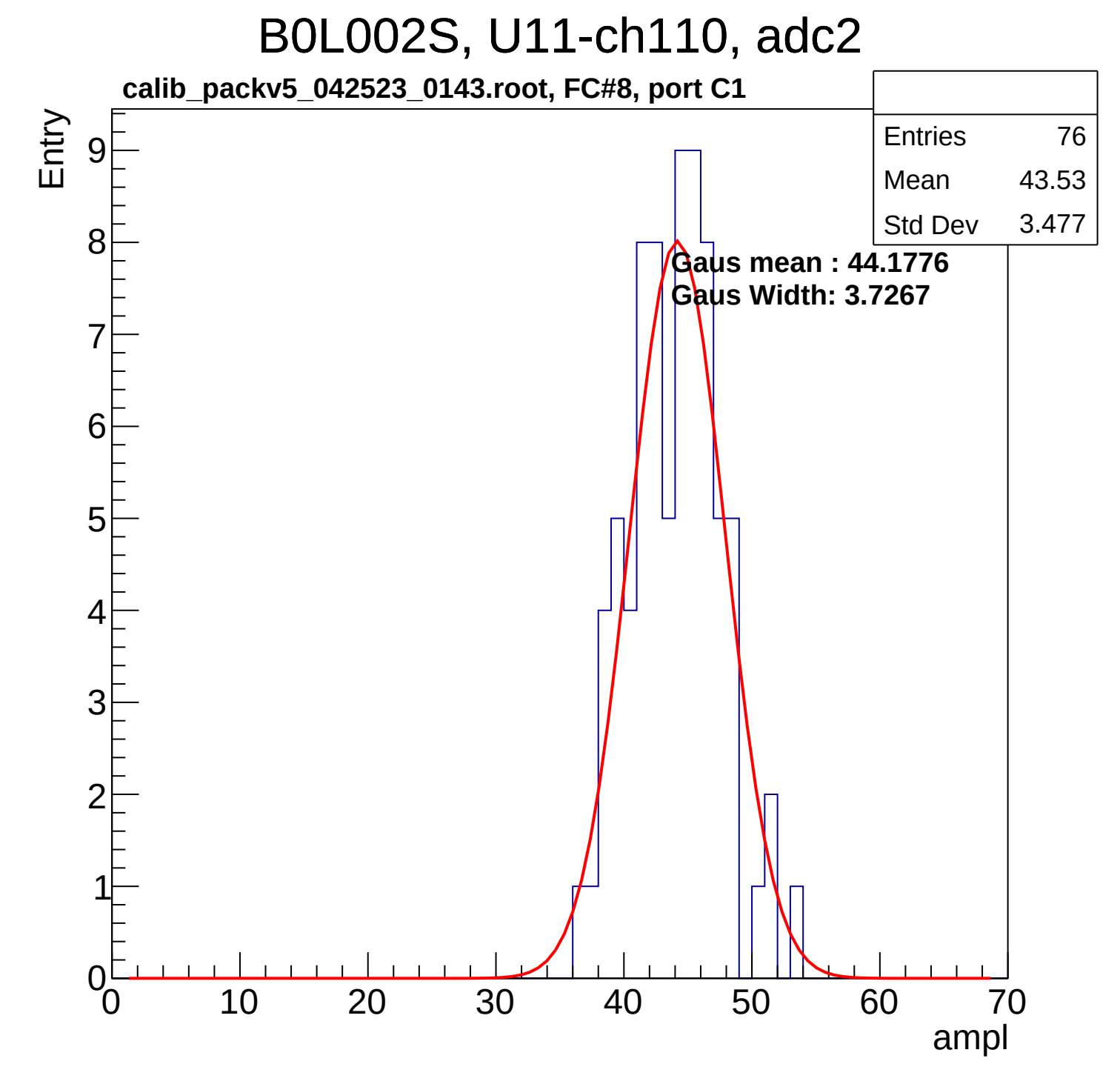

B0L002S, U11-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61.16
Std Dev 1.826
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

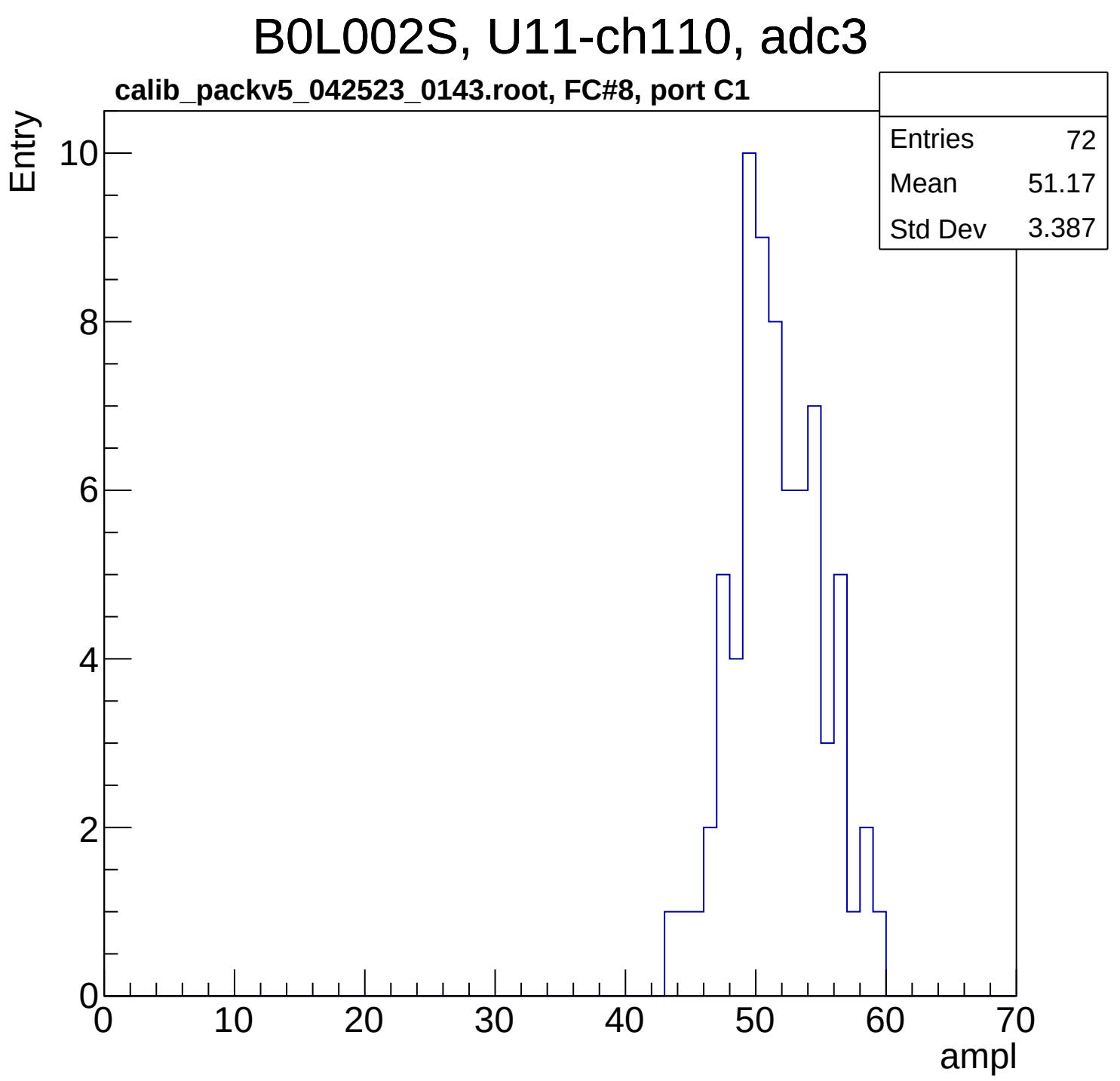

B0L002S, U11-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

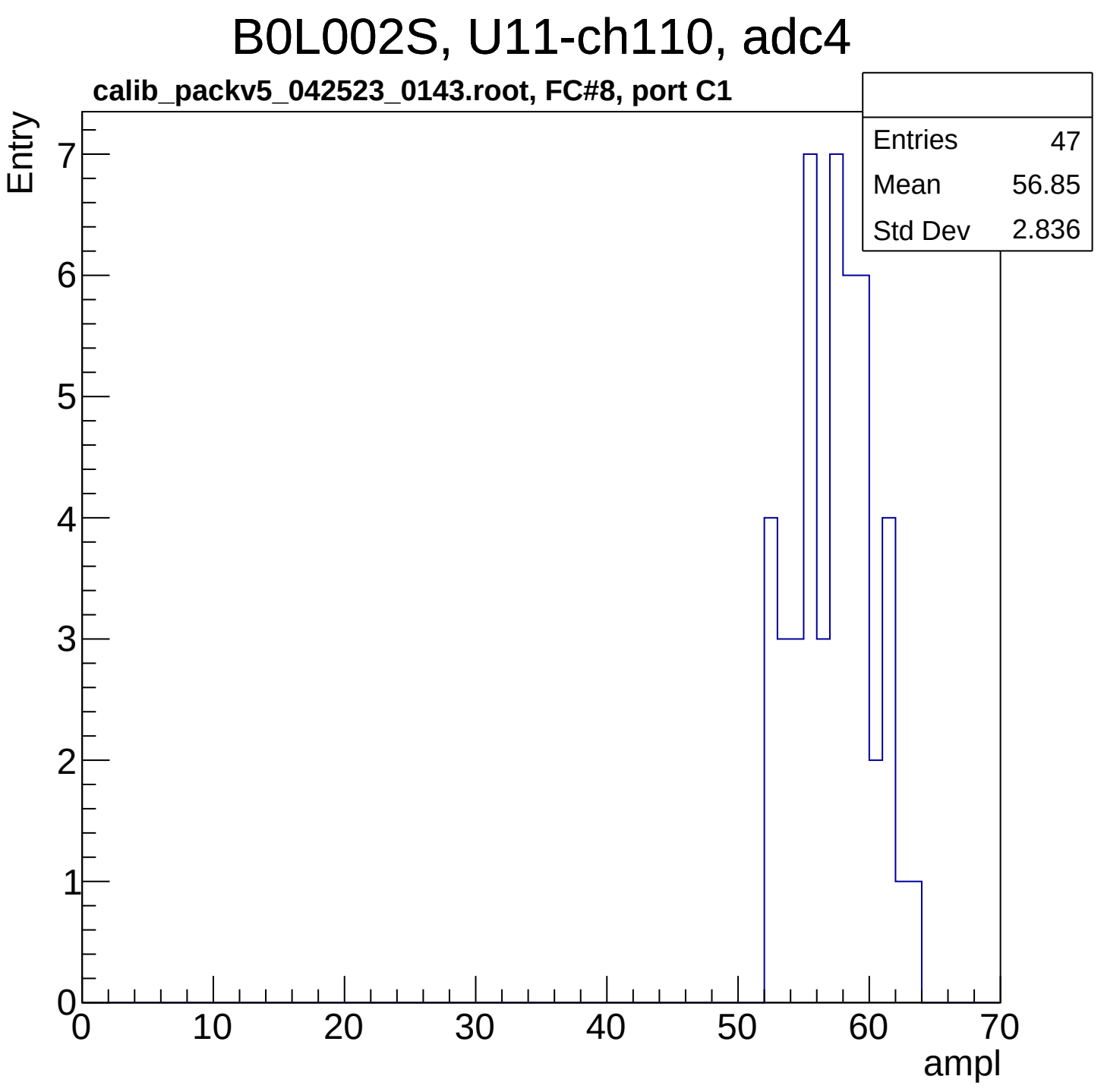

B0L002S, U11-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

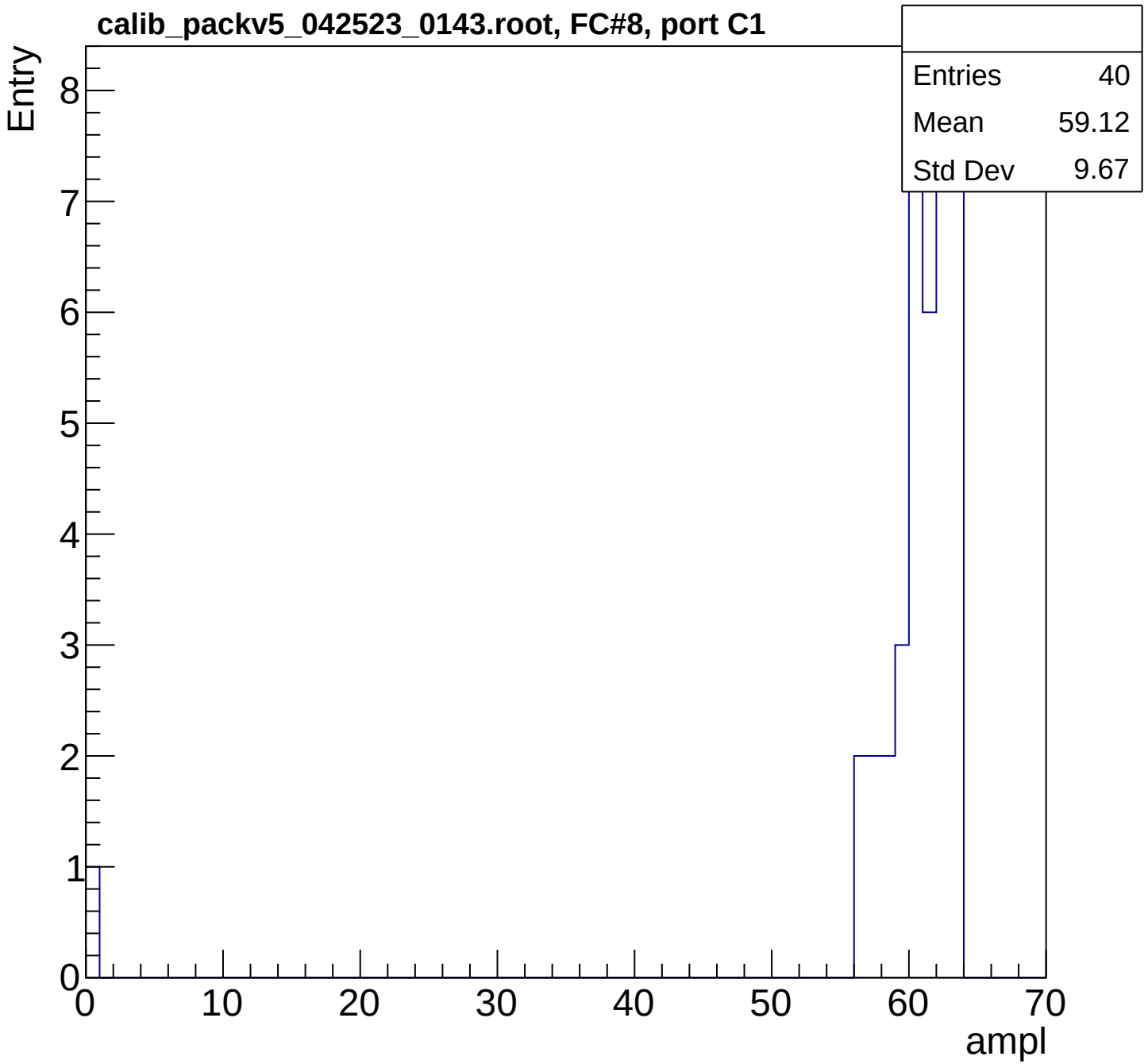

B0L002S, U11-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.91
Std Dev 5.009
Gaus mean : 31.0053
Gaus Width: 3.4409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 37.41
Std Dev 3.385
Gaus mean : 37.7934
Gaus Width: 3.1752
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 43.53
Std Dev 3.477
Gaus mean : 44.1776
Gaus Width: 3.7267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.17
Std Dev 3.387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 56.85
Std Dev 2.836
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.12
Std Dev 9.67
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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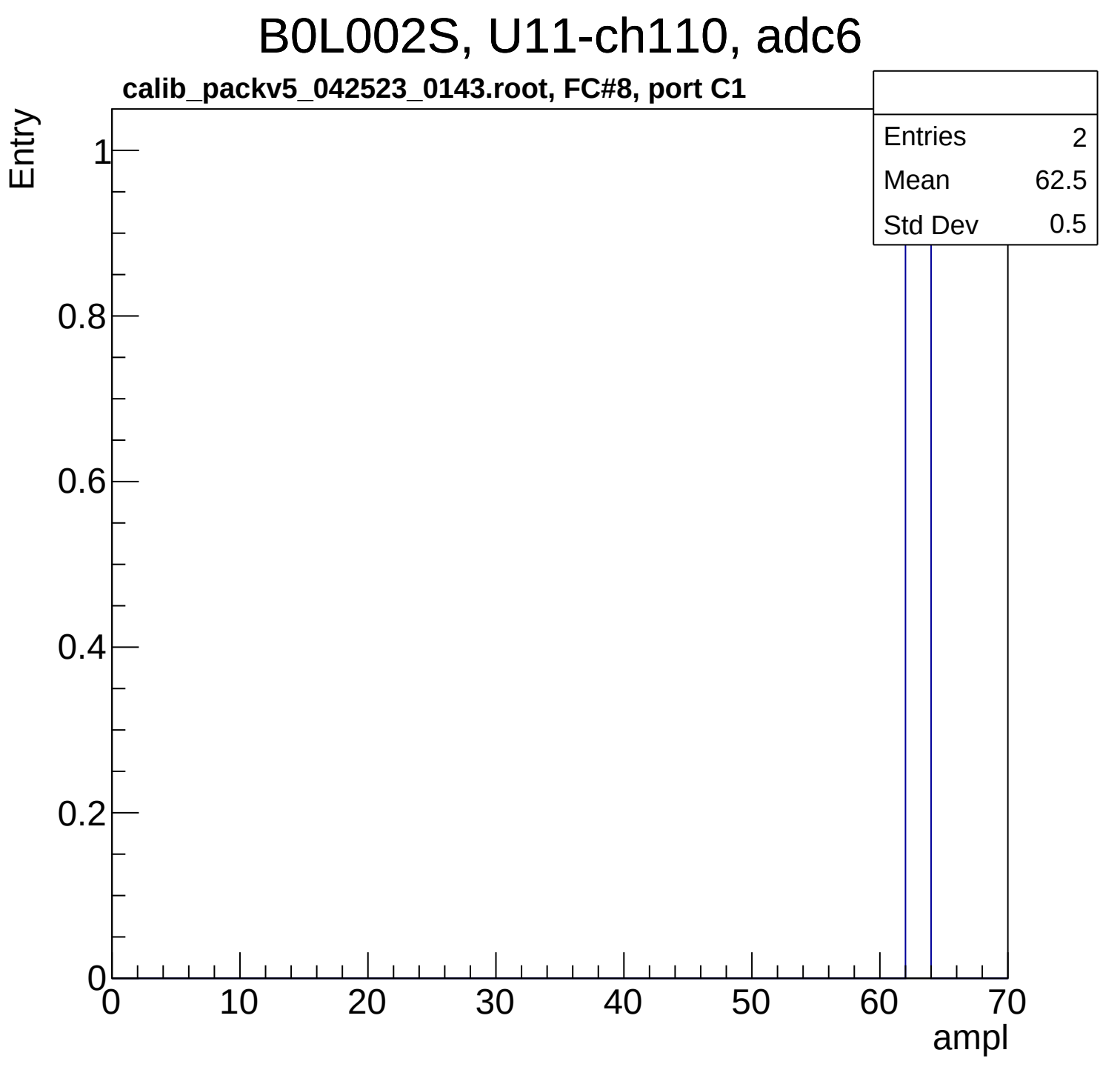
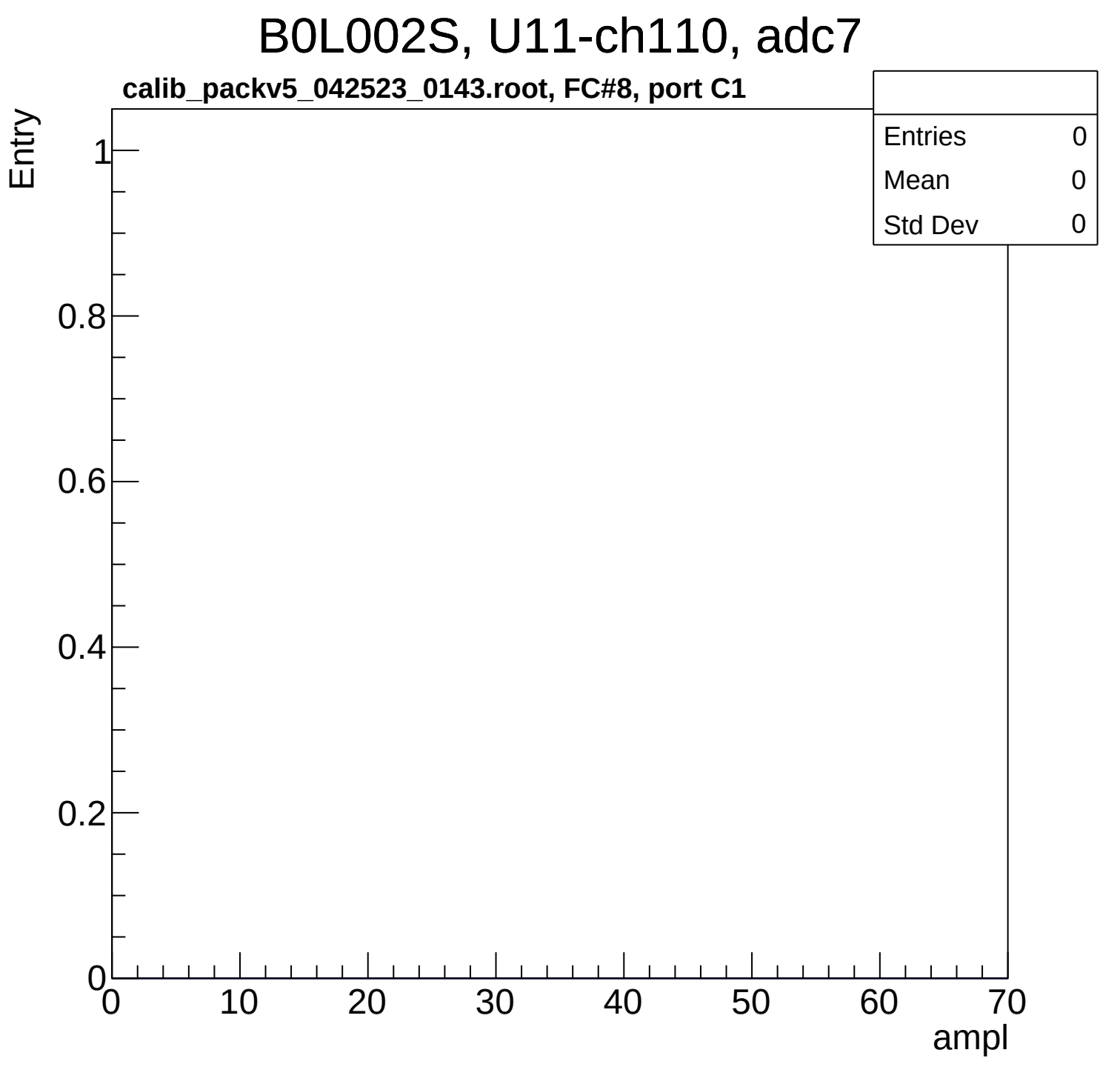
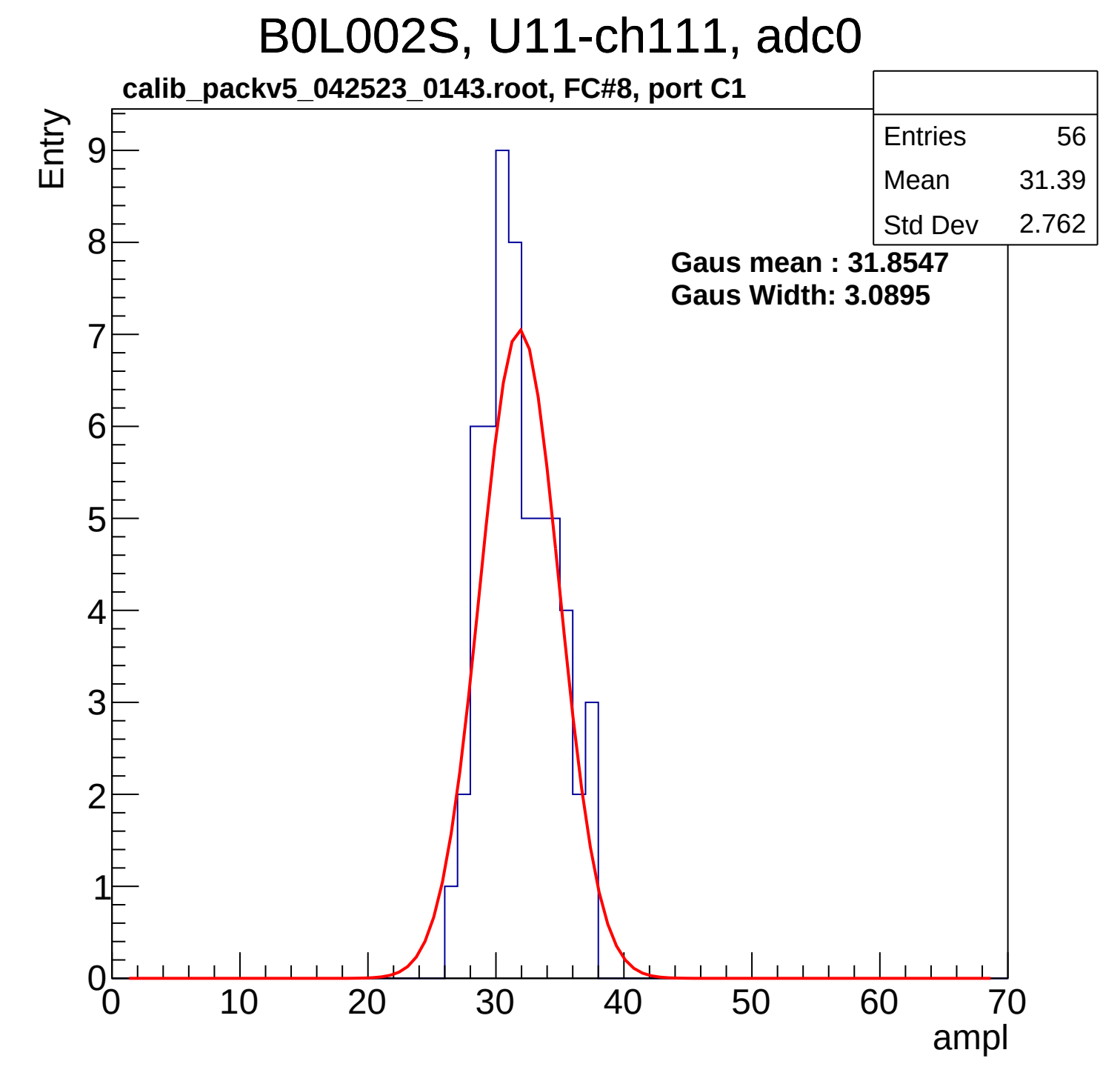
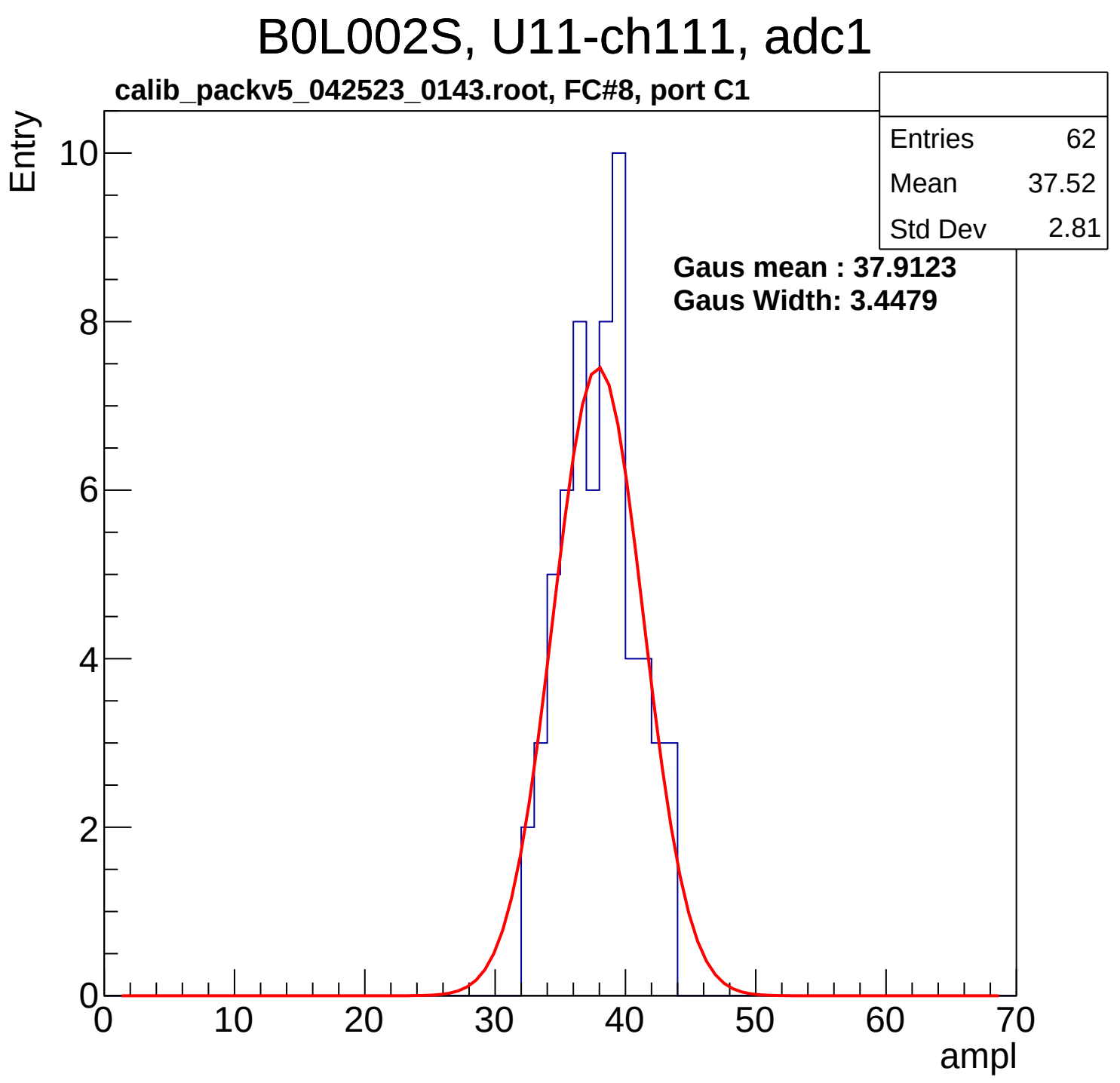
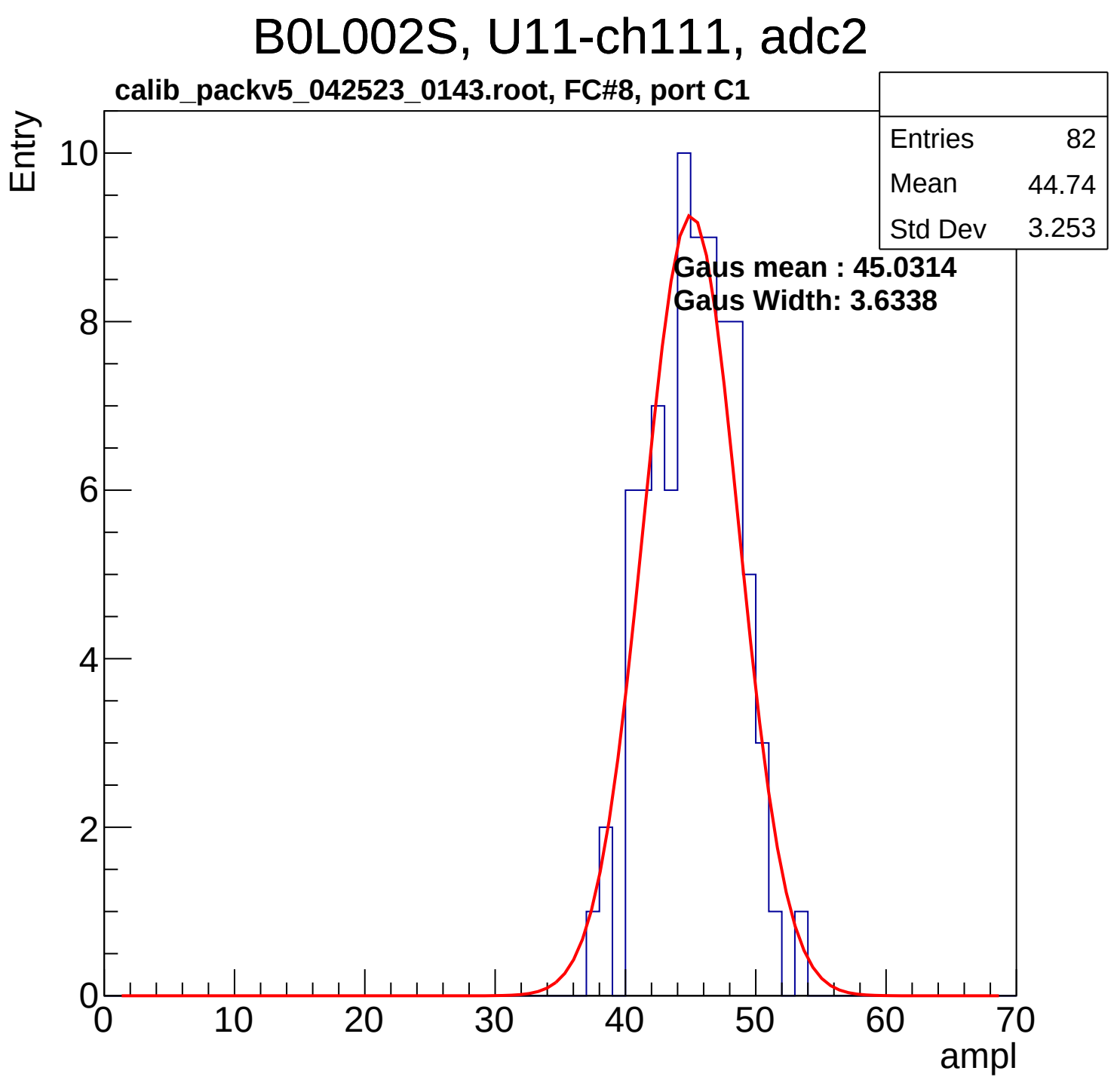
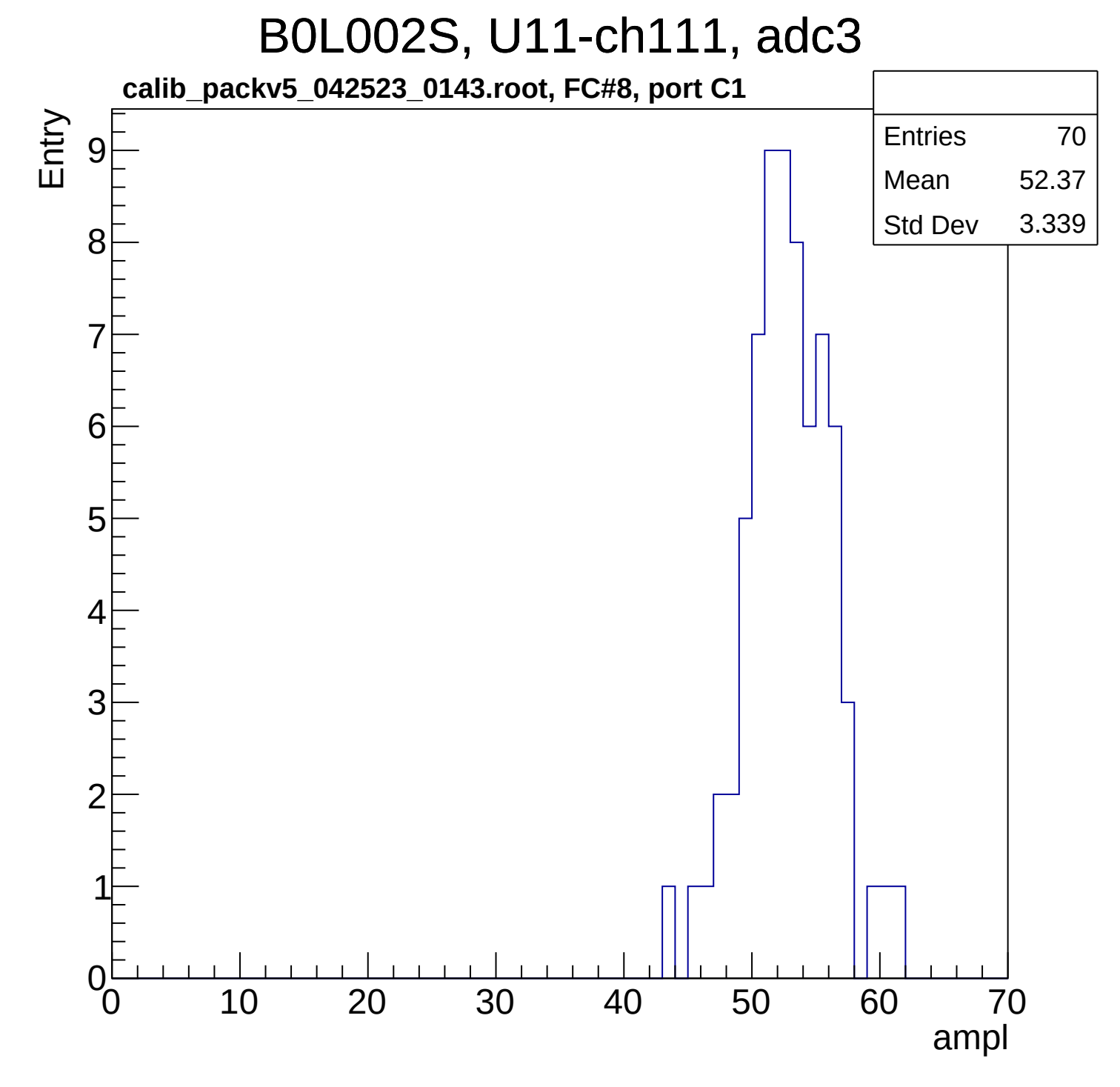

B0L002S, U11-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.39
Std Dev 2.762
Gaus mean : 31.8547
Gaus Width: 3.0895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.52
Std Dev 2.81
Gaus mean : 37.9123
Gaus Width: 3.4479
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 44.74
Std Dev 3.253
Gaus mean : 45.0314
Gaus Width: 3.6338
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 52.37
Std Dev 3.339
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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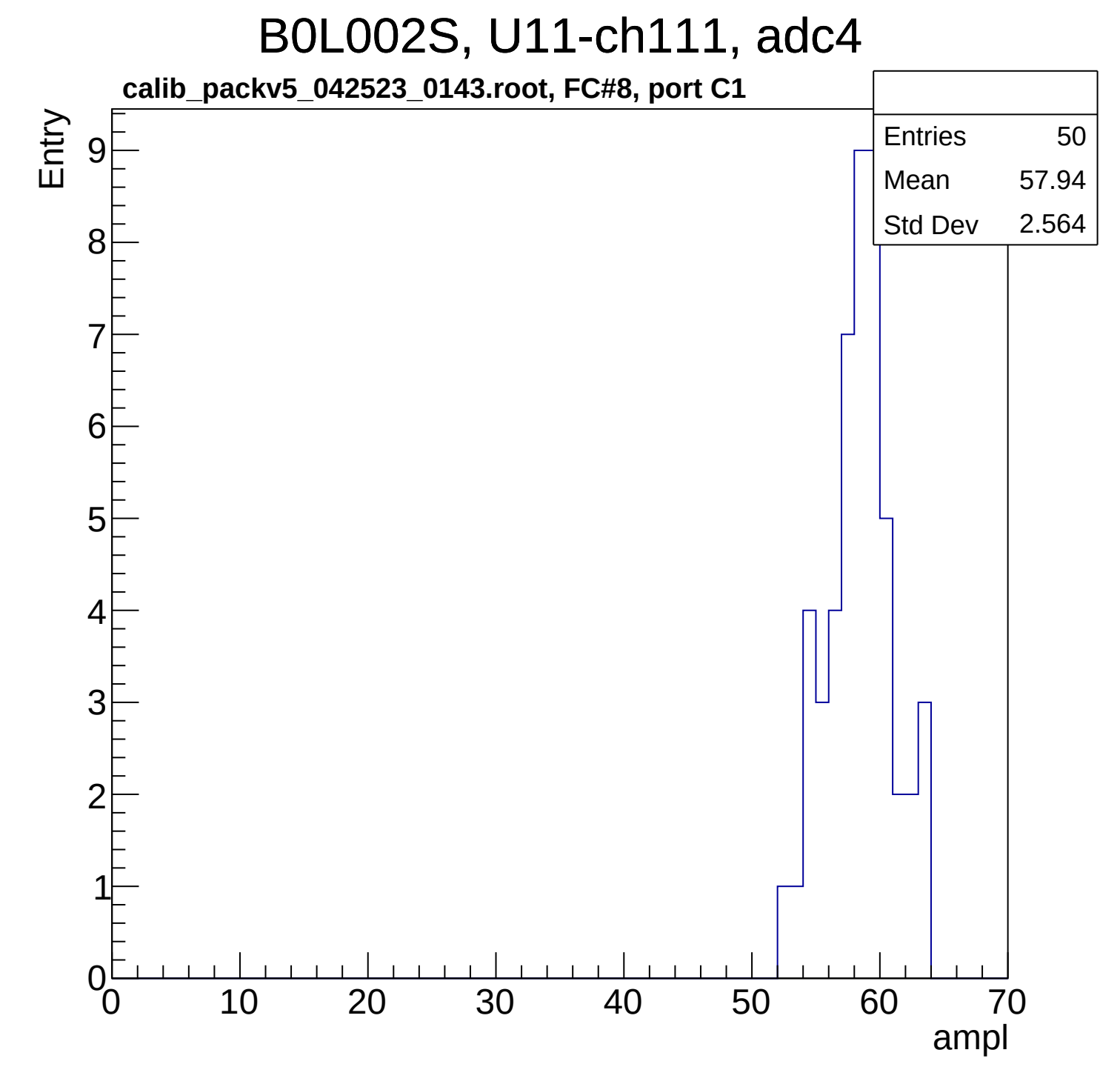
B0L002S, U11-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.94
Std Dev 2.564
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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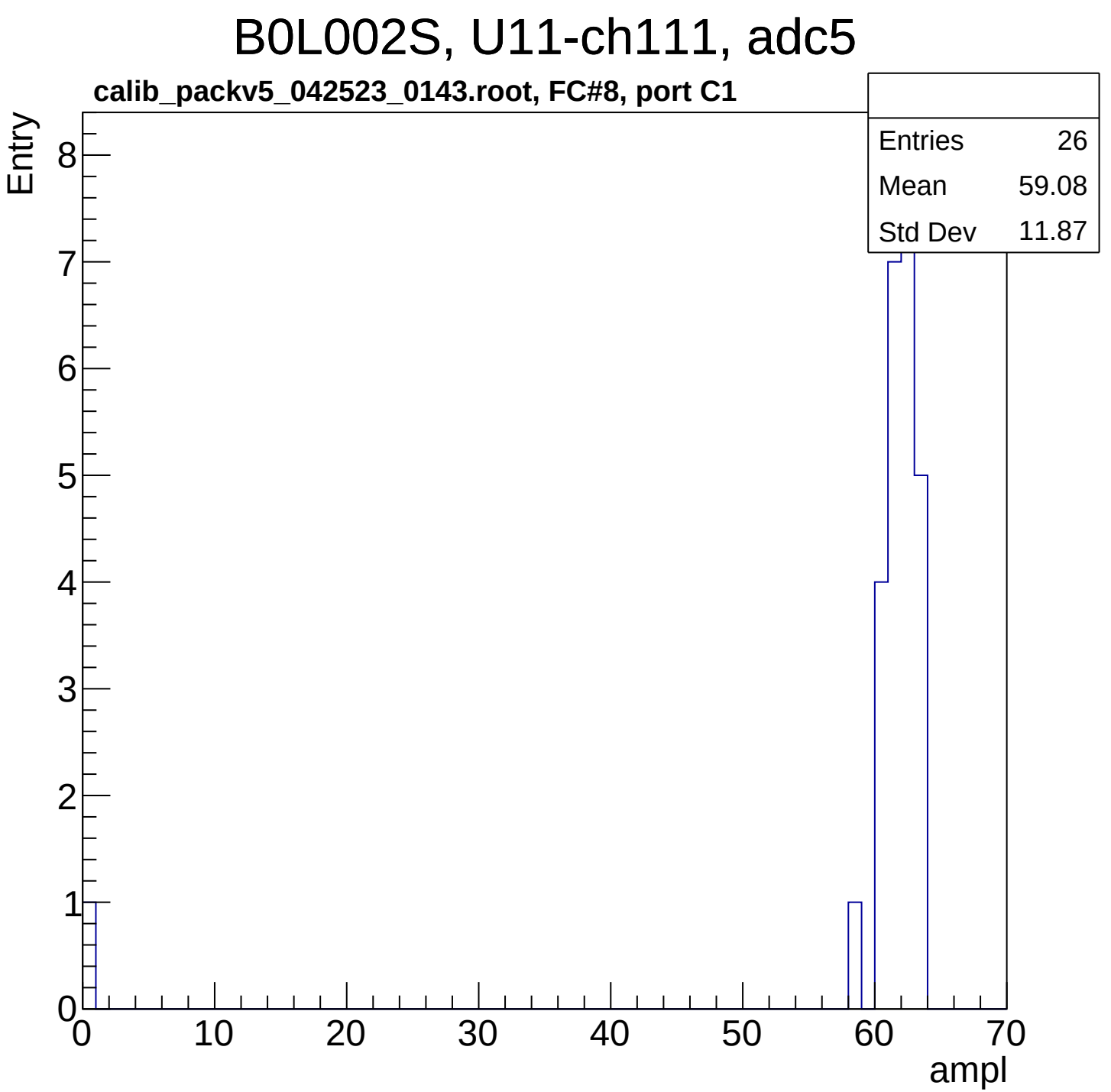
B0L002S, U11-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59.08
Std Dev 11.87
Entry
ampl
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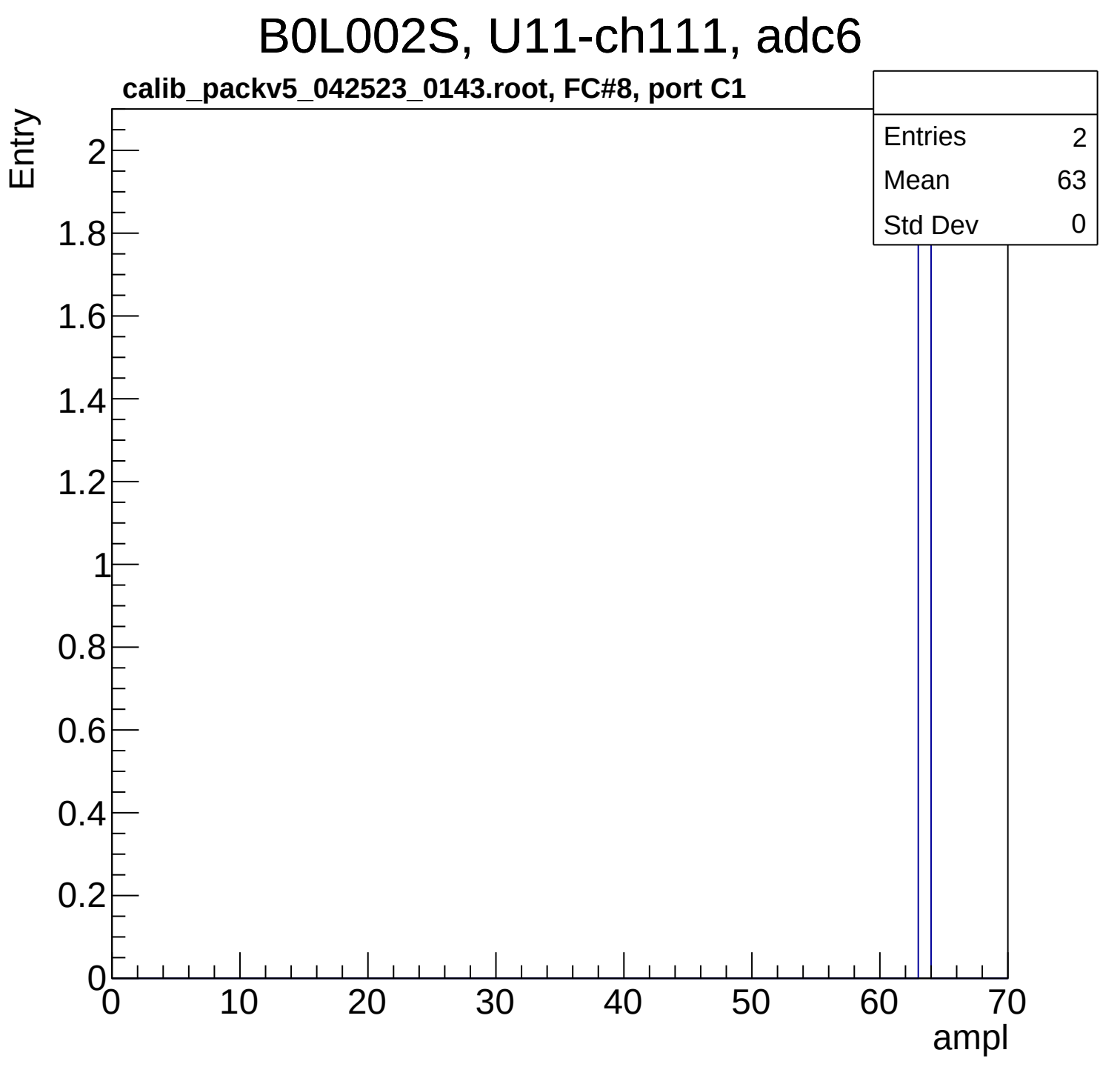
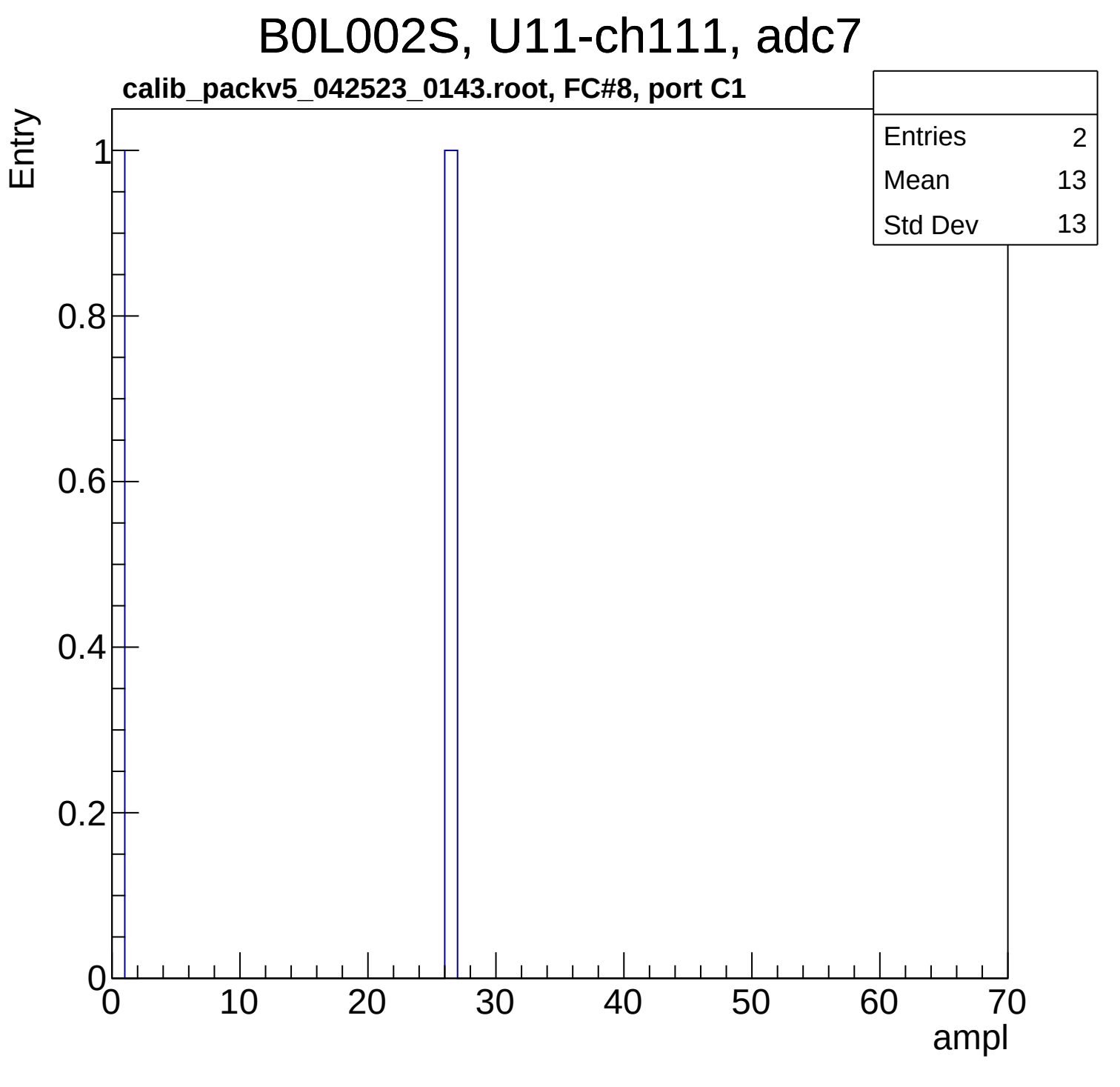
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U11-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
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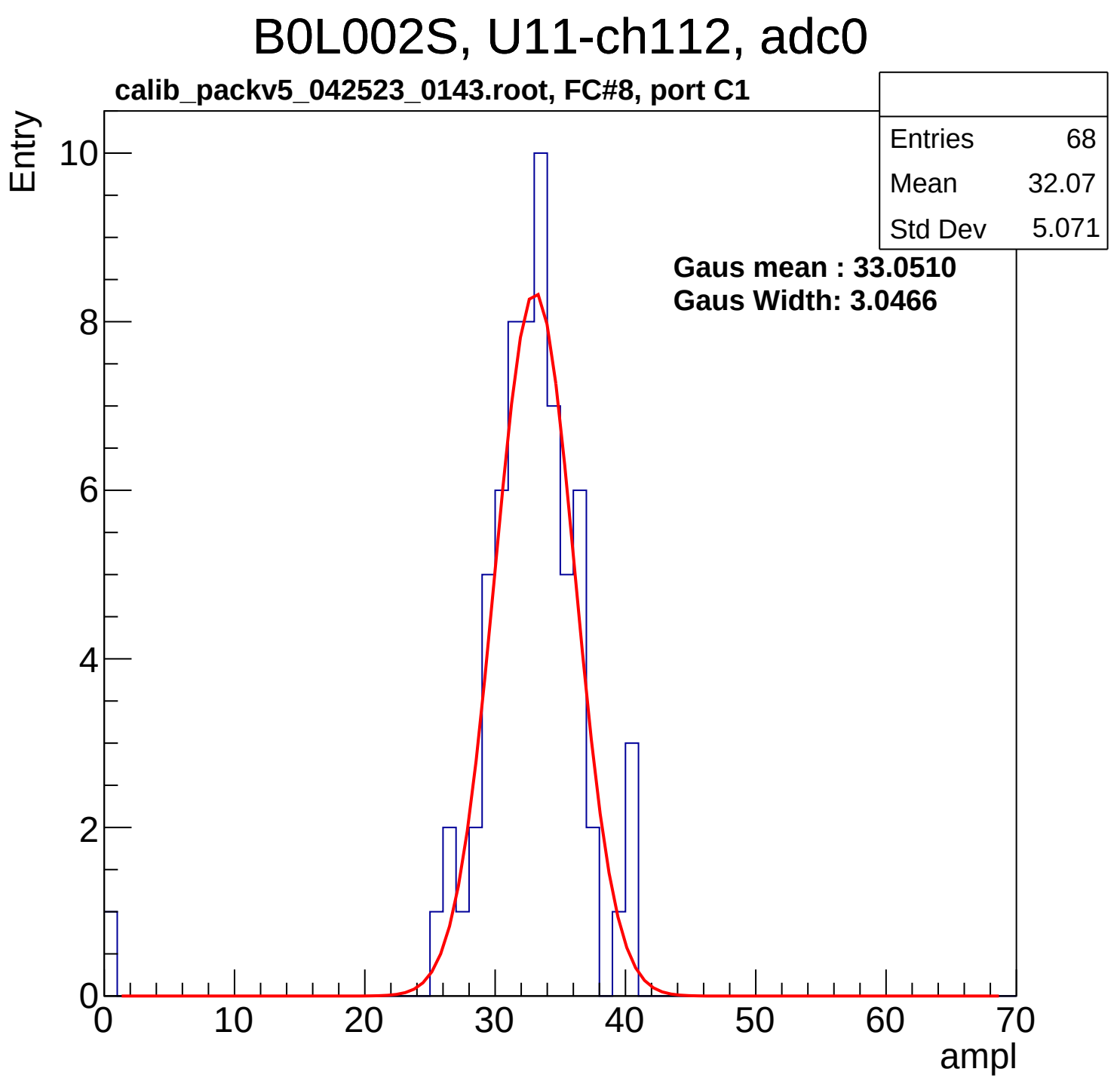
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 32.07
Std Dev 5.071
Gaus mean : 33.0510
Gaus Width: 3.0466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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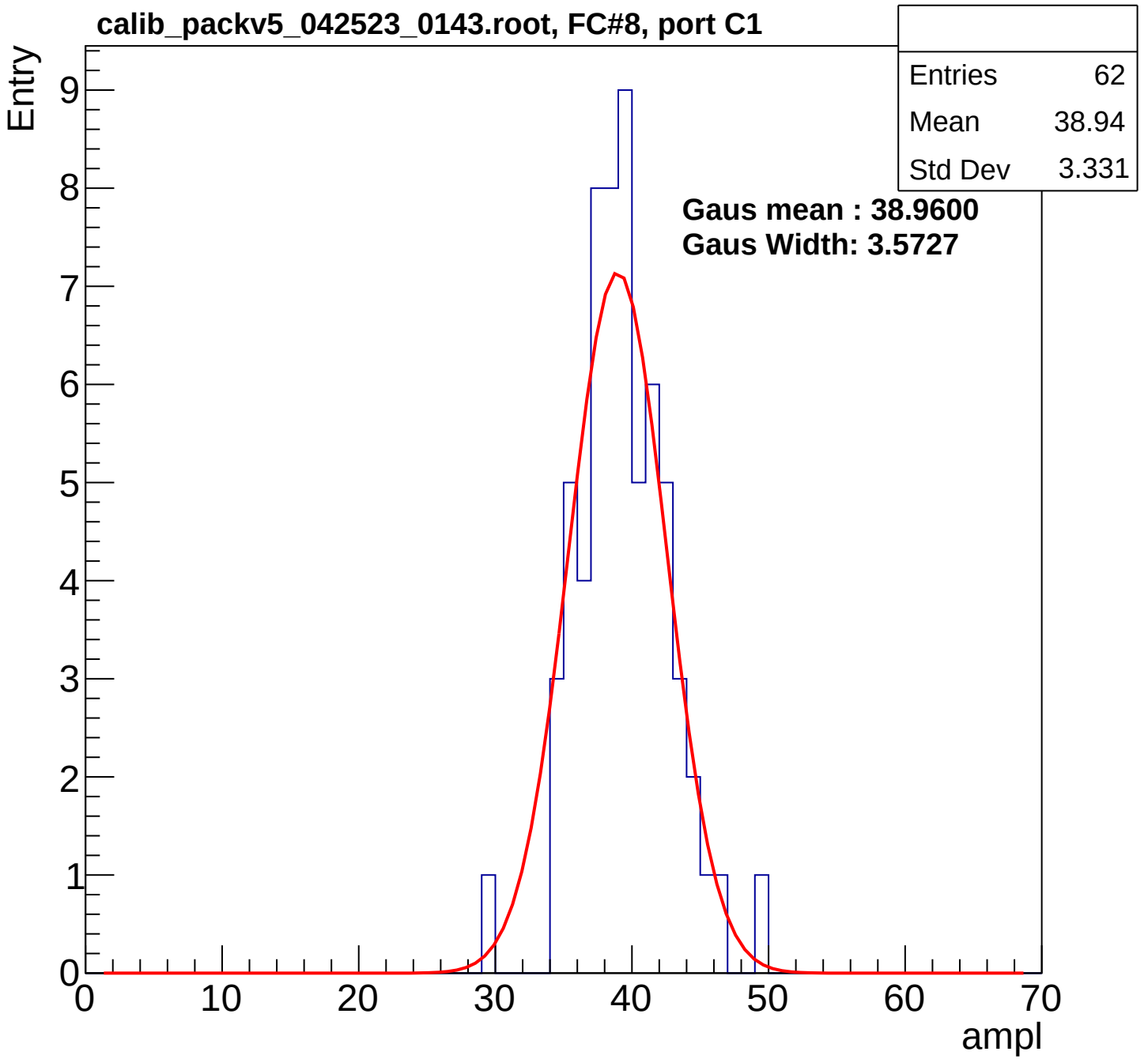
B0L002S, U11-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 38.94
Std Dev 3.331
Gaus mean : 38.9600
Gaus Width: 3.5727
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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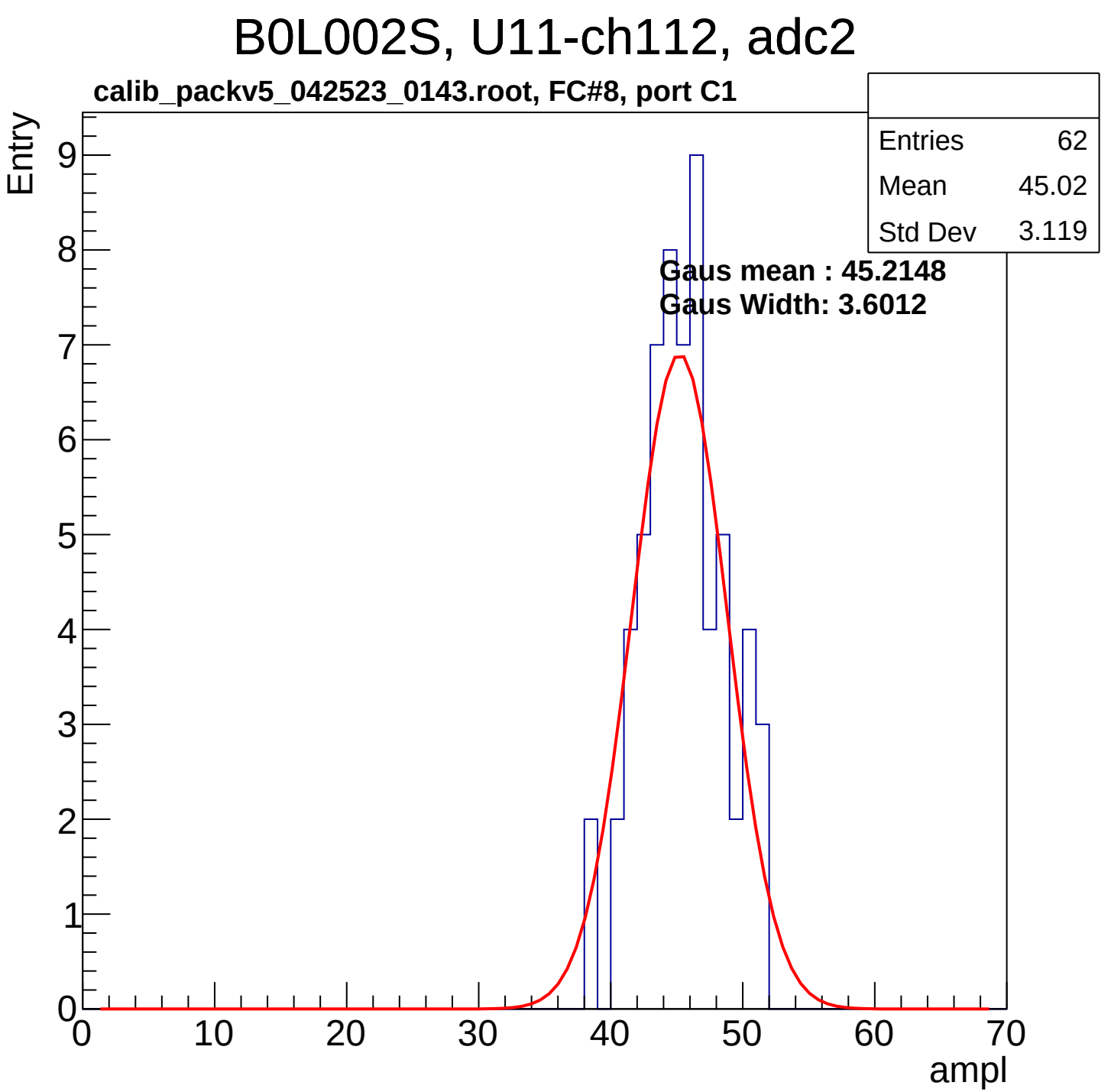
B0L002S, U11-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.02
Std Dev 3.119
Gaus mean : 45.2148
Gaus Width: 3.6012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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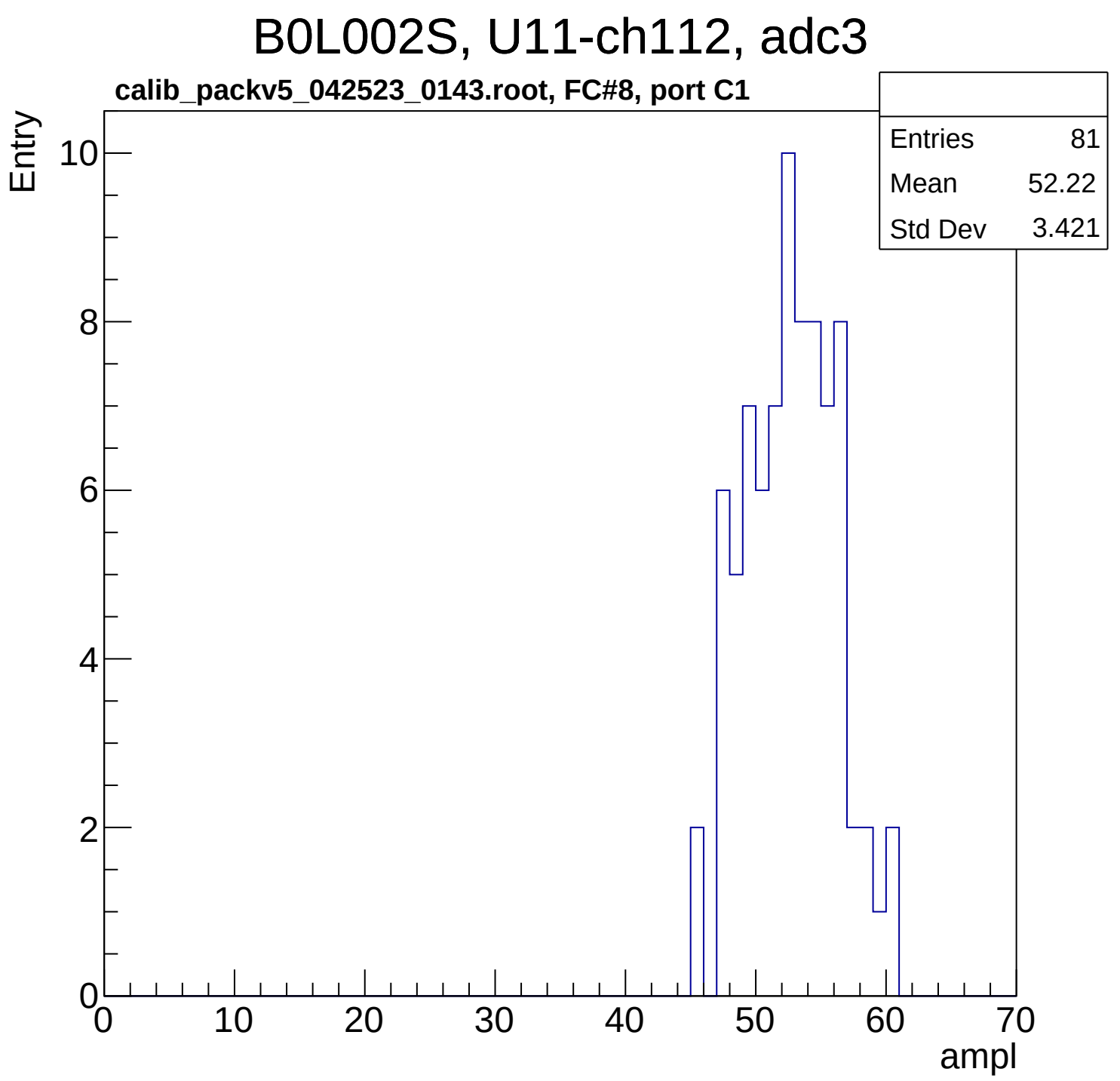
B0L002S, U11-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 52.22
Std Dev 3.421
Entry
ampl
0
2
4
6
8
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0
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20
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40
50
60
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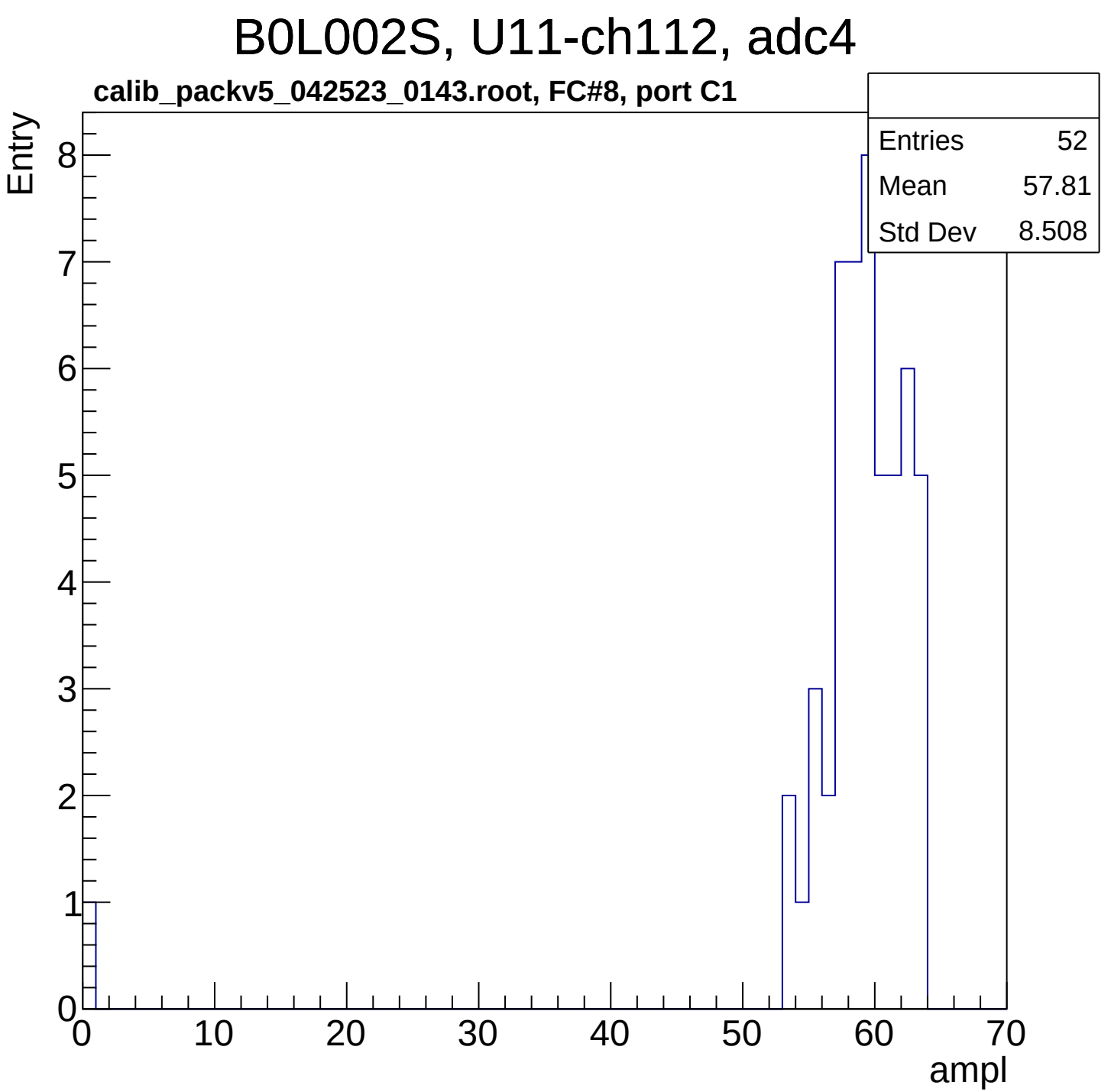
B0L002S, U11-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.81
Std Dev 8.508
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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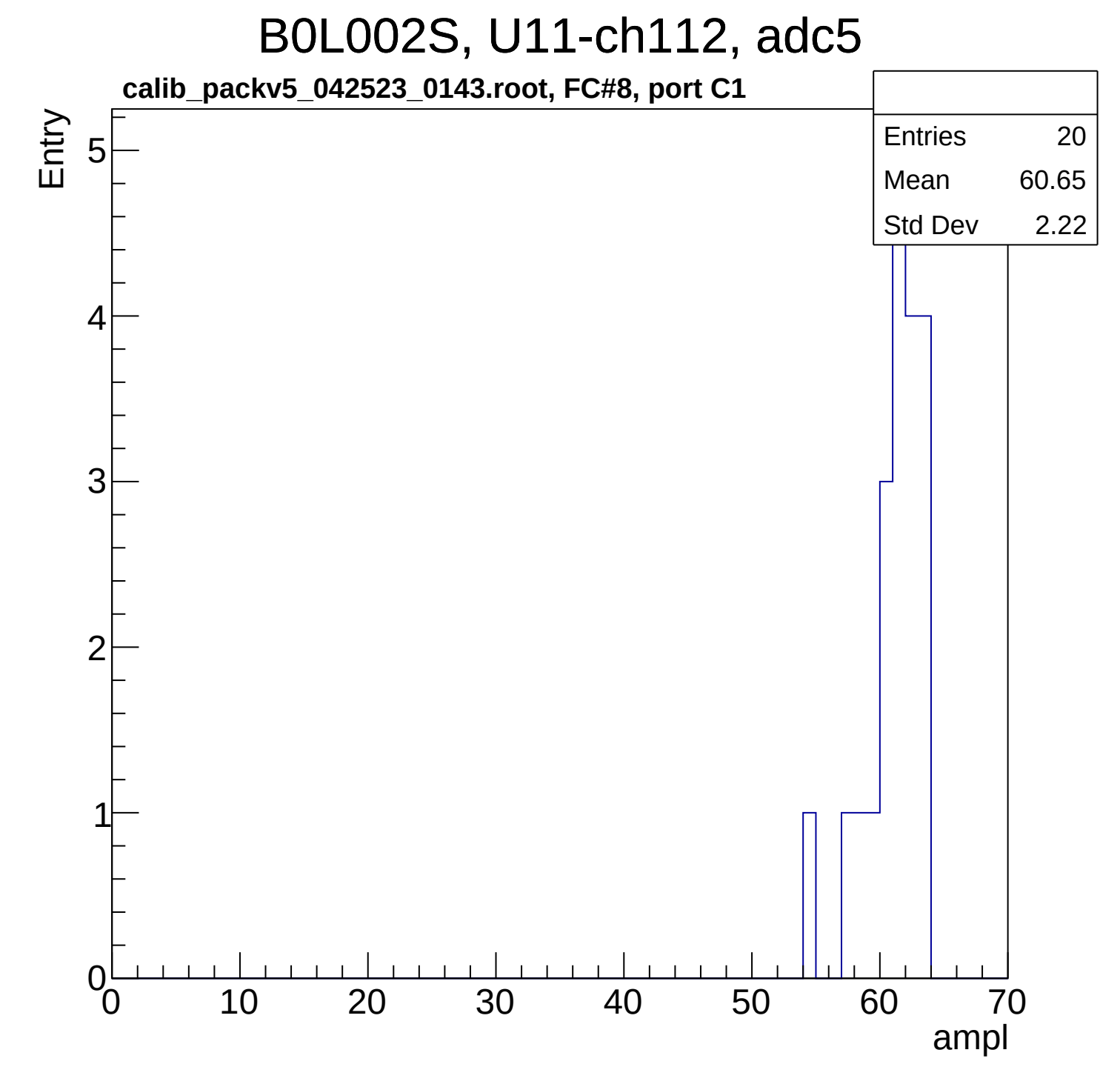
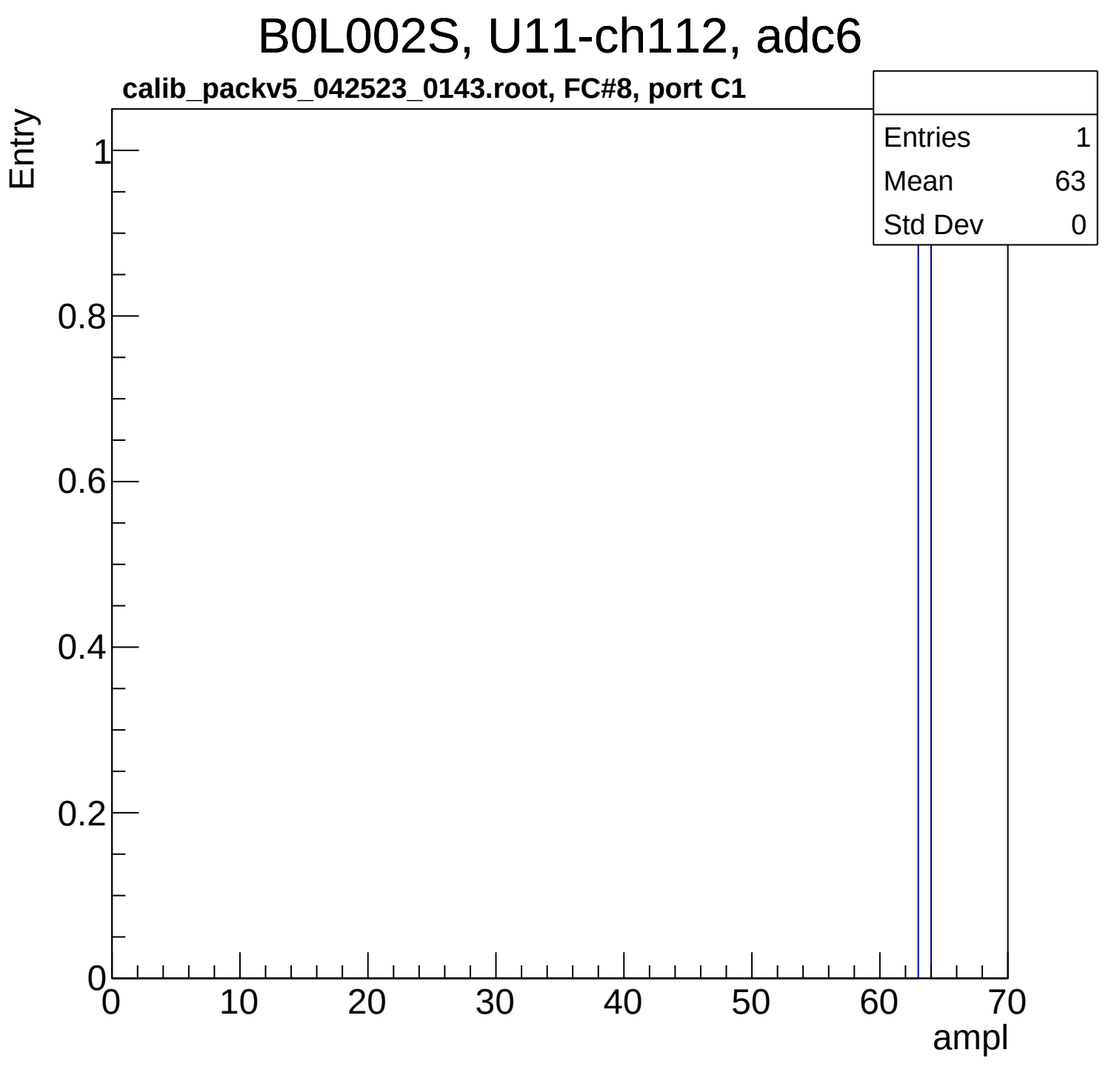
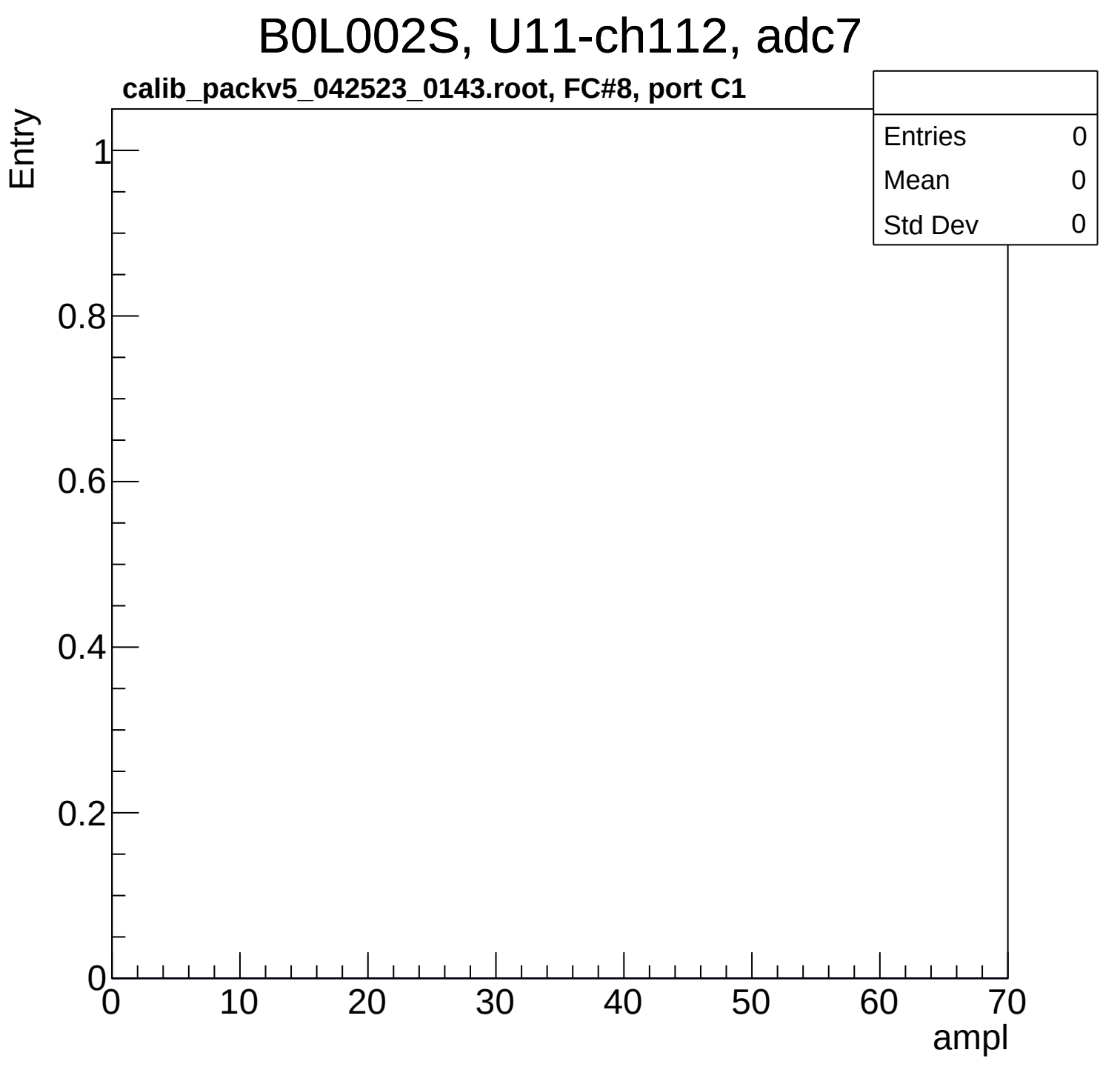
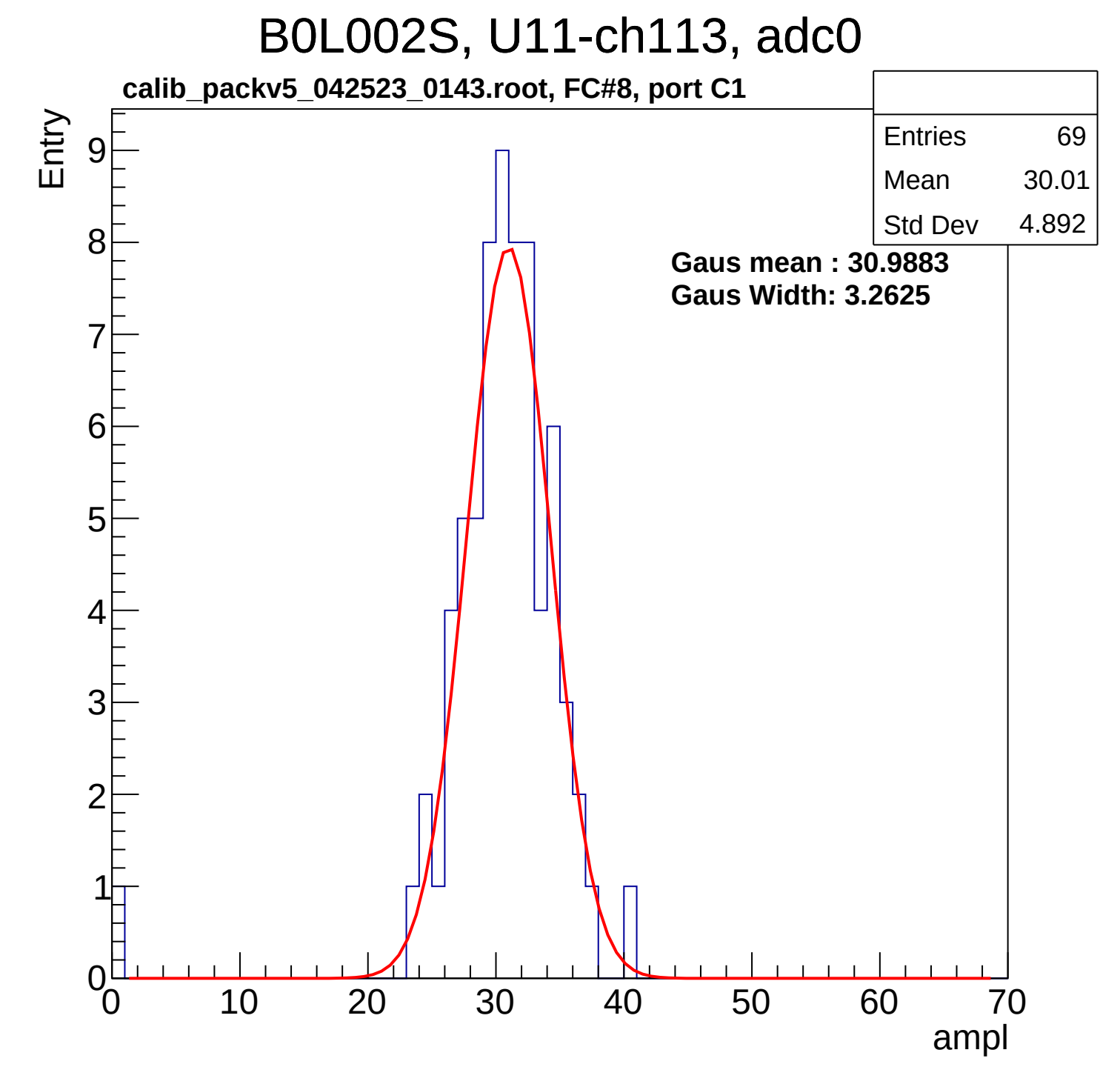
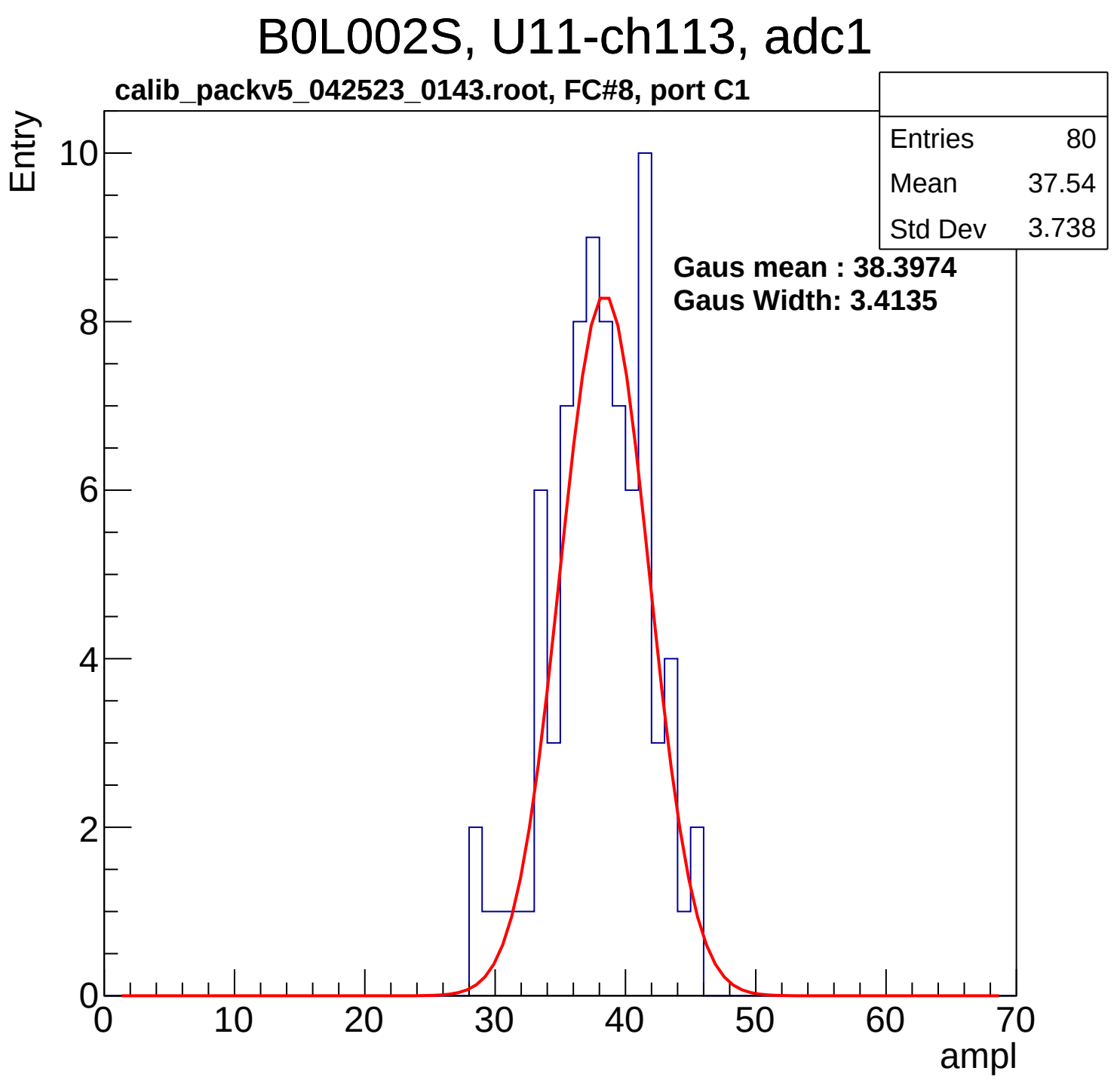
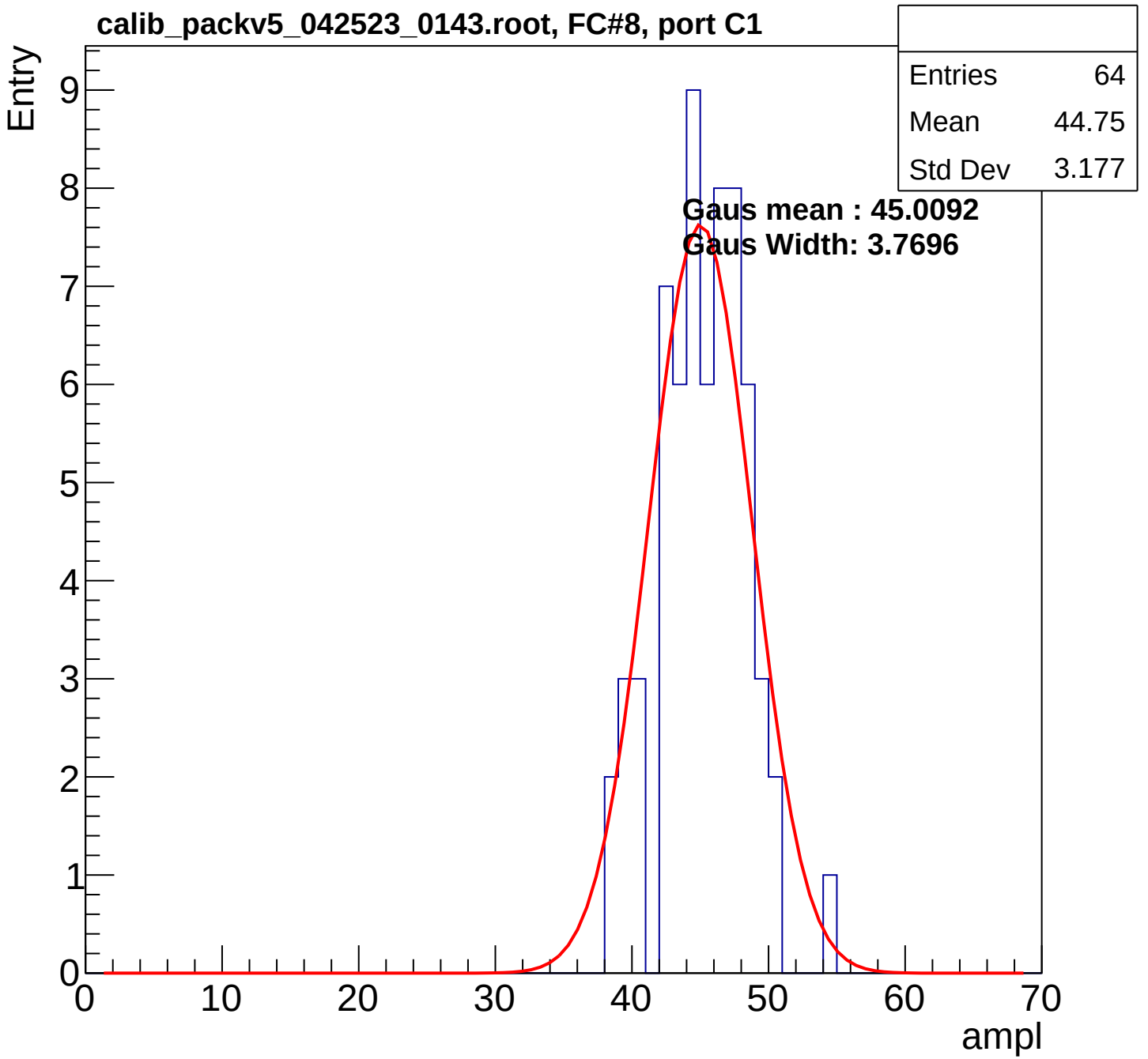

B0L002S, U11-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 60.65
Std Dev 2.22
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

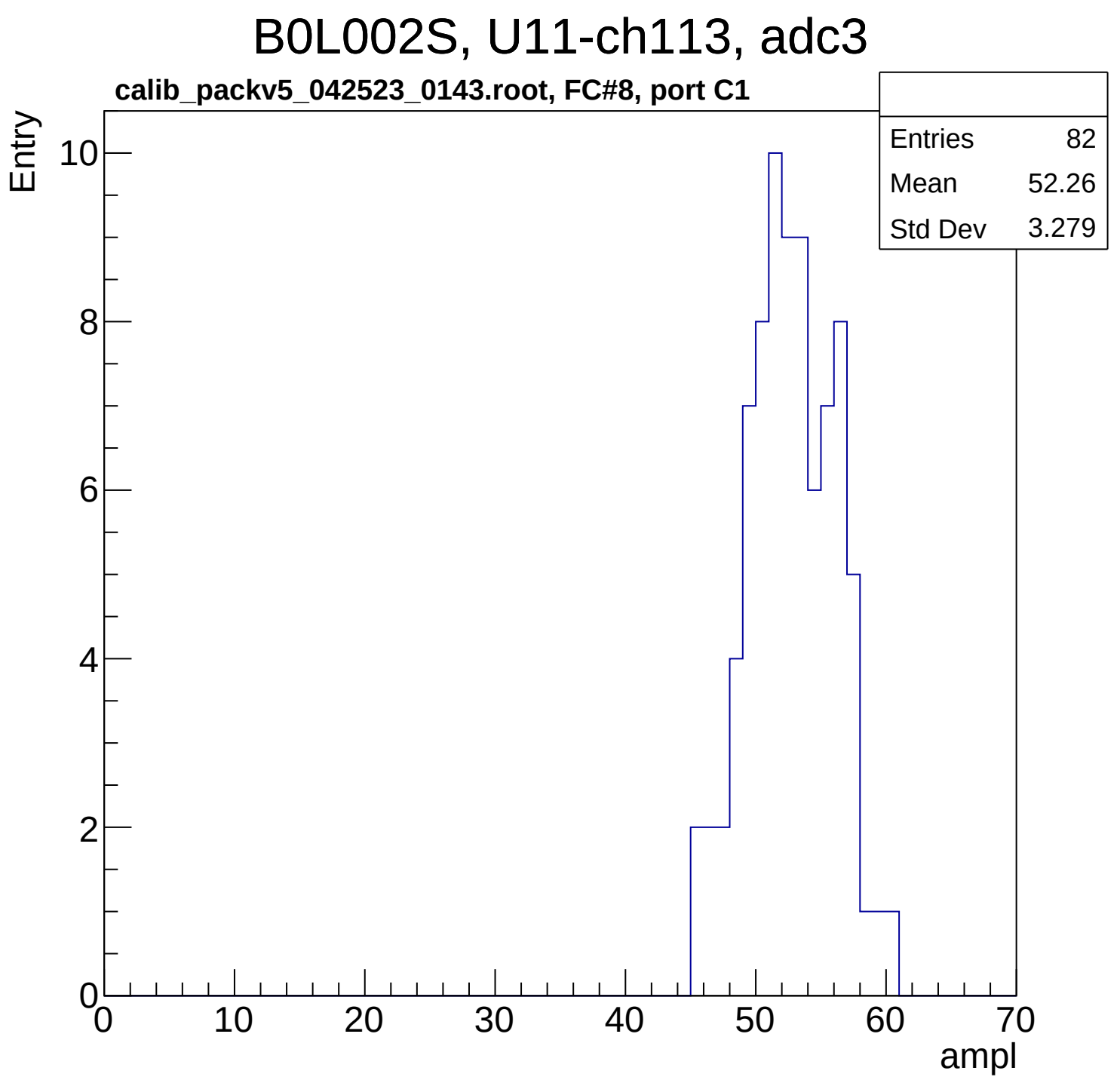

B0L002S, U11-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

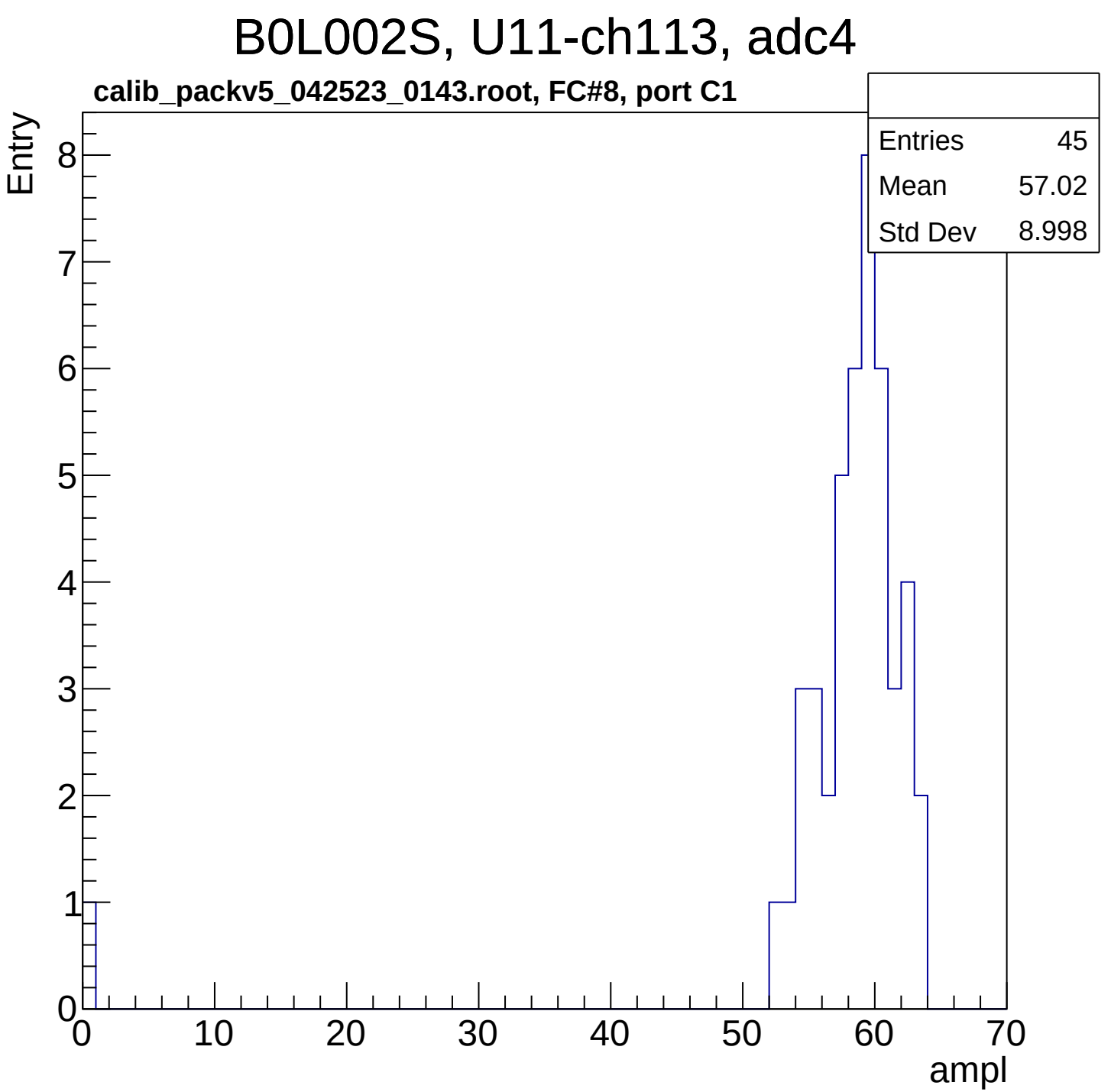

B0L002S, U11-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.01
Std Dev 4.892
Gaus mean : 30.9883
Gaus Width: 3.2625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.54
Std Dev 3.738
Gaus mean : 38.3974
Gaus Width: 3.4135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.75
Std Dev 3.177
Gaus mean : 45.0092
Gaus Width: 3.7696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 52.26
Std Dev 3.279
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57.02
Std Dev 8.998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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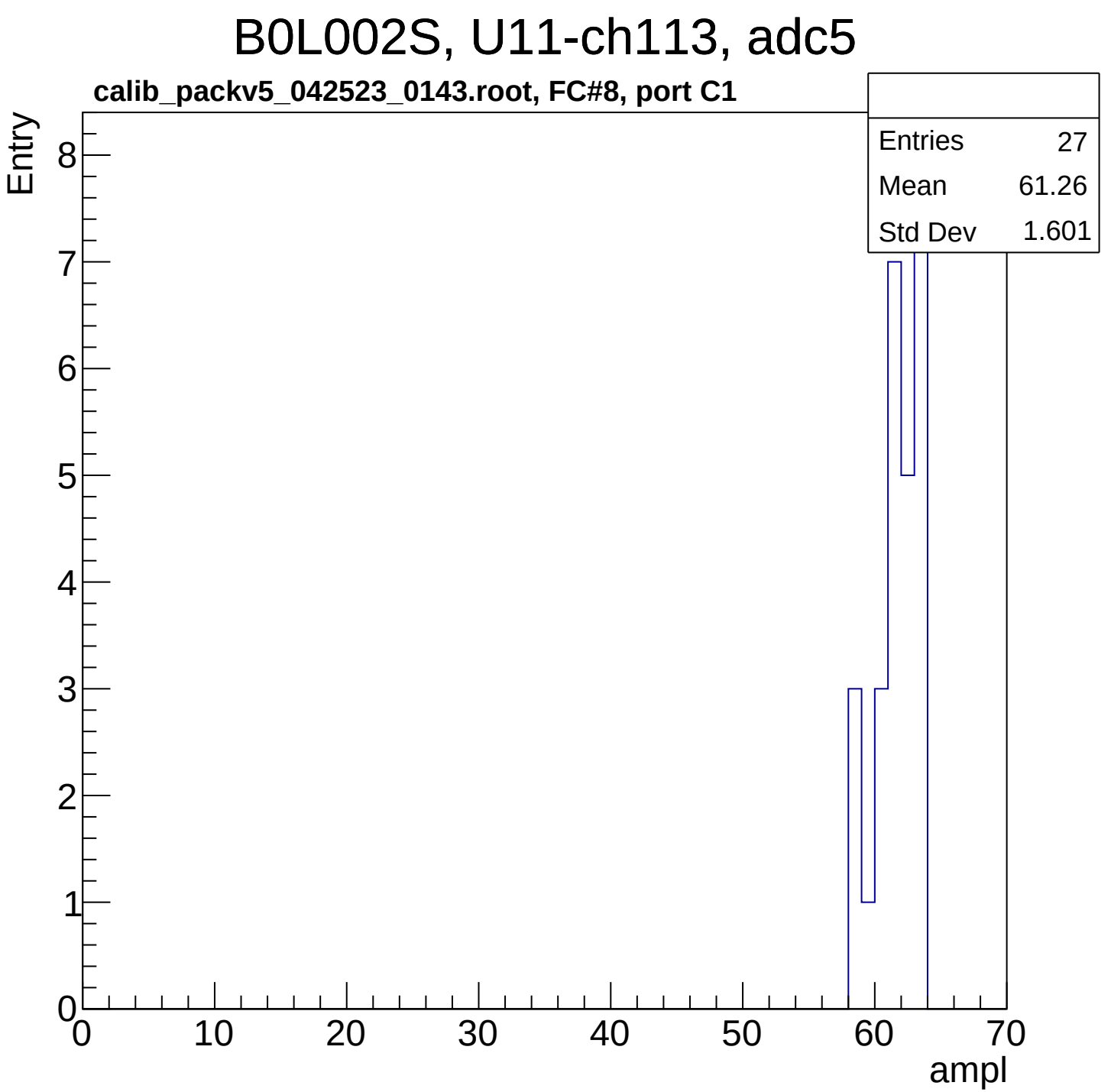
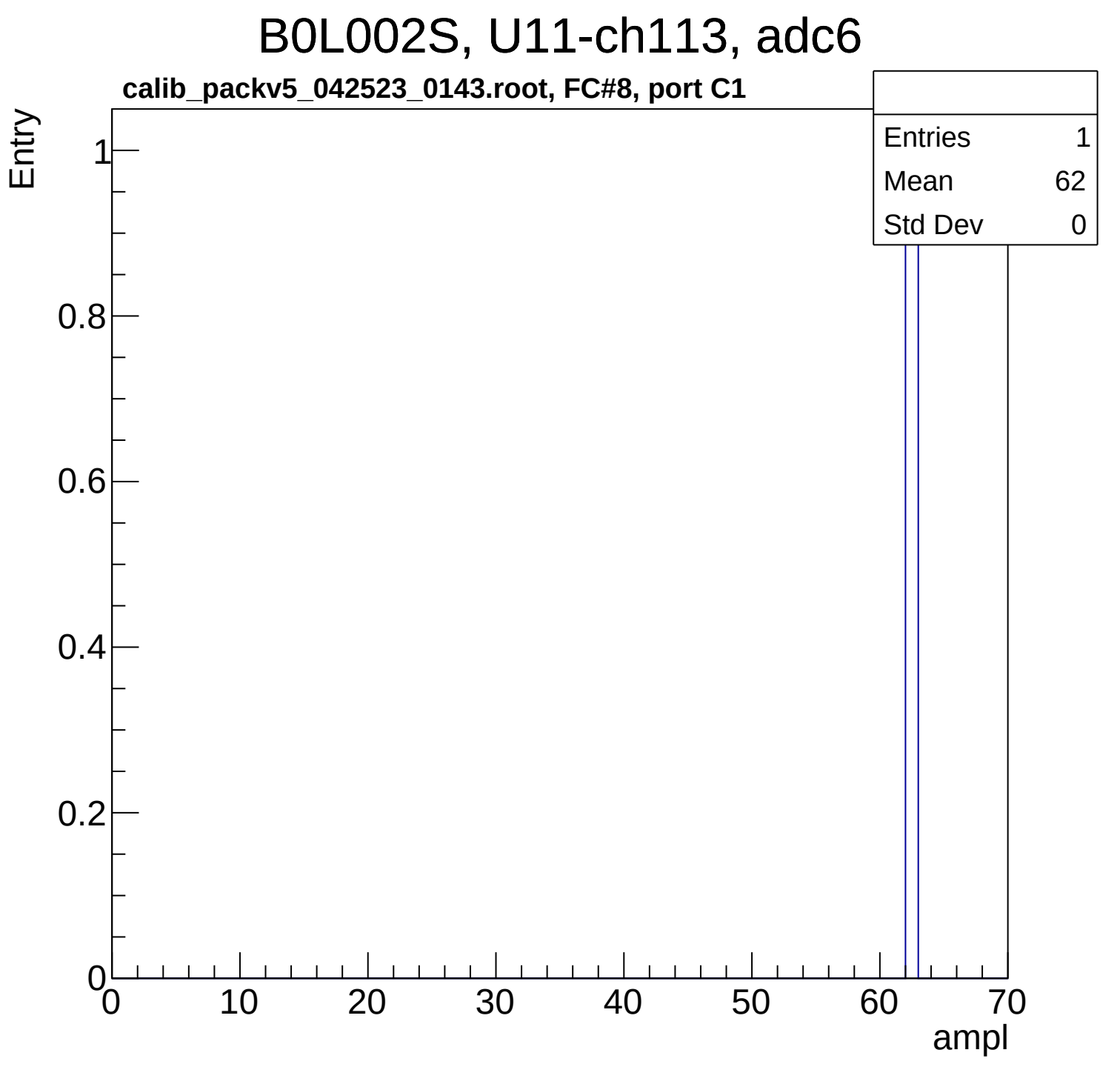
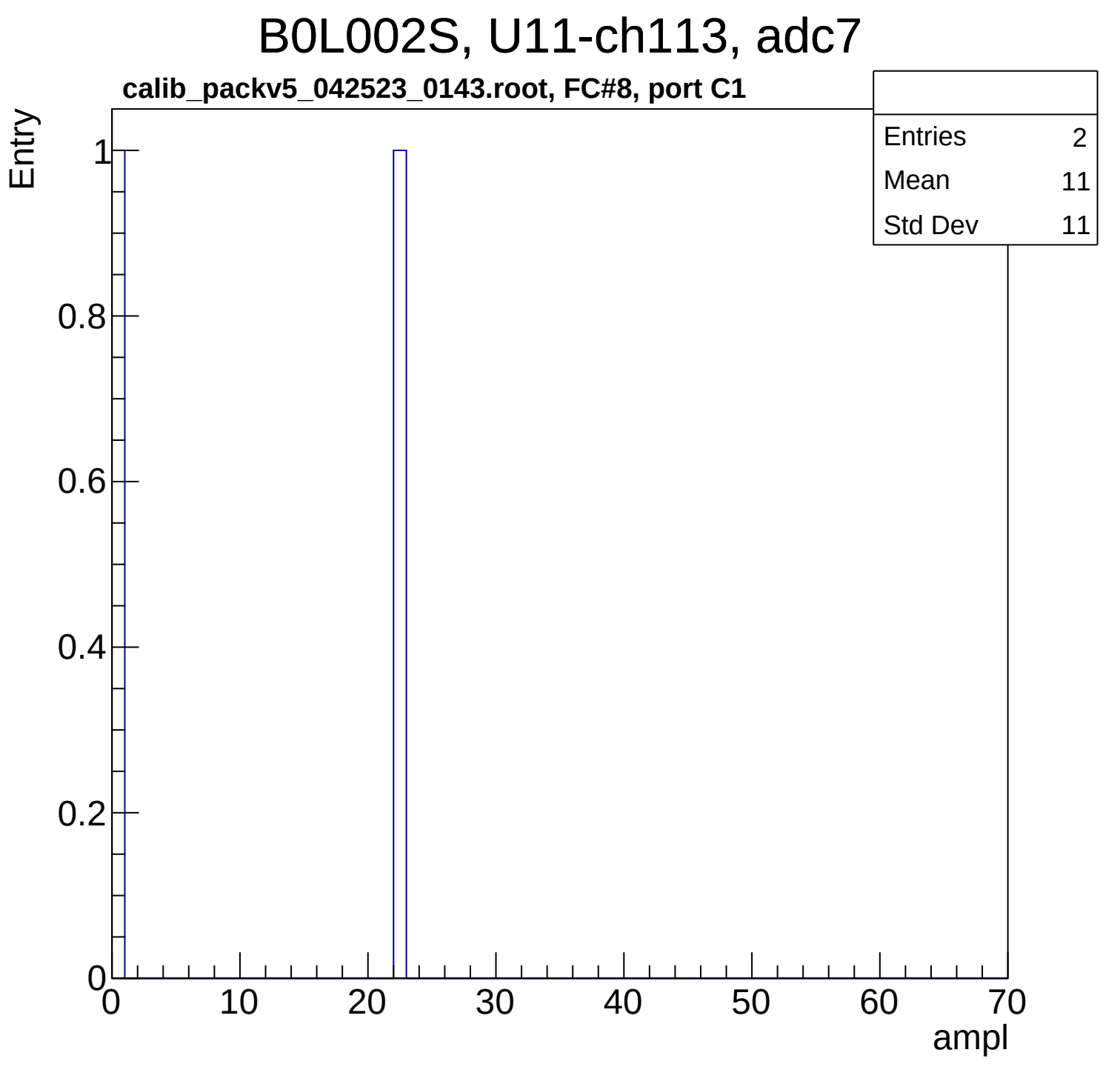
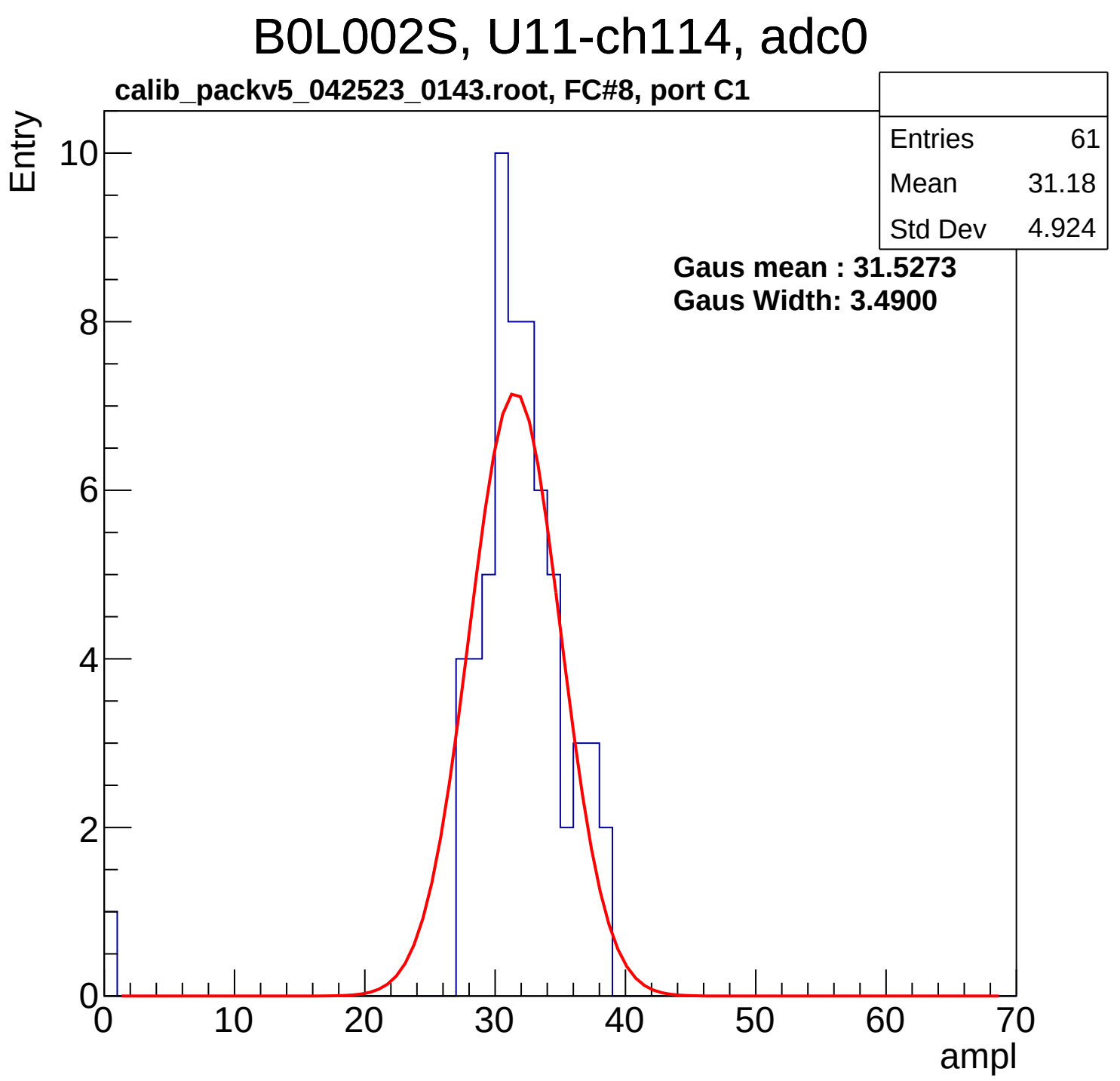
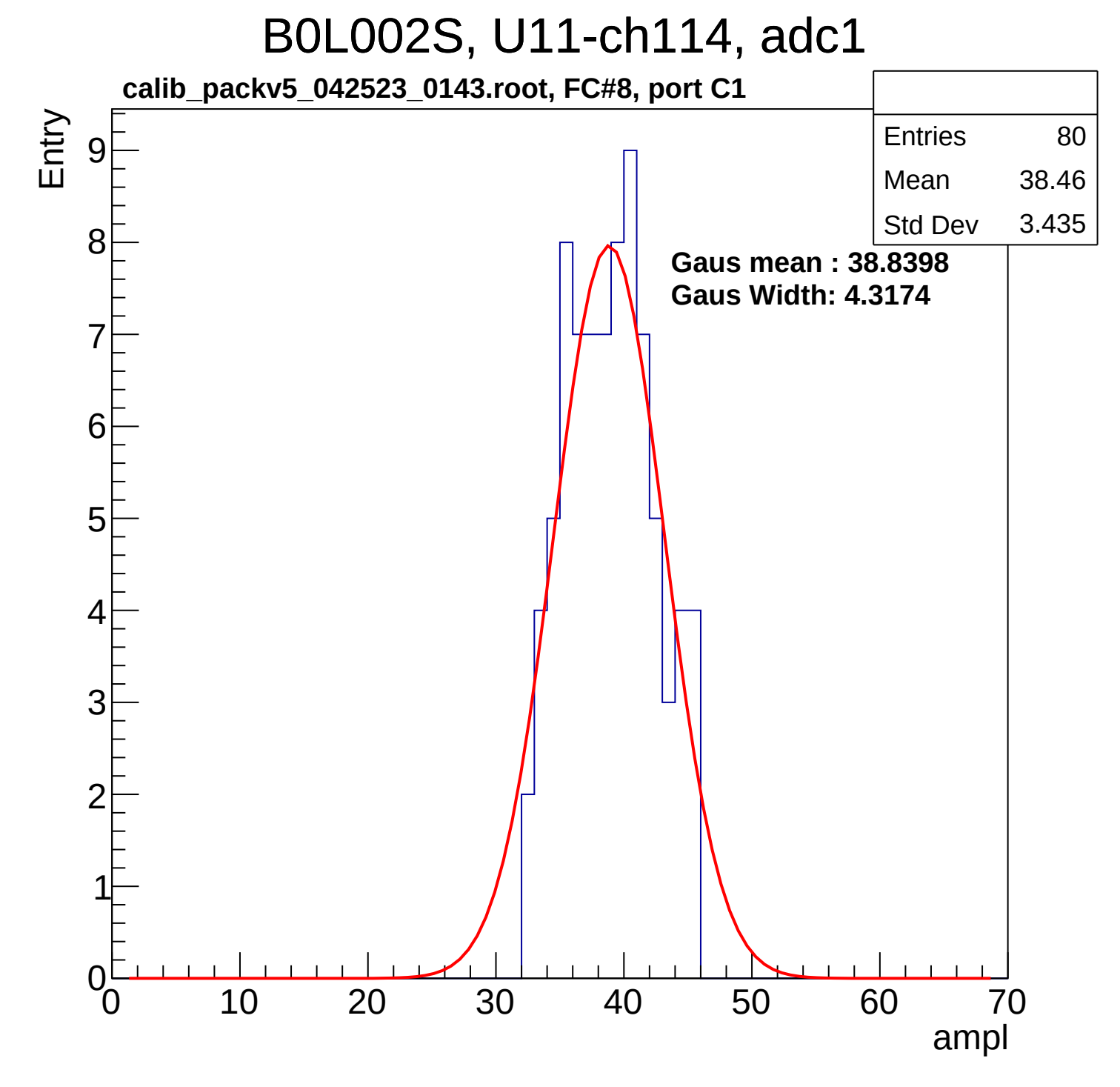

B0L002S, U11-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 61.26
Std Dev 1.601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 31.18
Std Dev 4.924
Gaus mean : 31.5273
Gaus Width: 3.4900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.46
Std Dev 3.435
Gaus mean : 38.8398
Gaus Width: 4.3174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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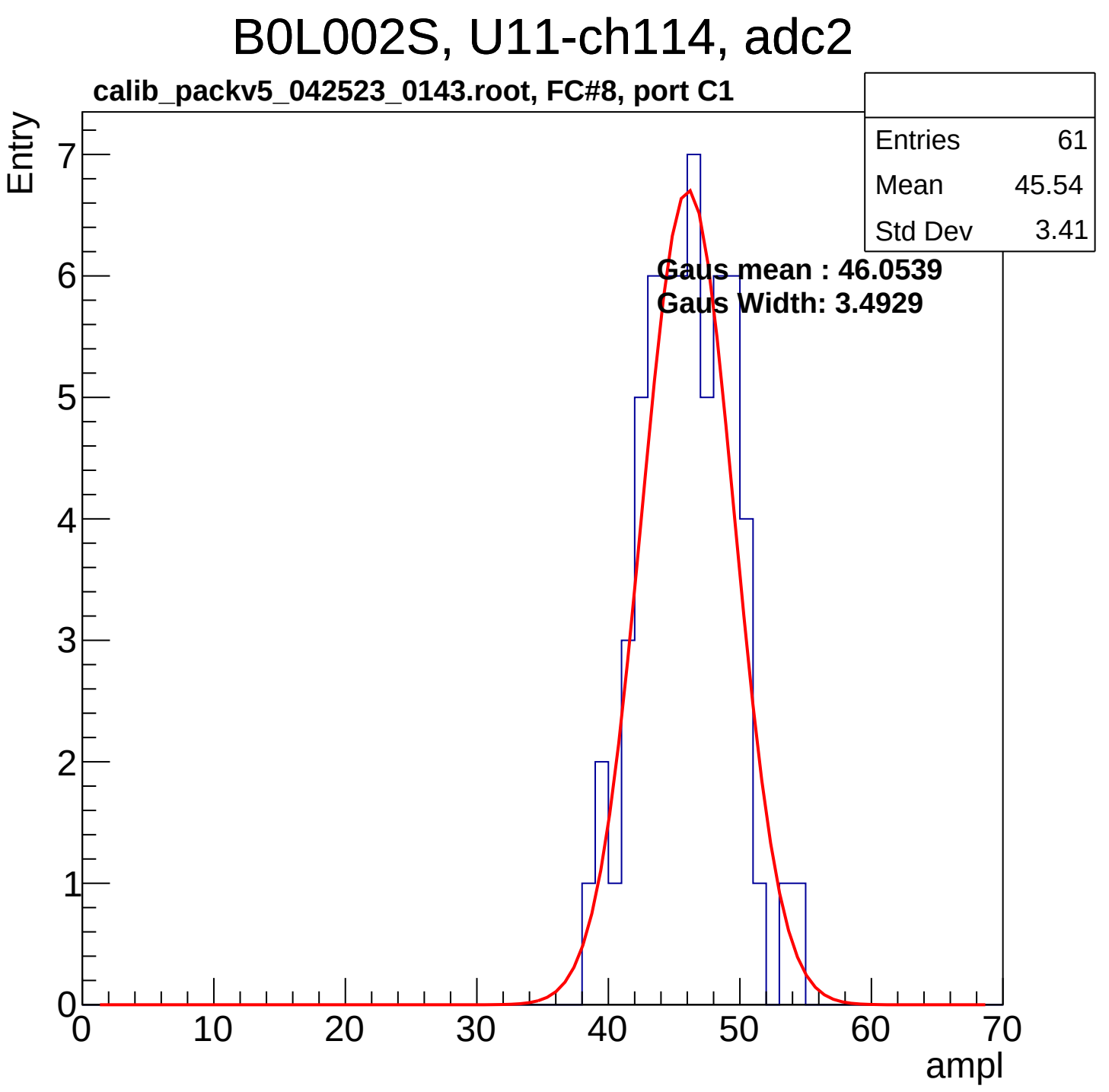
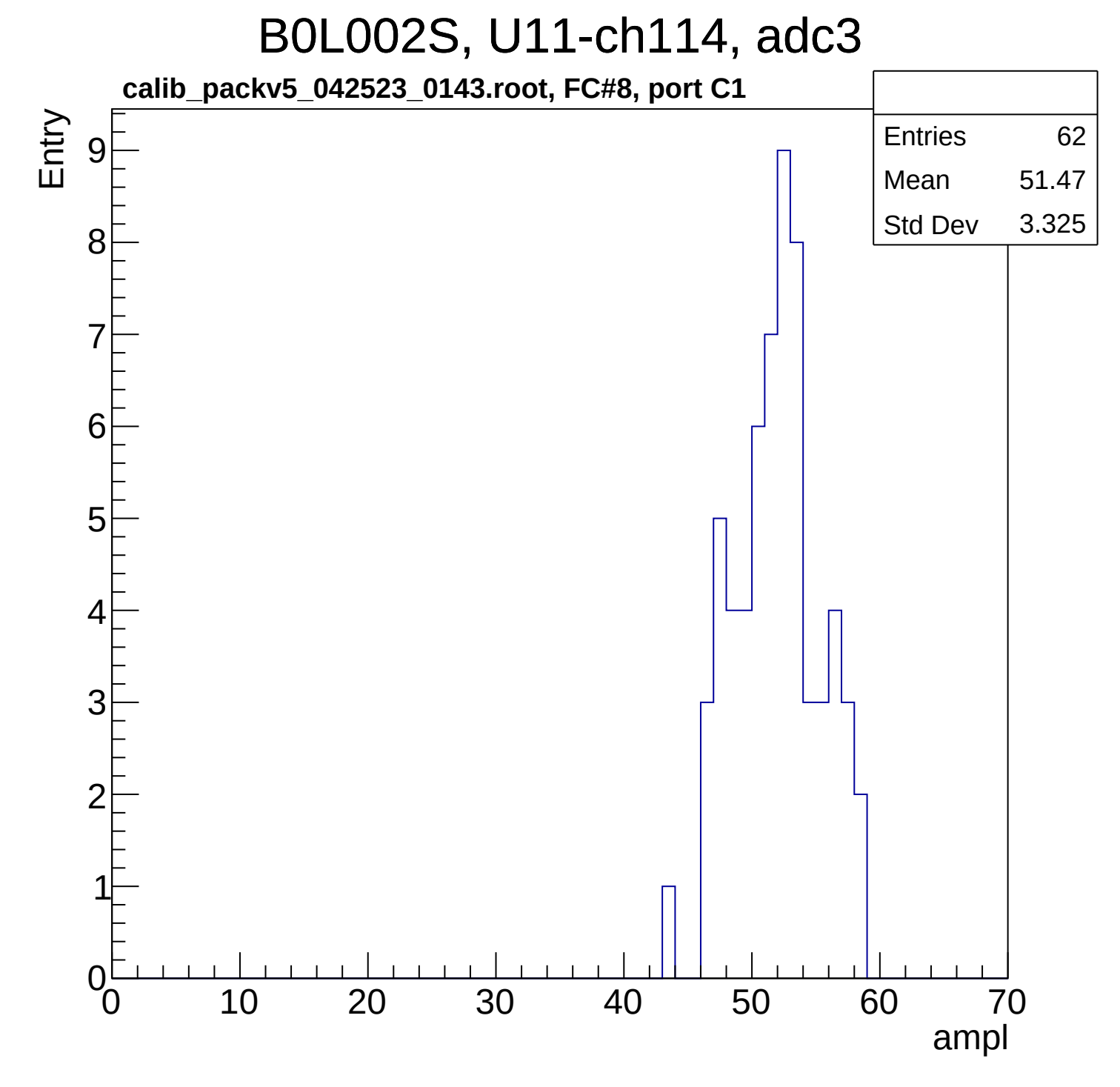
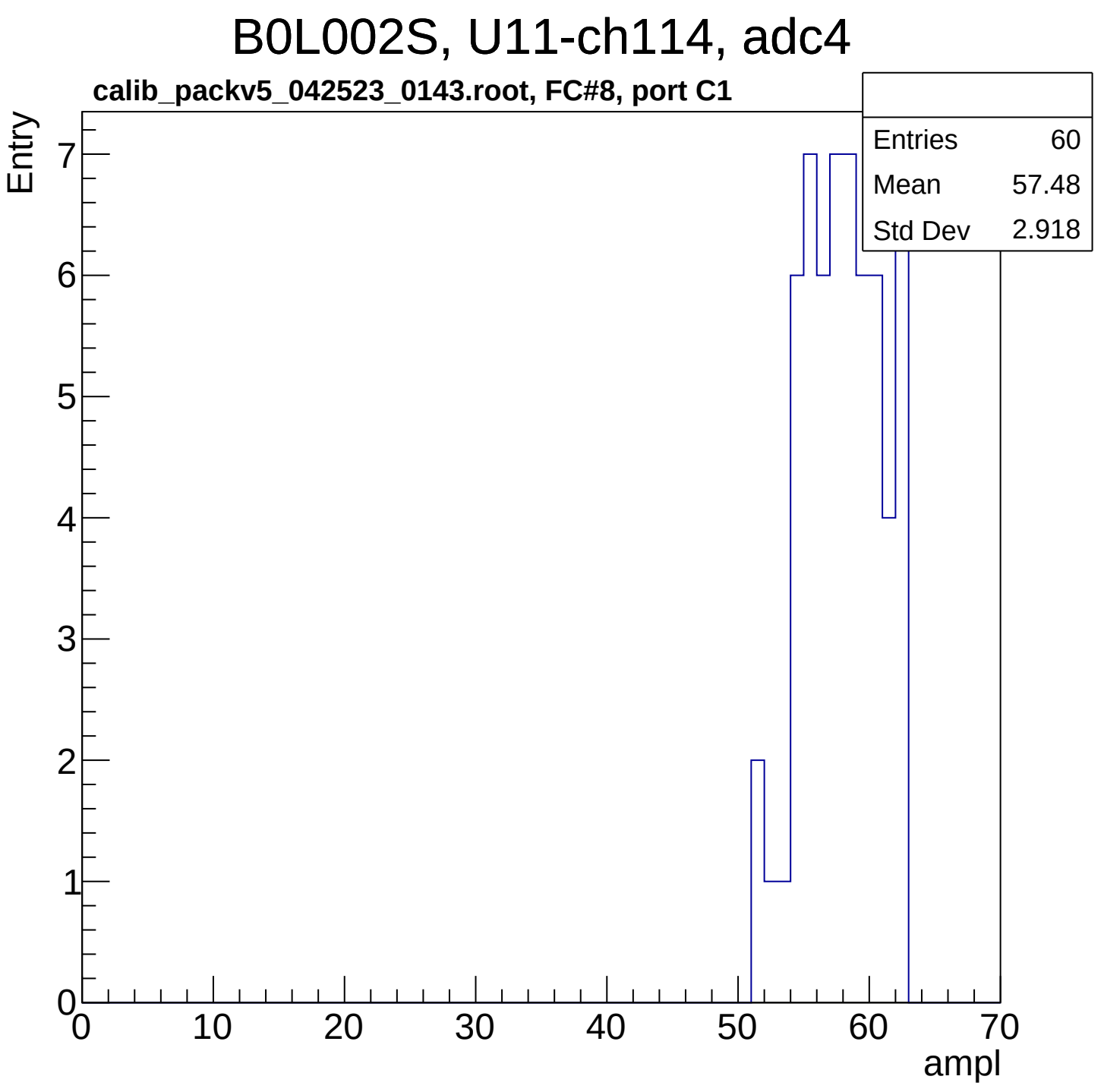
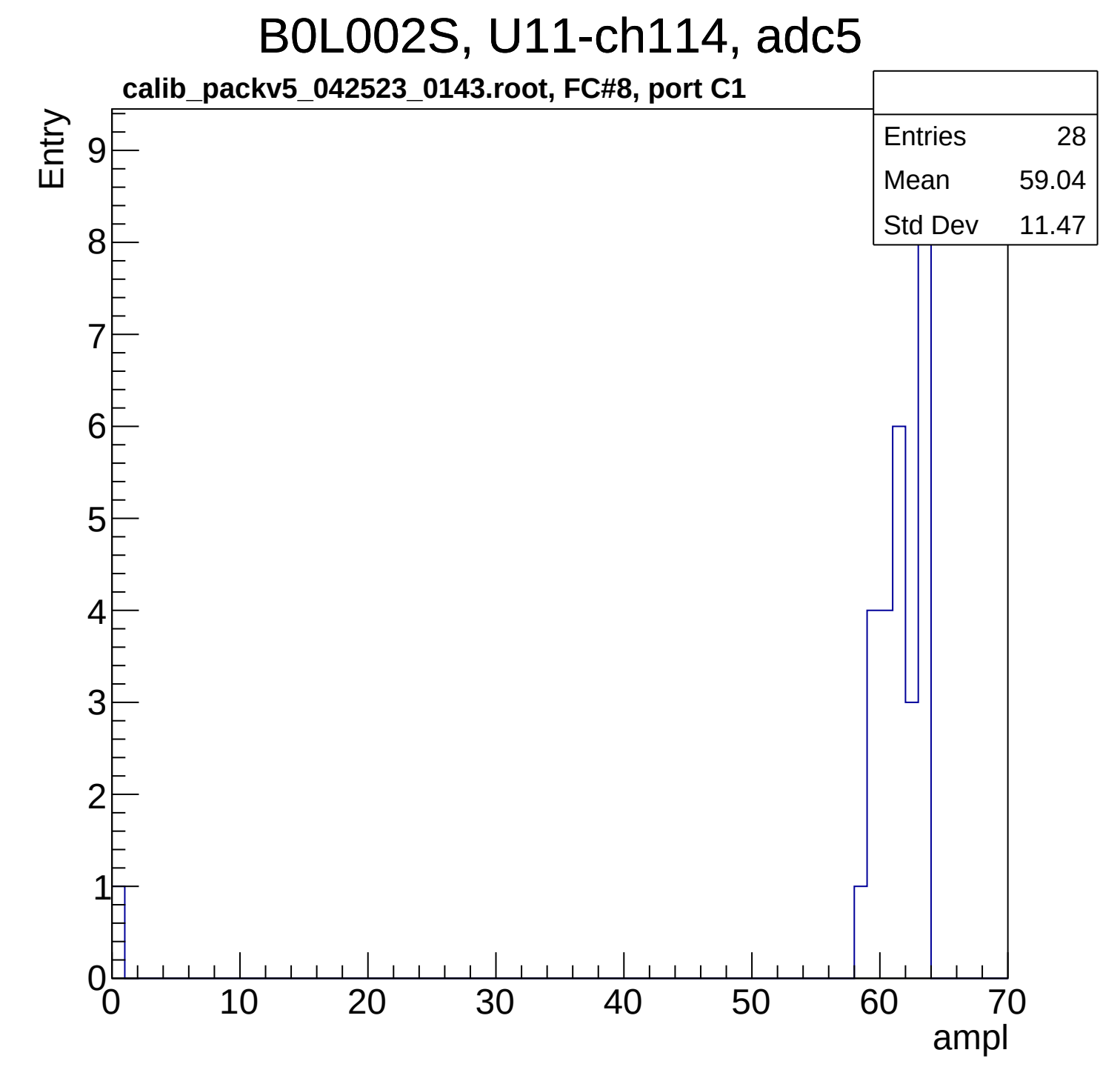

B0L002S, U11-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 45.54
Std Dev 3.41
Gaus mean : 46.0539
Gaus Width: 3.4929
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.47
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.48
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 59.04
Std Dev 11.47
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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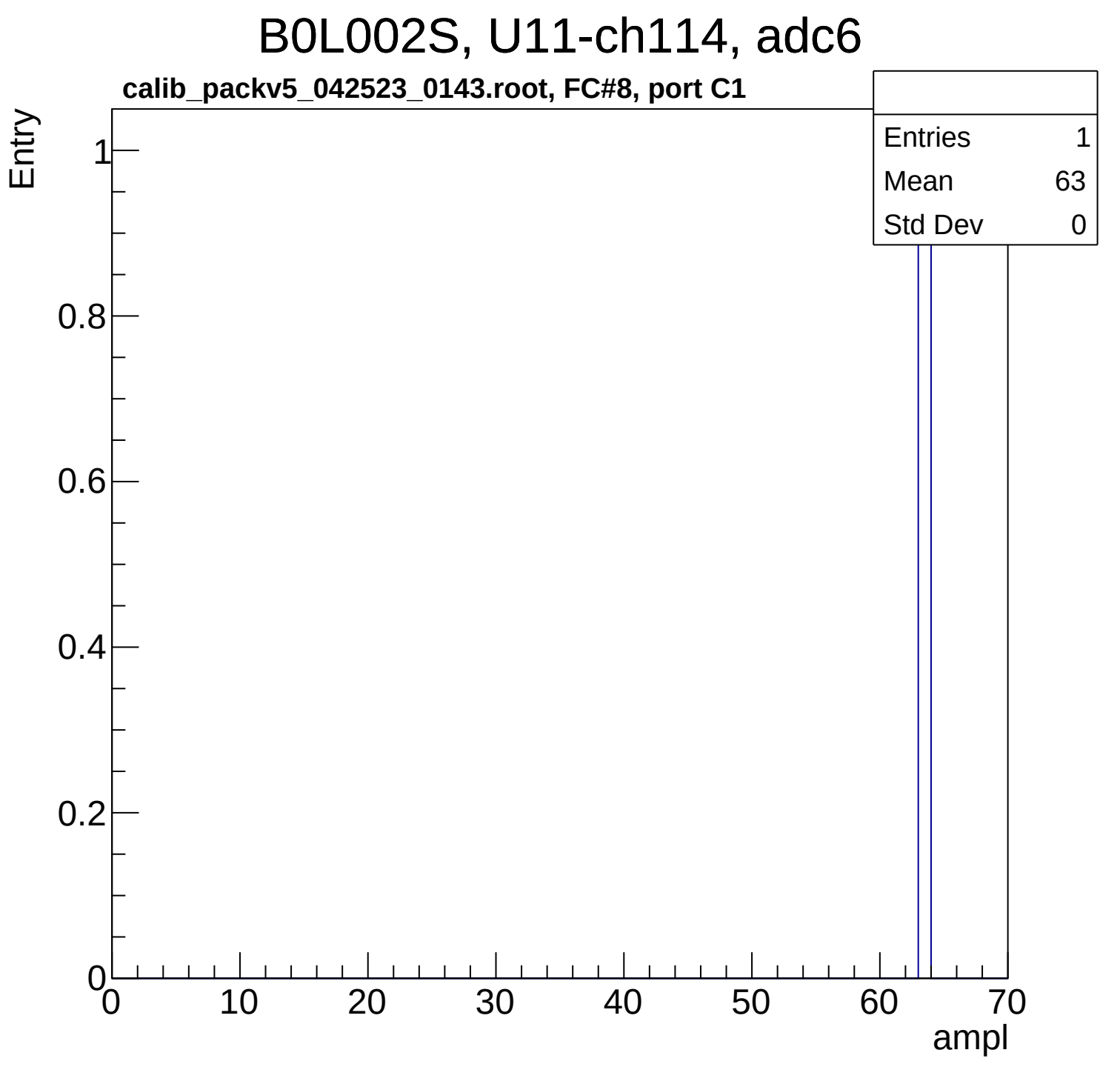
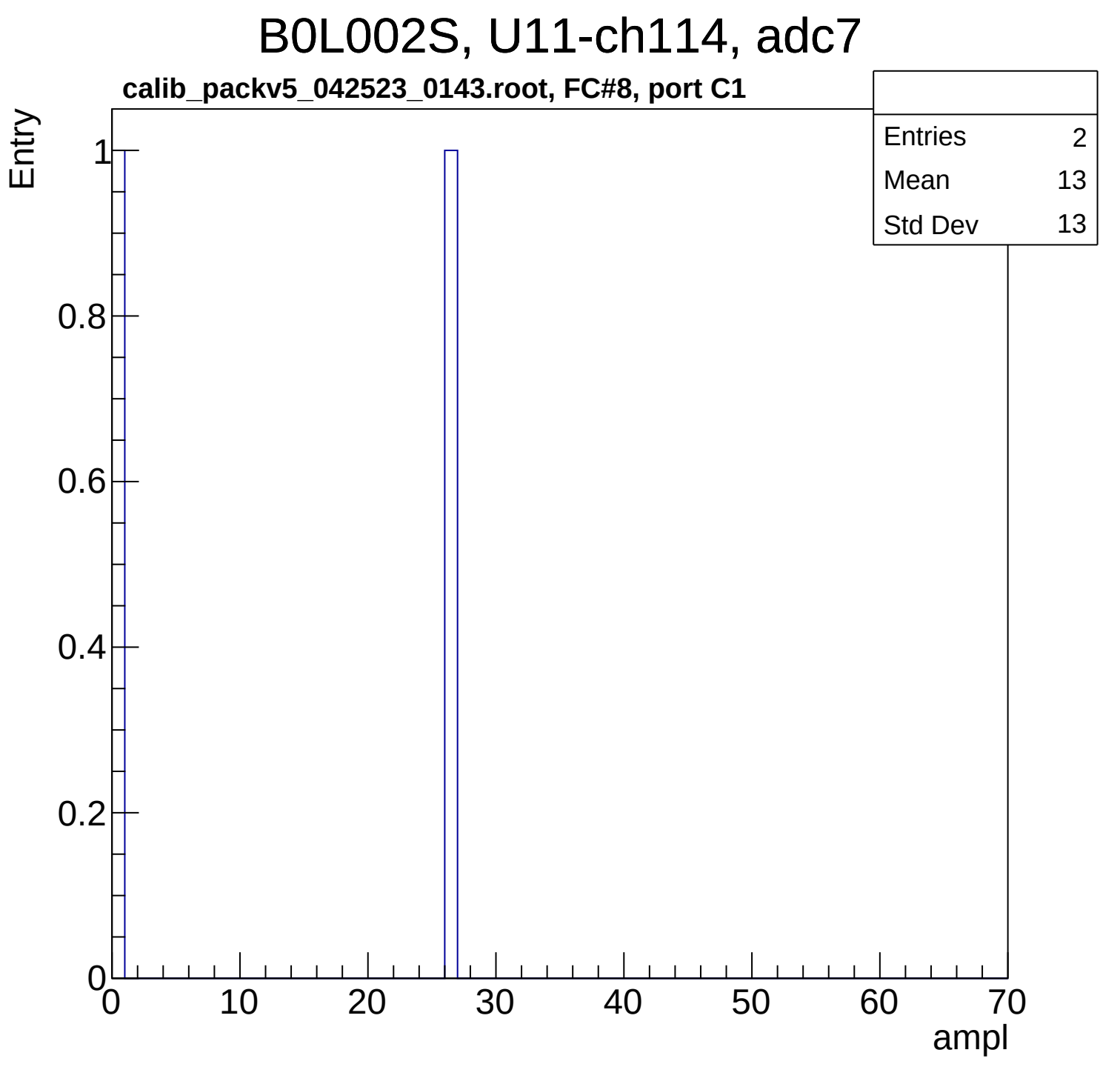
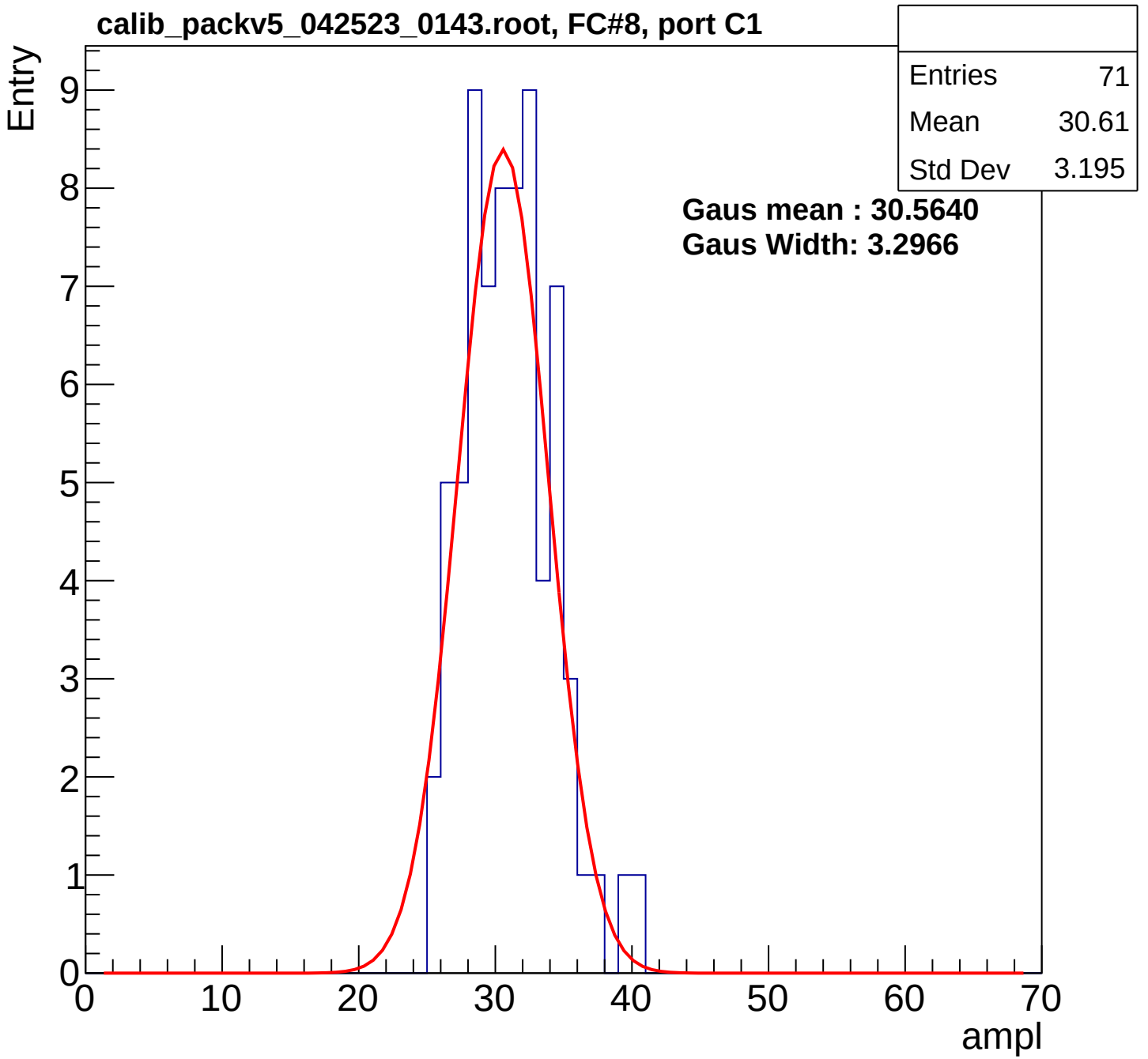
40
50
60
70


B0L002S, U11-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.61
Std Dev 3.195
Gaus mean : 30.5640
Gaus Width: 3.2966
Entry
ampl
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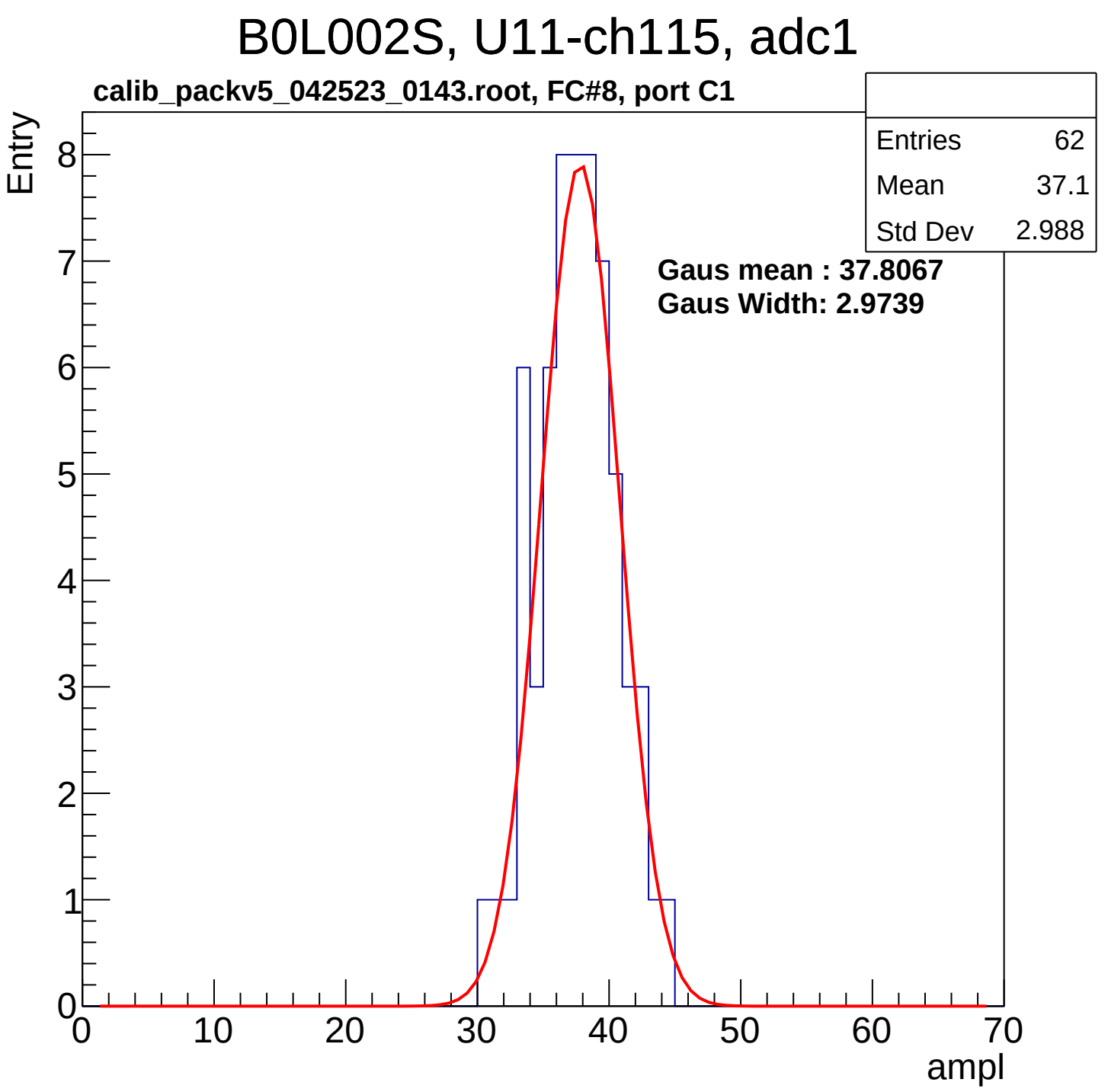
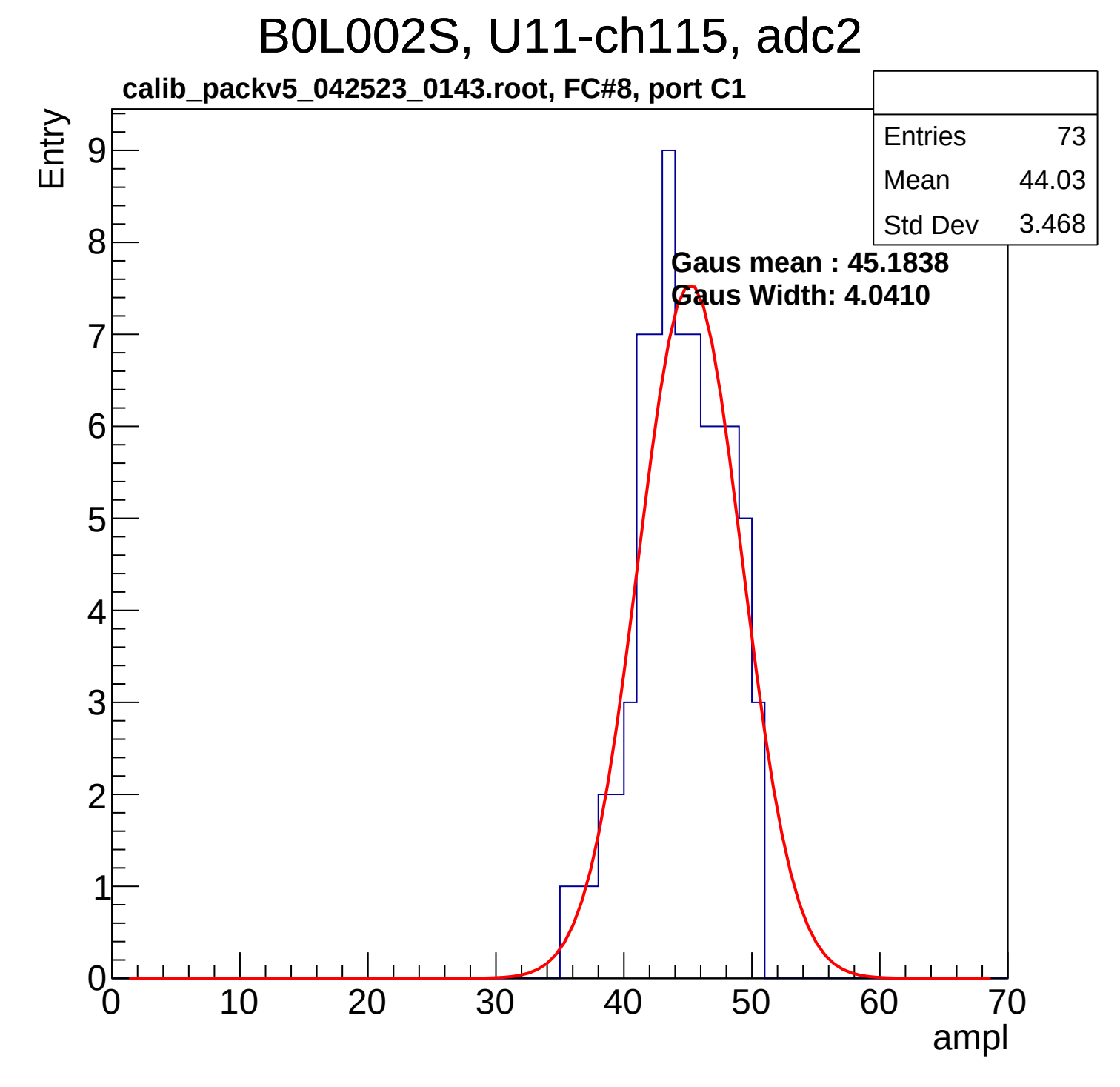
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.1
Std Dev 2.988
Gaus mean : 37.8067
Gaus Width: 2.9739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.03
Std Dev 3.468
Gaus mean : 45.1838
Gaus Width: 4.0410
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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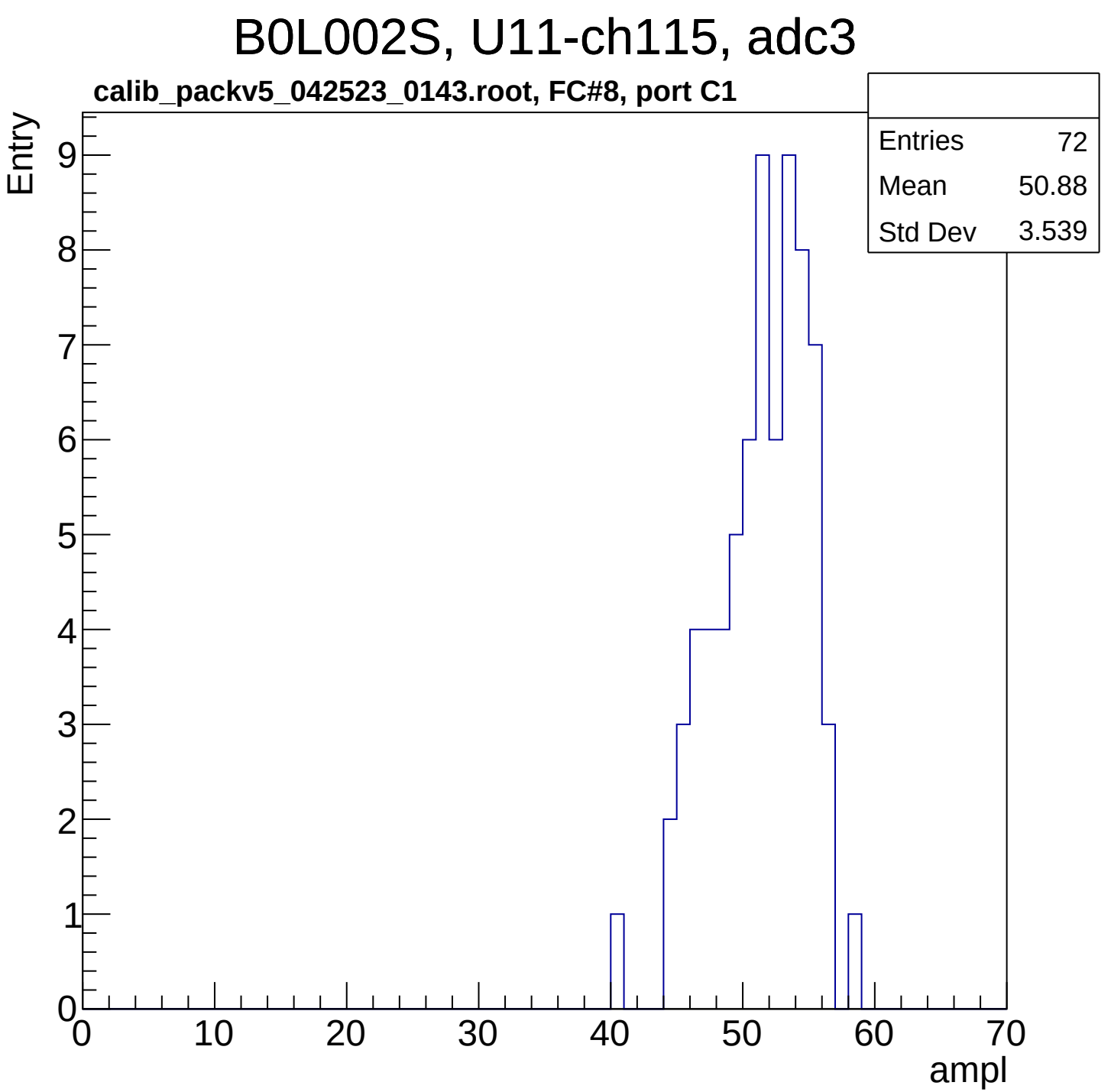
B0L002S, U11-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.88
Std Dev 3.539
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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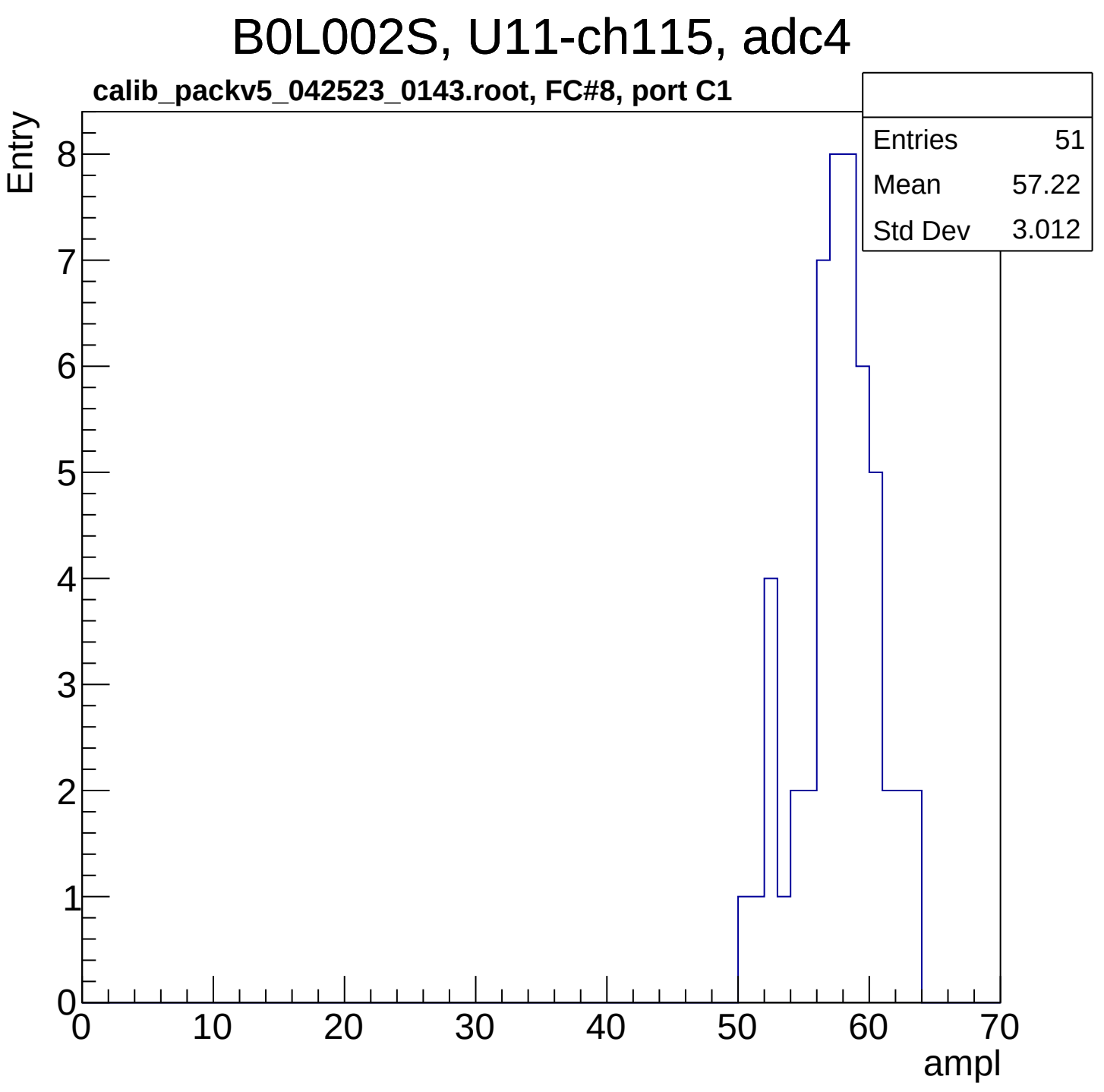
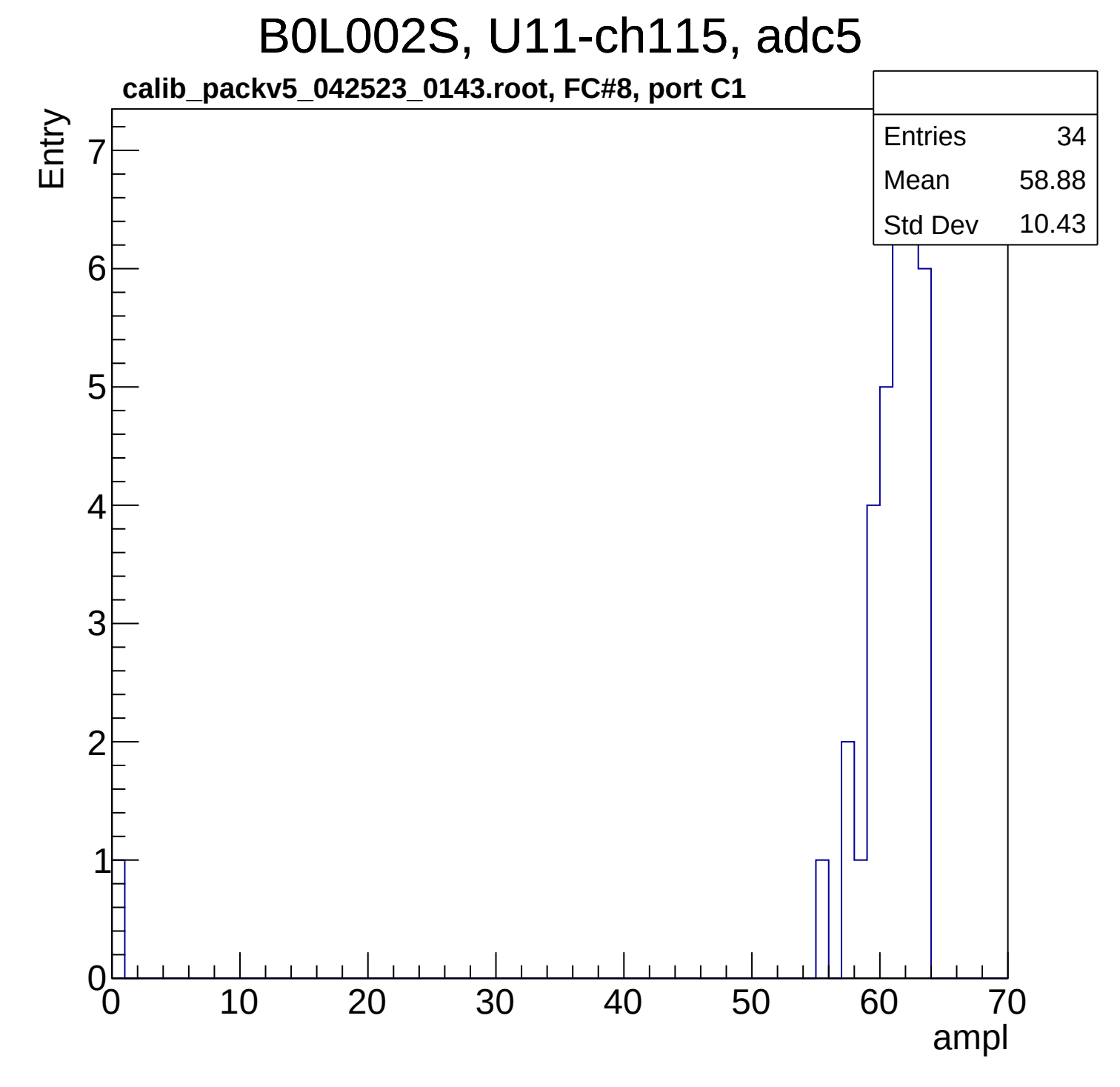
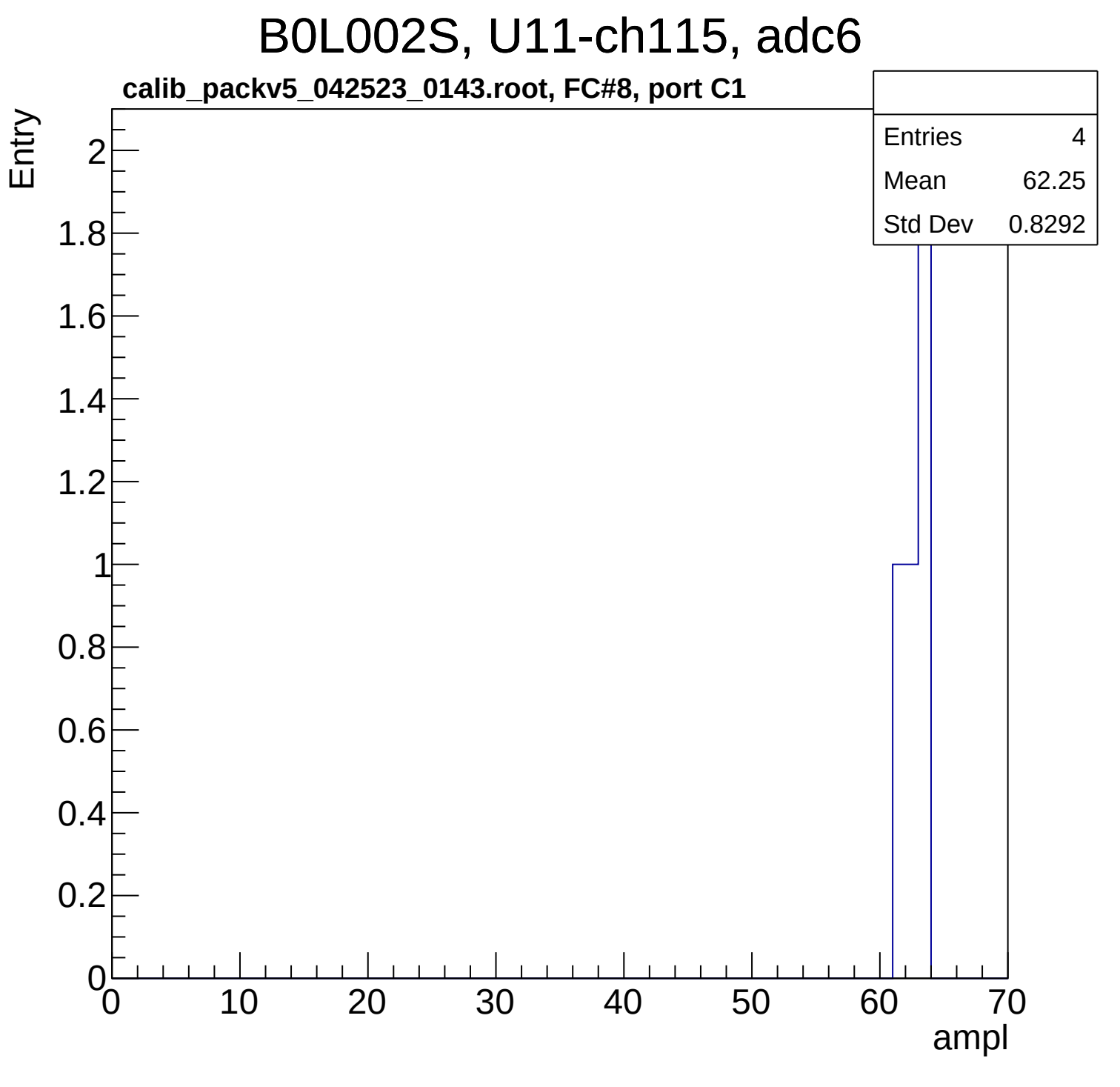

B0L002S, U11-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.22
Std Dev 3.012
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.88
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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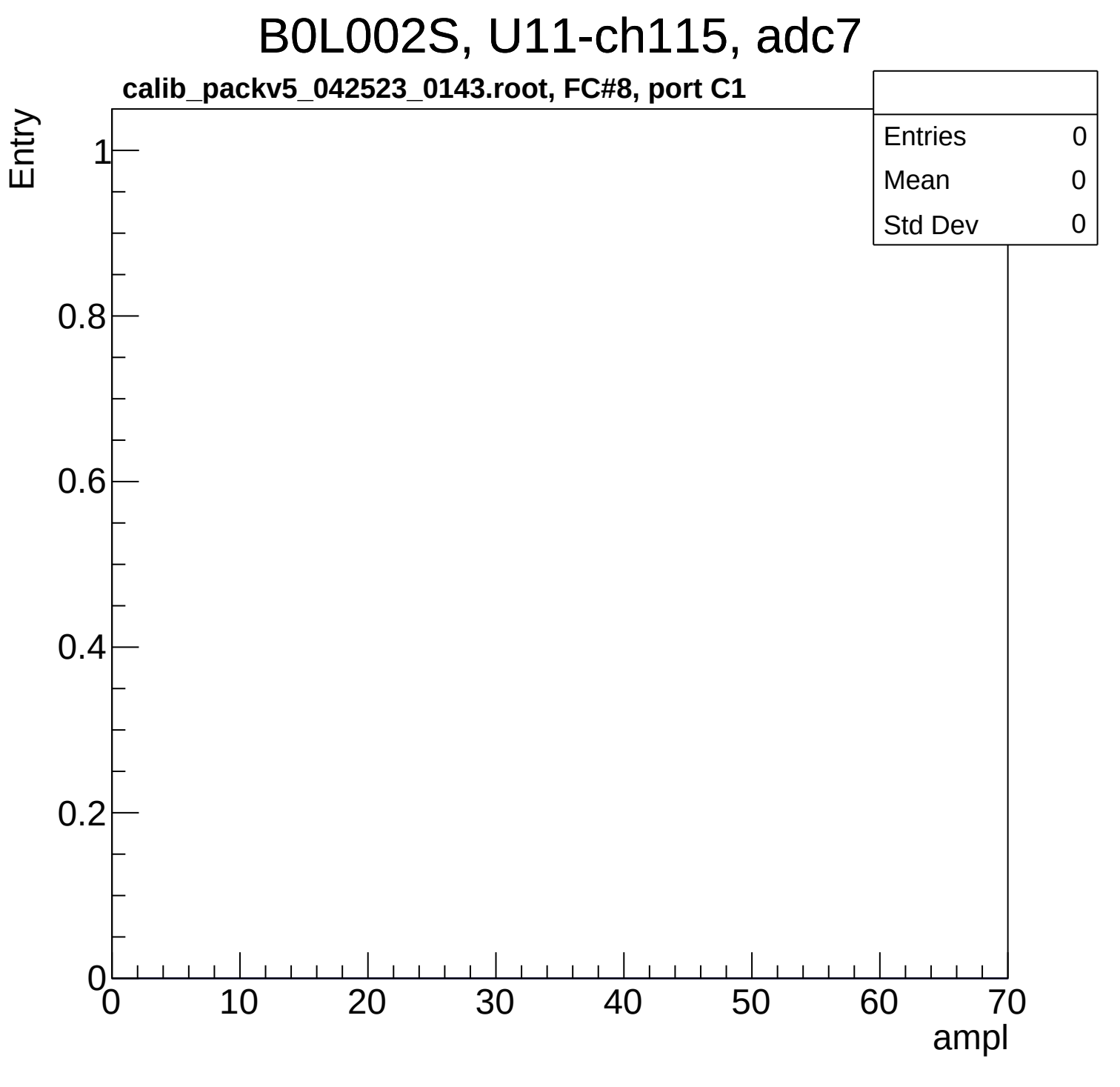
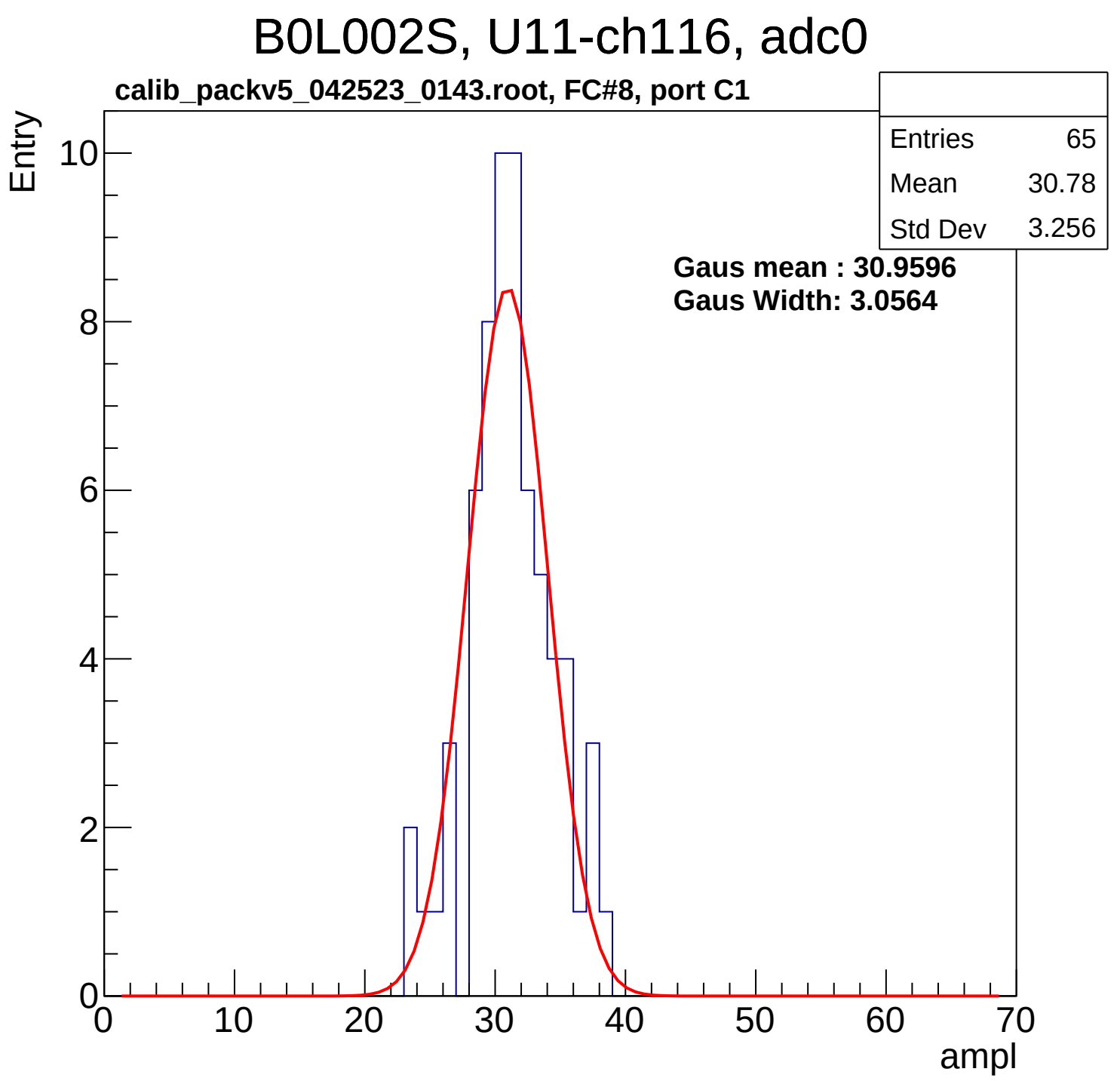
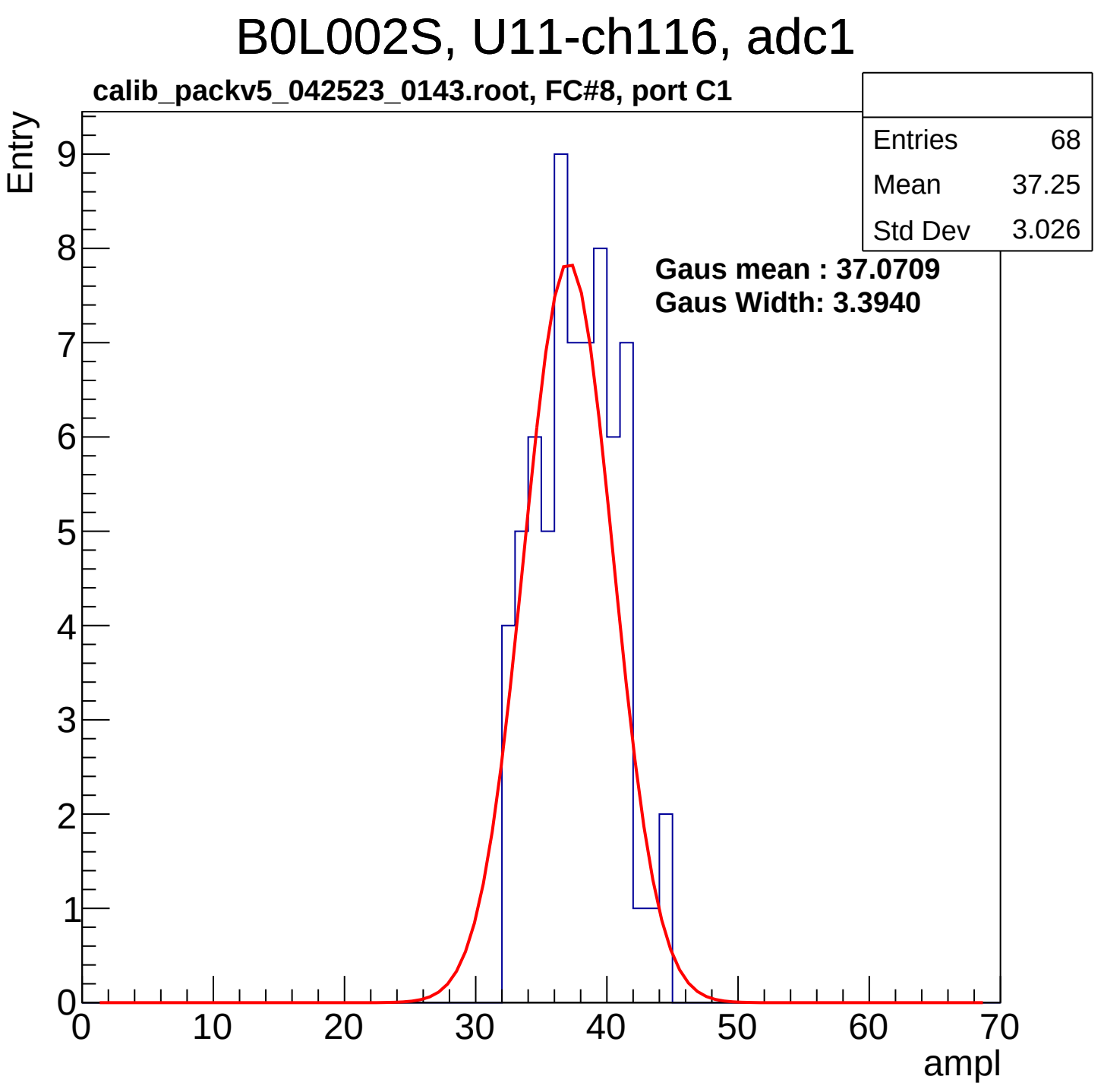

B0L002S, U11-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.78
Std Dev 3.256
Gaus mean : 30.9596
Gaus Width: 3.0564
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.25
Std Dev 3.026
Gaus mean : 37.0709
Gaus Width: 3.3940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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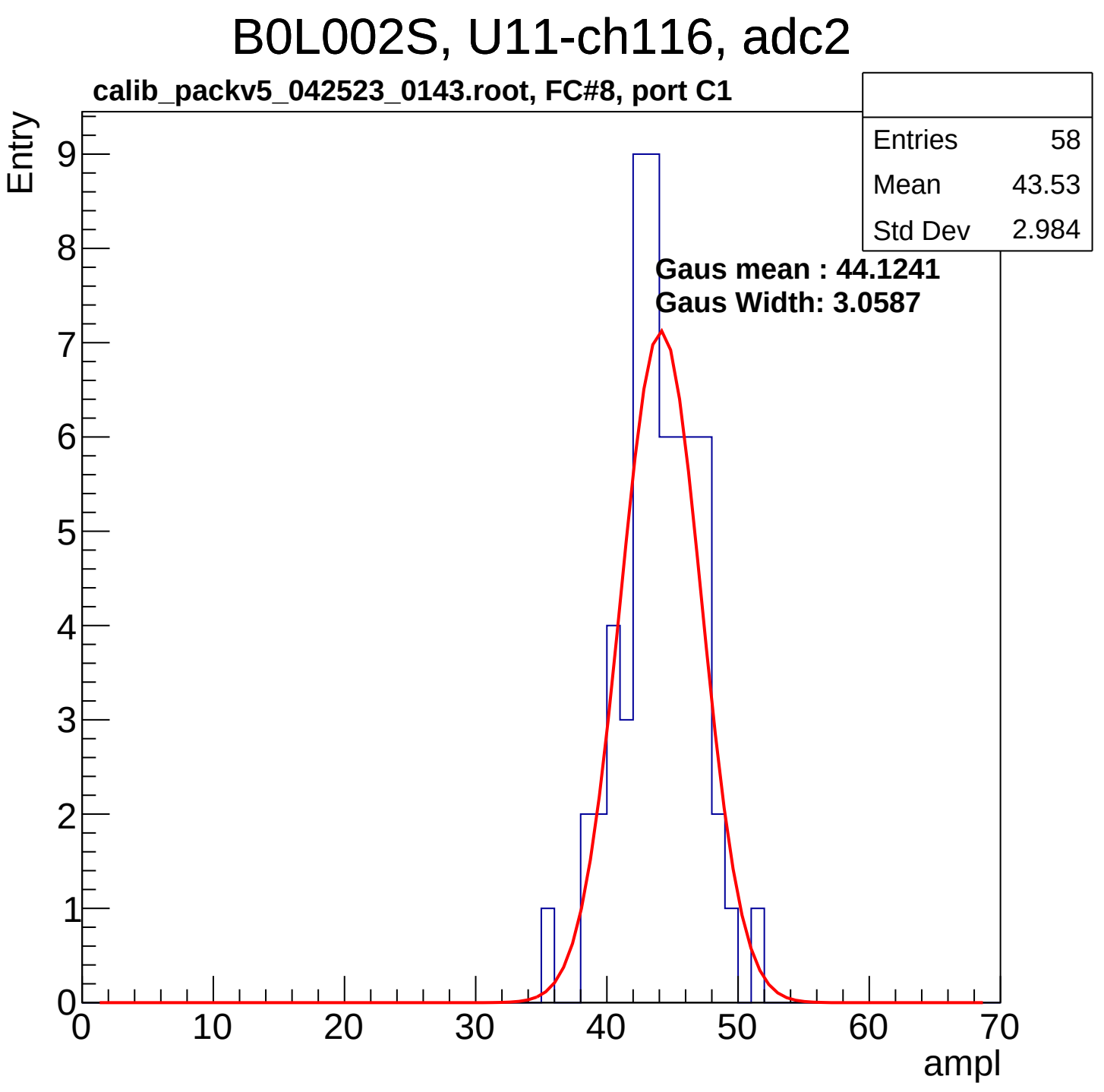
B0L002S, U11-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.53
Std Dev 2.984
Gaus mean : 44.1241
Gaus Width: 3.0587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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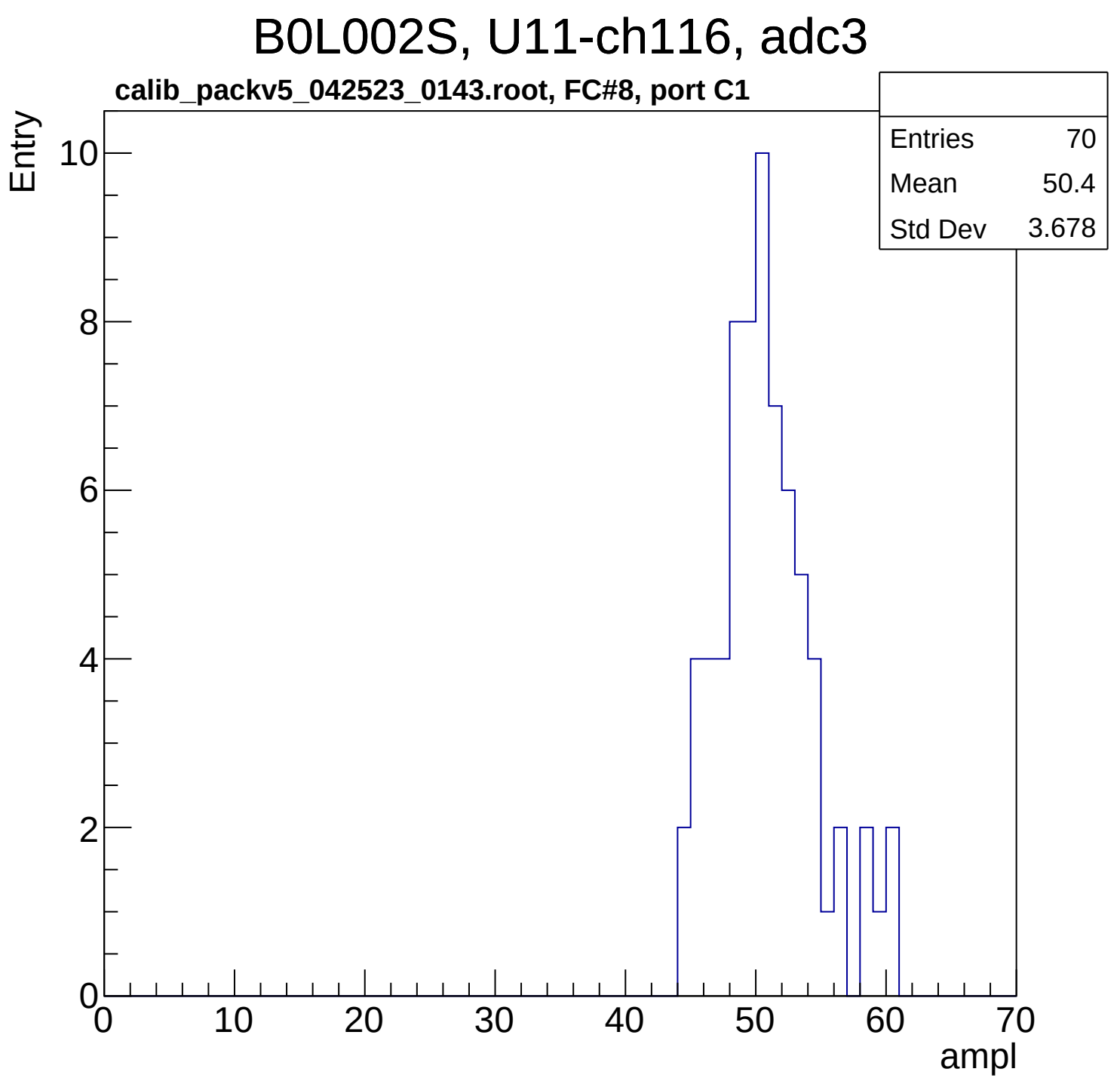
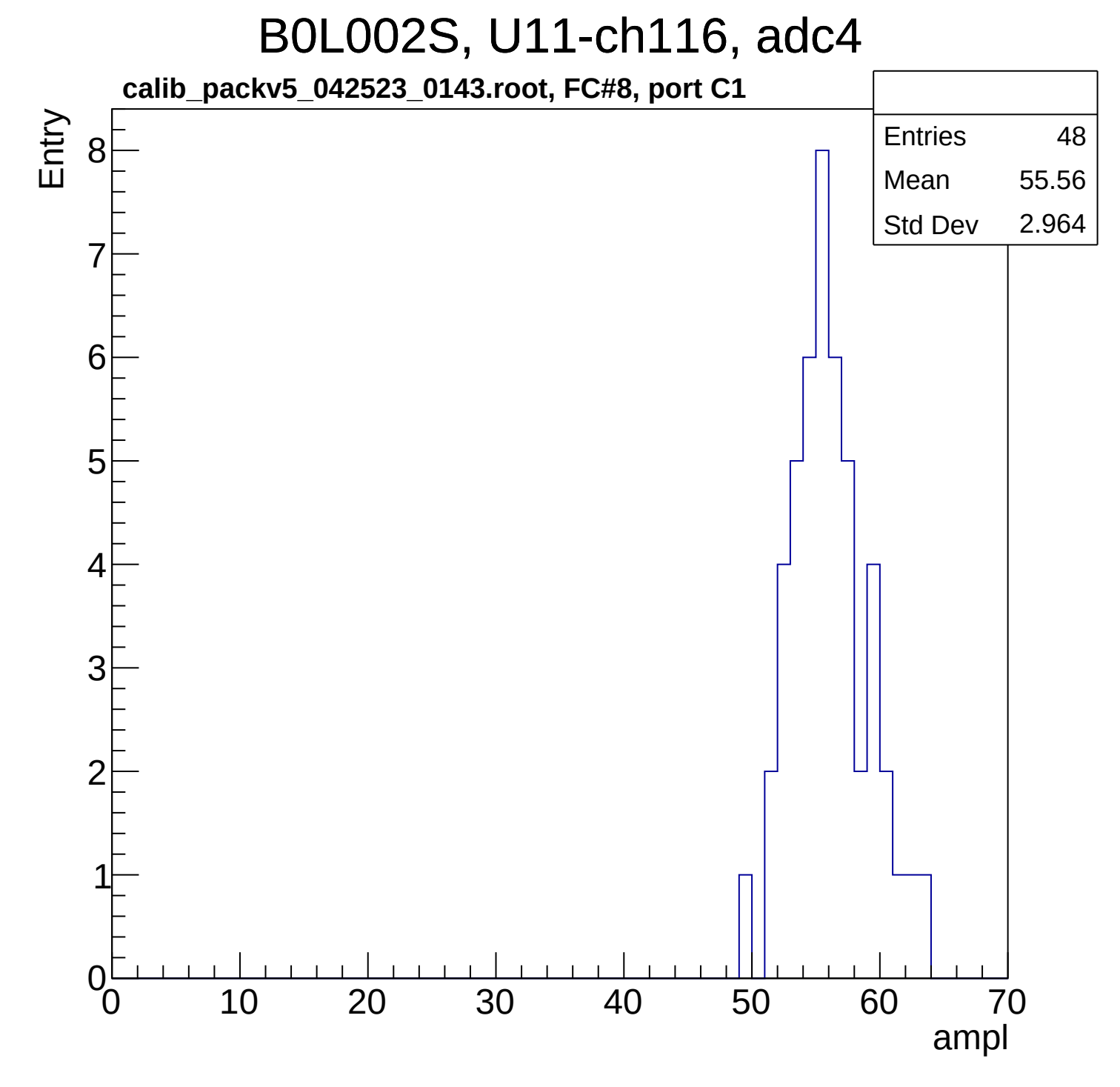
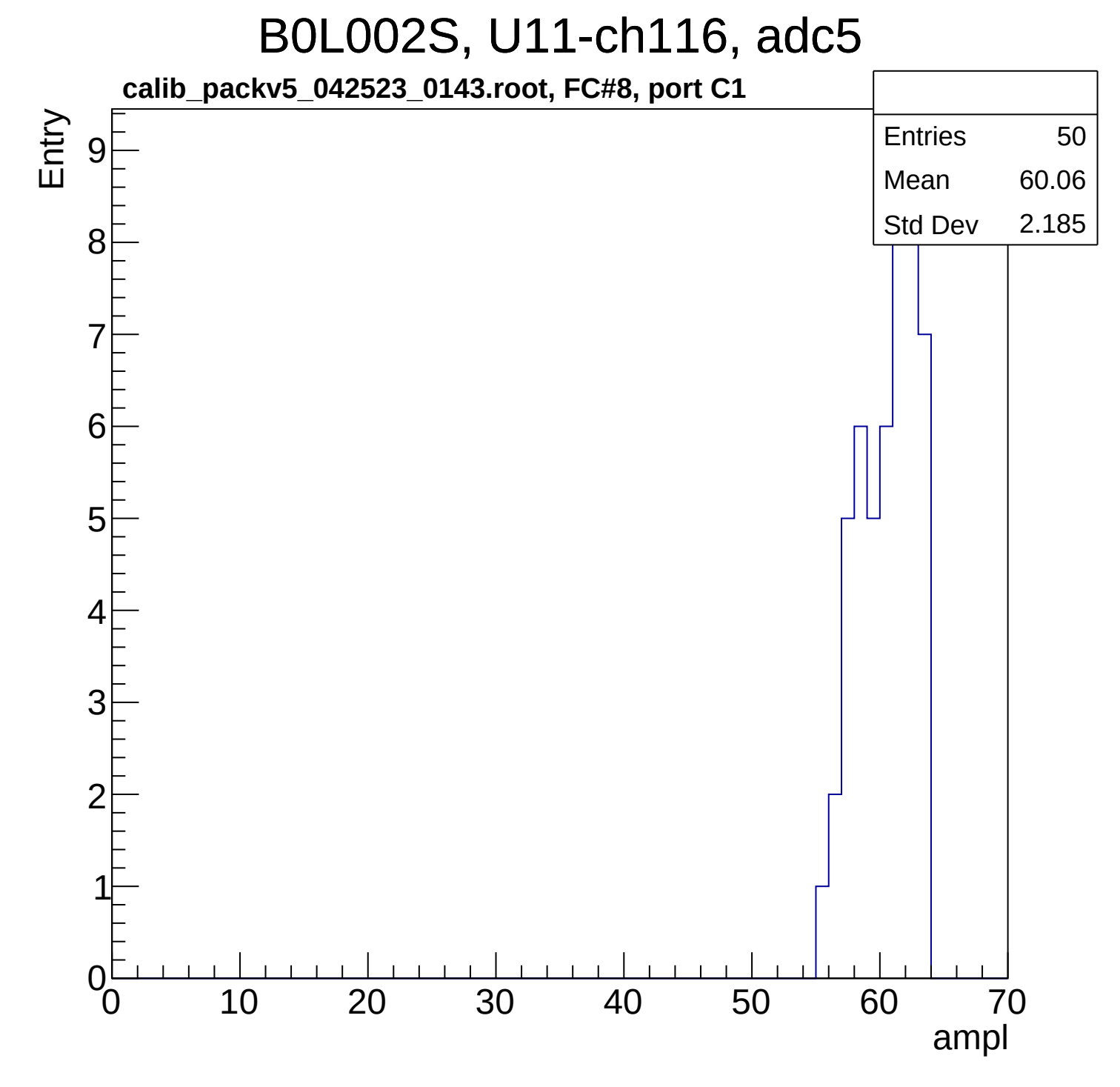
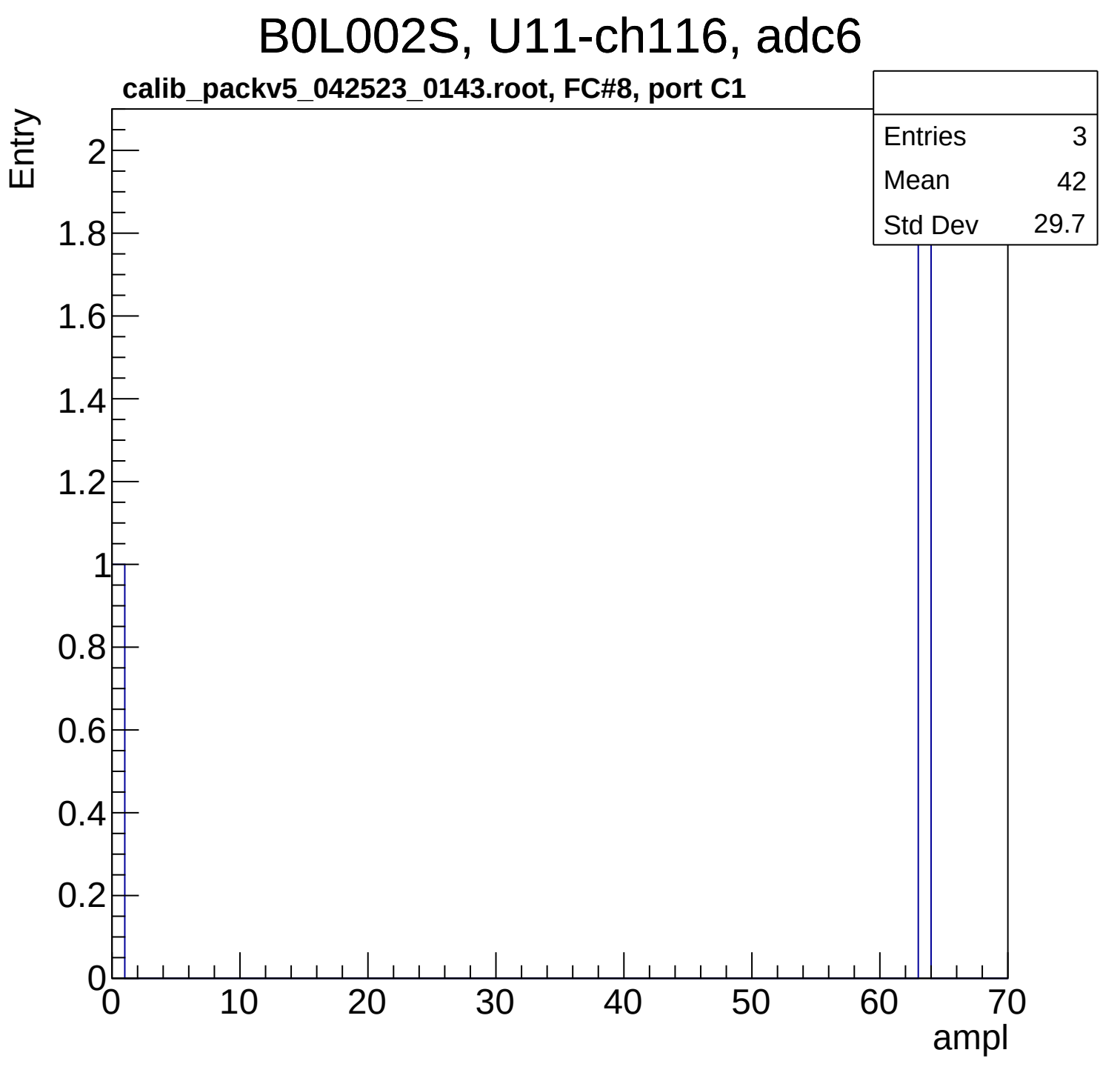
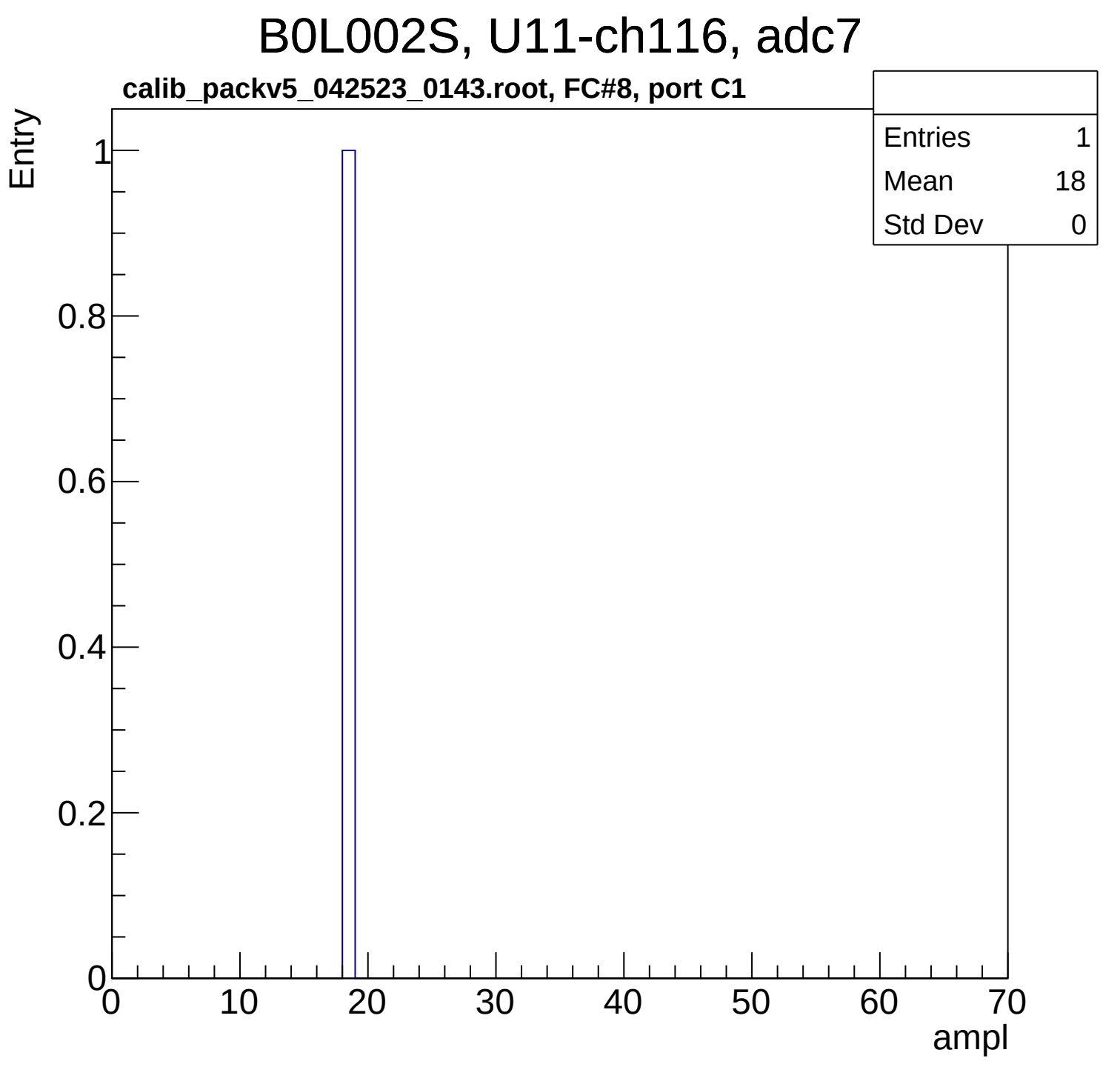
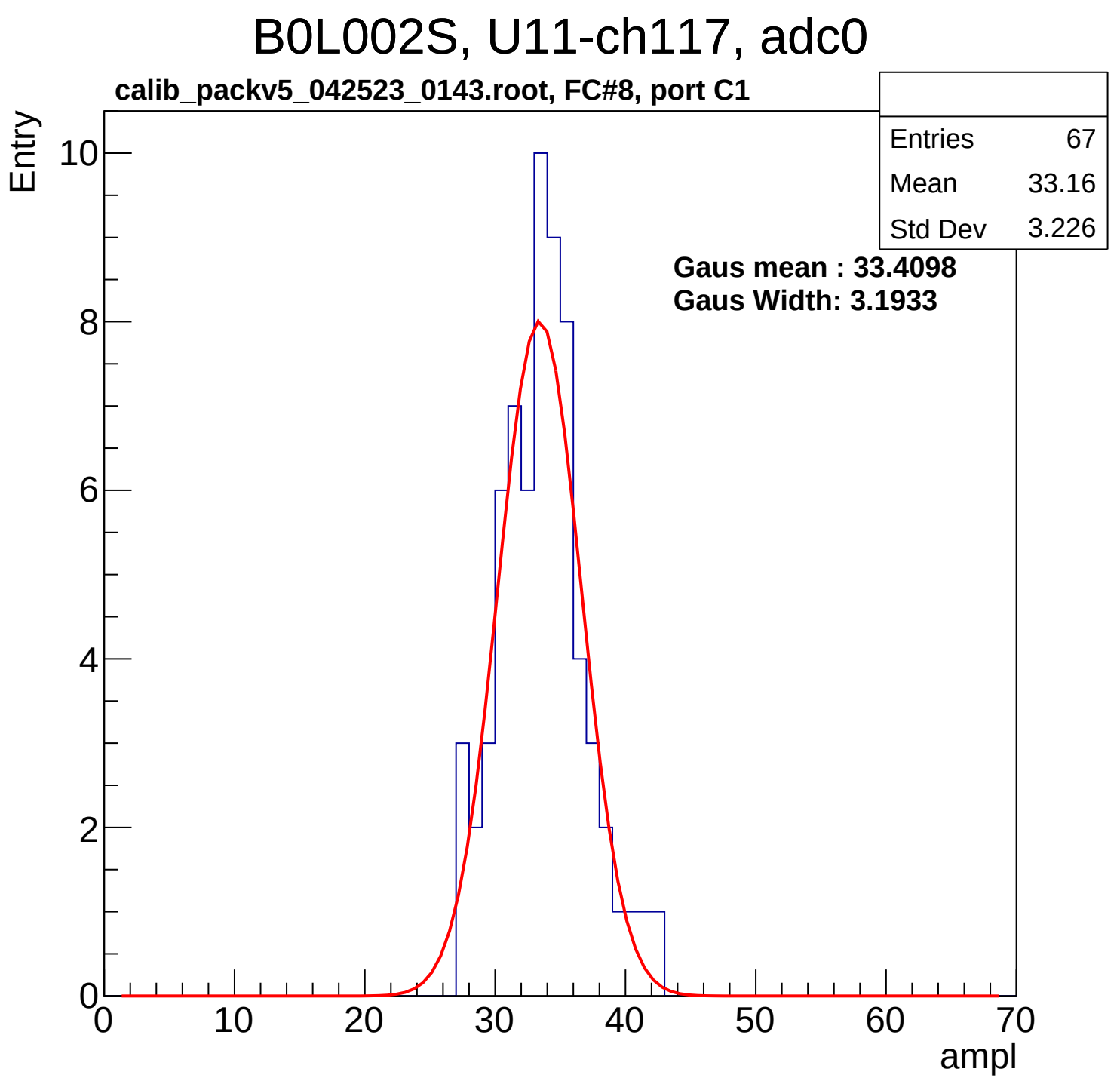

B0L002S, U11-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 50.4
Std Dev 3.678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

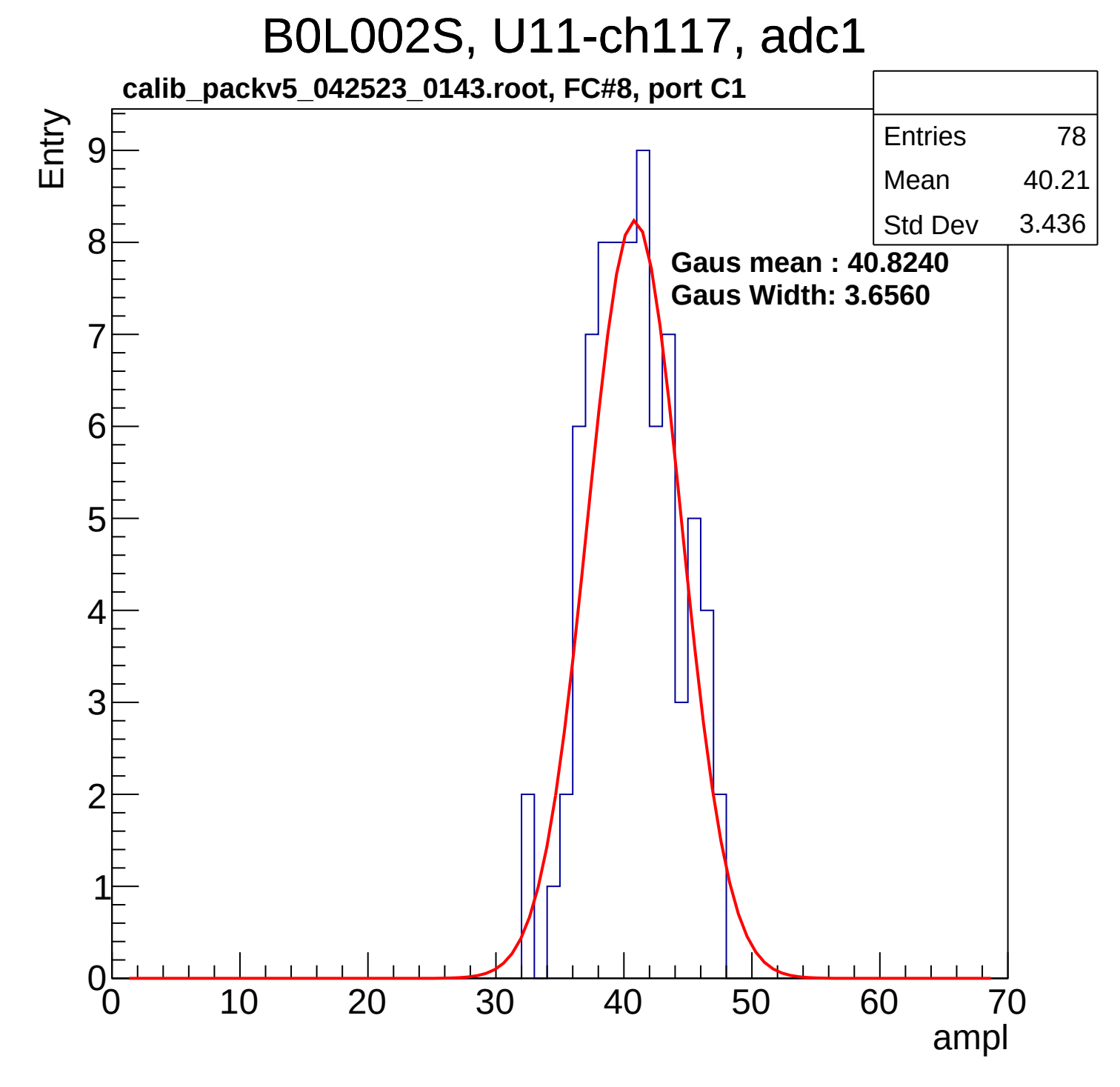

B0L002S, U11-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 55.56
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

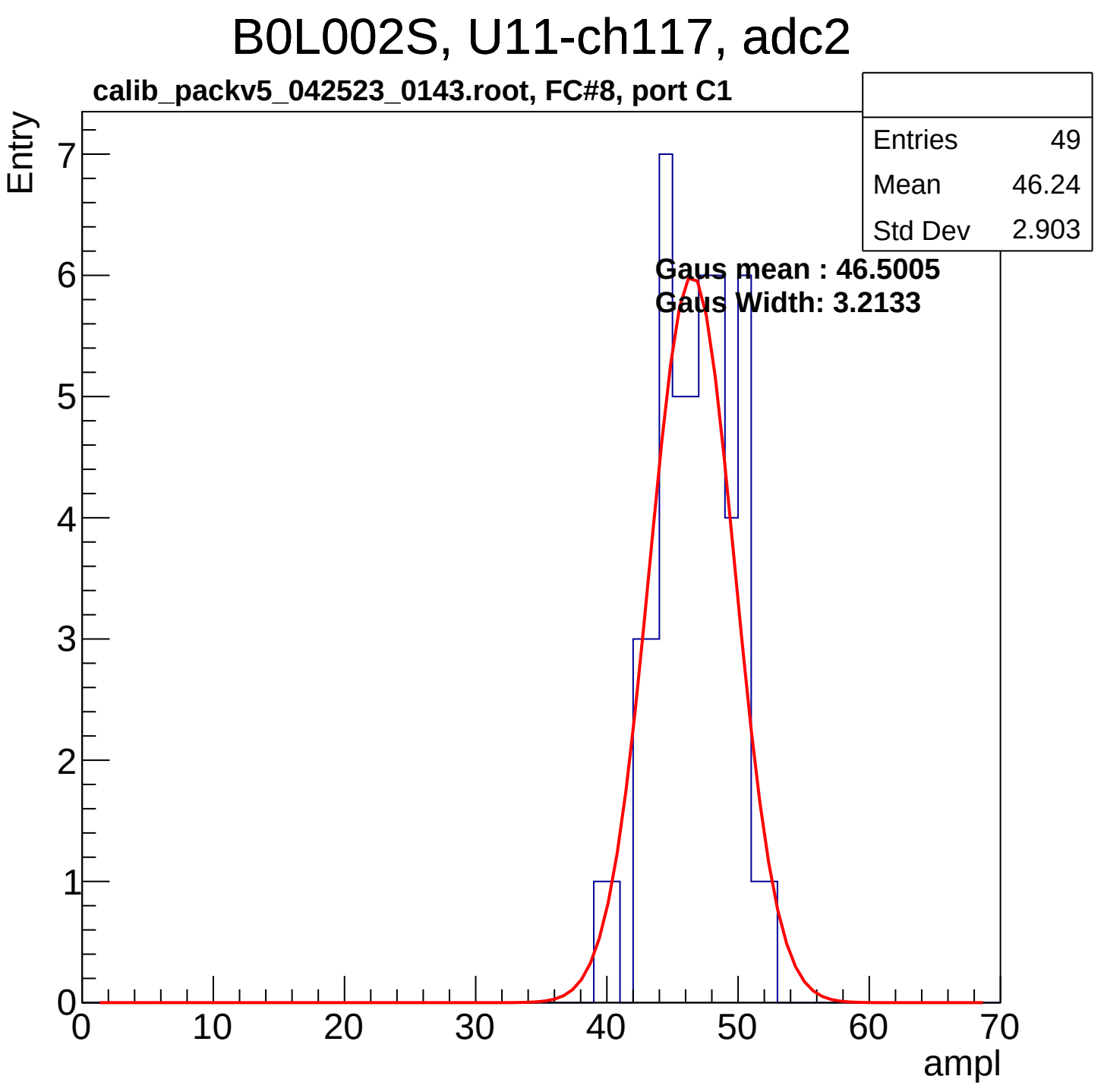

B0L002S, U11-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 60.06
Std Dev 2.185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

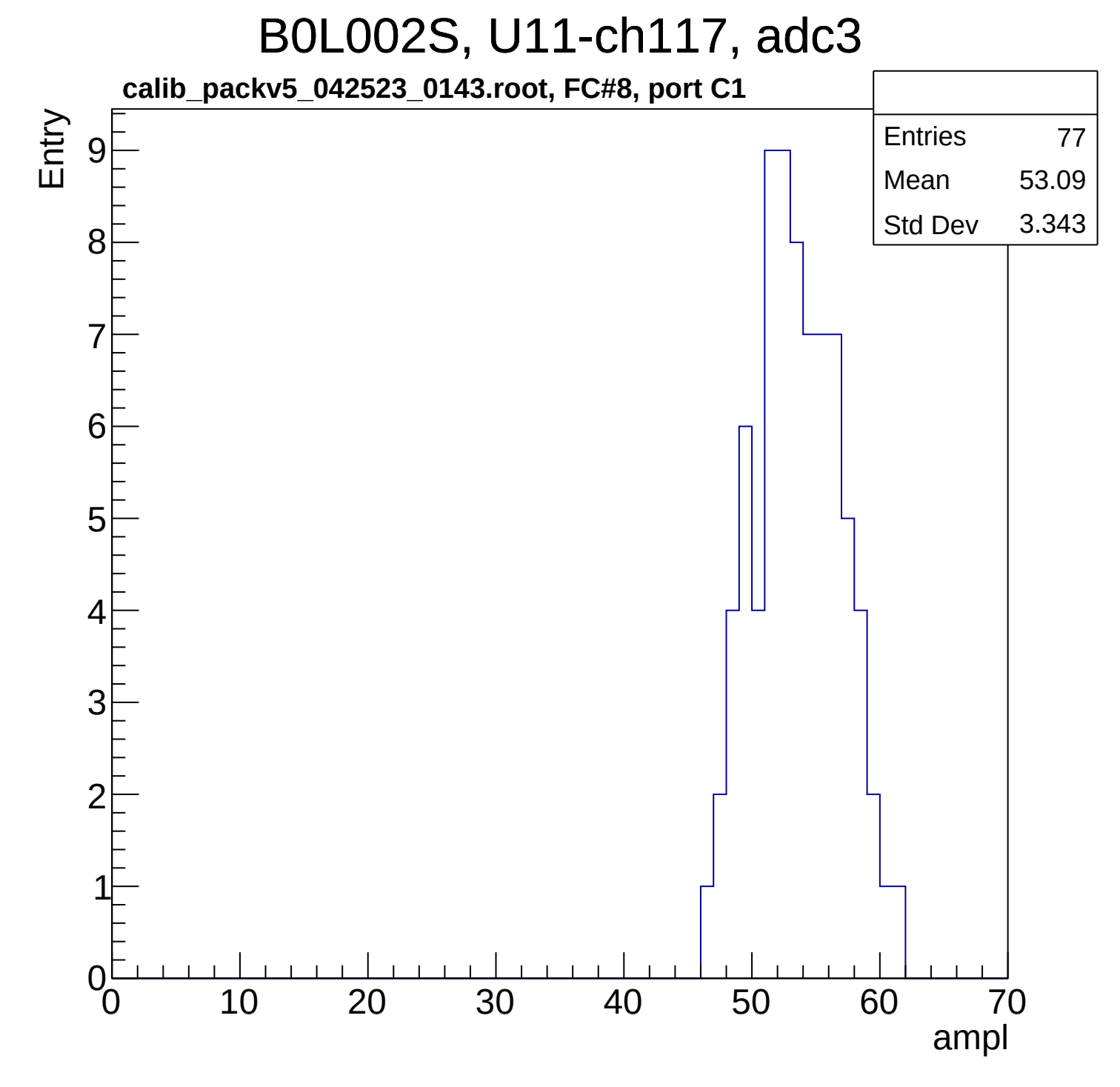

B0L002S, U11-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

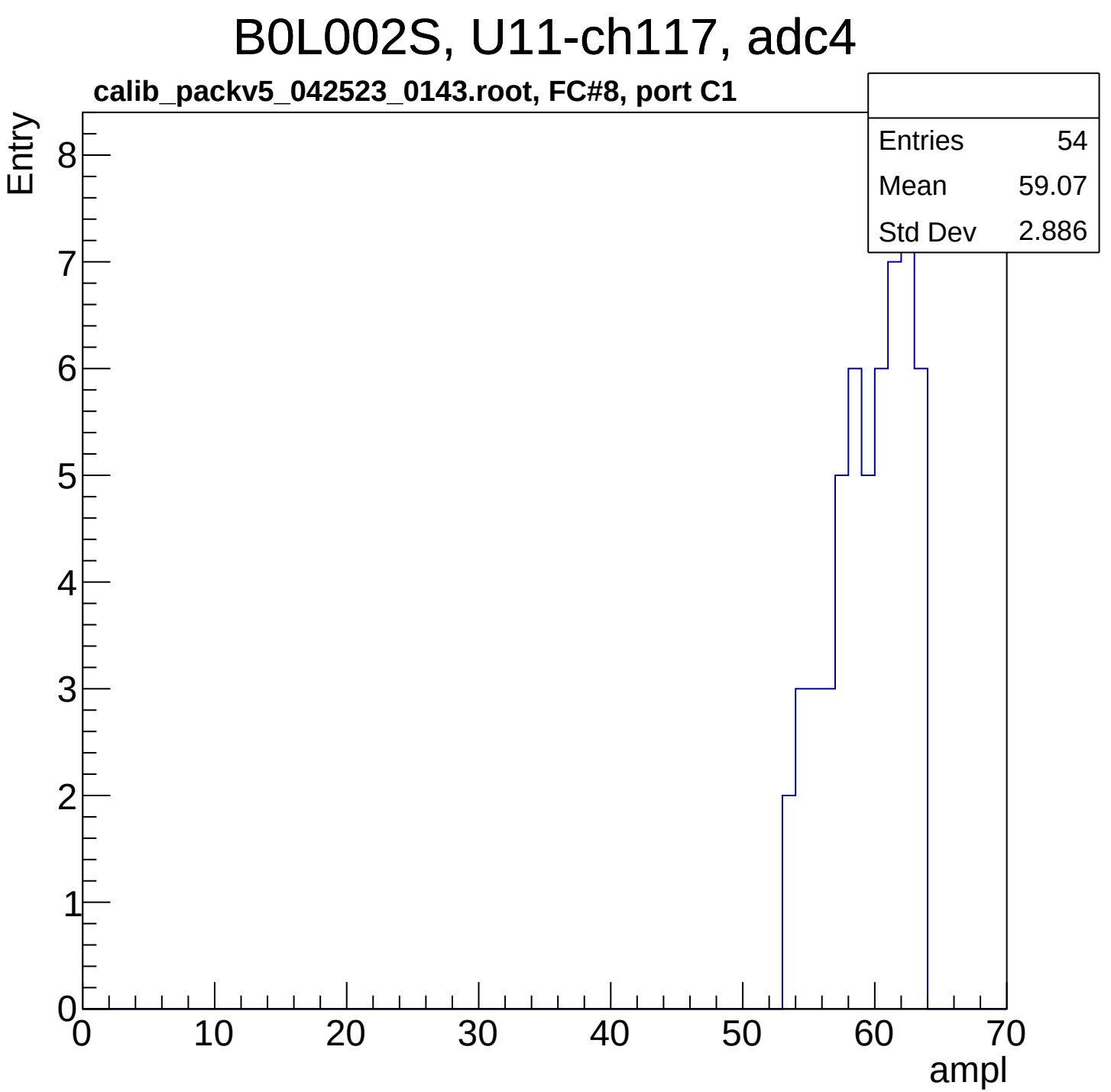

B0L002S, U11-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

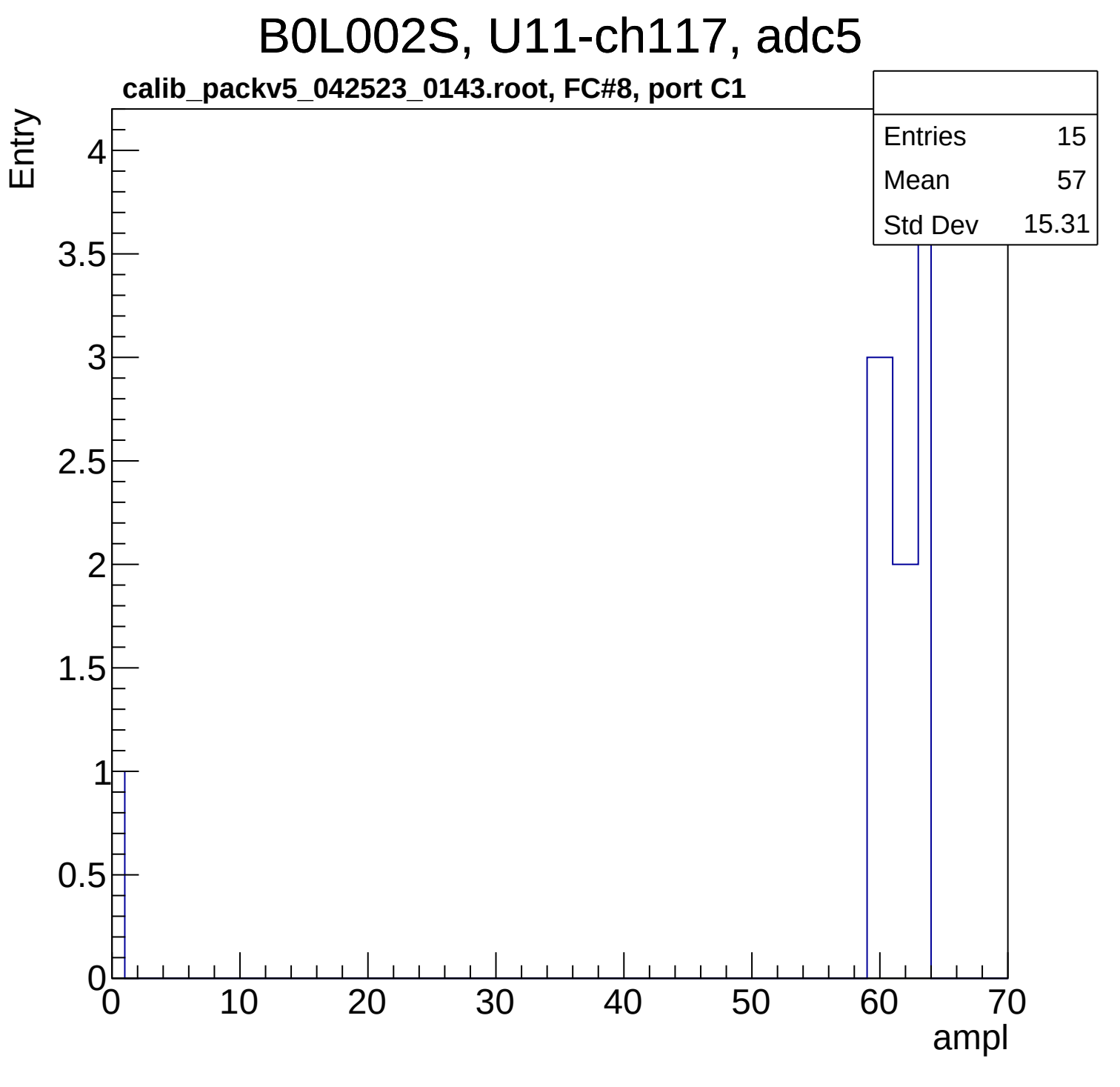

B0L002S, U11-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 33.16
Std Dev 3.226
Gaus mean : 33.4098
Gaus Width: 3.1933
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

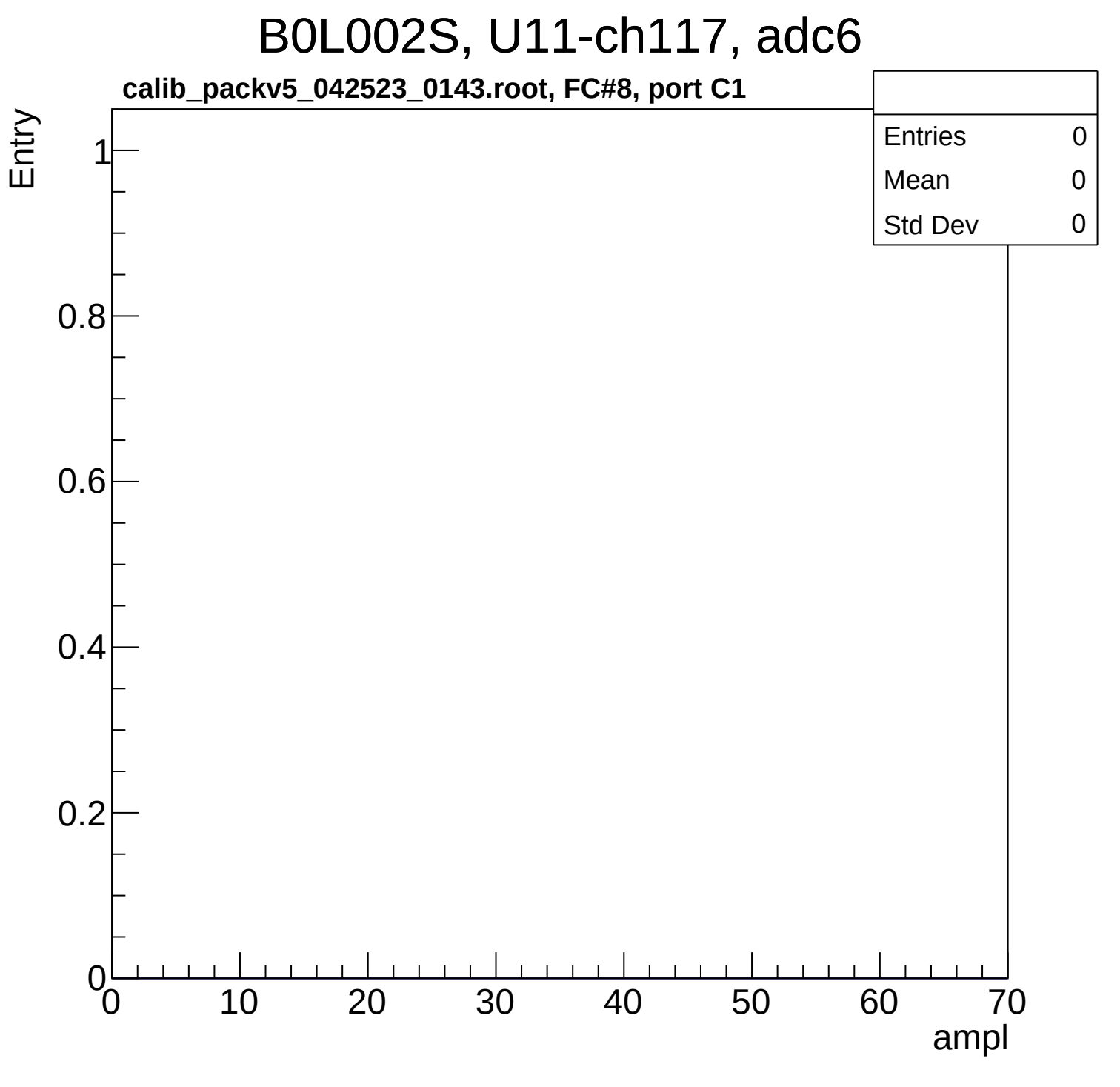

B0L002S, U11-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 40.21
Std Dev 3.436
Gaus mean : 40.8240
Gaus Width: 3.6560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

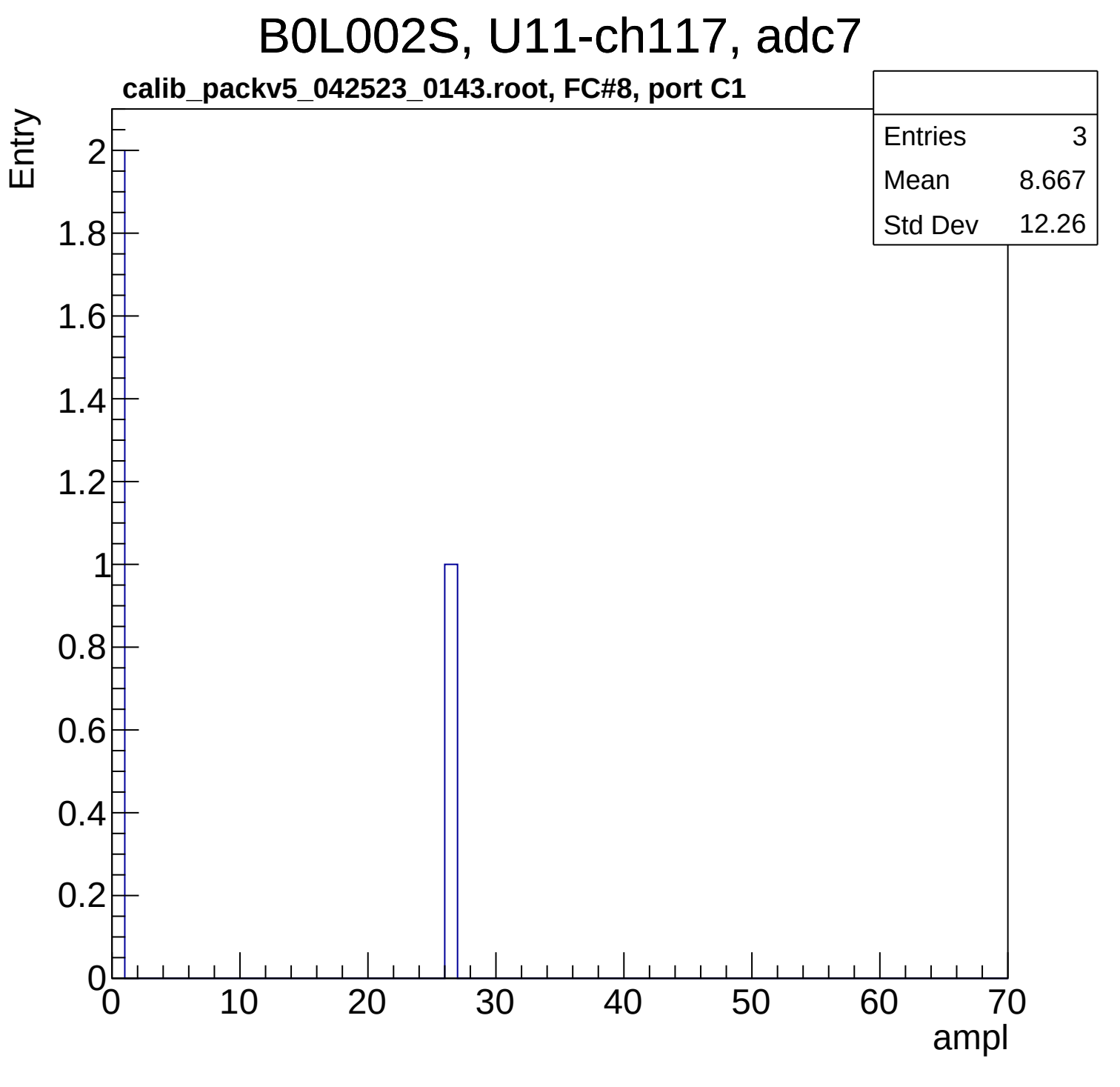

B0L002S, U11-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 46.24
Std Dev 2.903
Gaus mean : 46.5005
Gaus Width: 3.2133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

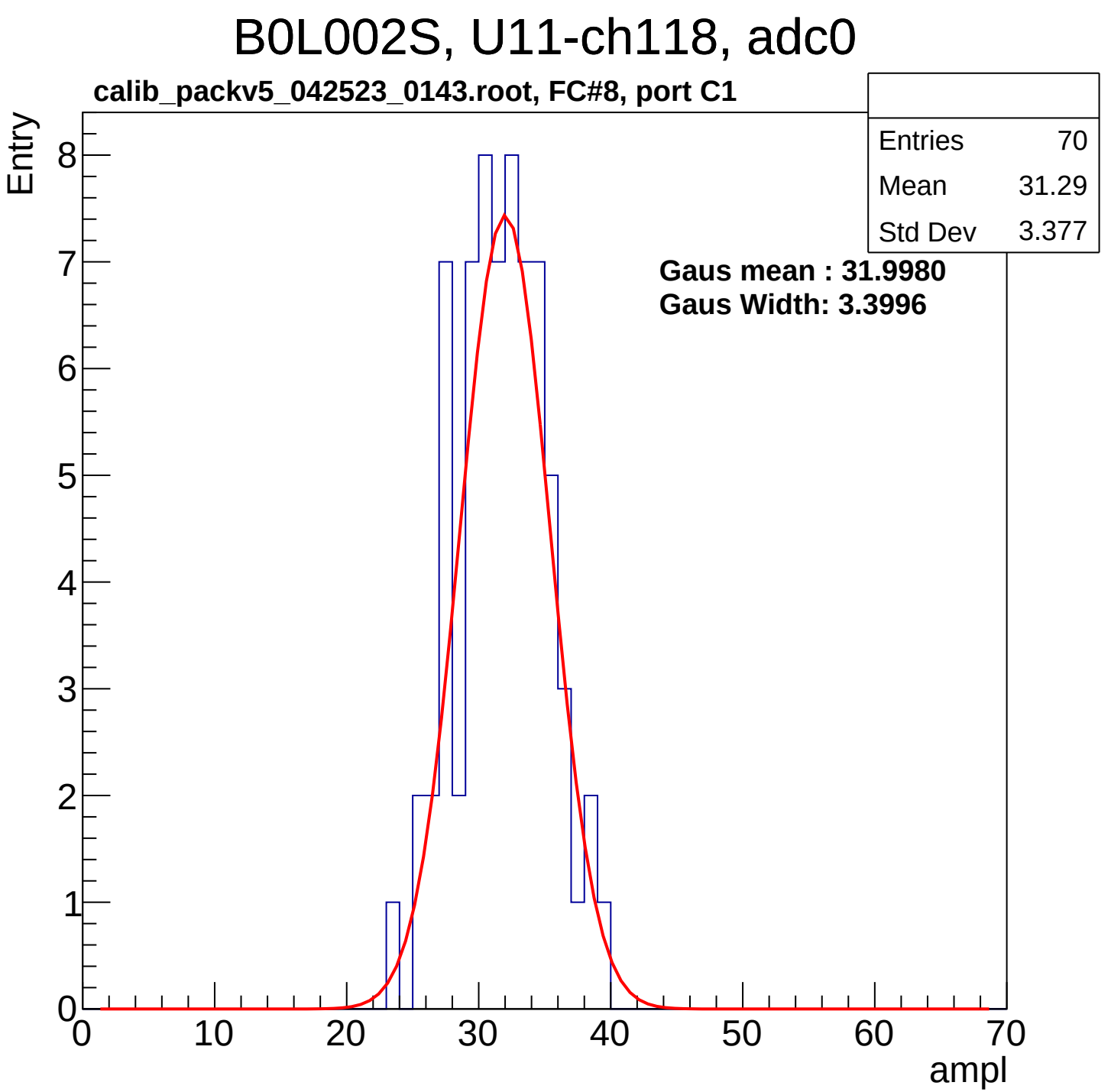

B0L002S, U11-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 53.09
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

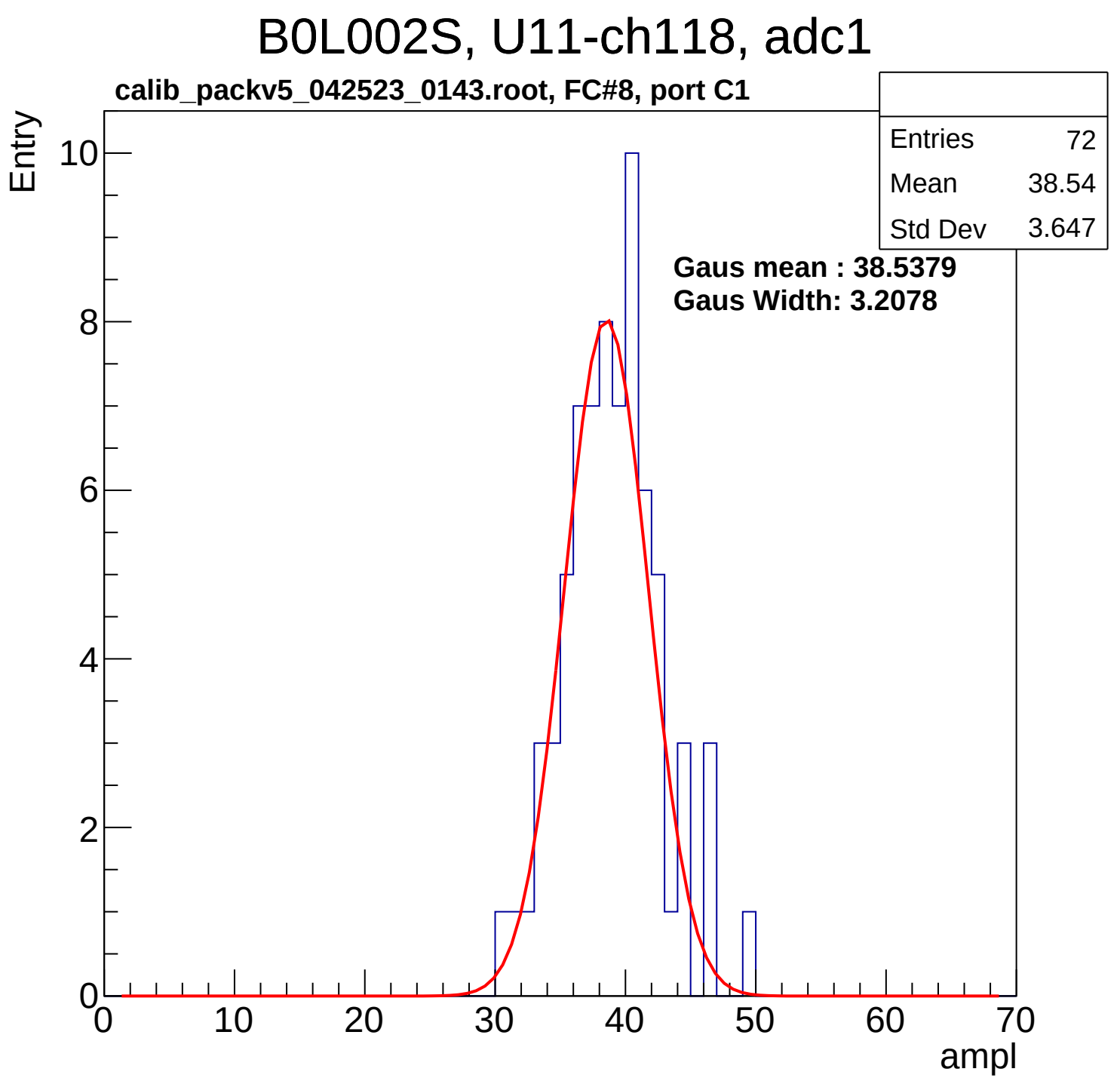

B0L002S, U11-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 59.07
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

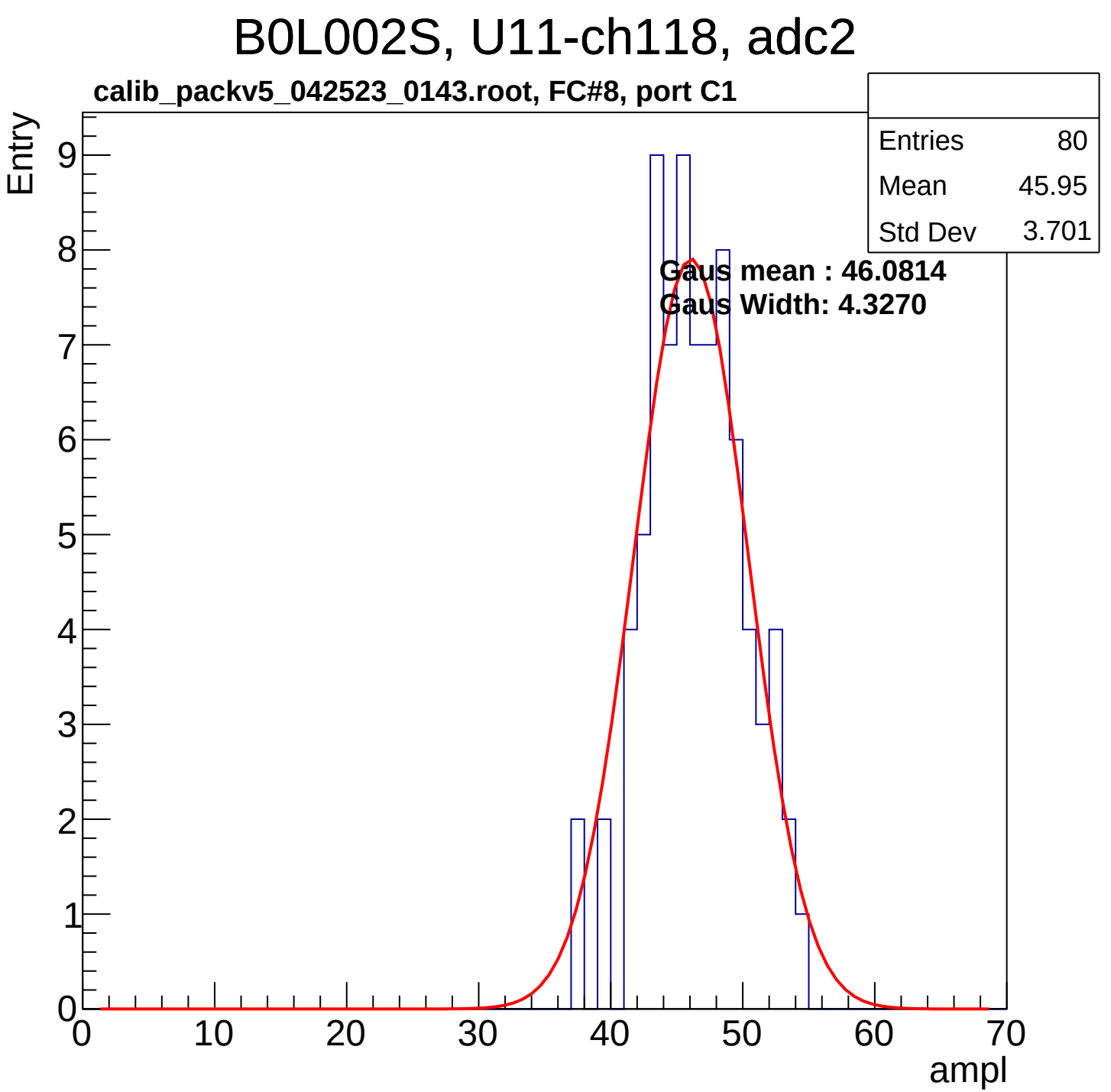

B0L002S, U11-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 57
Std Dev 15.31
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

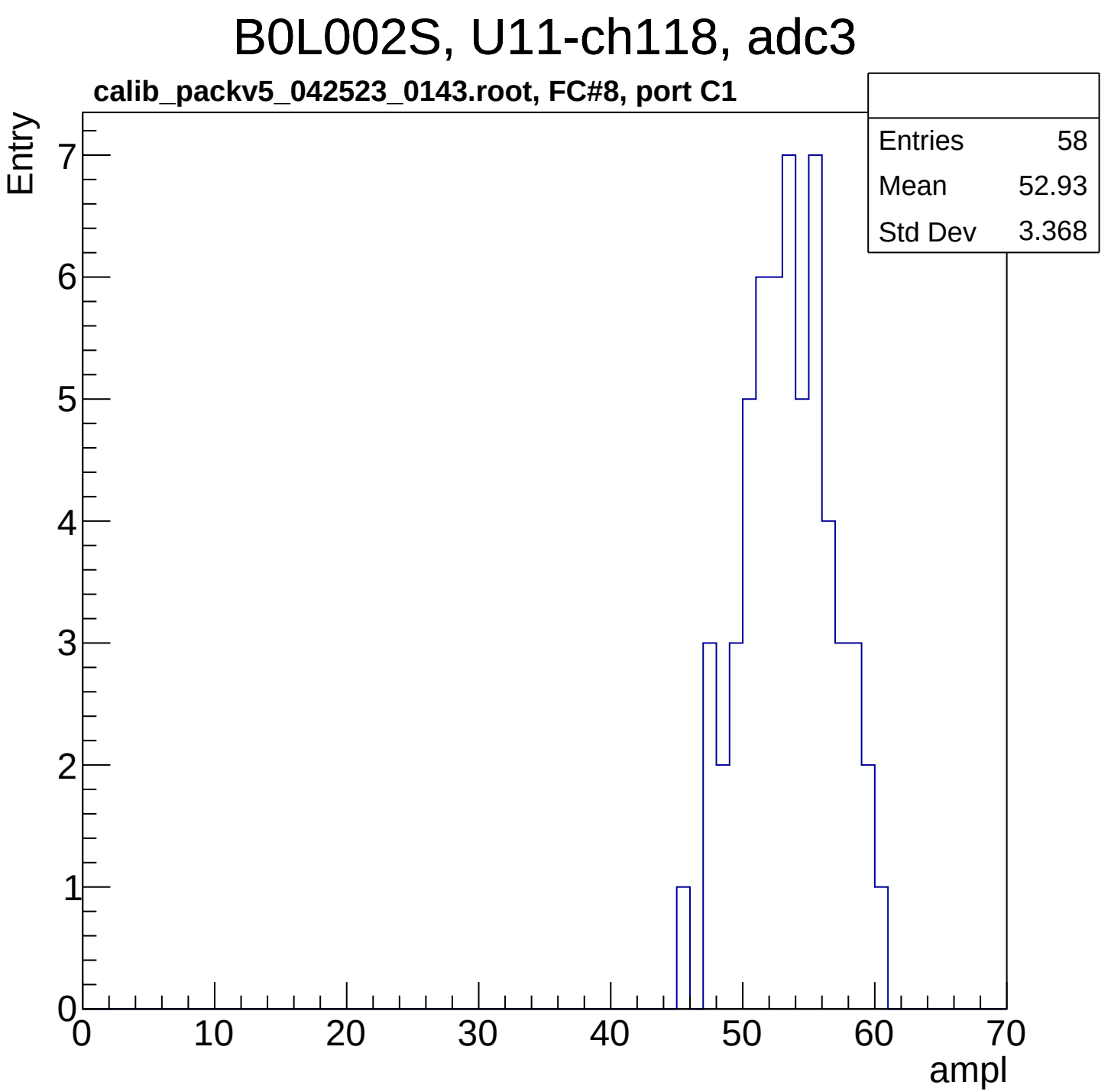

B0L002S, U11-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

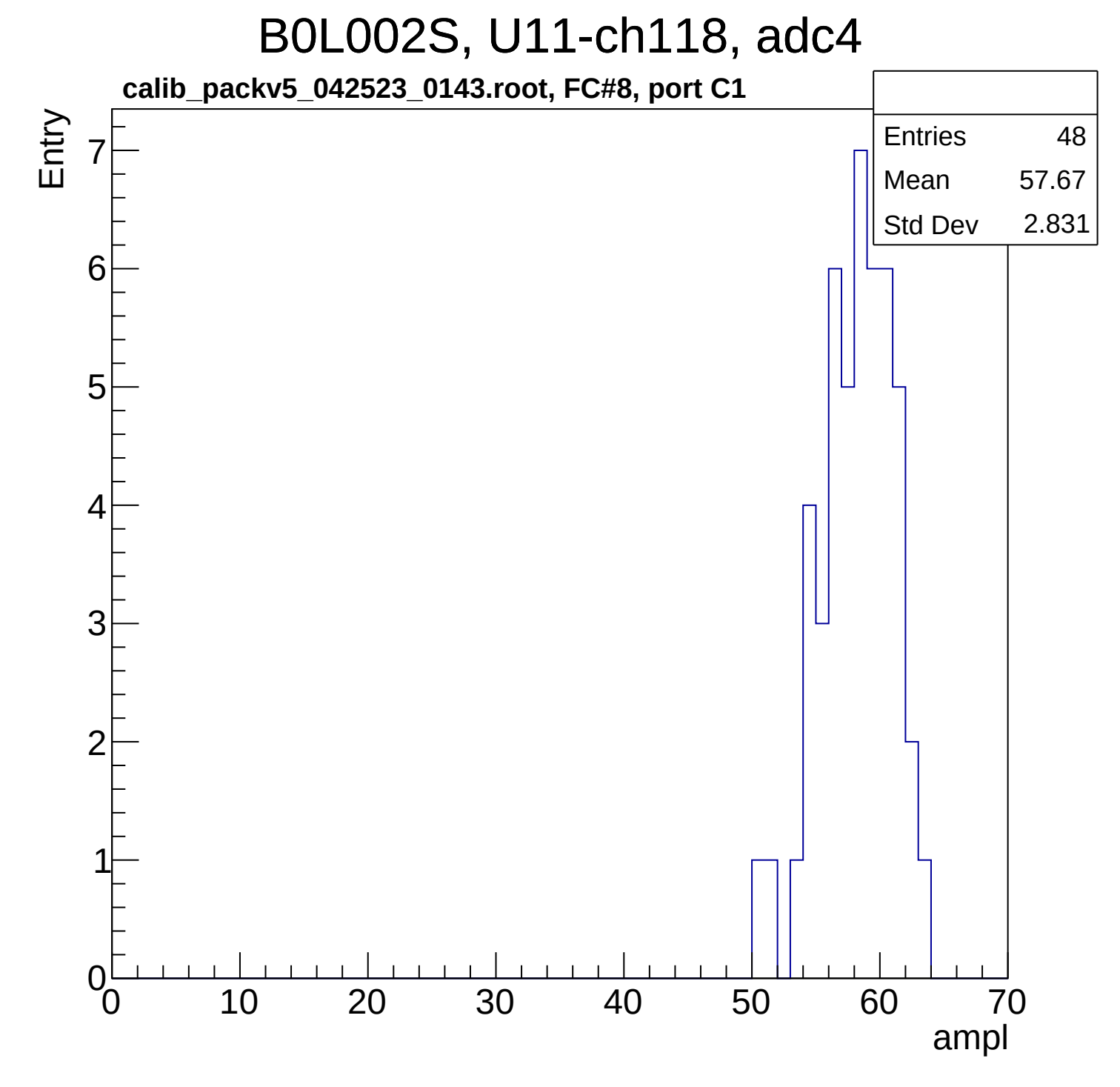

B0L002S, U11-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8.667
Std Dev 12.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

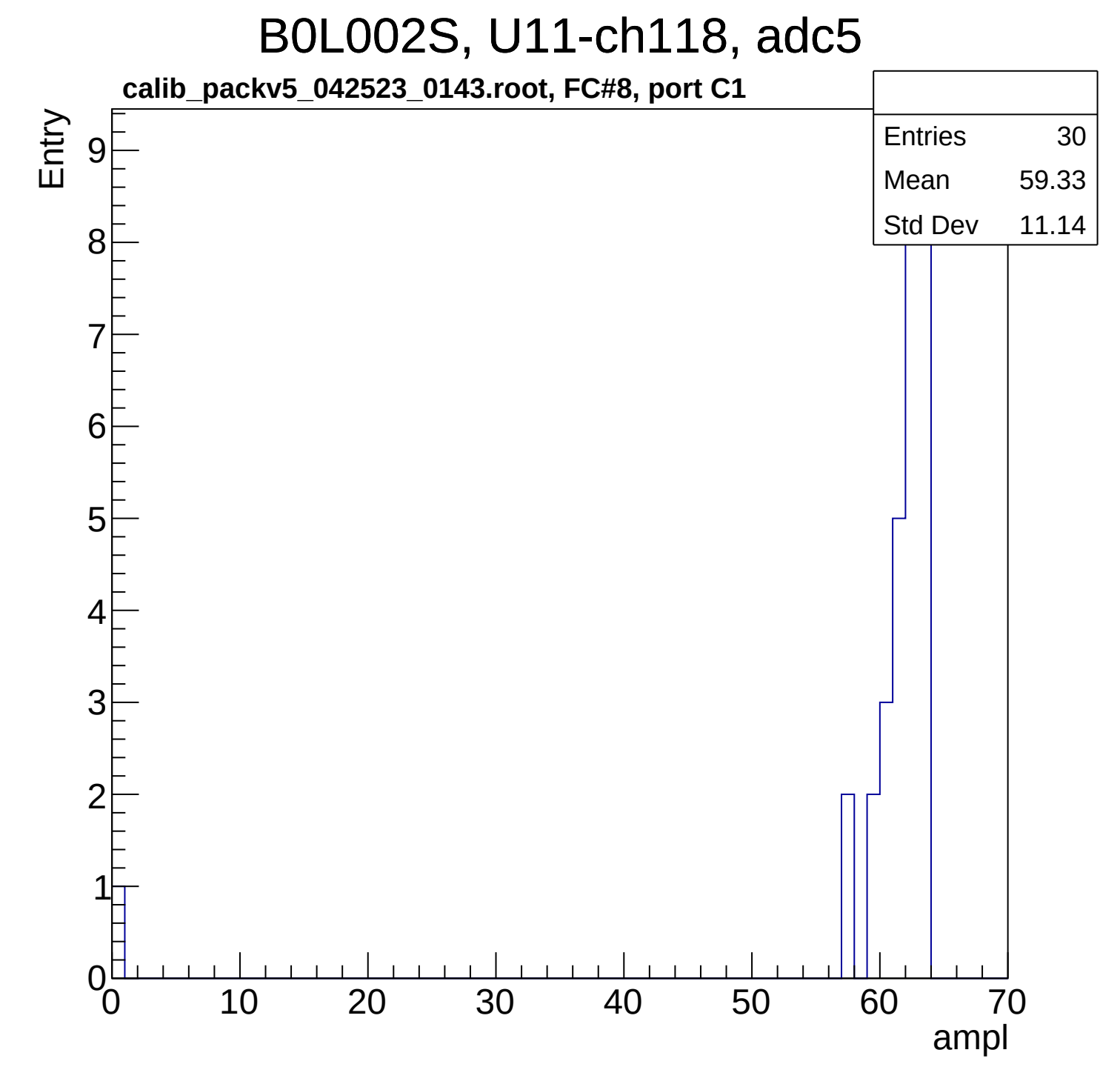

B0L002S, U11-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.29
Std Dev 3.377
Gaus mean : 31.9980
Gaus Width: 3.3996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.54
Std Dev 3.647
Gaus mean : 38.5379
Gaus Width: 3.2078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 45.95
Std Dev 3.701
Gaus mean : 46.0814
Gaus Width: 4.3270
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 52.93
Std Dev 3.368
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.67
Std Dev 2.831
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.33
Std Dev 11.14
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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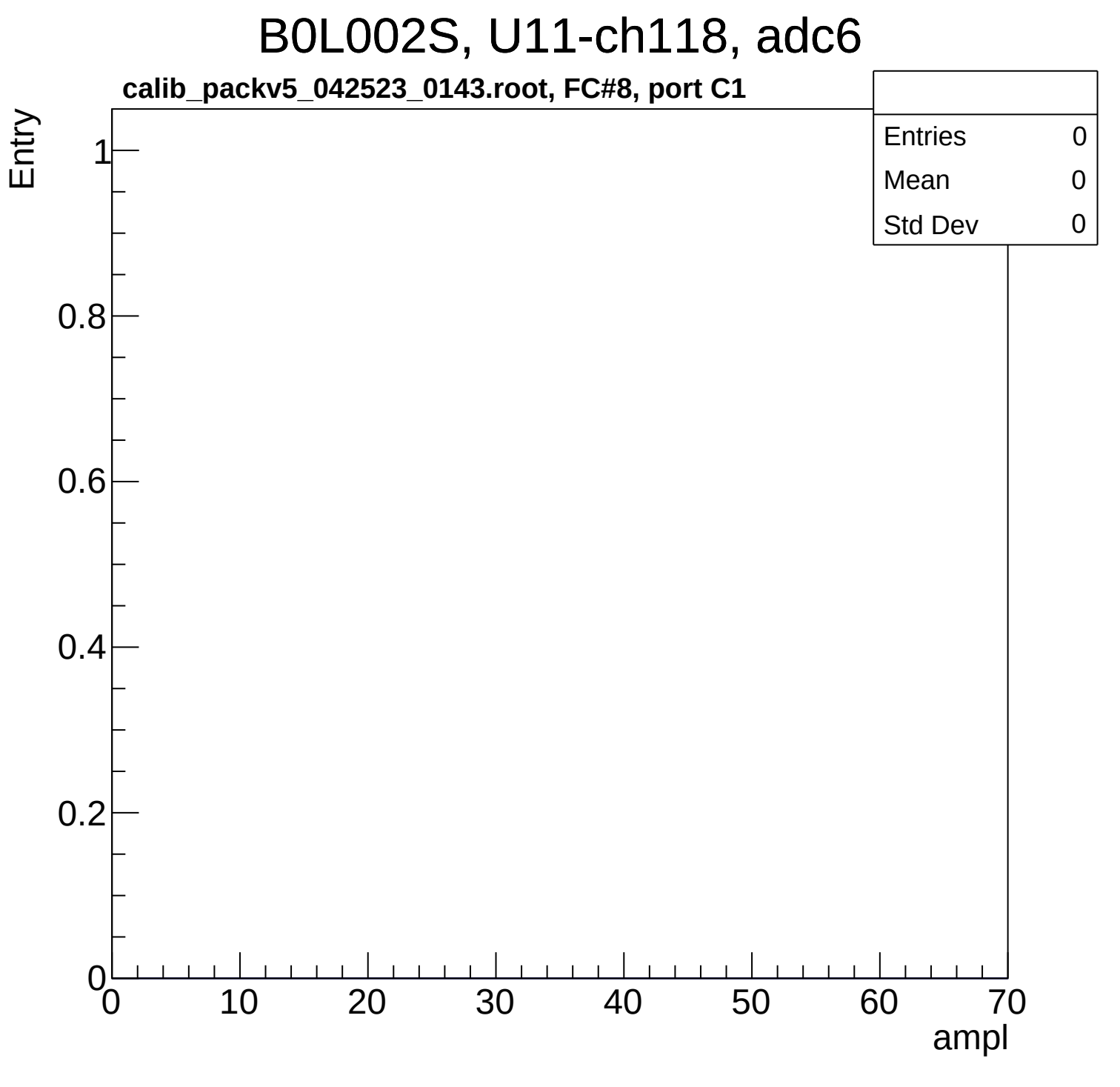
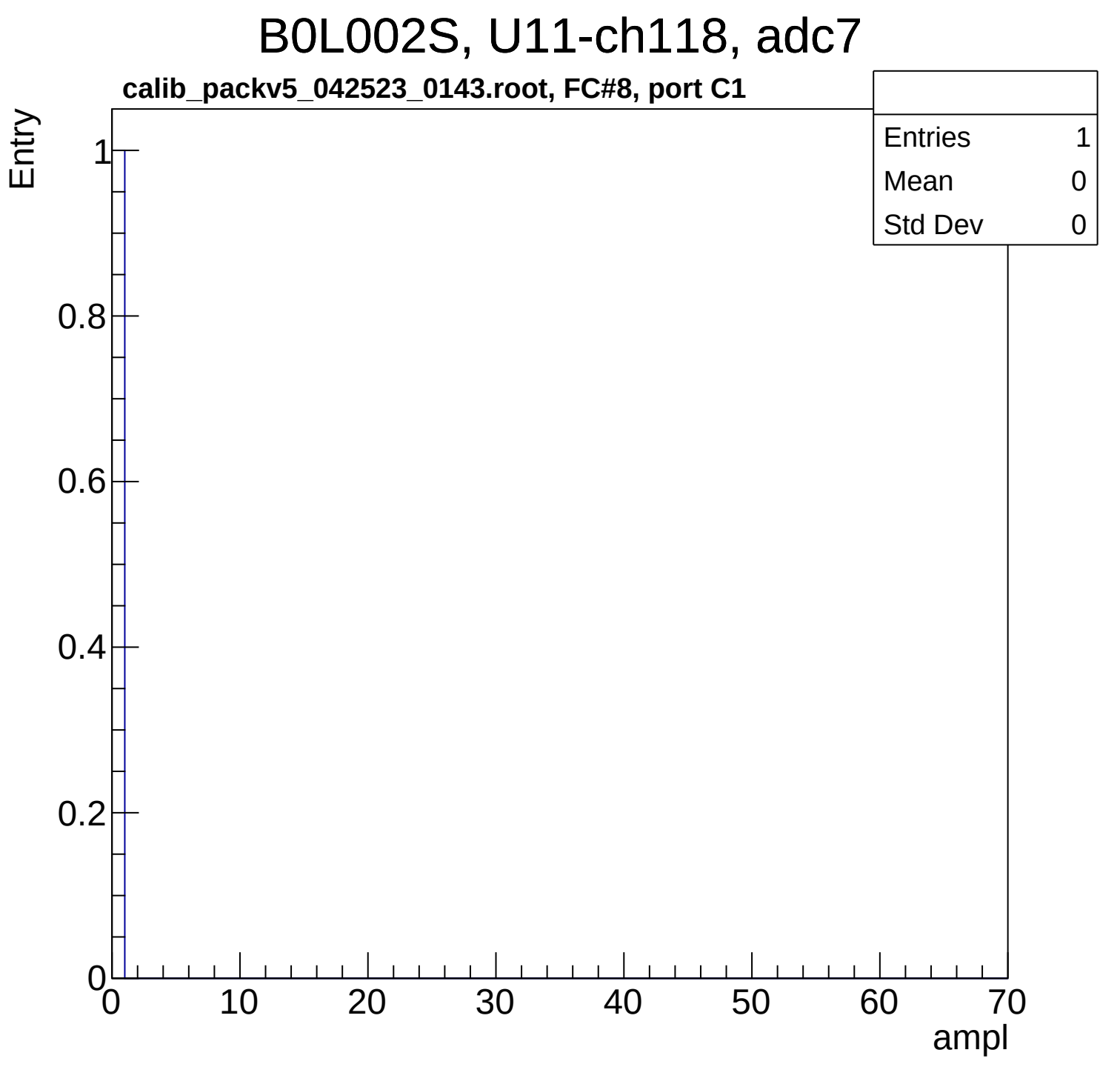
40
50
60
70


B0L002S, U11-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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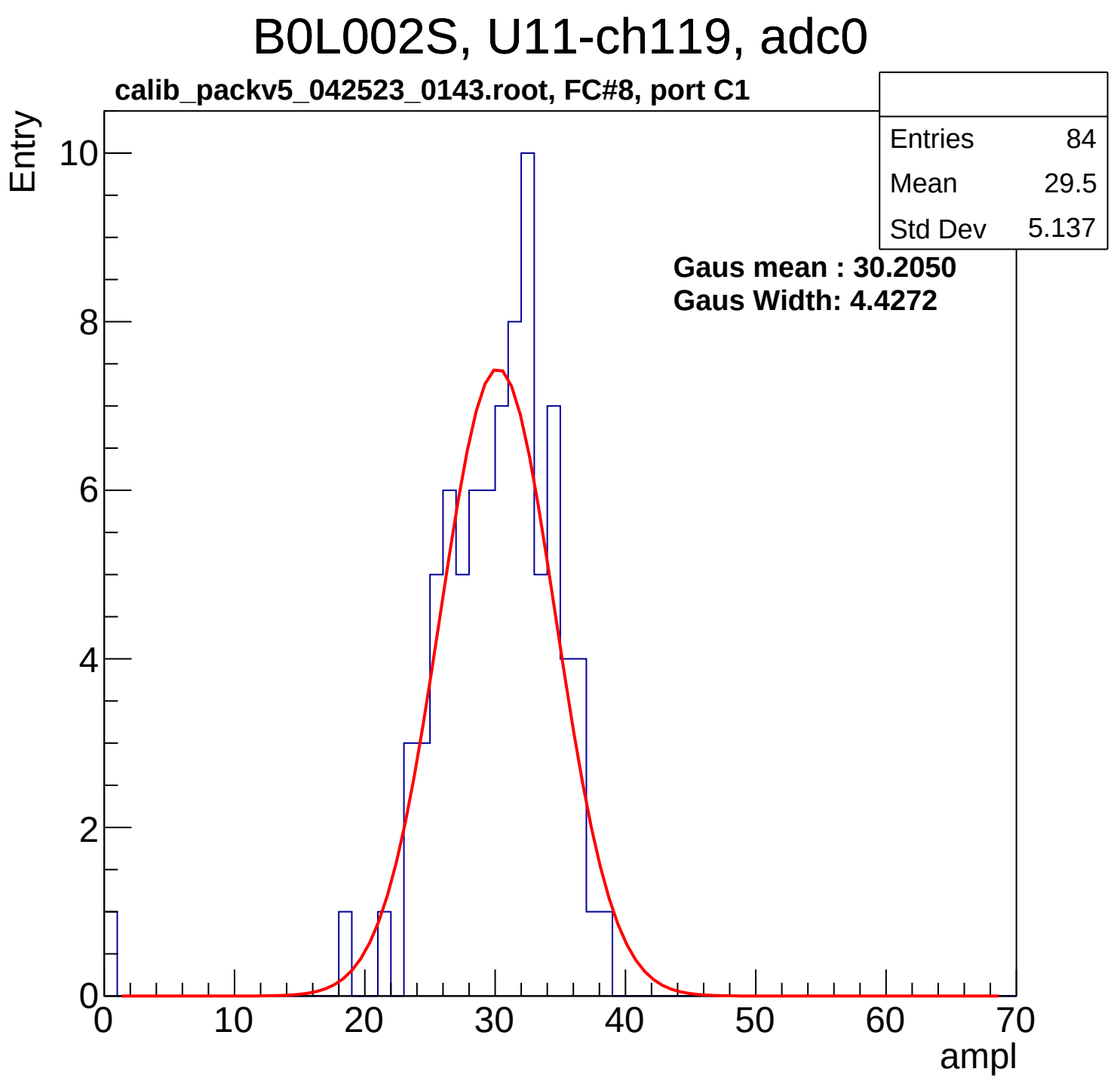
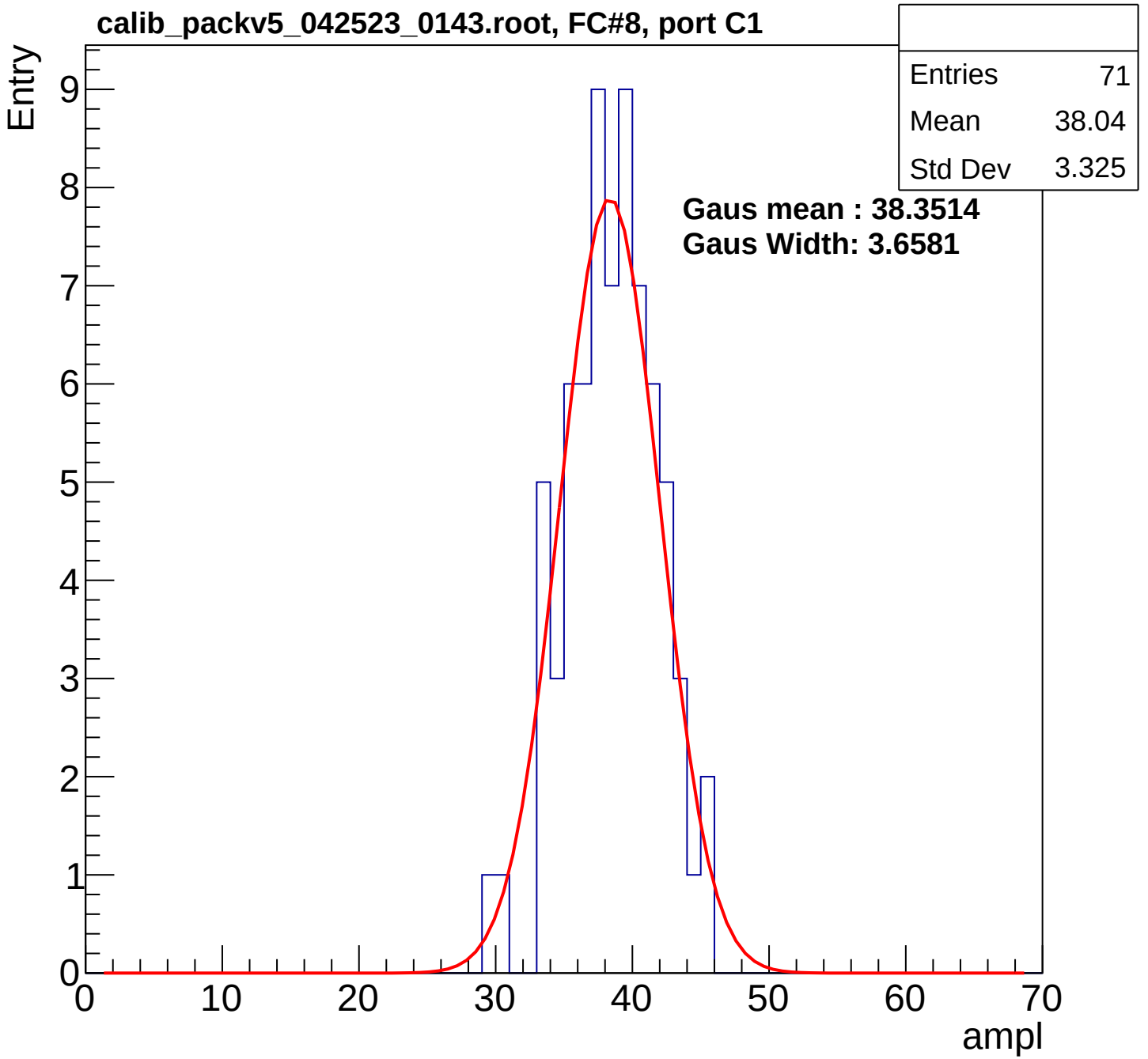
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.5
Std Dev 5.137
Gaus mean : 30.2050
Gaus Width: 4.4272
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.04
Std Dev 3.325
Gaus mean : 38.3514
Gaus Width: 3.6581
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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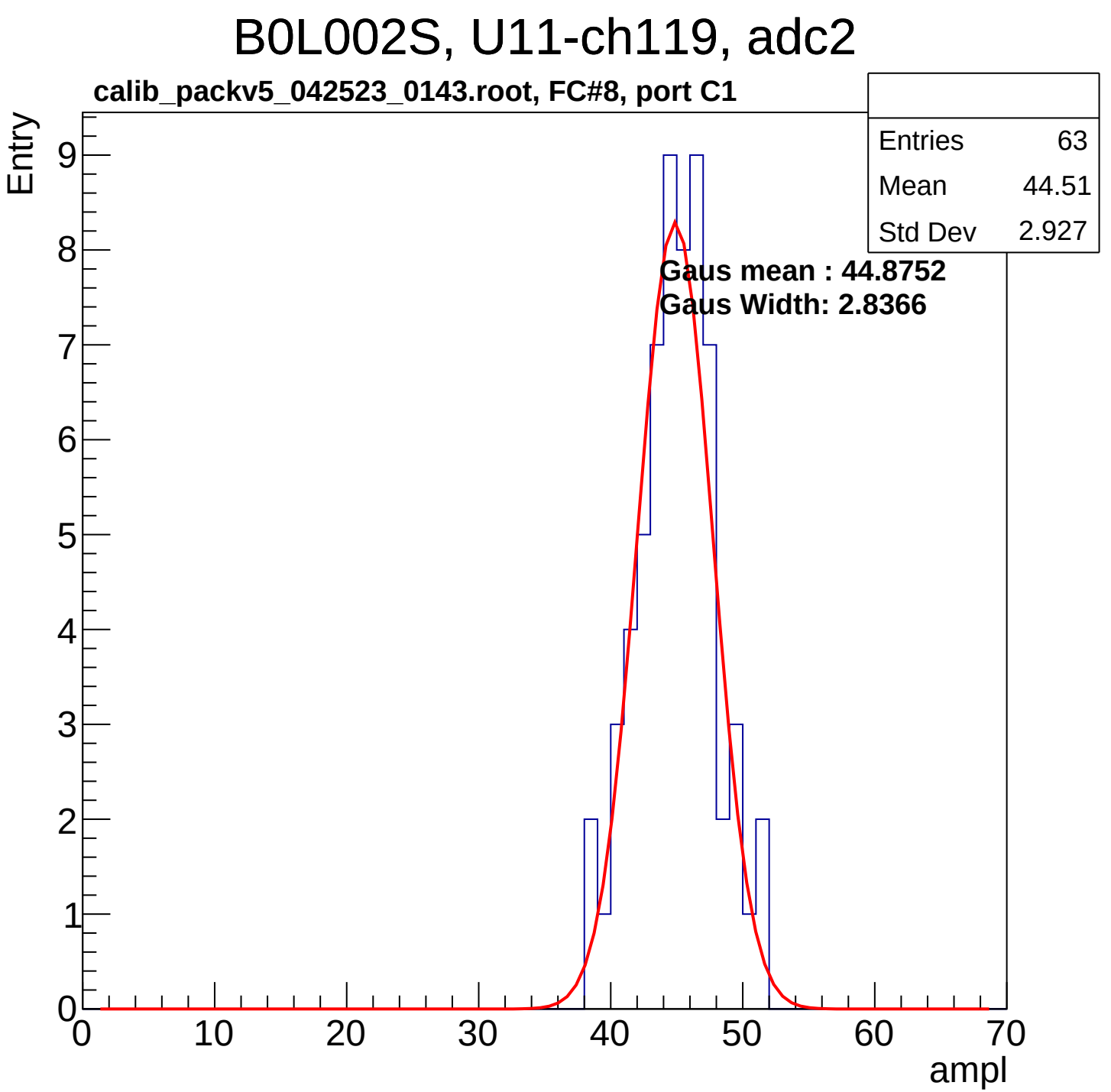
B0L002S, U11-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.51
Std Dev 2.927
Gaus mean : 44.8752
Gaus Width: 2.8366
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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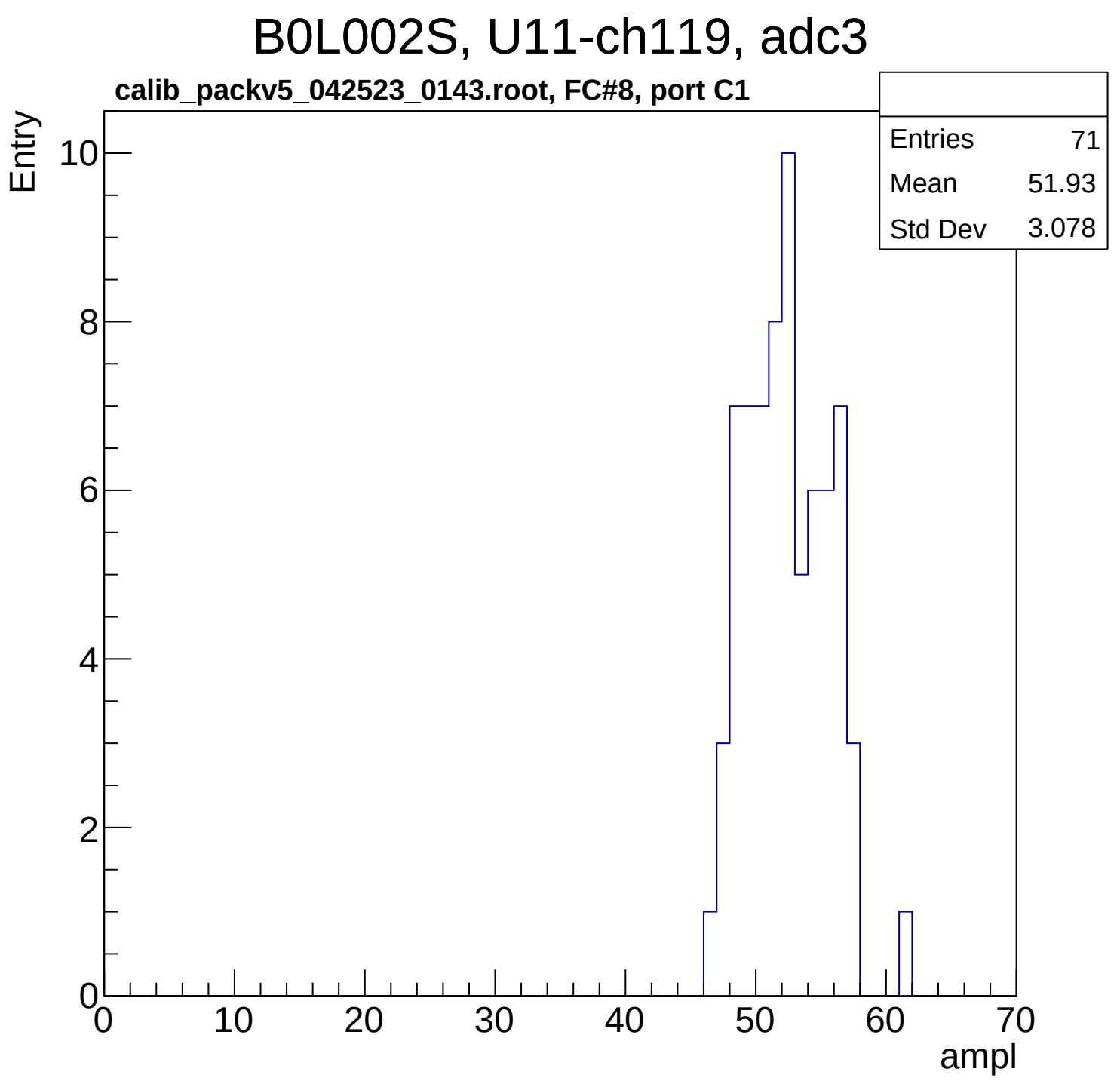
B0L002S, U11-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.93
Std Dev 3.078
Entry
ampl
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0
10
20
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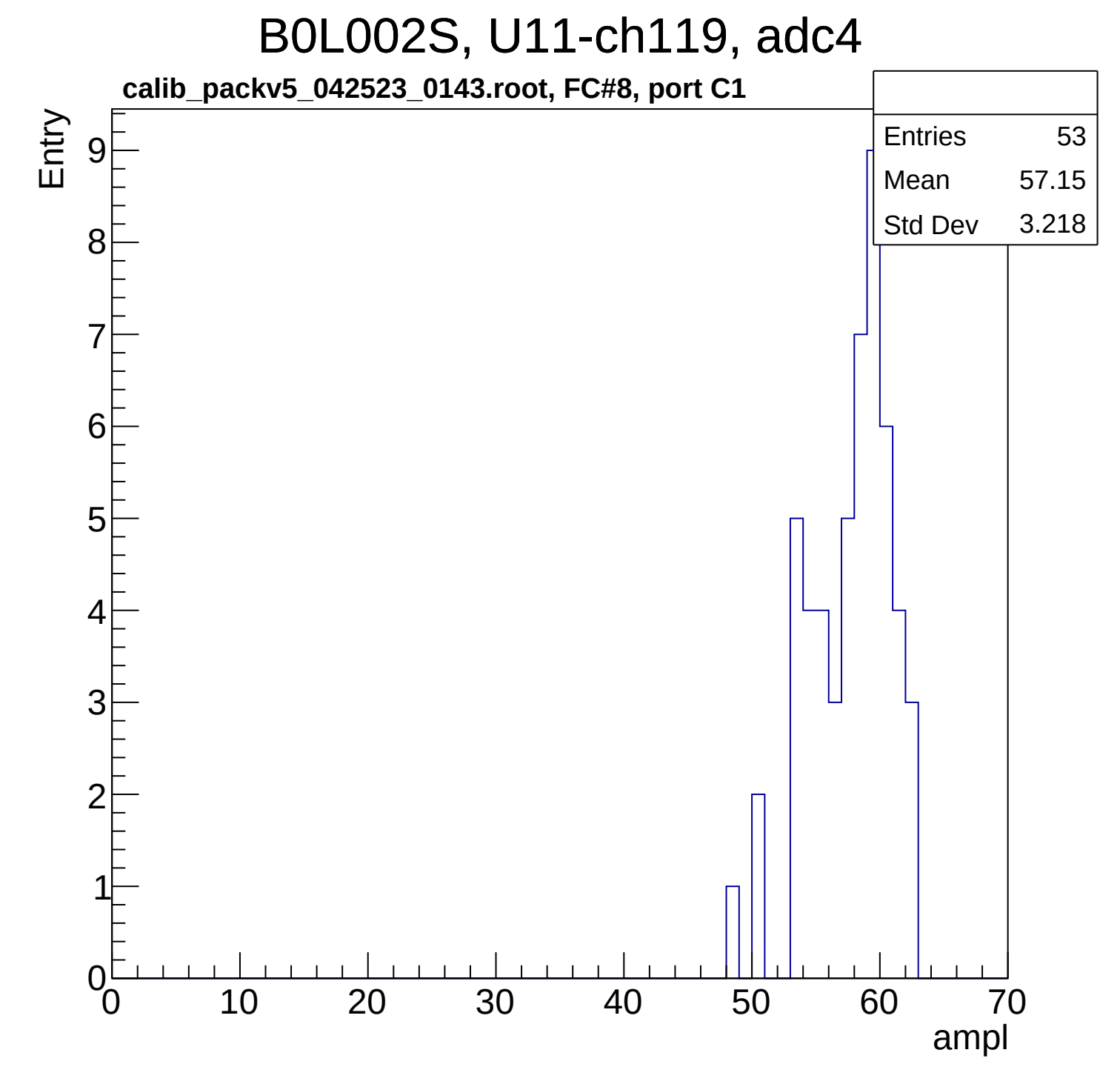
40
50
60
70


B0L002S, U11-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.15
Std Dev 3.218
Entry
ampl
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2
3
4
5
6
7
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9
0
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20
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40
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60
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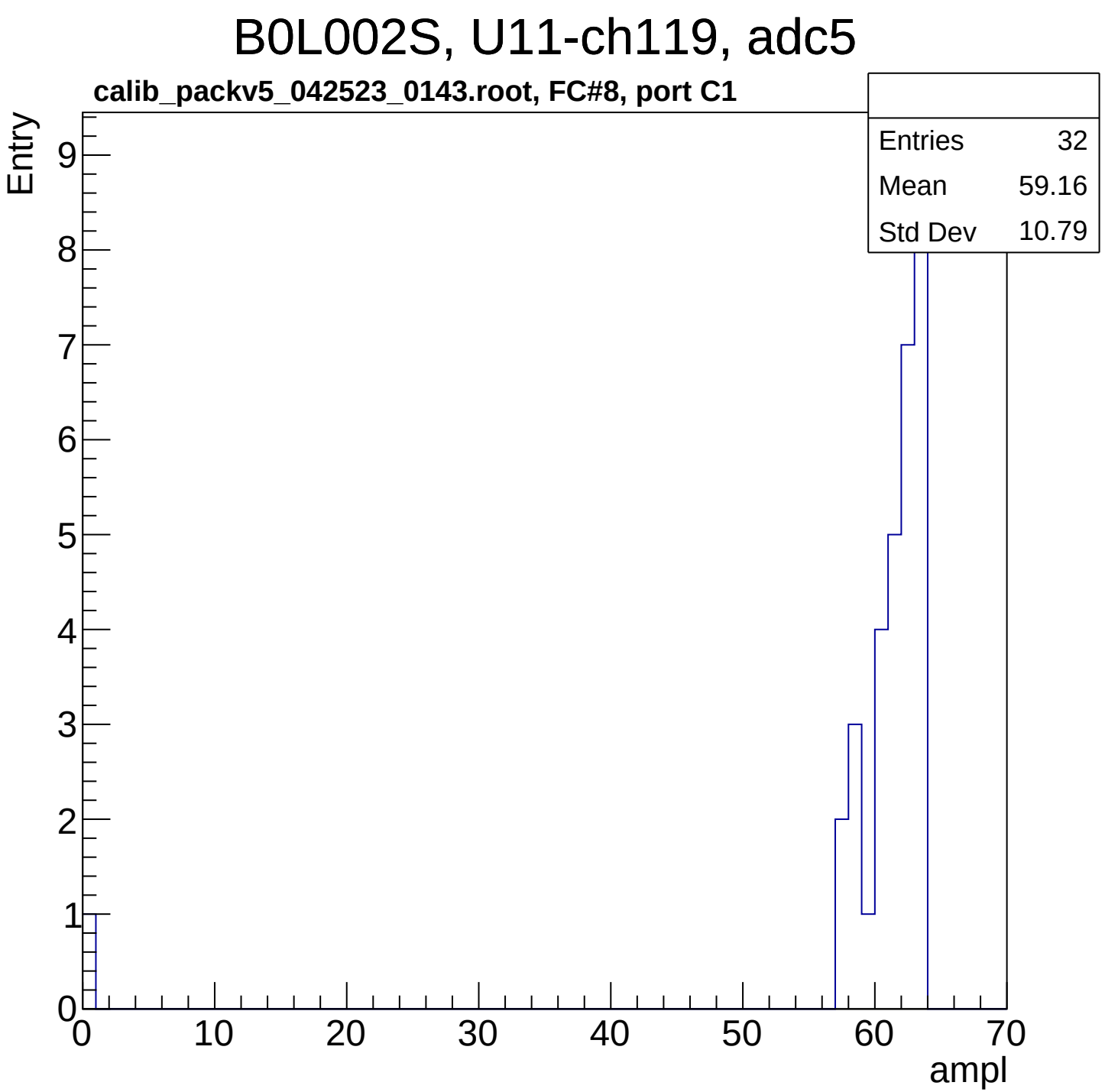
B0L002S, U11-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.16
Std Dev 10.79
Entry
ampl
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2
3
4
5
6
7
8
9
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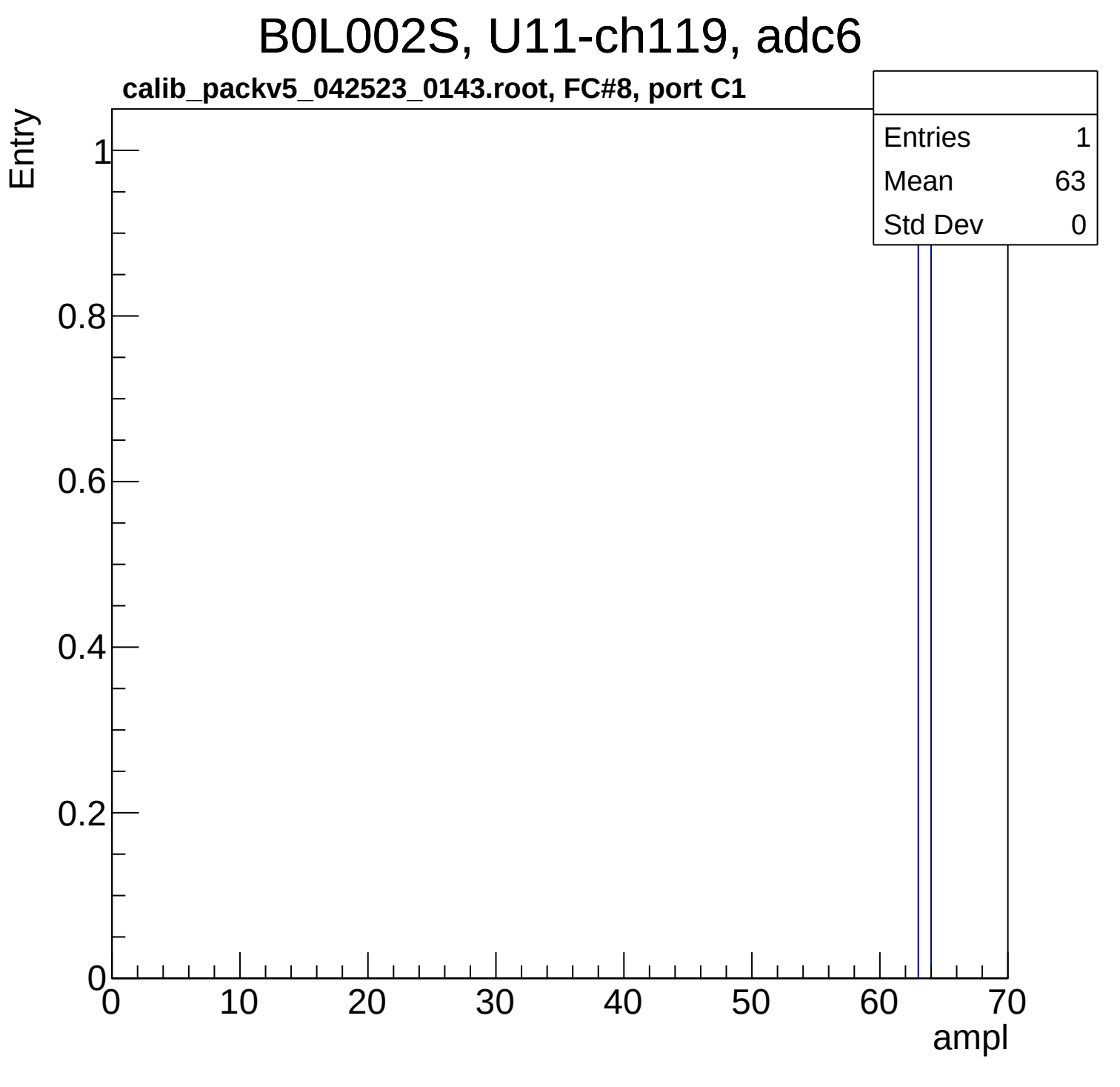
B0L002S, U11-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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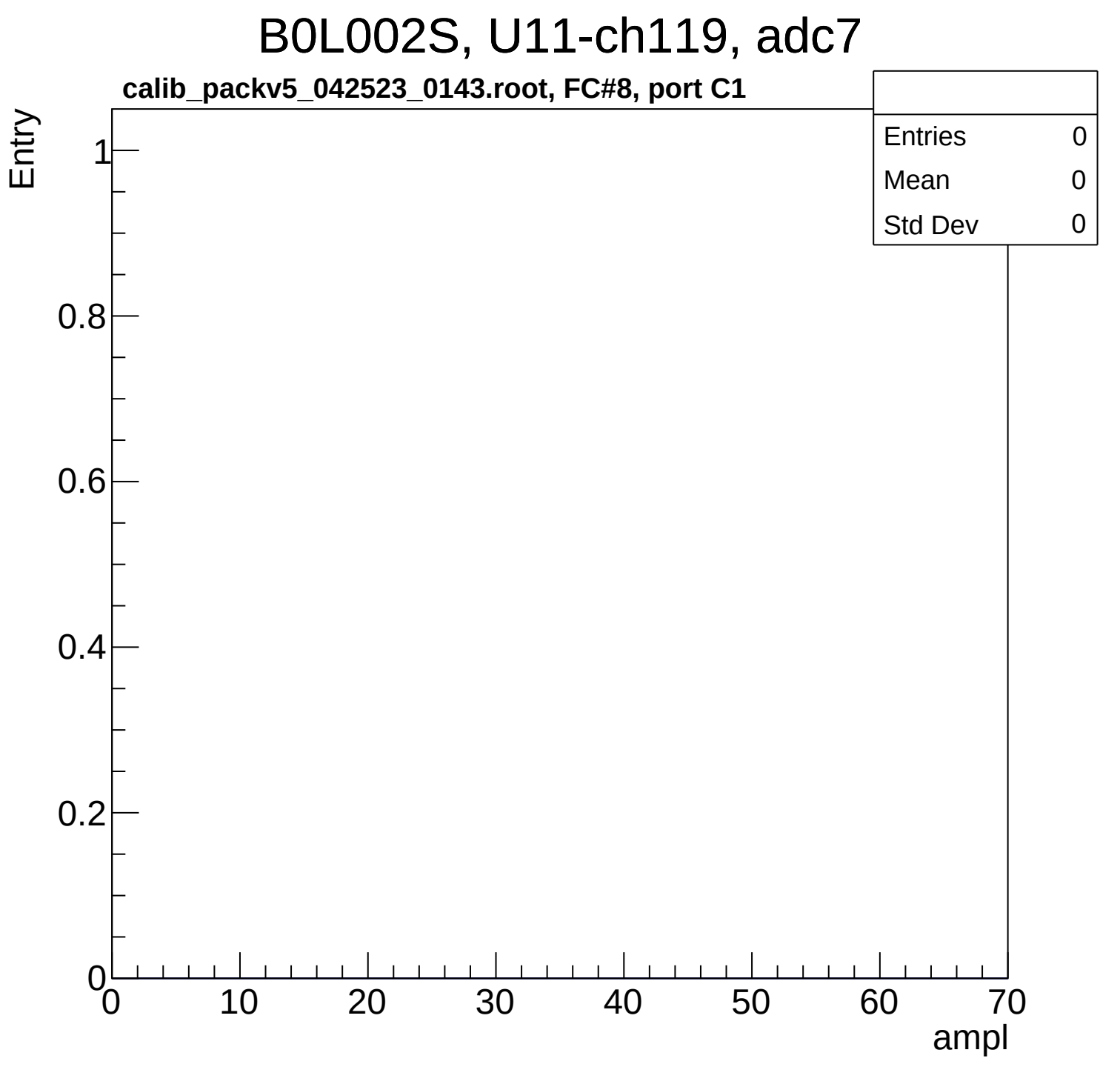
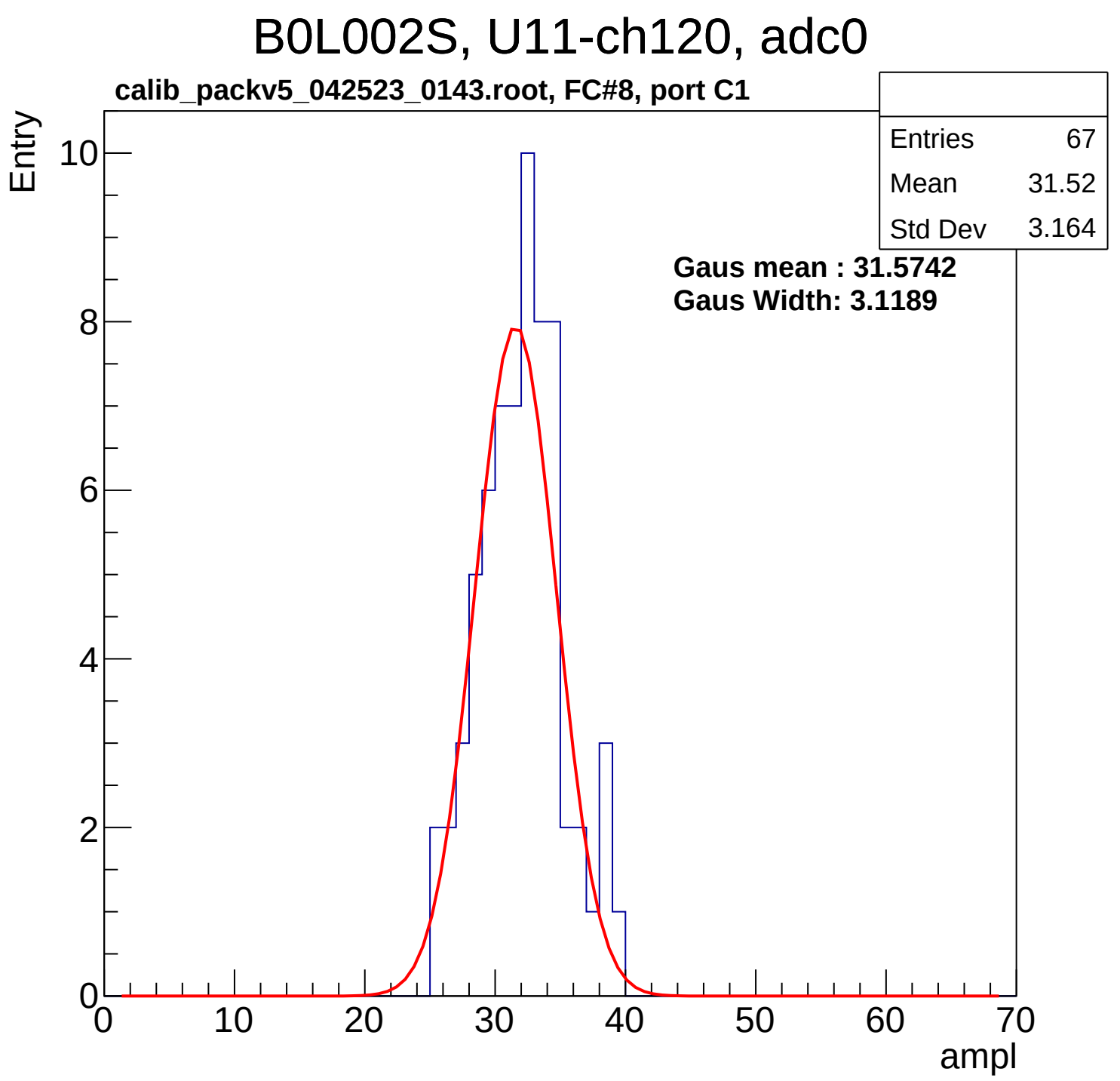
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.52
Std Dev 3.164
Gaus mean : 31.5742
Gaus Width: 3.1189
Entry
ampl
0
2
4
6
8
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0
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40
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60
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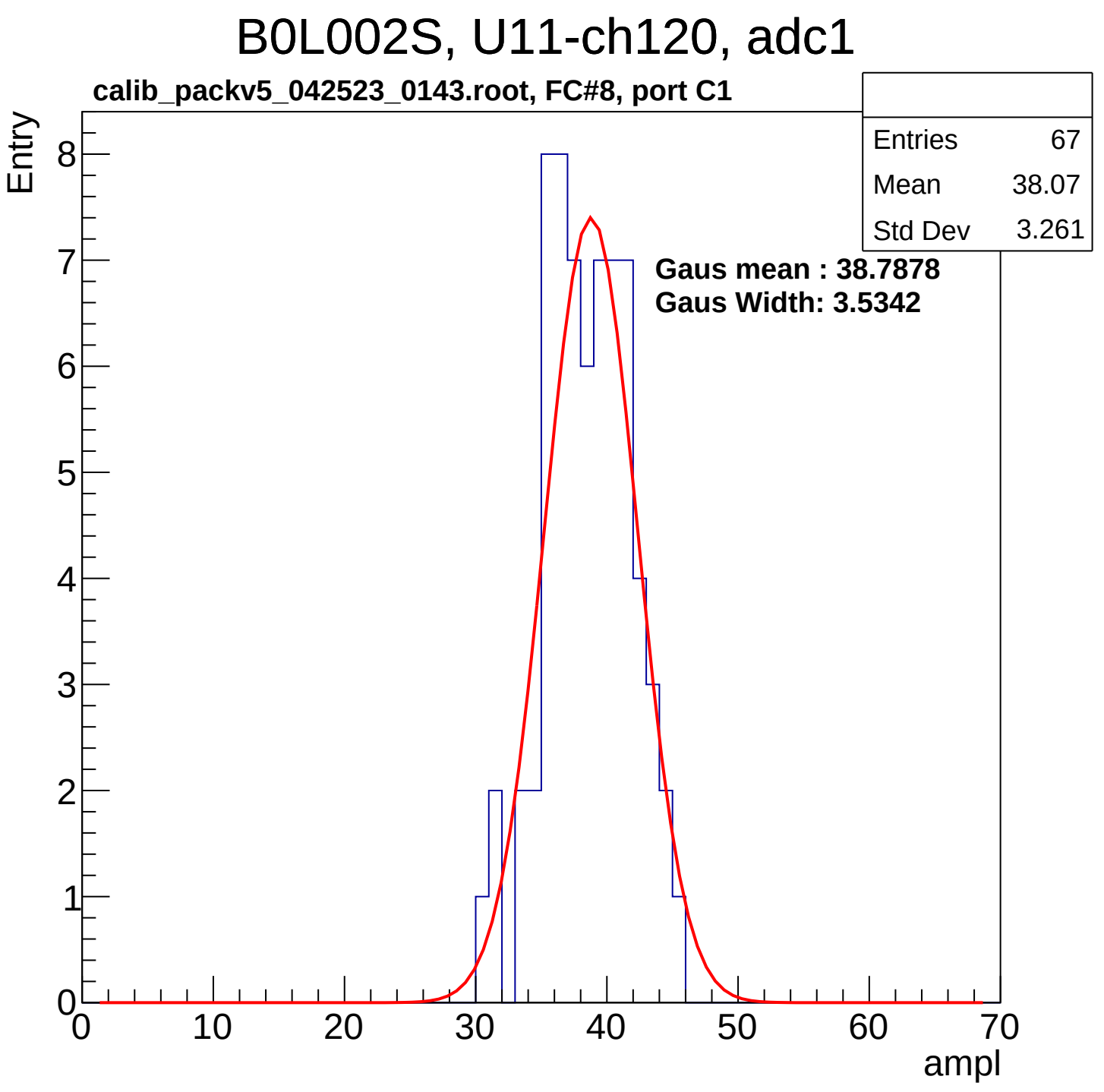
B0L002S, U11-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.07
Std Dev 3.261
Gaus mean : 38.7878
Gaus Width: 3.5342
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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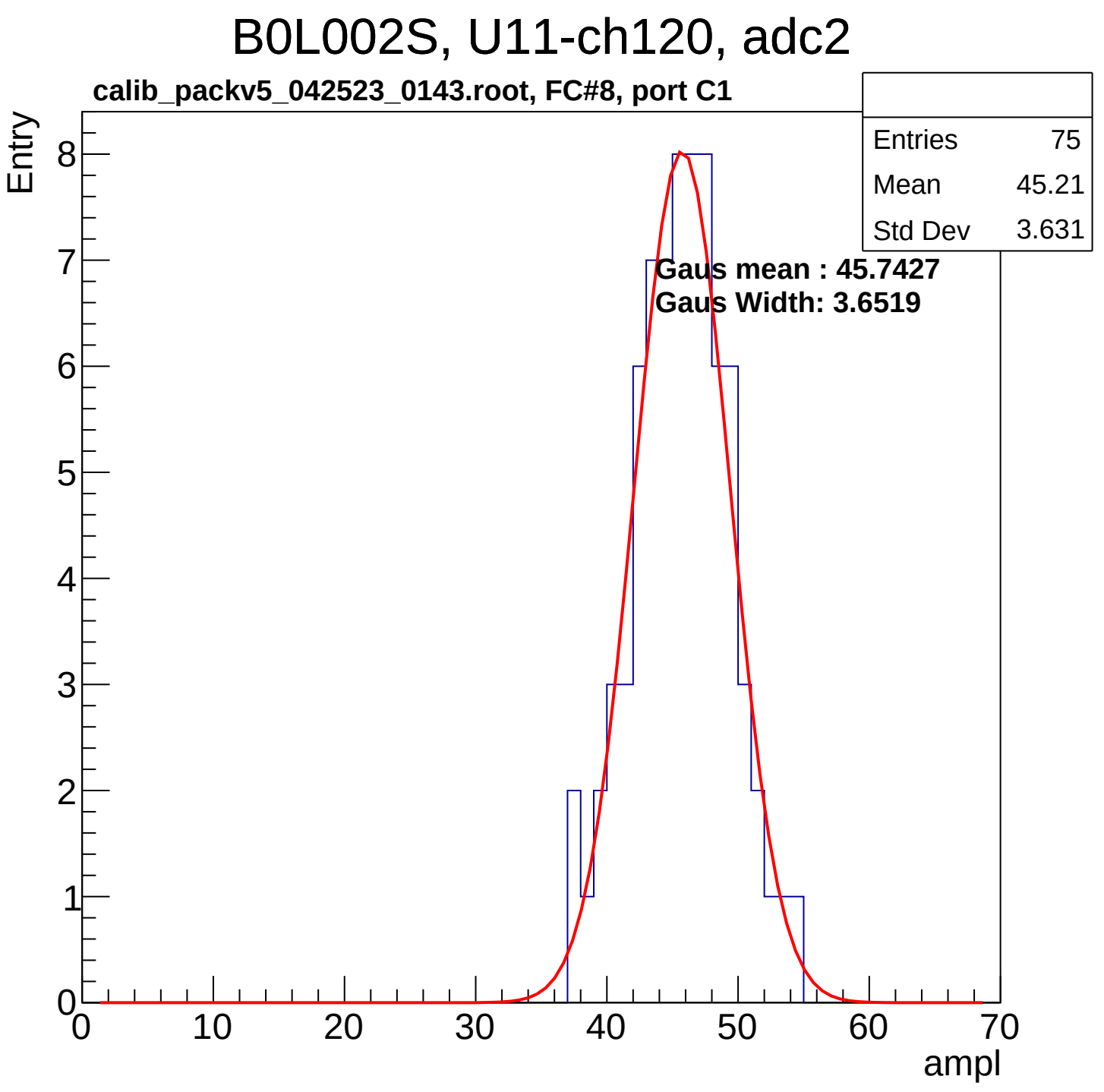
B0L002S, U11-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.21
Std Dev 3.631
Gaus mean : 45.7427
Gaus Width: 3.6519
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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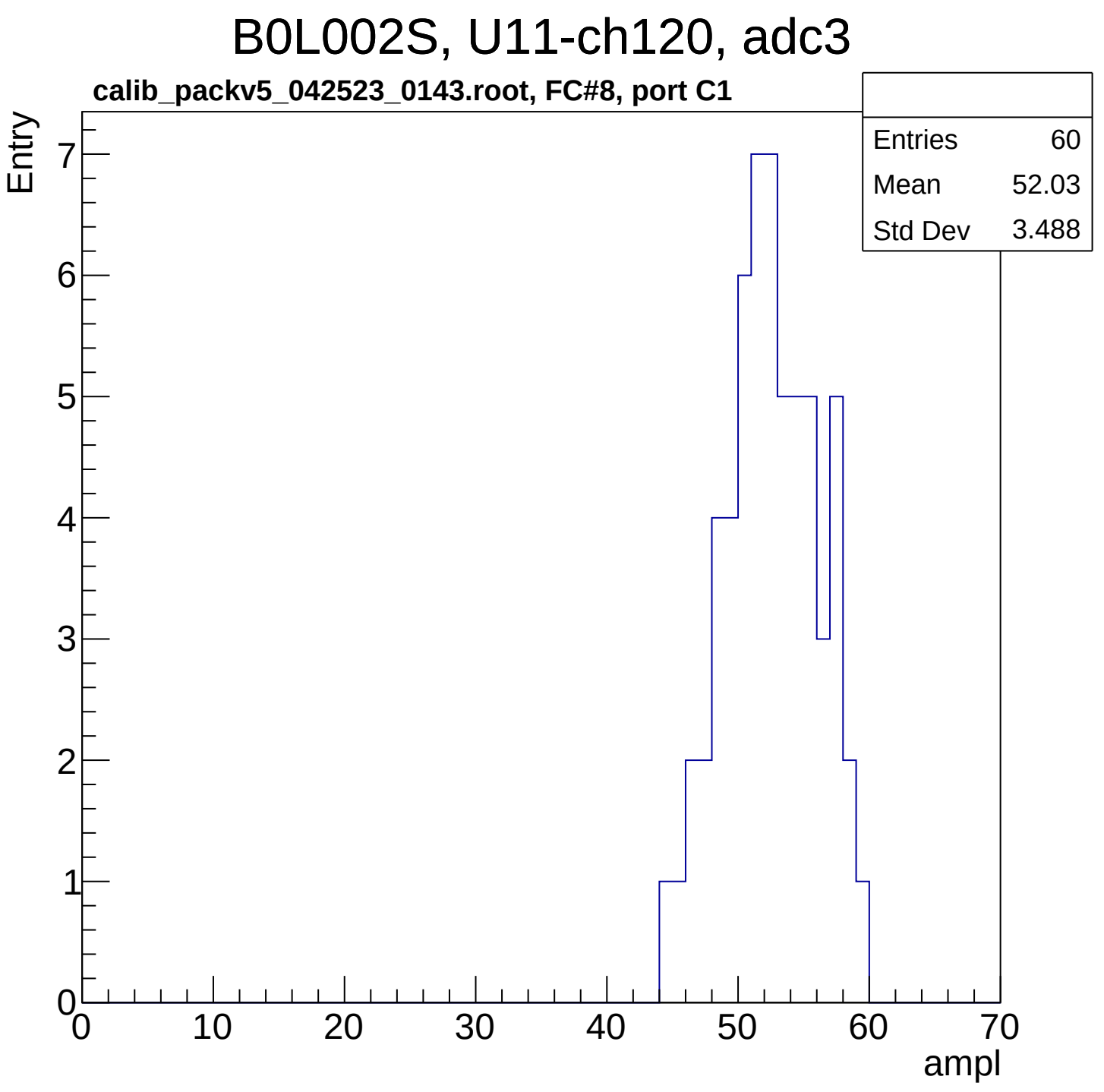
B0L002S, U11-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.03
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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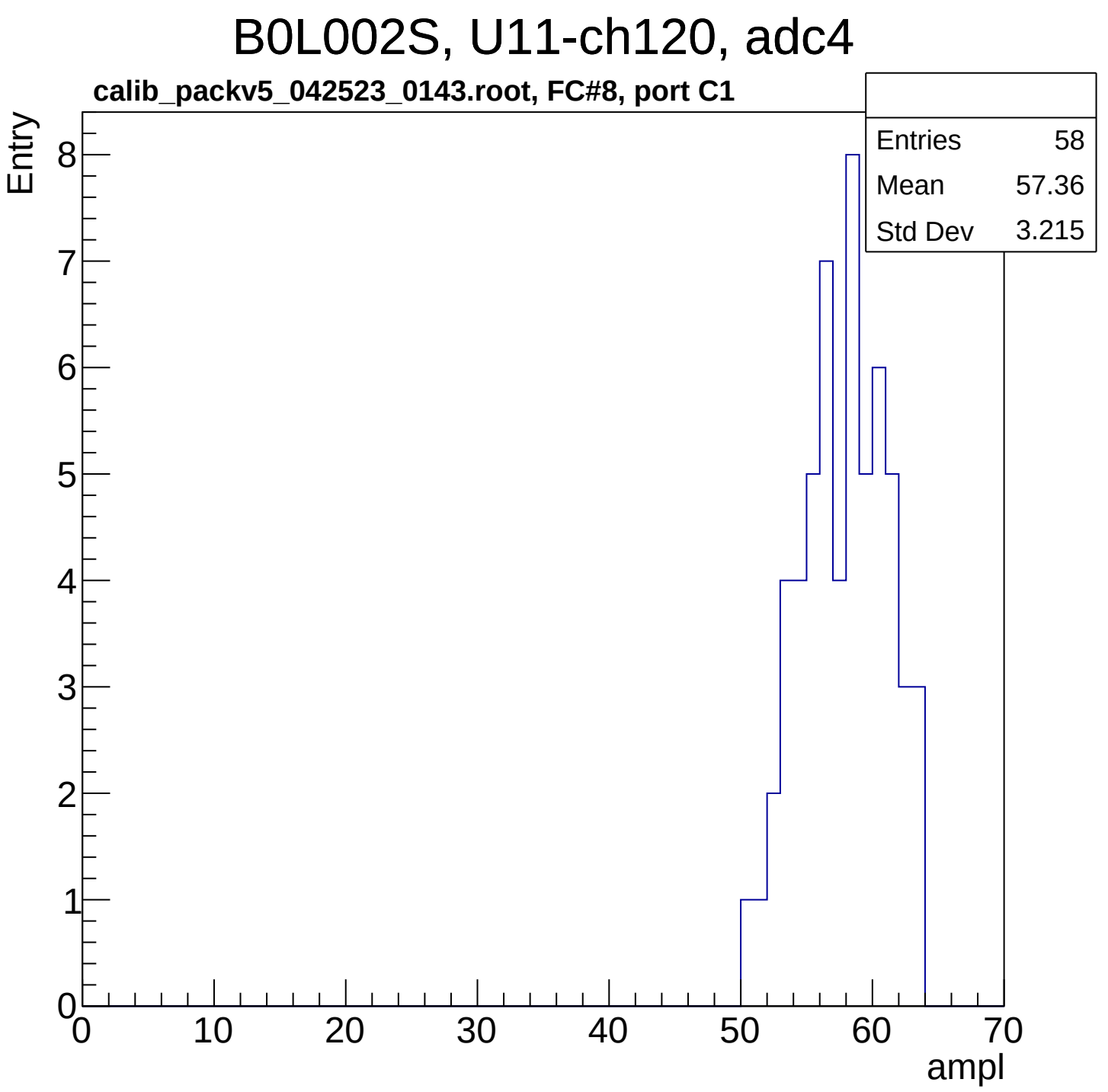
B0L002S, U11-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.36
Std Dev 3.215
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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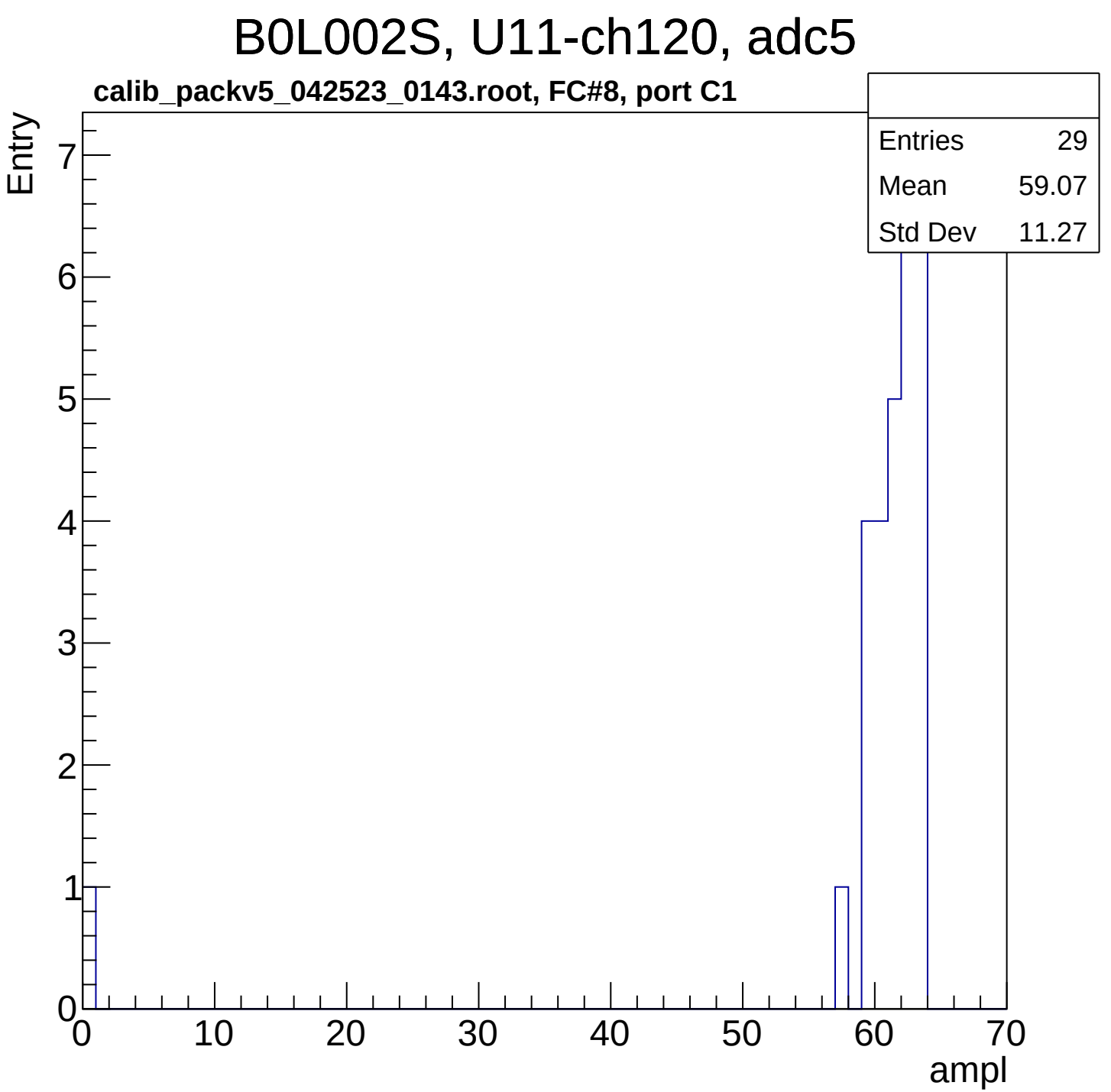
B0L002S, U11-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.07
Std Dev 11.27
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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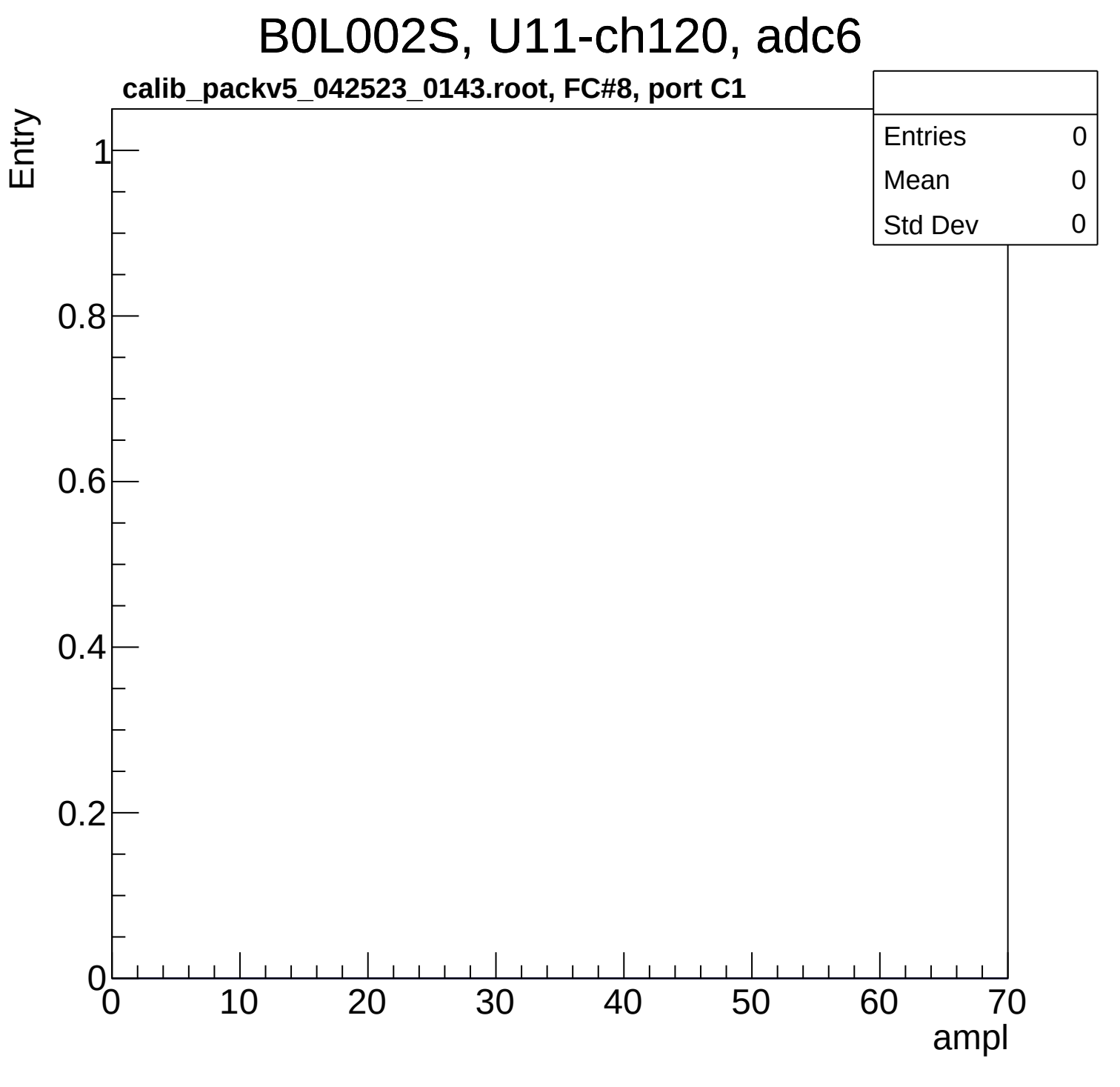
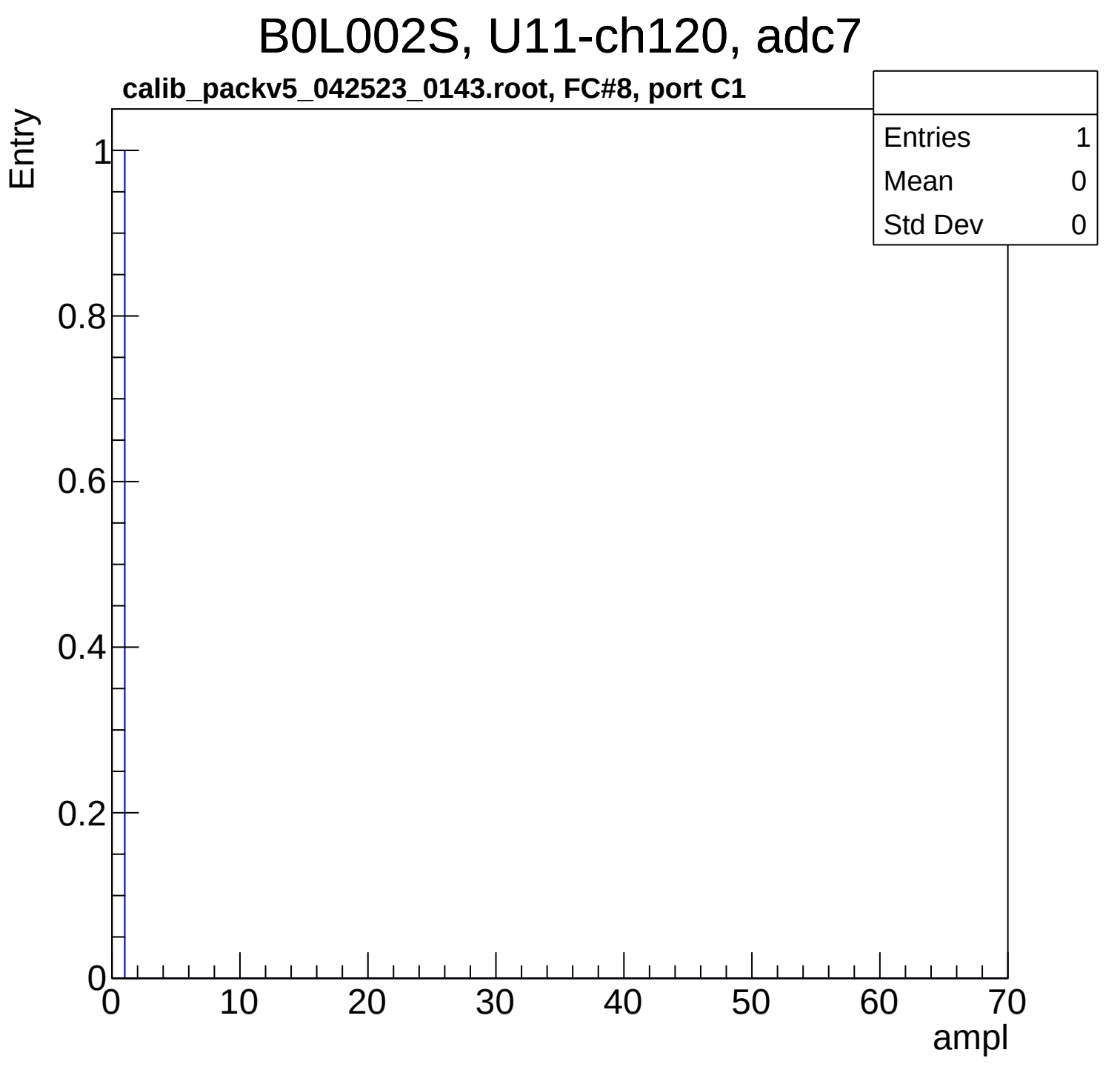
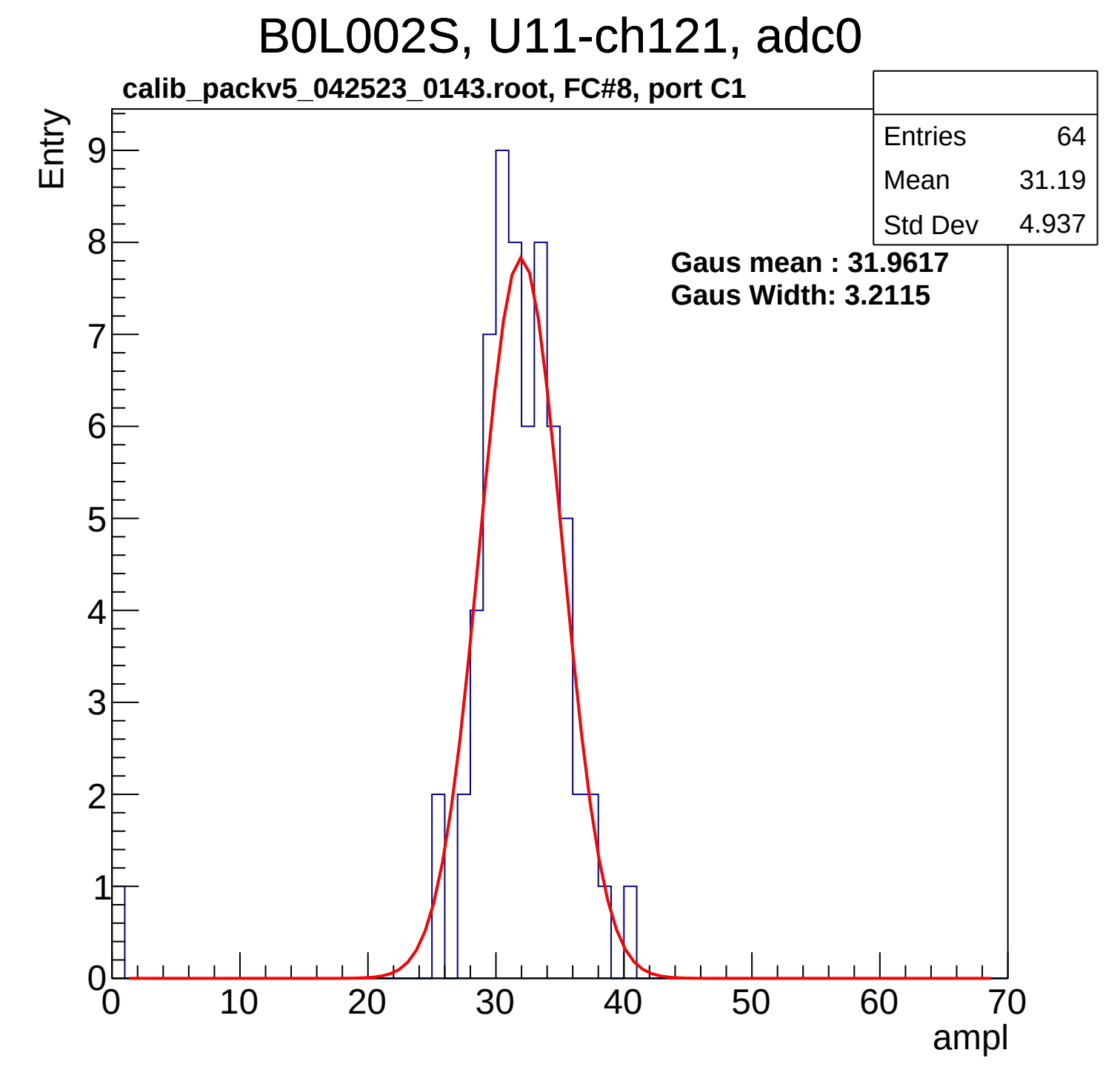
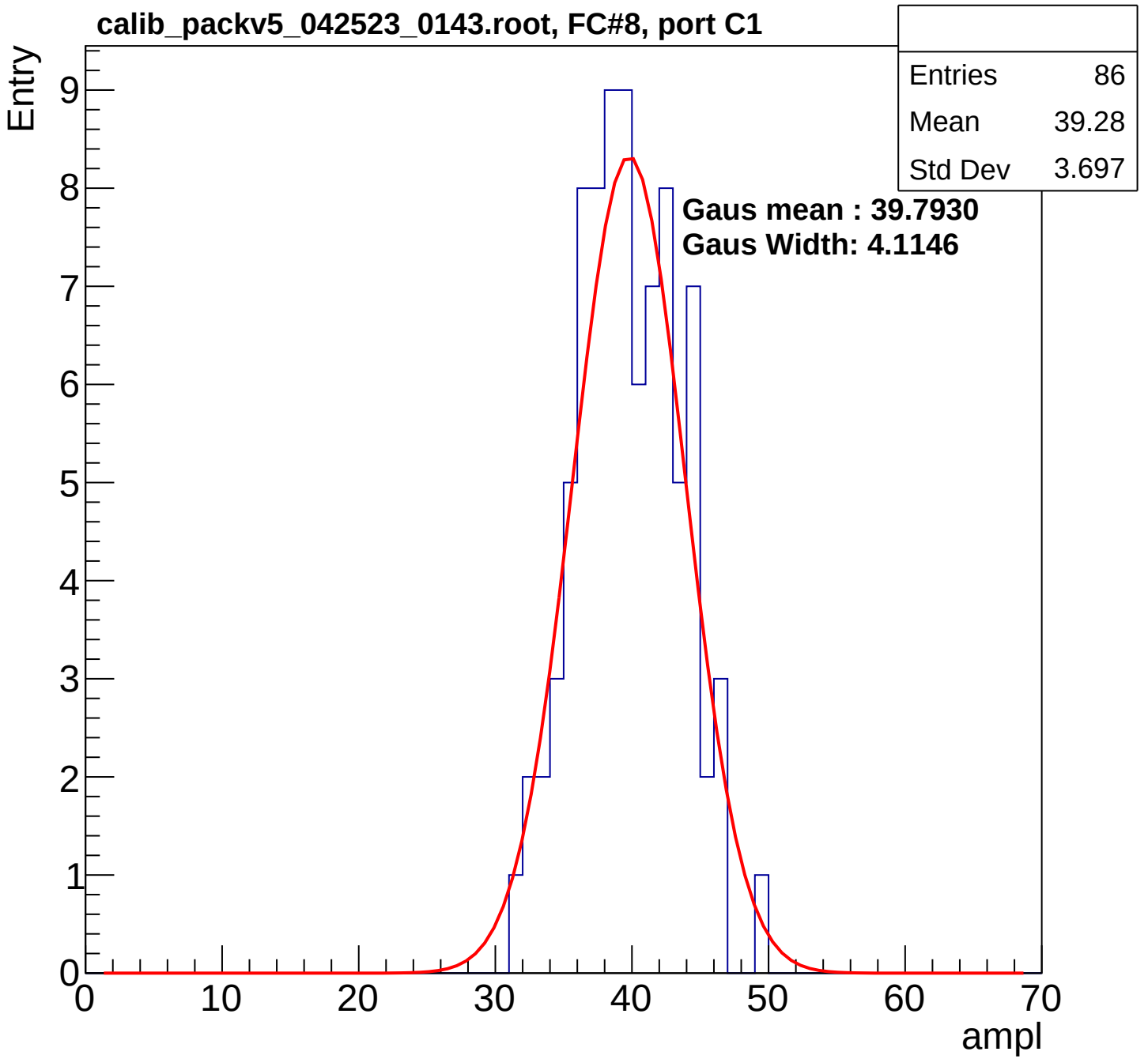
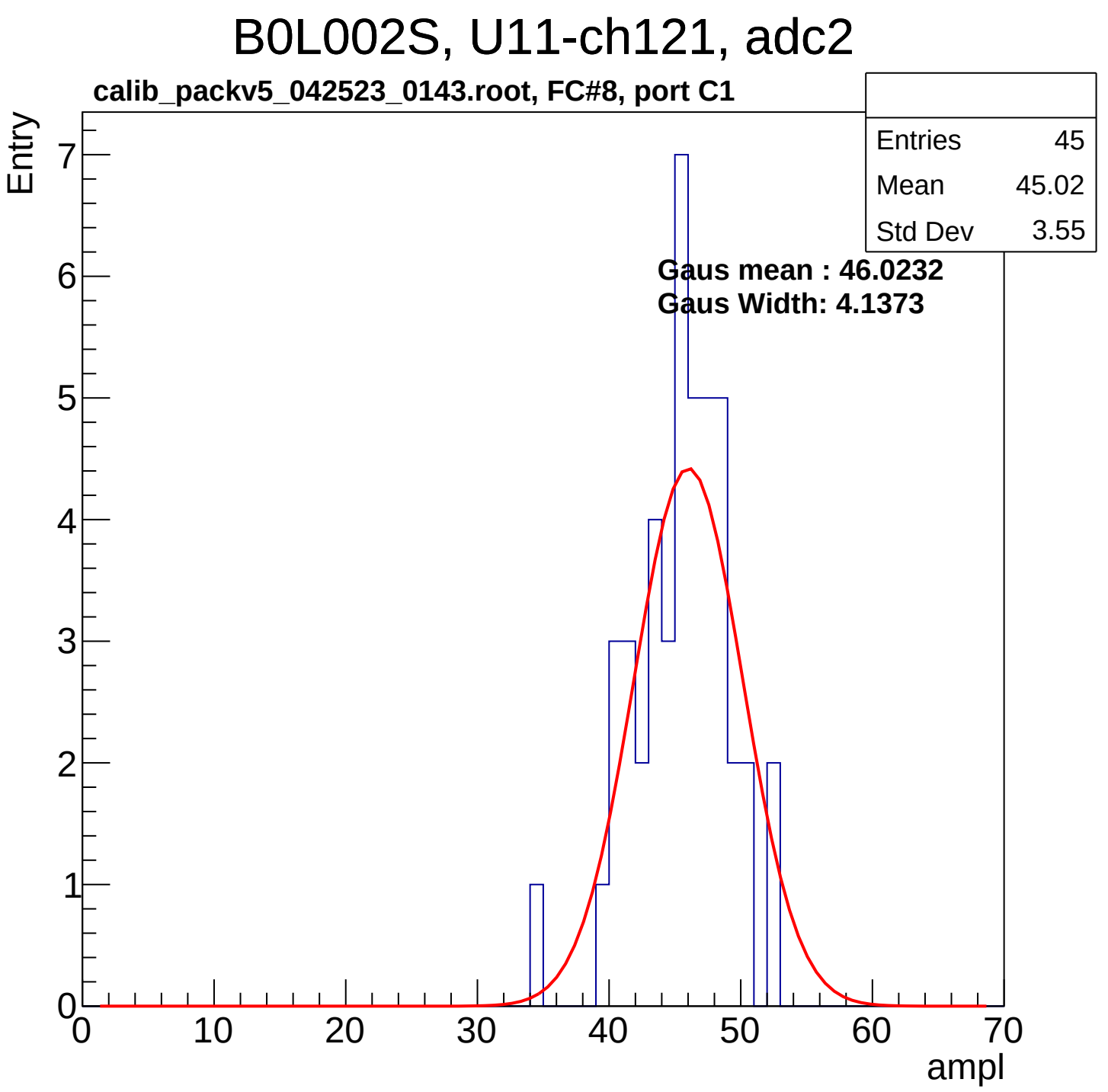

B0L002S, U11-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.19
Std Dev 4.937
Gaus mean : 31.9617
Gaus Width: 3.2115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 39.28
Std Dev 3.697
Gaus mean : 39.7930
Gaus Width: 4.1146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 45.02
Std Dev 3.55
Gaus mean : 46.0232
Gaus Width: 4.1373
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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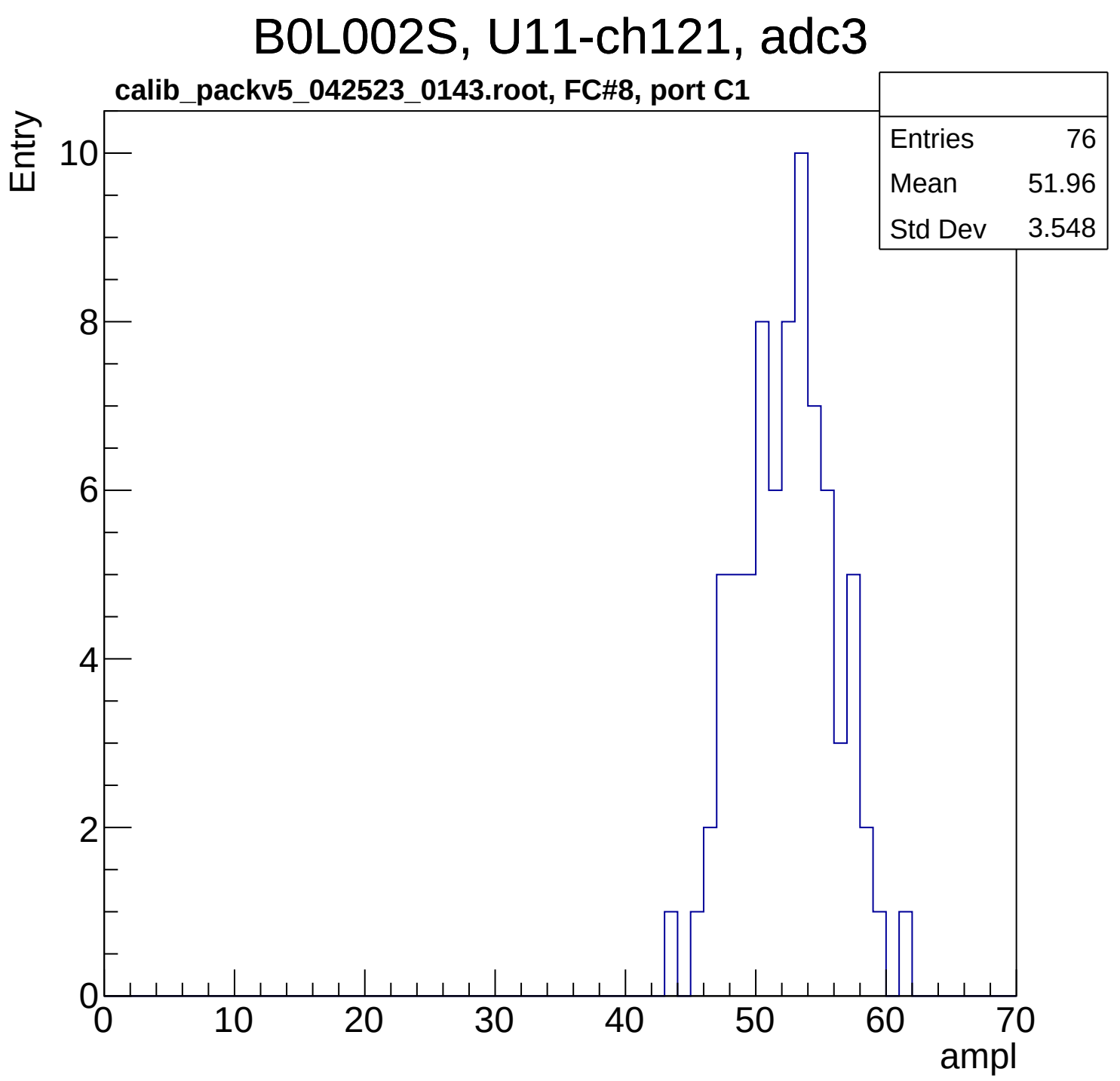
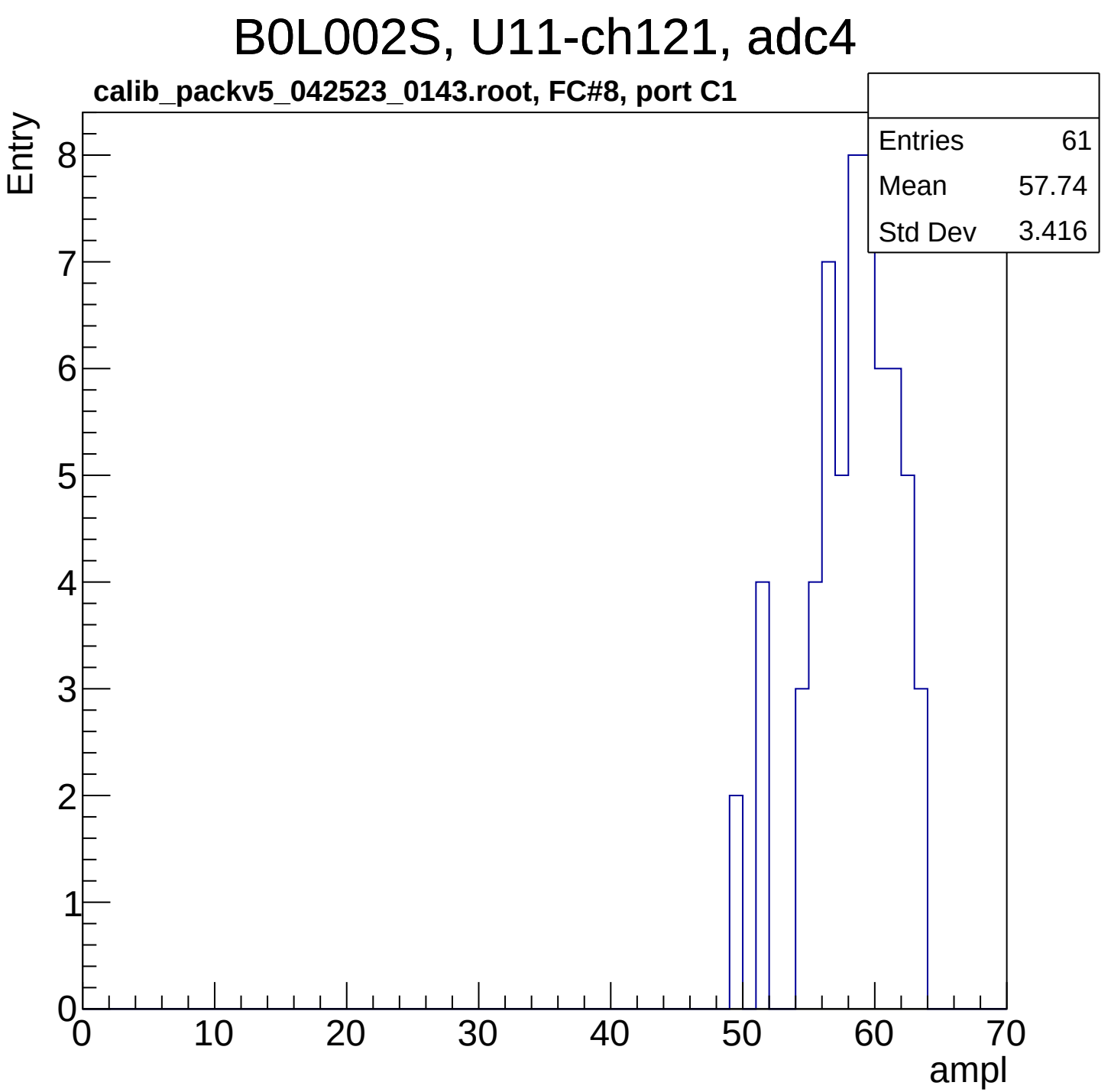
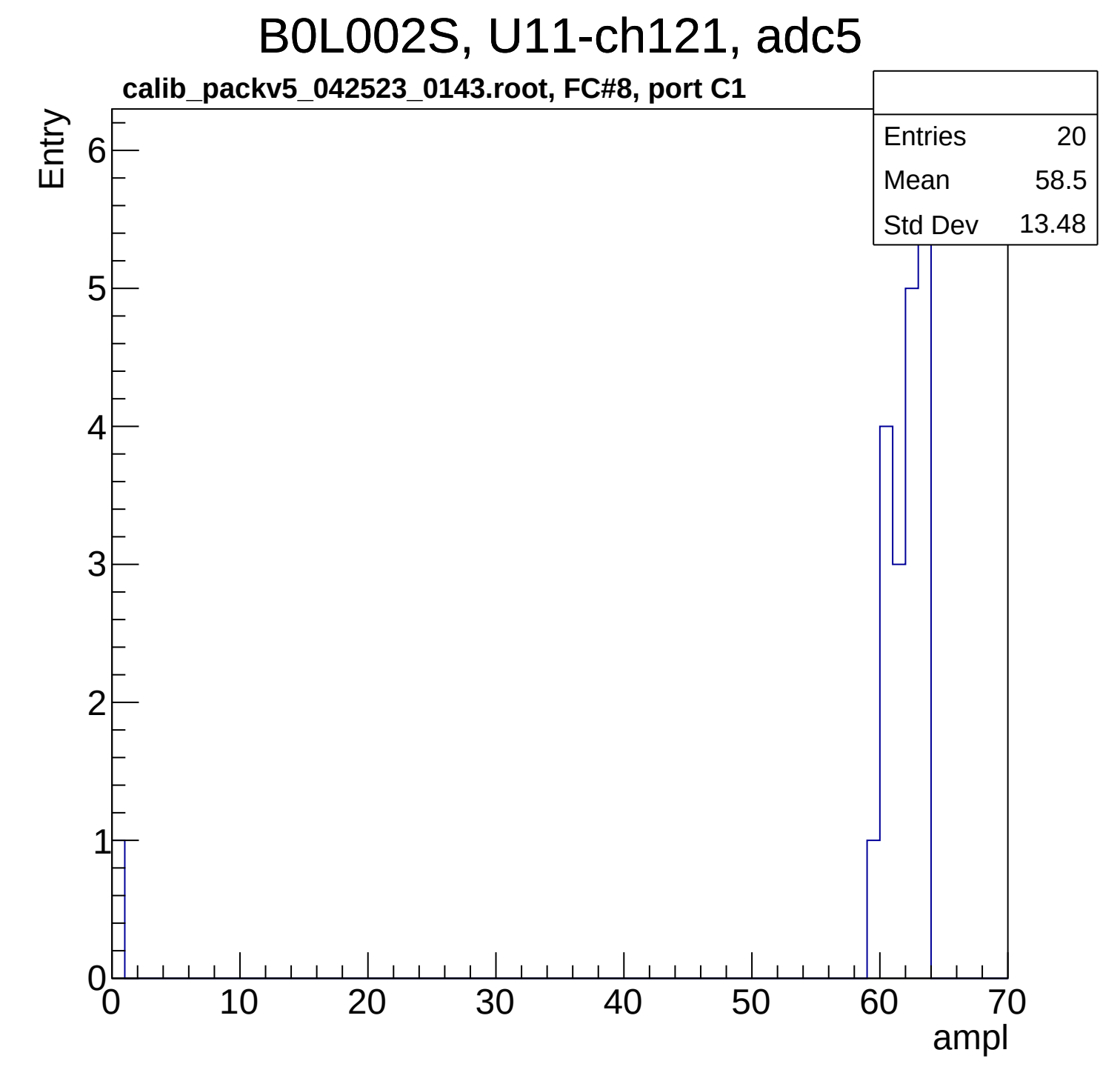
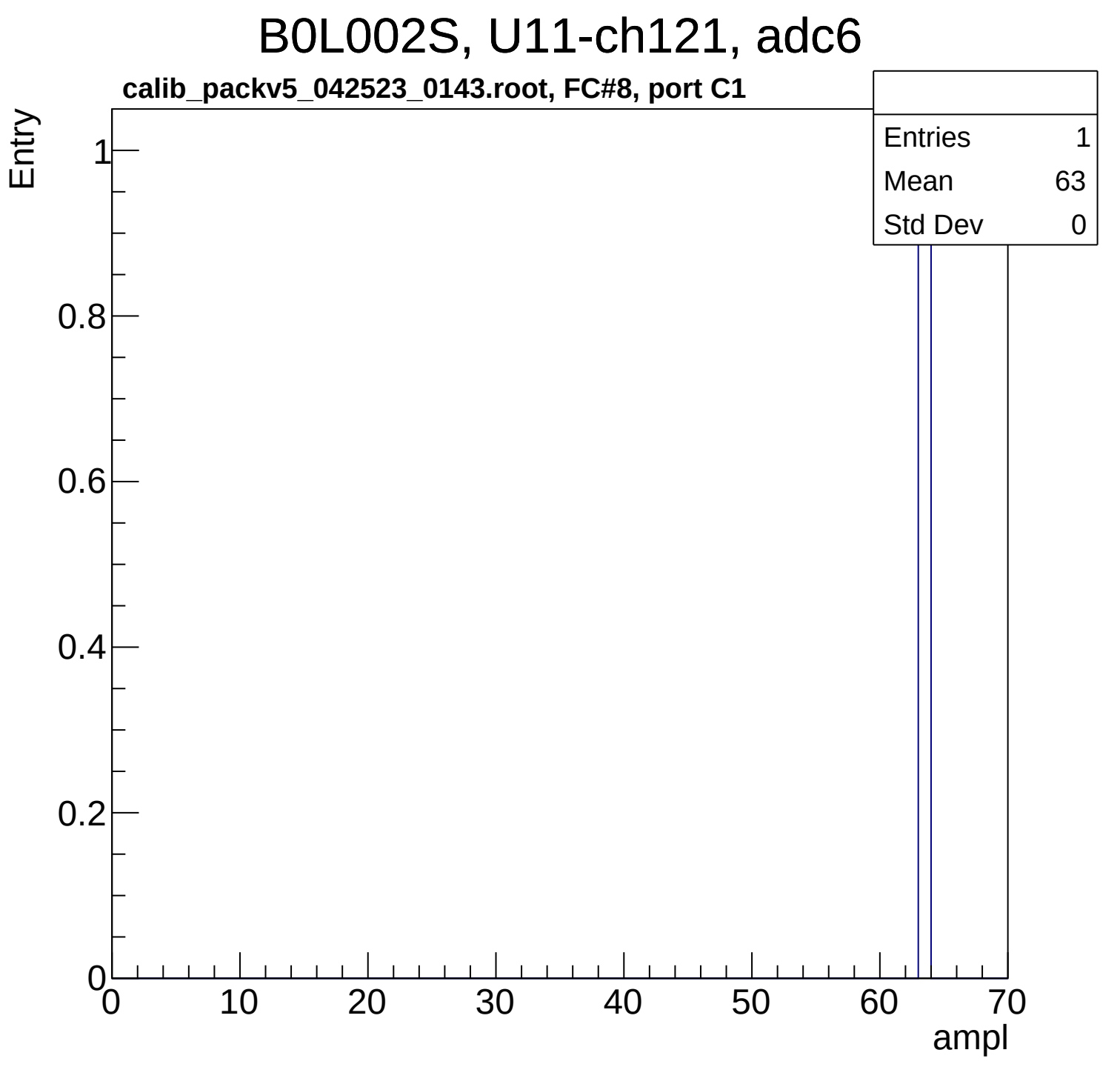
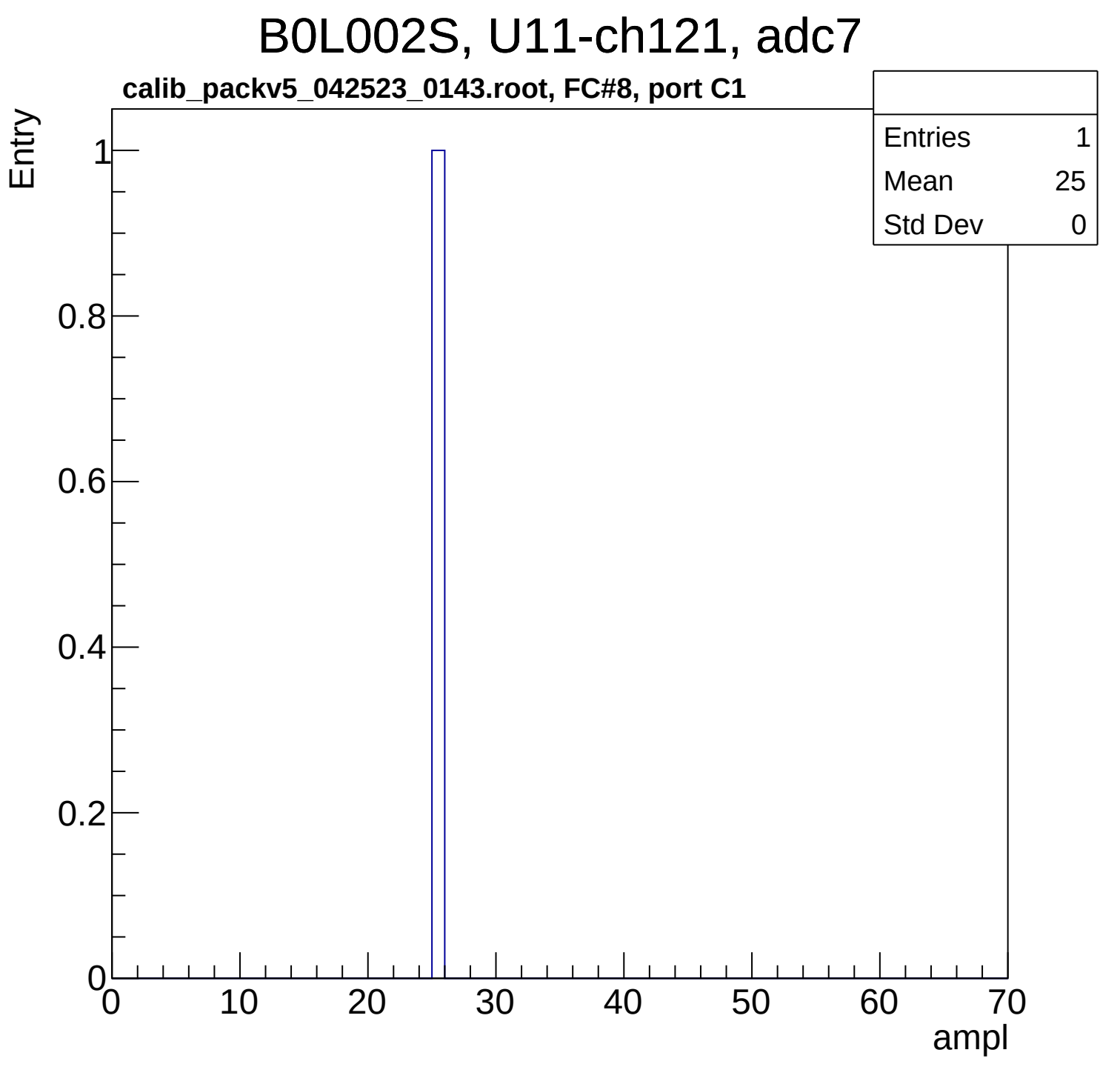

B0L002S, U11-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.96
Std Dev 3.548
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.74
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 58.5
Std Dev 13.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
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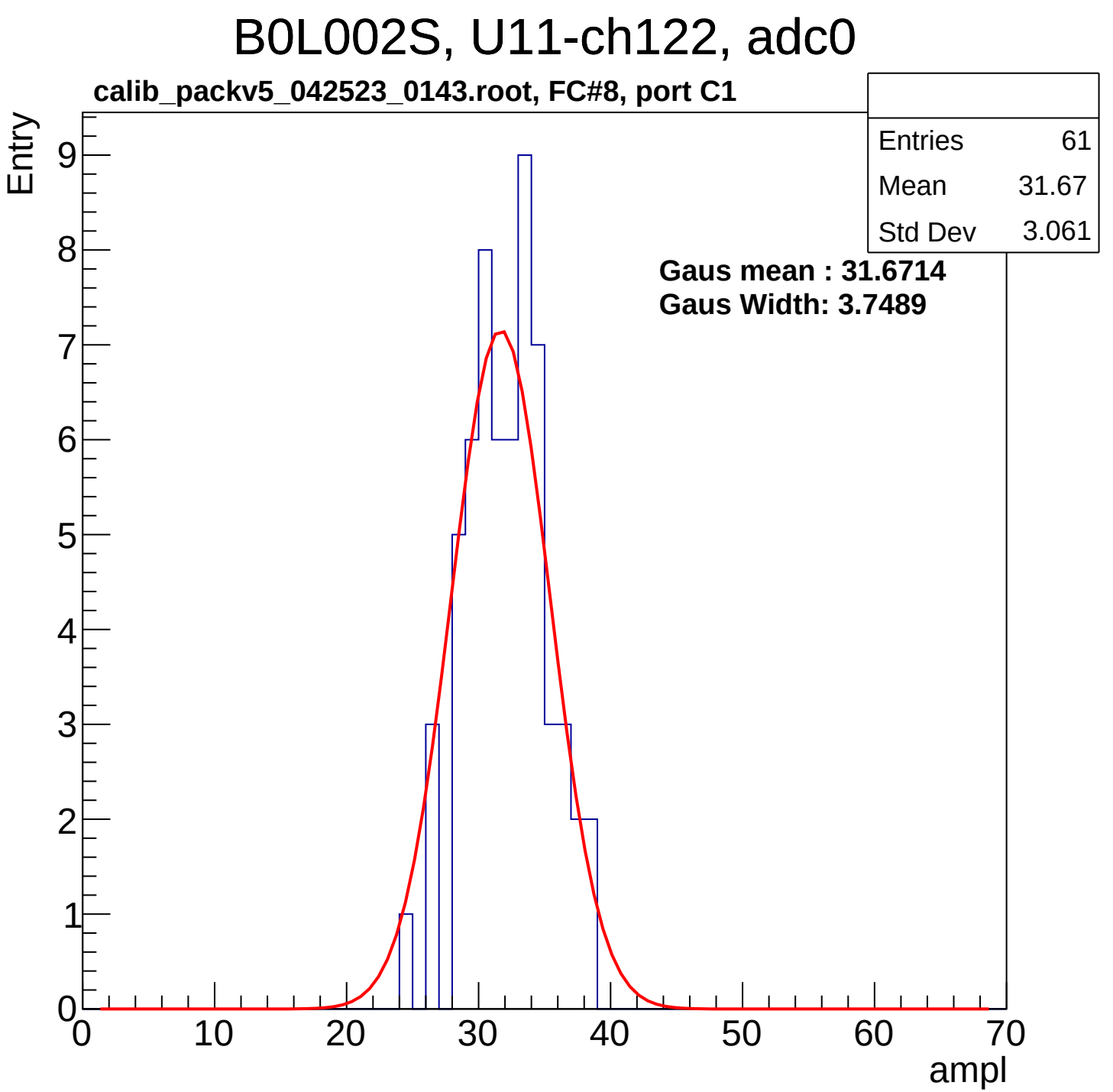
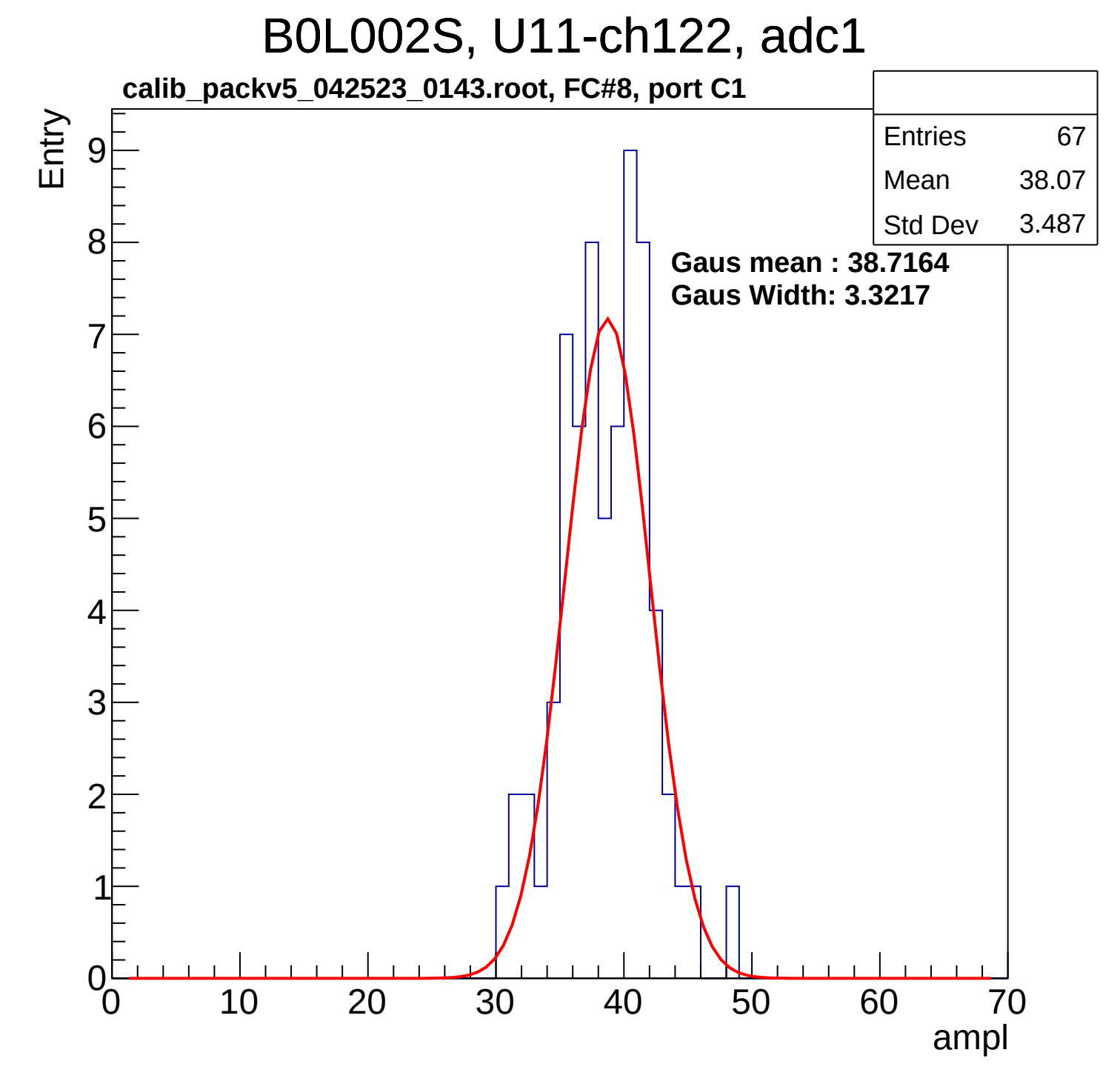
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 31.67
Std Dev 3.061
Gaus mean : 31.6714
Gaus Width: 3.7489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.07
Std Dev 3.487
Gaus mean : 38.7164
Gaus Width: 3.3217
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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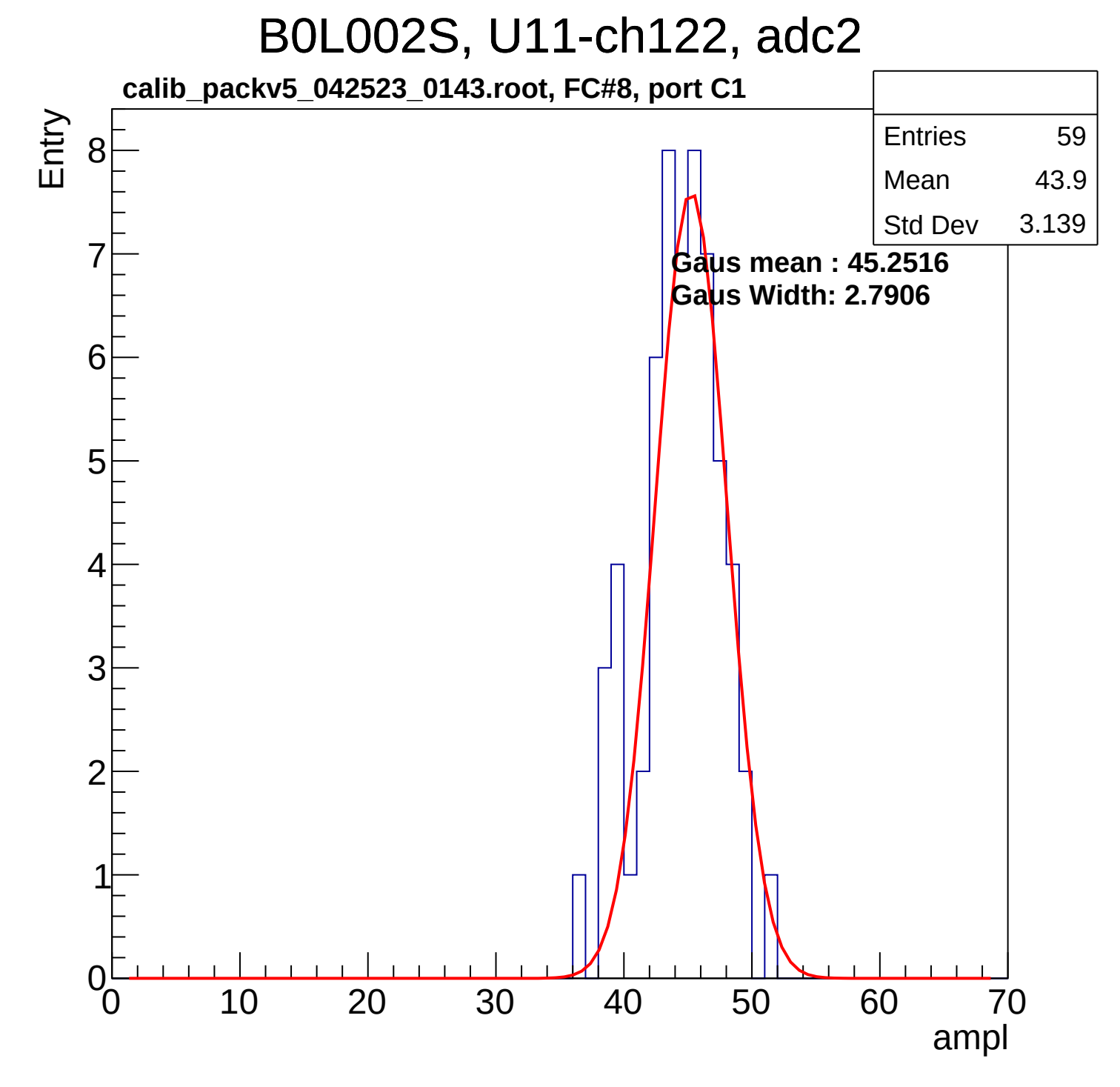
20
30
40
50
60
70


B0L002S, U11-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.9
Std Dev 3.139
Gaus mean : 45.2516
Gaus Width: 2.7906
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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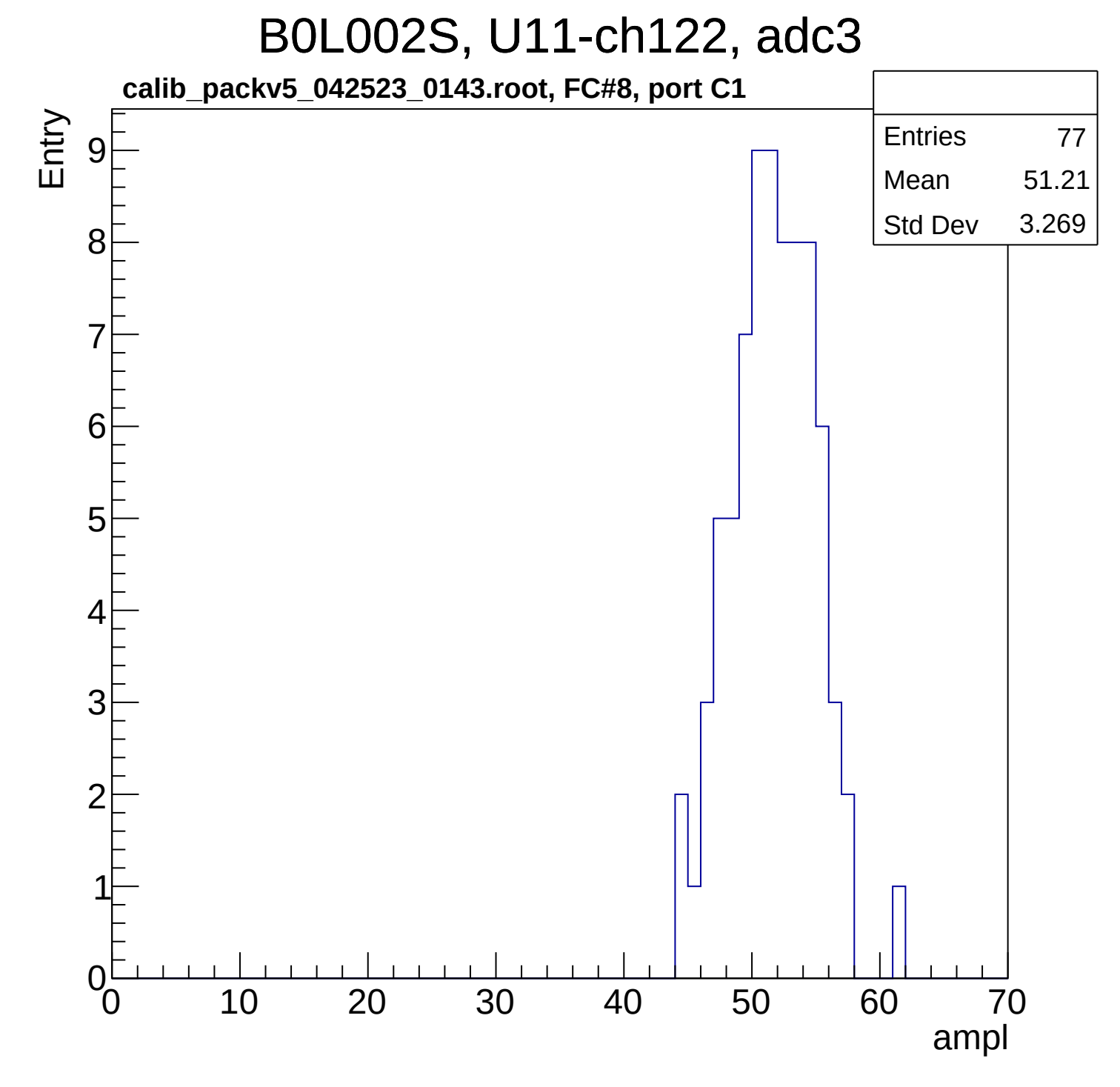
B0L002S, U11-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.21
Std Dev 3.269
Entry
ampl
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3
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5
6
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0
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60
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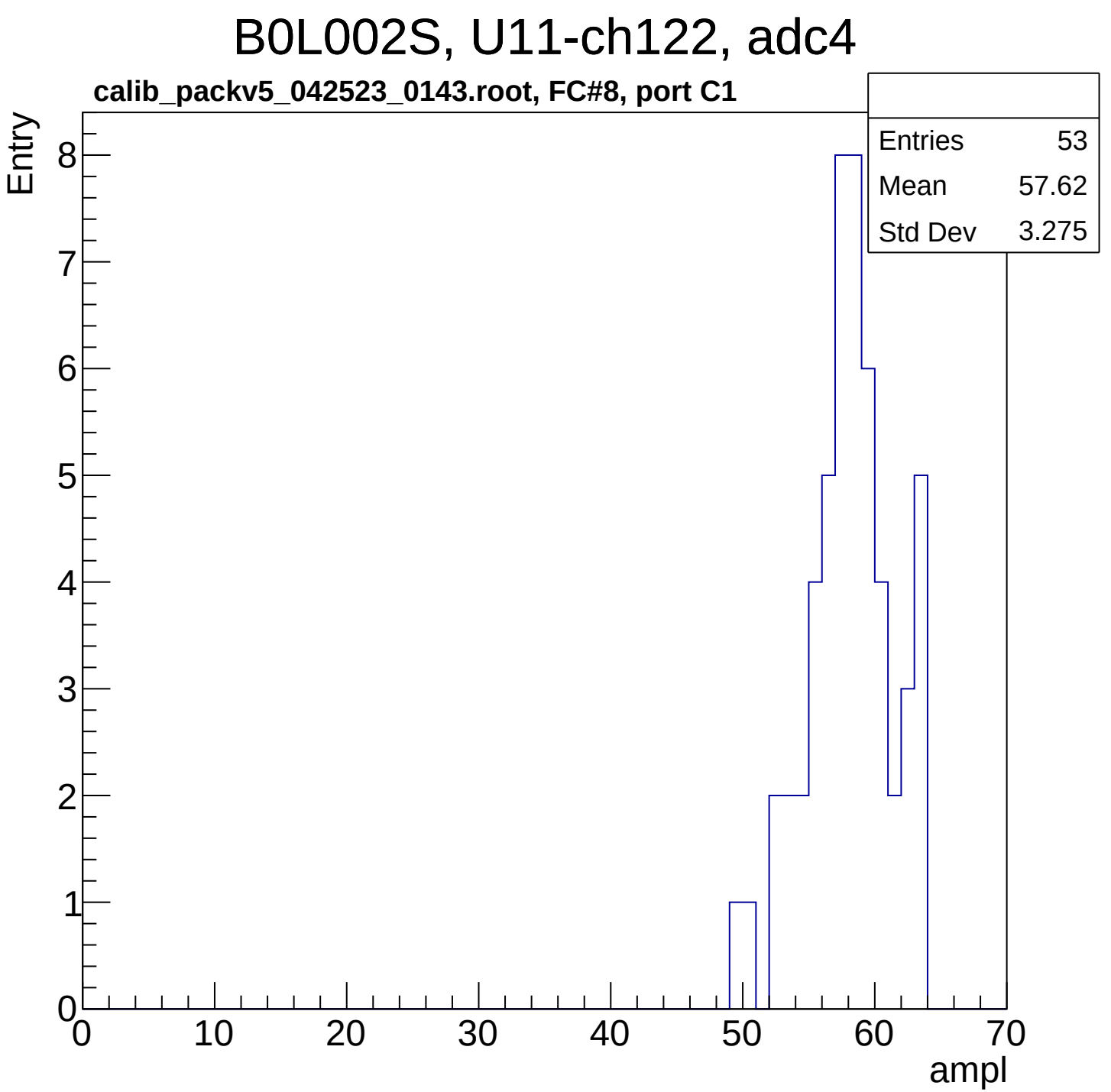
B0L002S, U11-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.62
Std Dev 3.275
Entry
ampl
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2
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4
5
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7
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0
10
20
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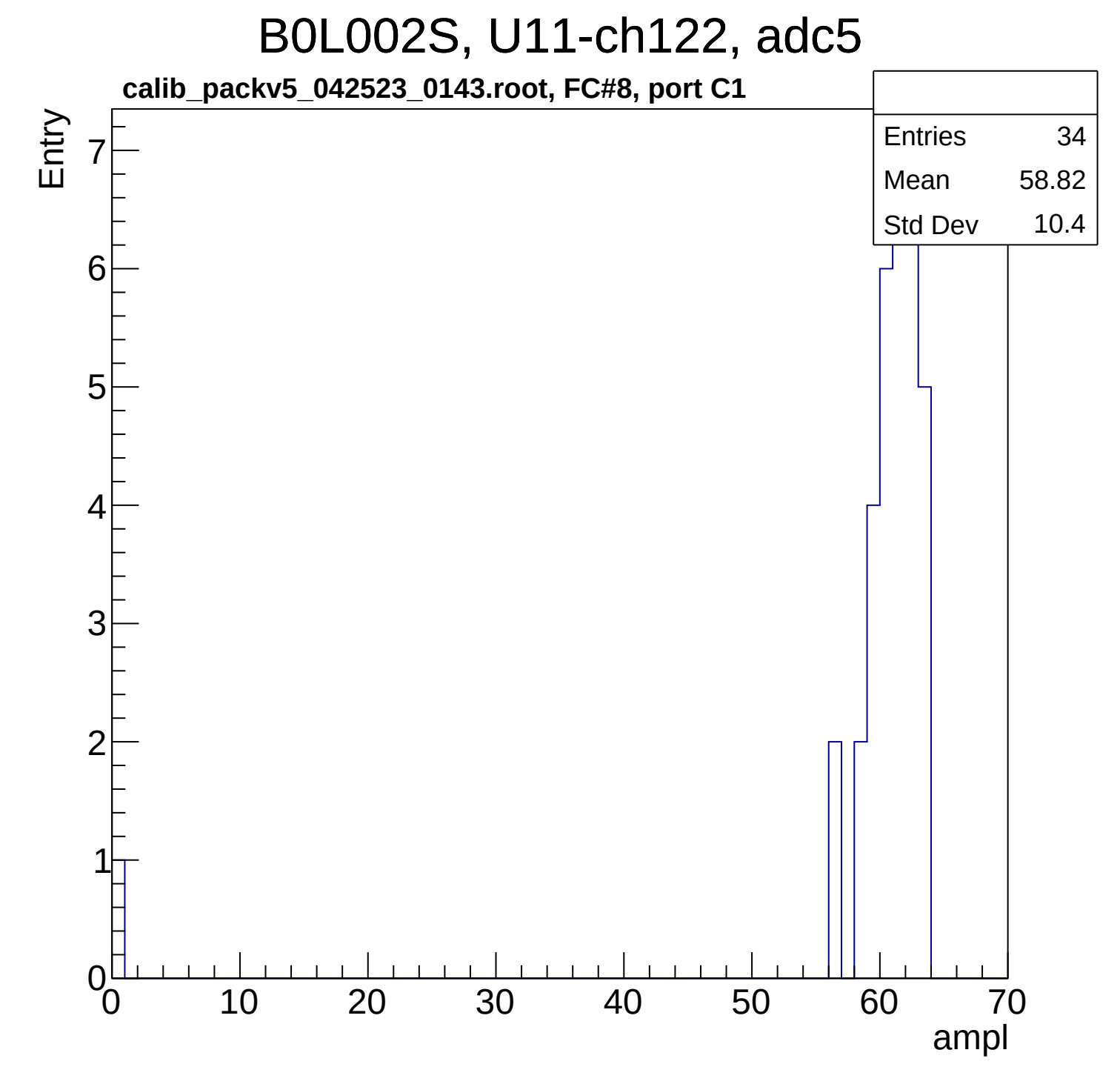
B0L002S, U11-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.82
Std Dev 10.4
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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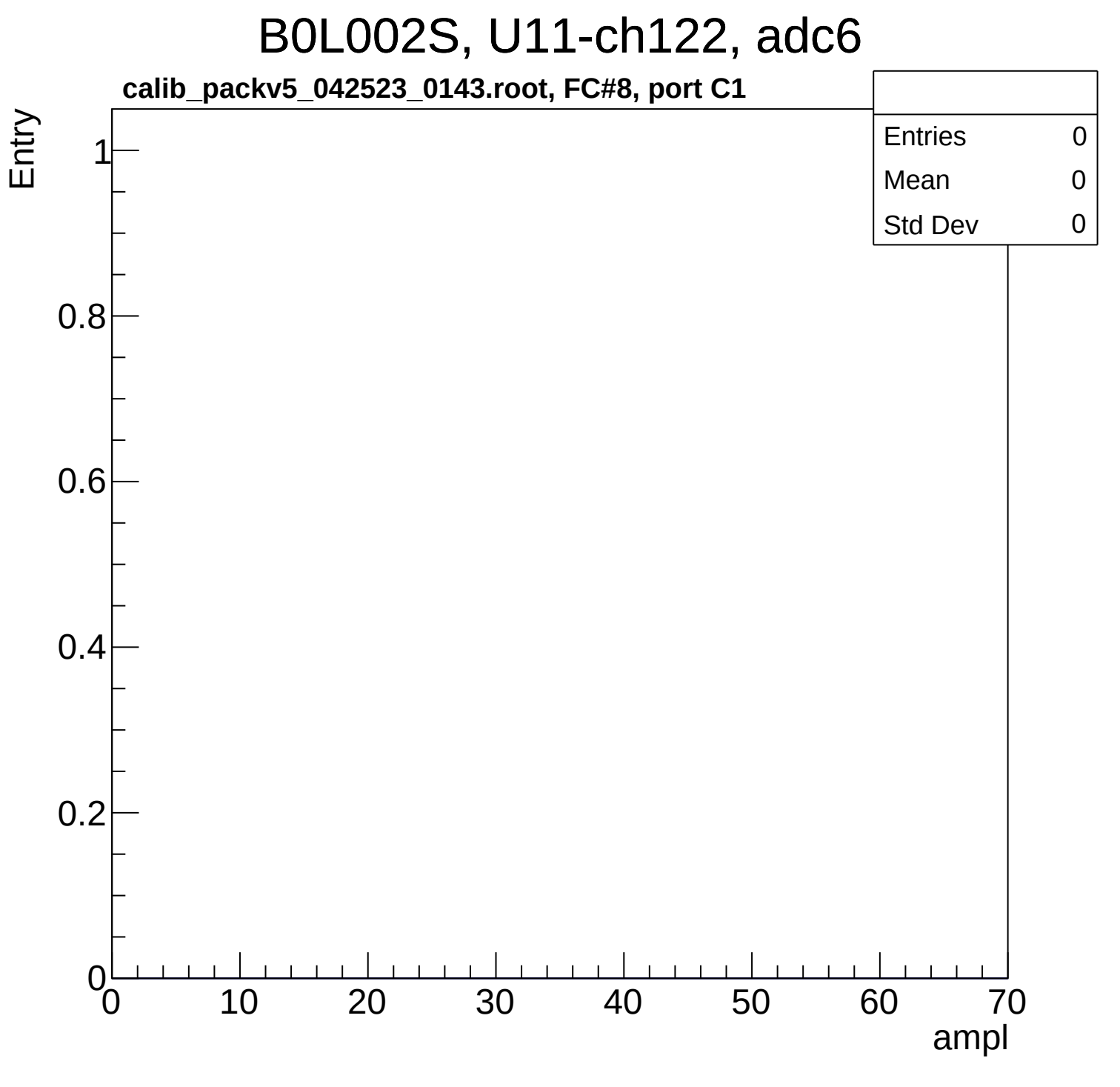
B0L002S, U11-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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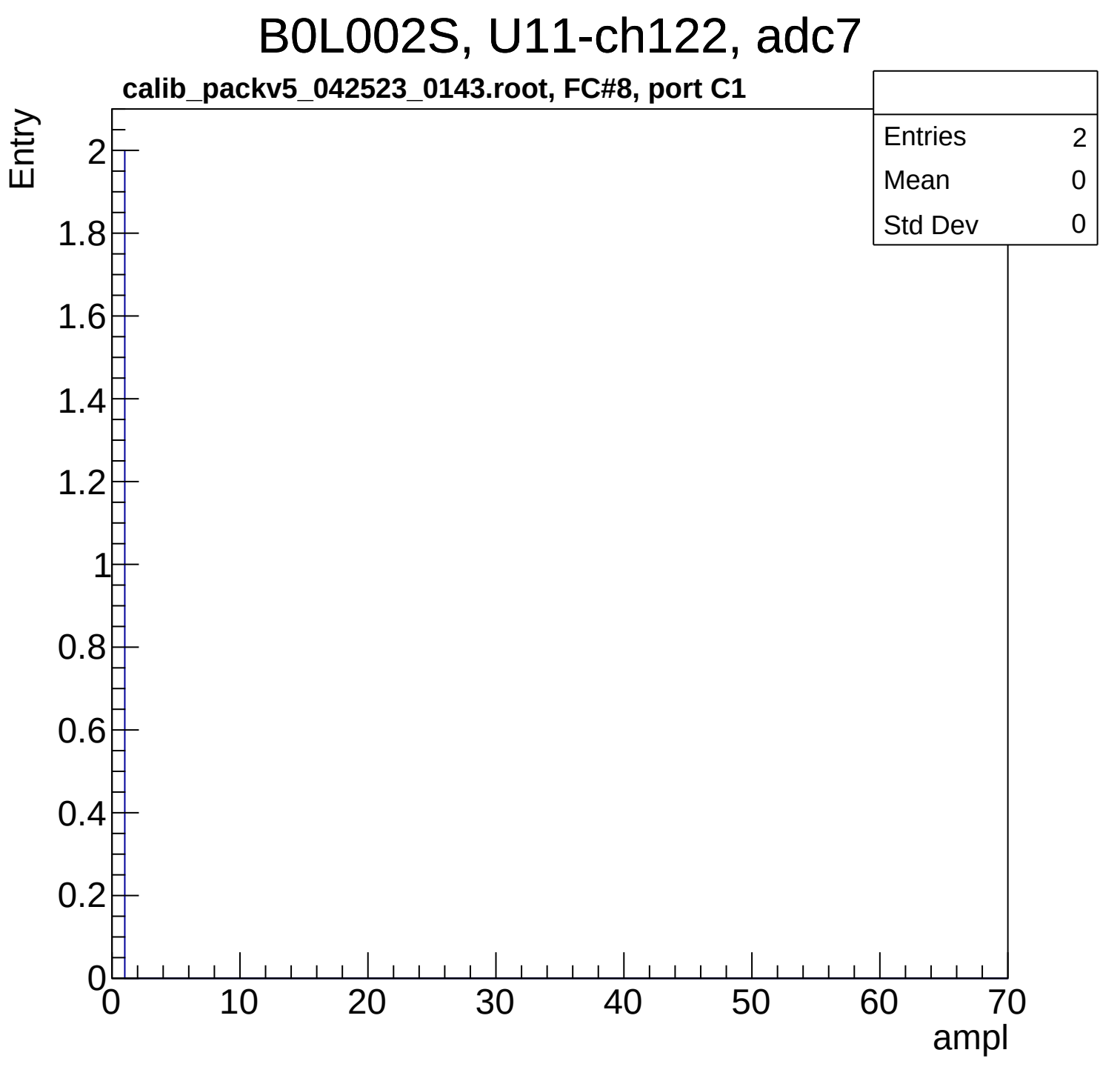
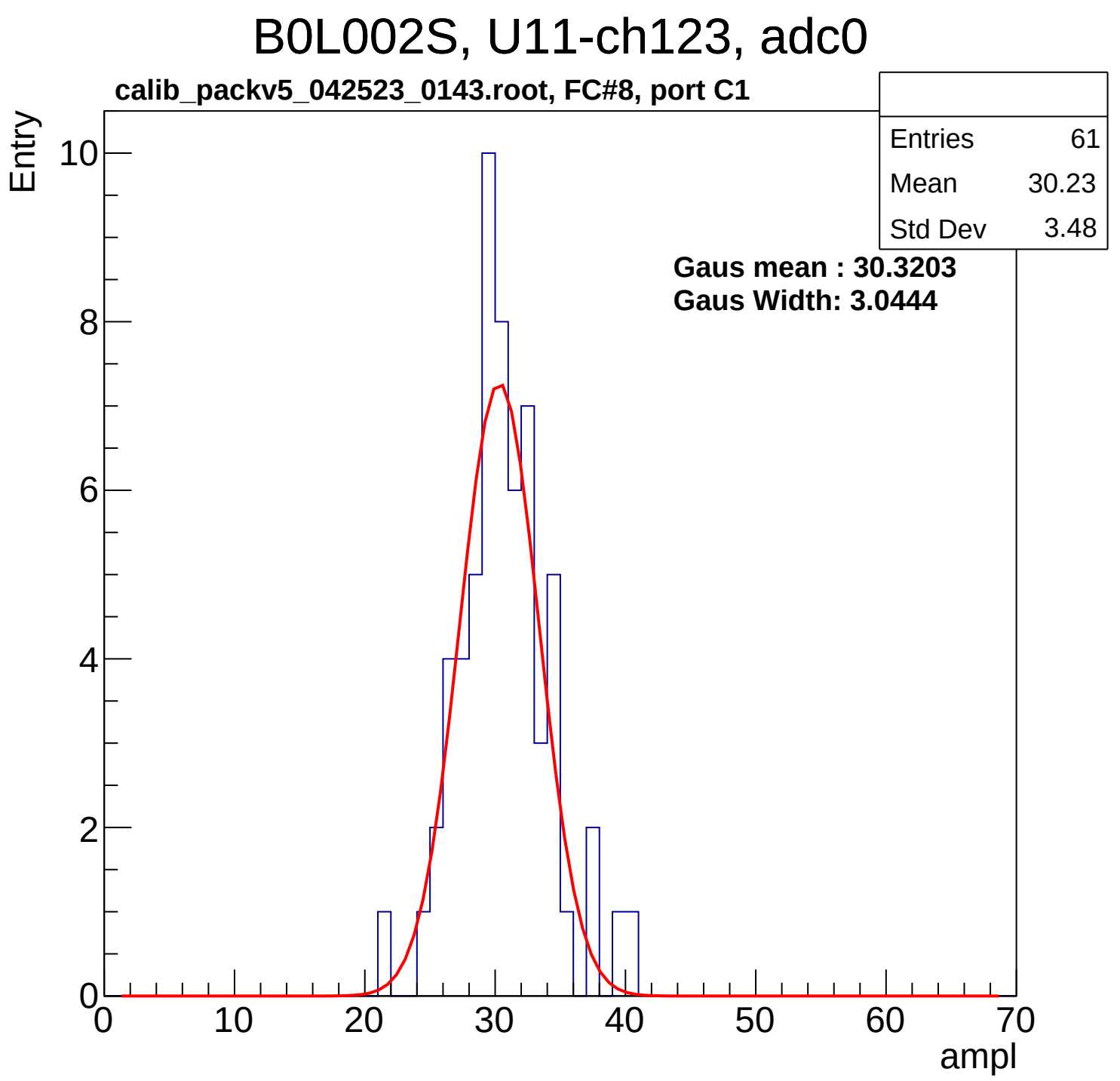
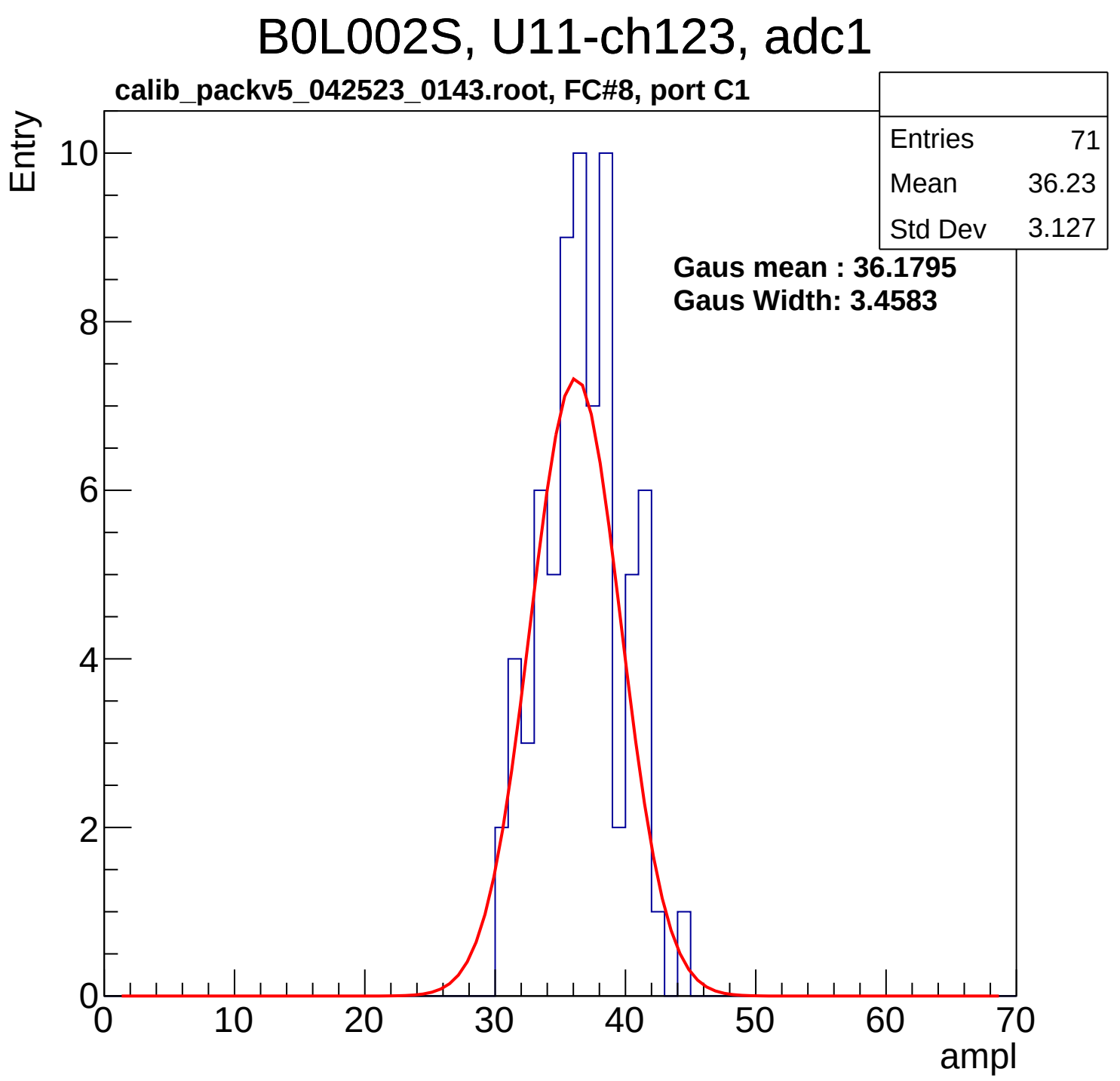
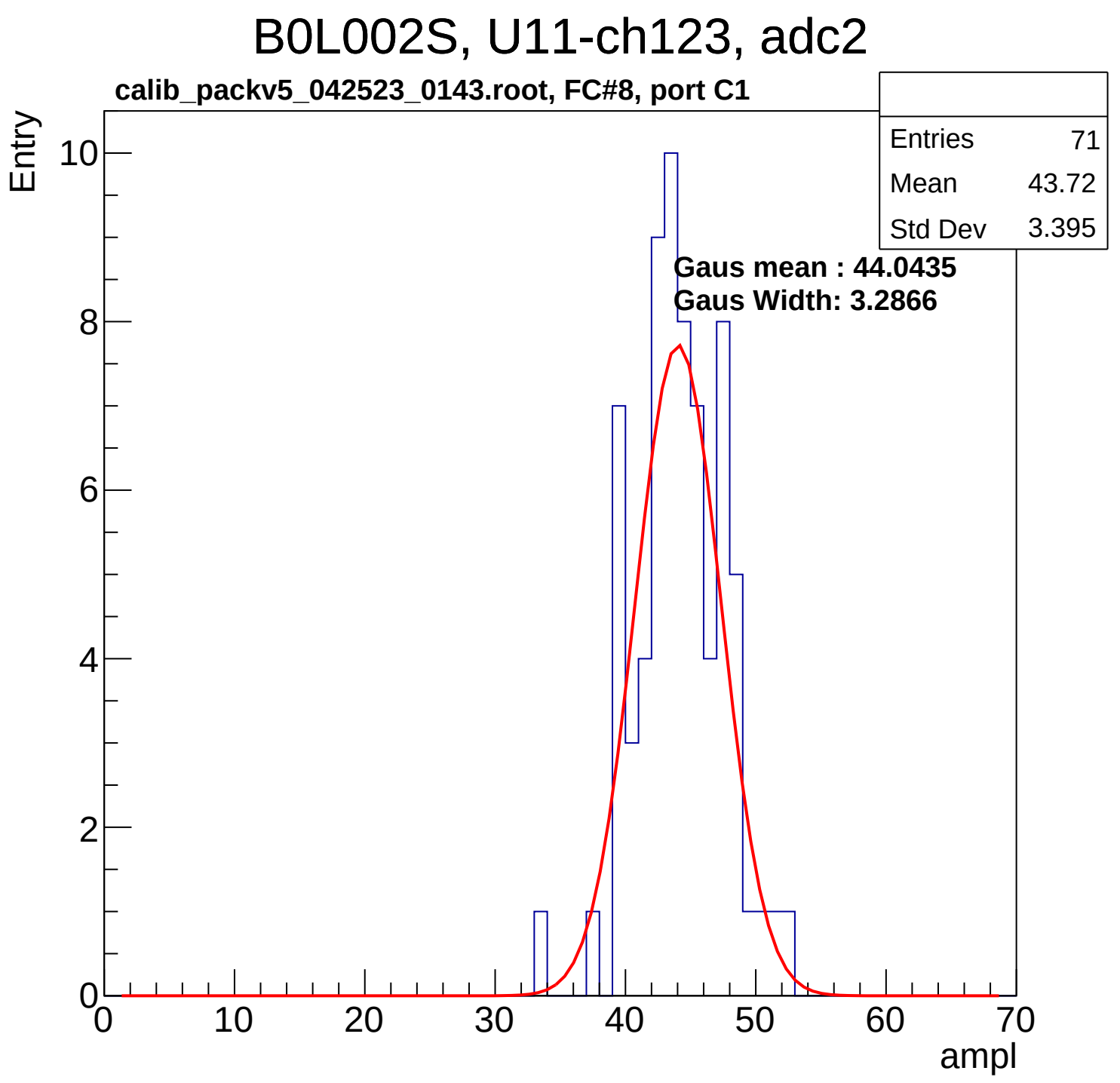
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.23
Std Dev 3.48
Gaus mean : 30.3203
Gaus Width: 3.0444
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.23
Std Dev 3.127
Gaus mean : 36.1795
Gaus Width: 3.4583
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.72
Std Dev 3.395
Gaus mean : 44.0435
Gaus Width: 3.2866
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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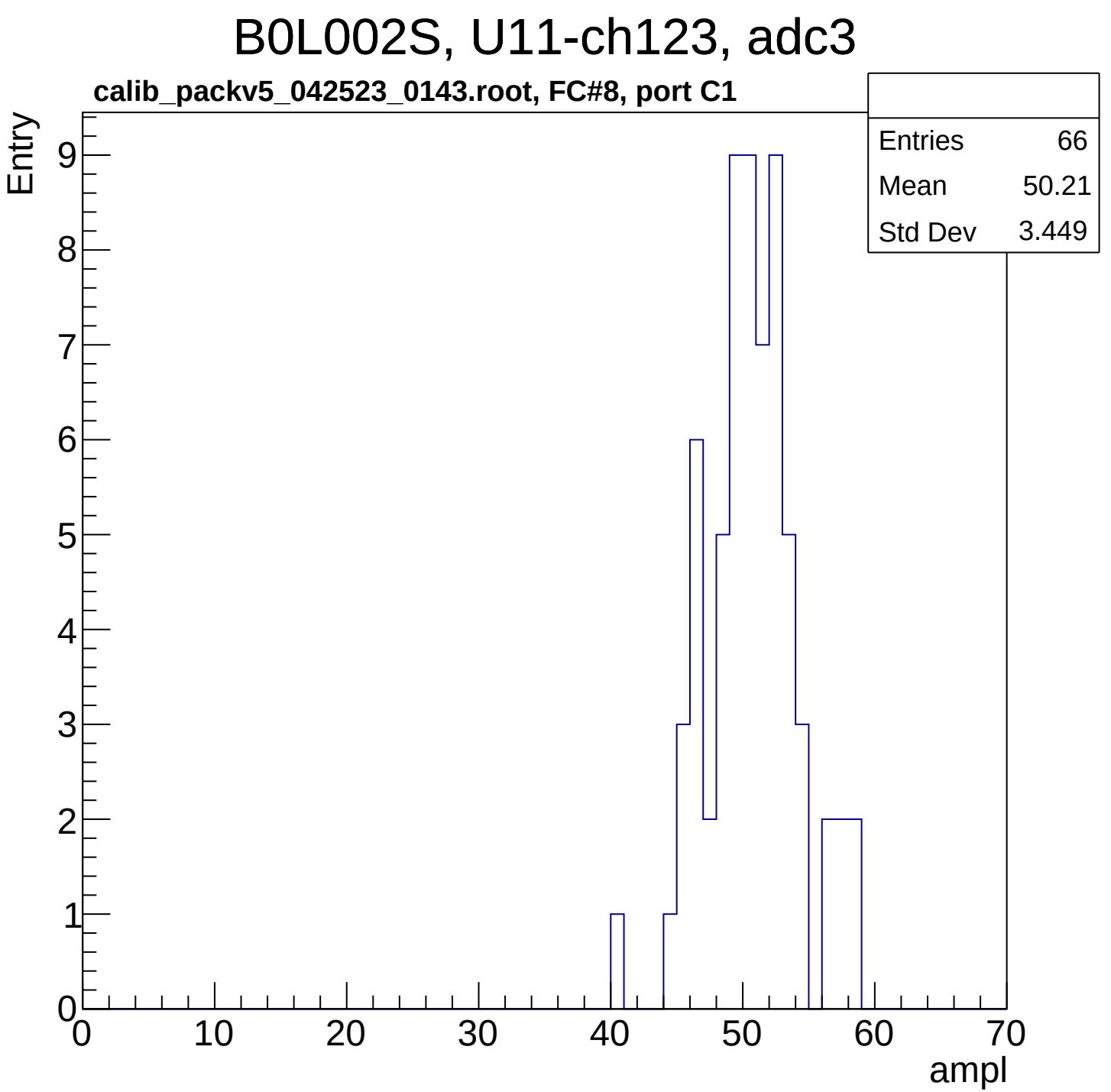
B0L002S, U11-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.21
Std Dev 3.449
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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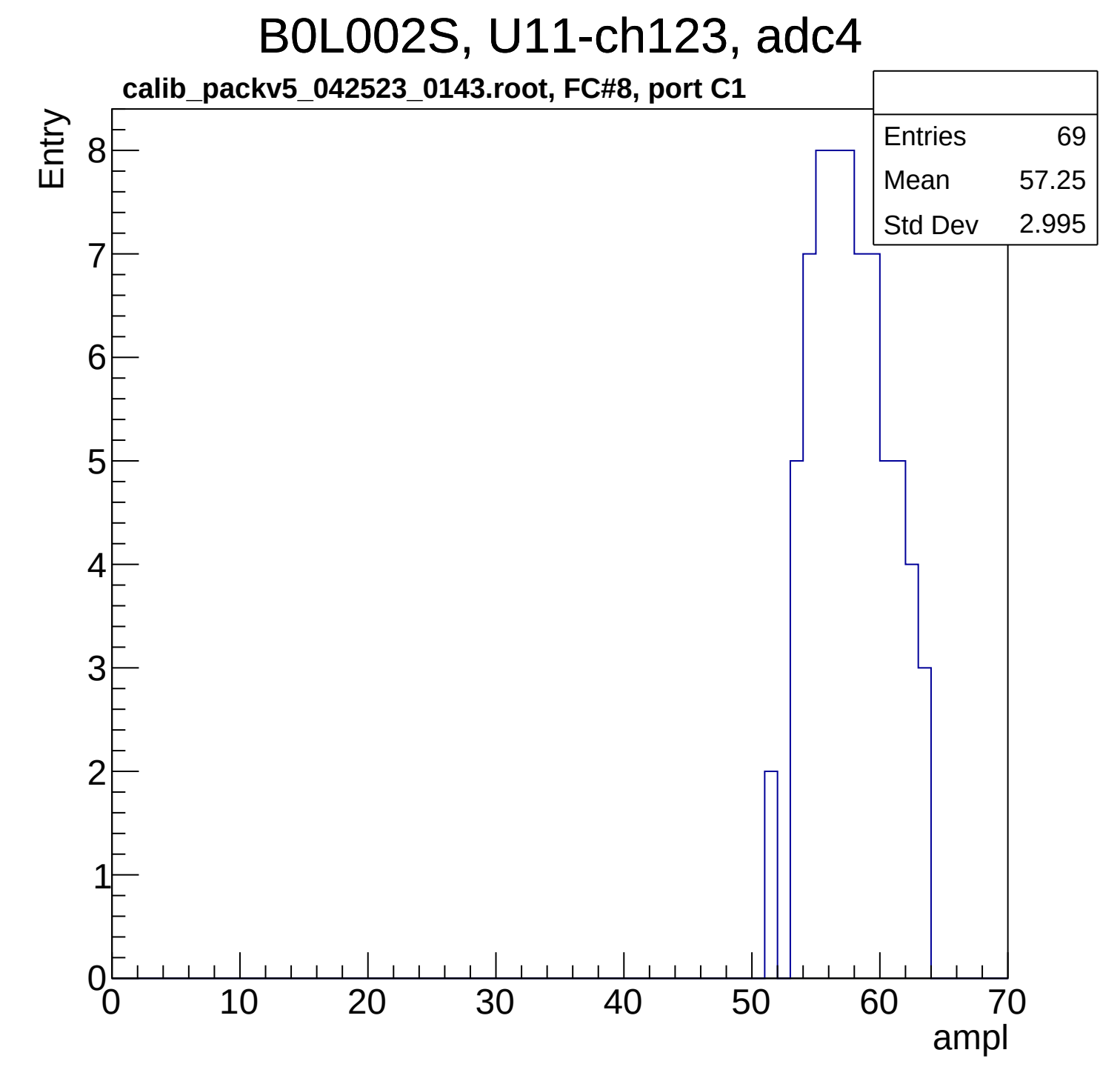
B0L002S, U11-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 57.25
Std Dev 2.995
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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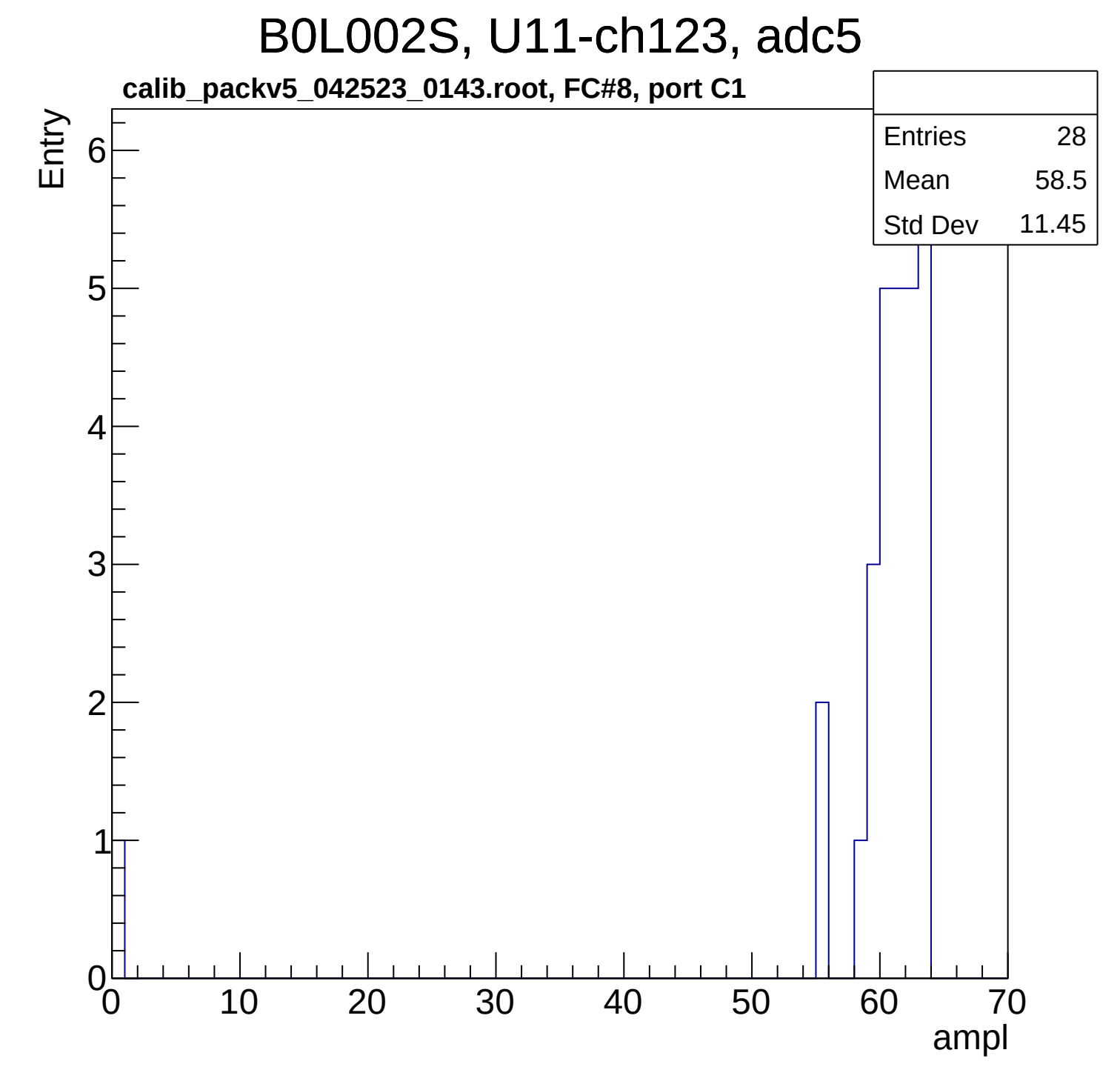
B0L002S, U11-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.5
Std Dev 11.45
Entry
0
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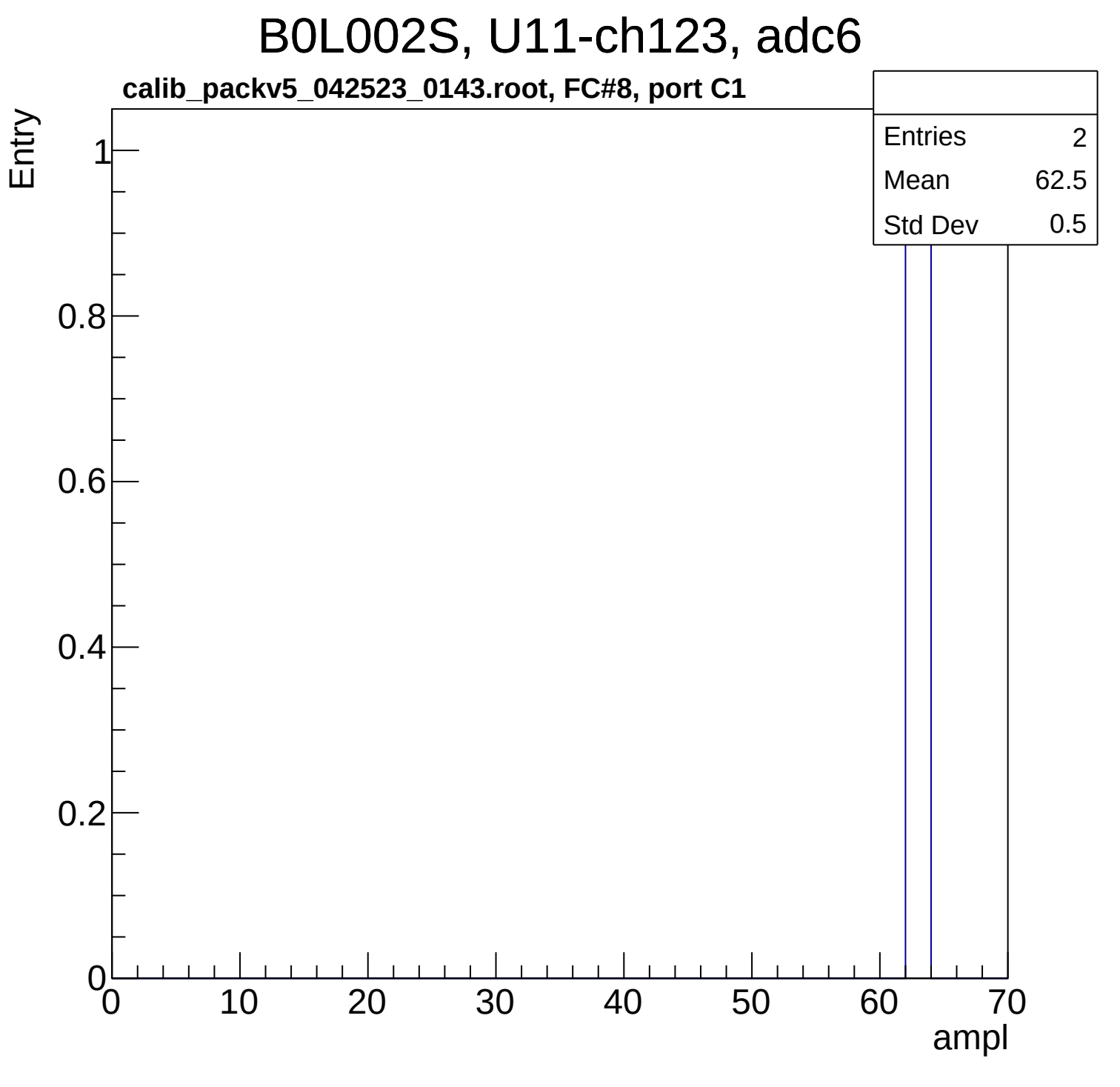
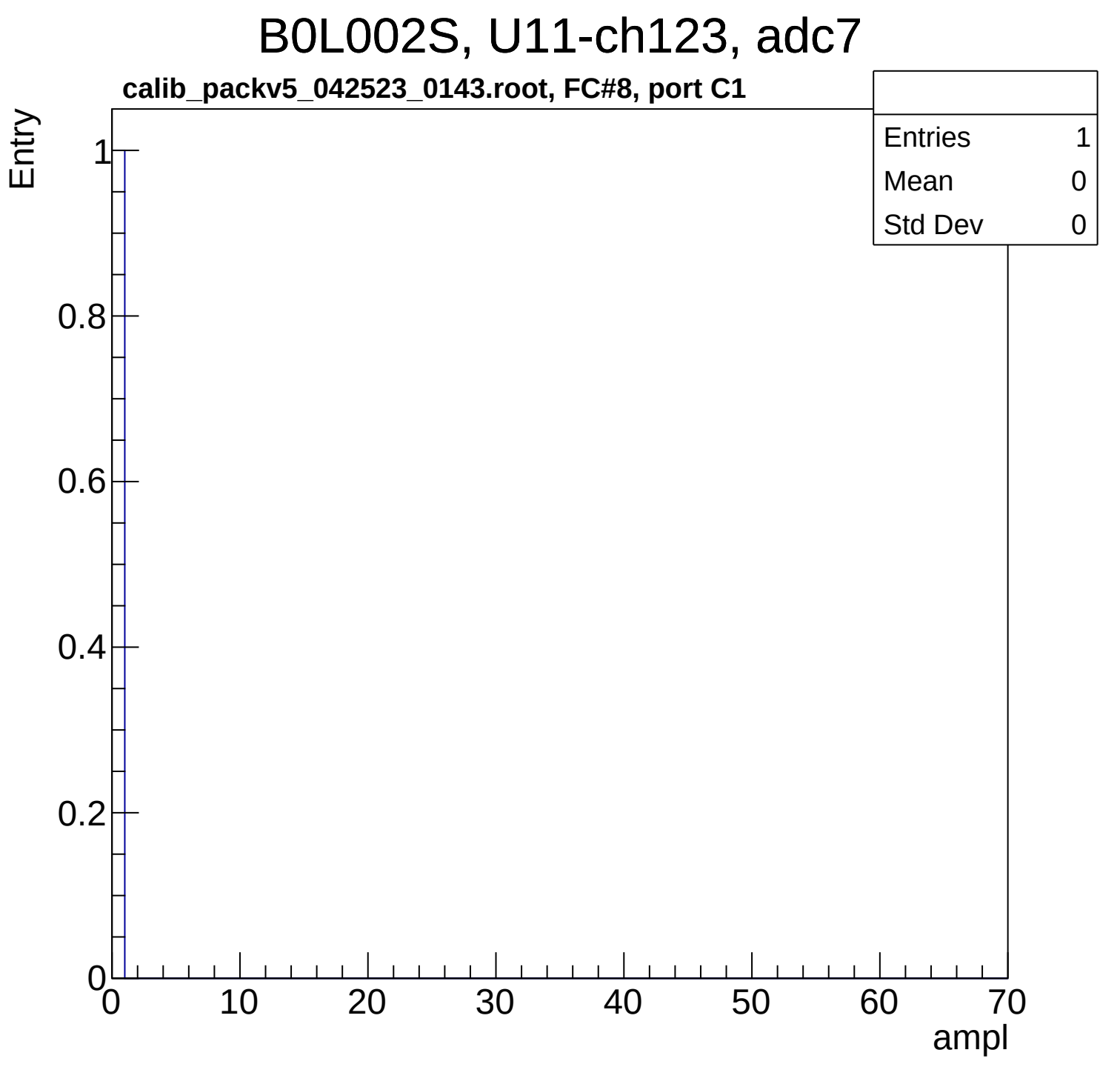
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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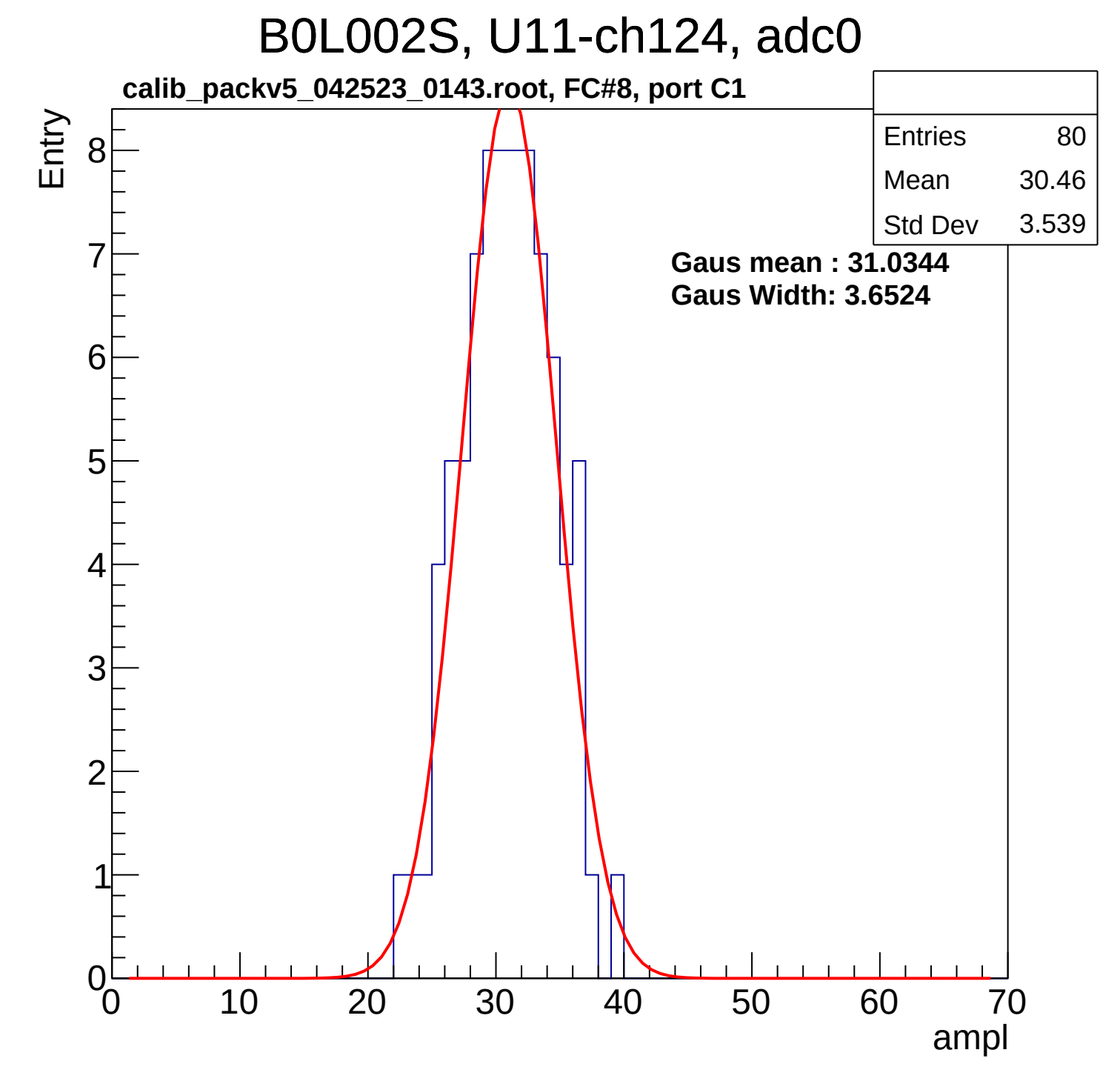
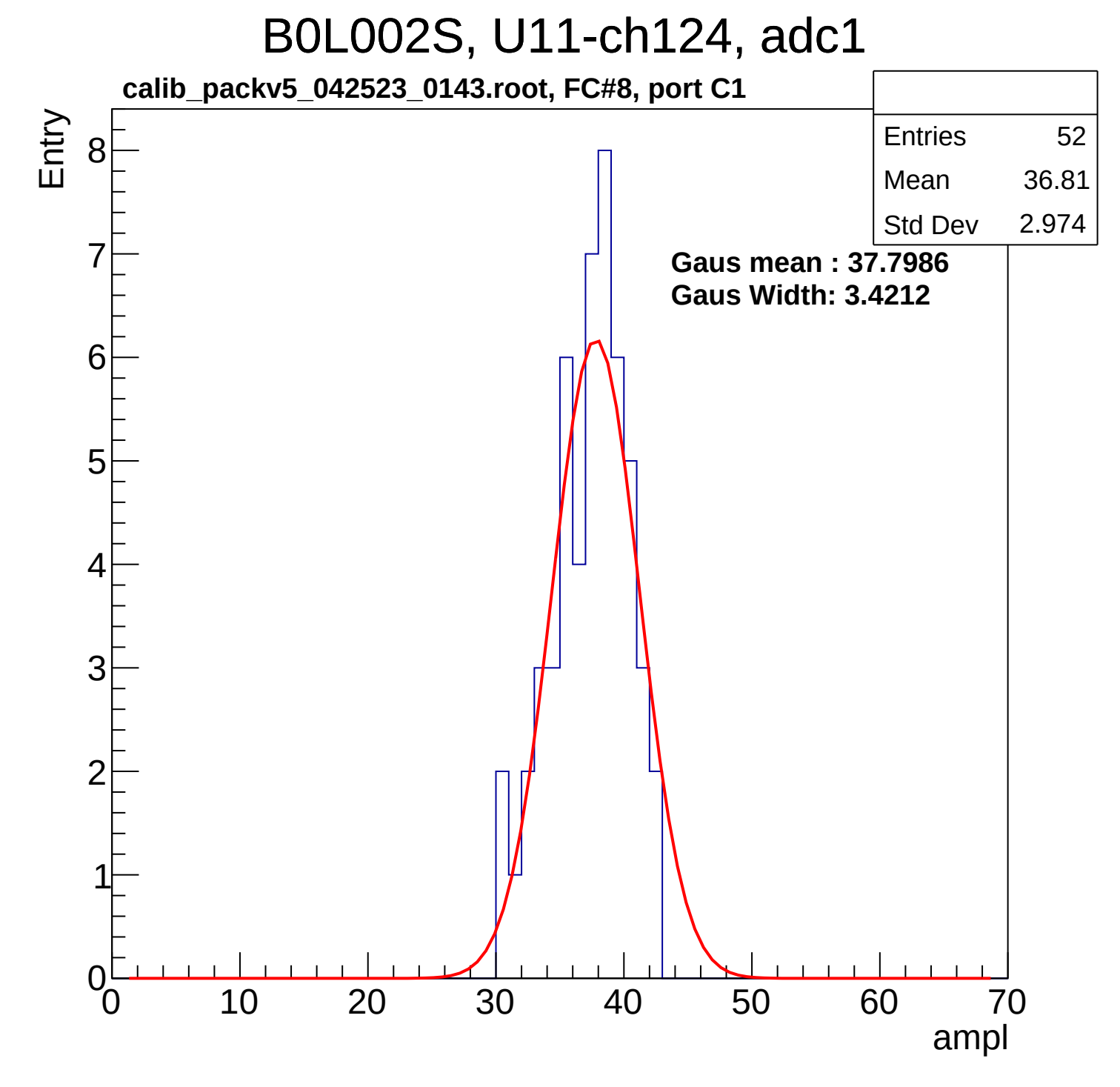
40
50
60
70
ampl


B0L002S, U11-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.46
Std Dev 3.539
Gaus mean : 31.0344
Gaus Width: 3.6524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 36.81
Std Dev 2.974
Gaus mean : 37.7986
Gaus Width: 3.4212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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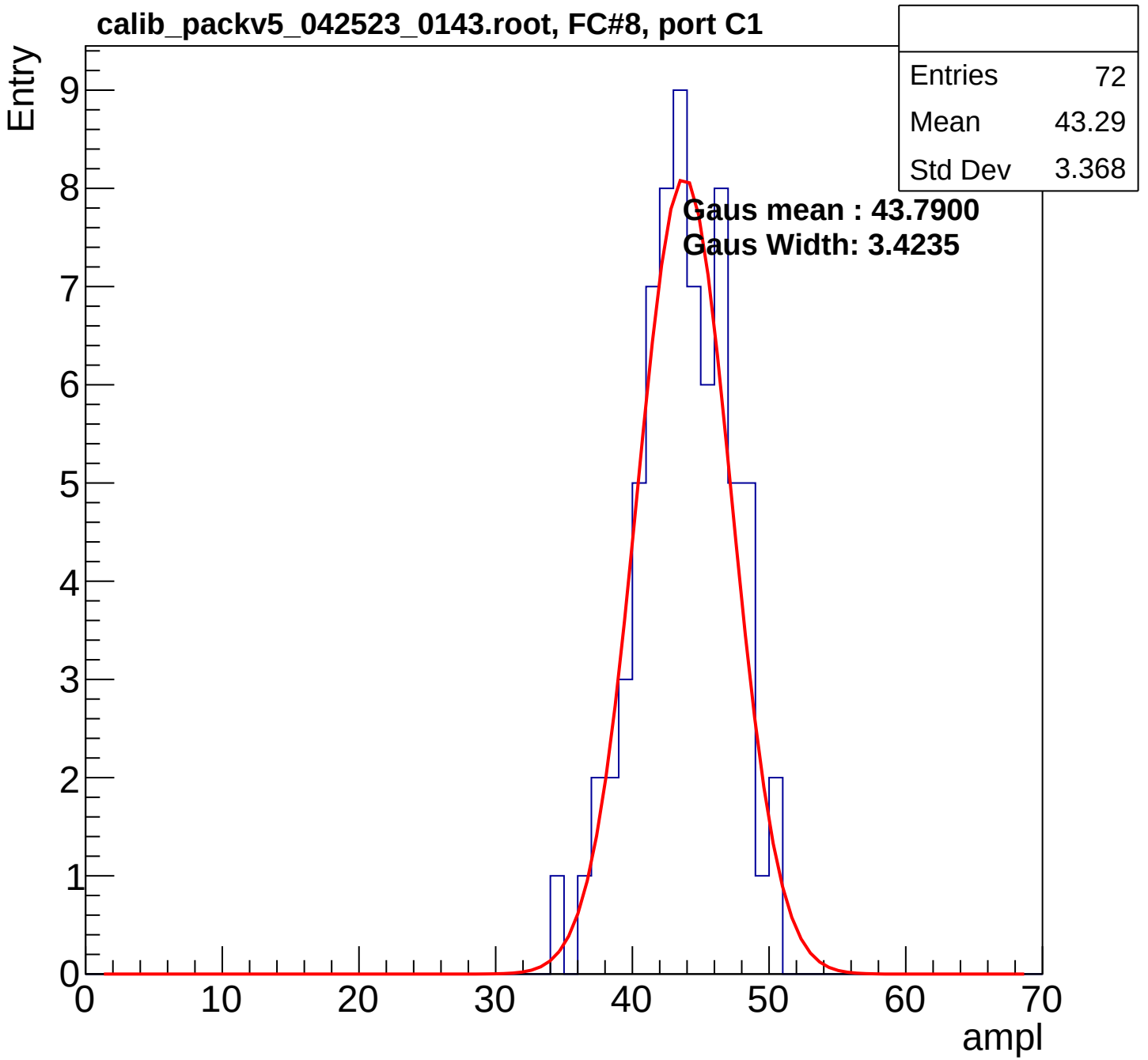
B0L002S, U11-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.29
Std Dev 3.368
Gaus mean : 43.7900
Gaus Width: 3.4235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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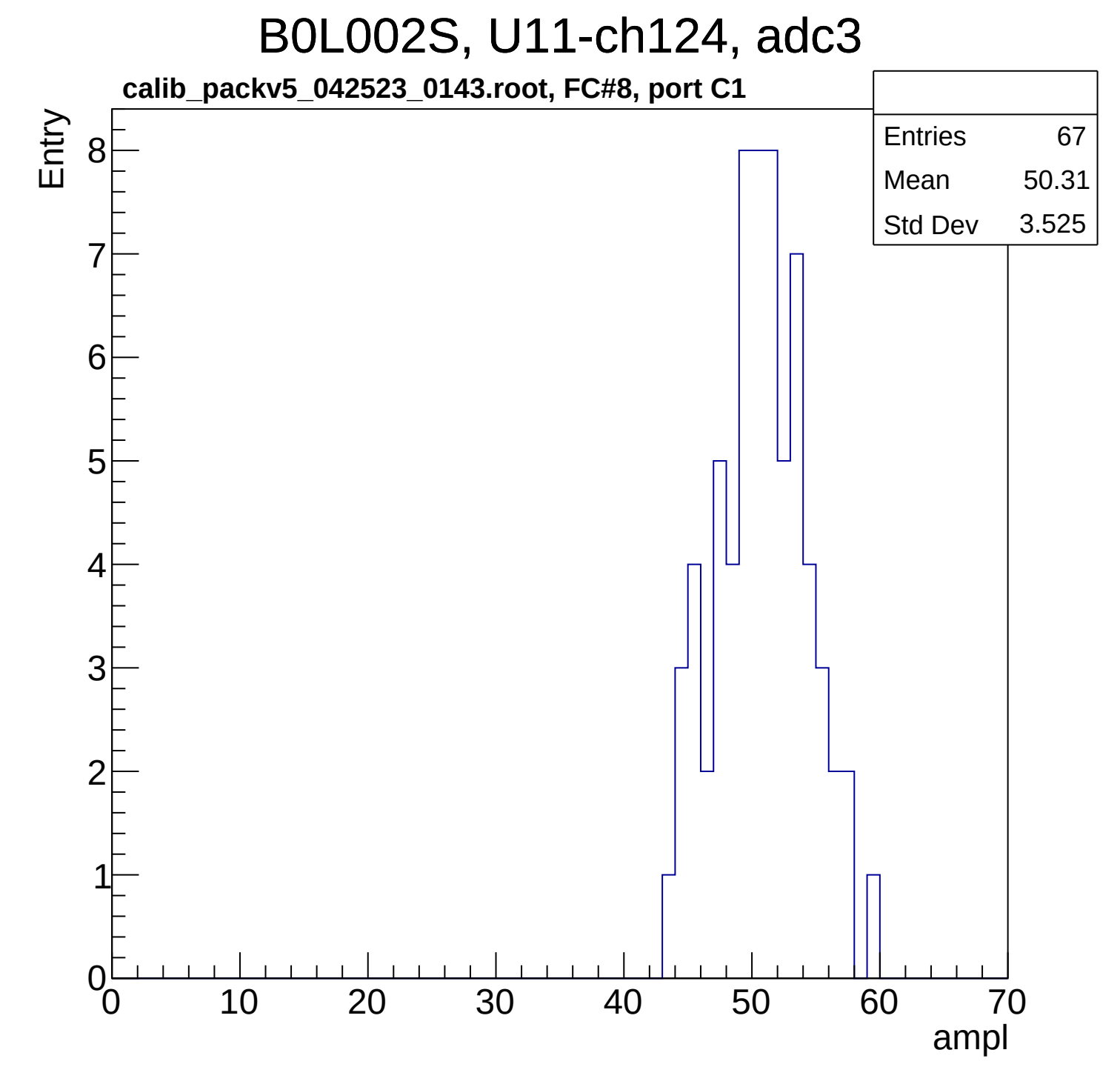
B0L002S, U11-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.31
Std Dev 3.525
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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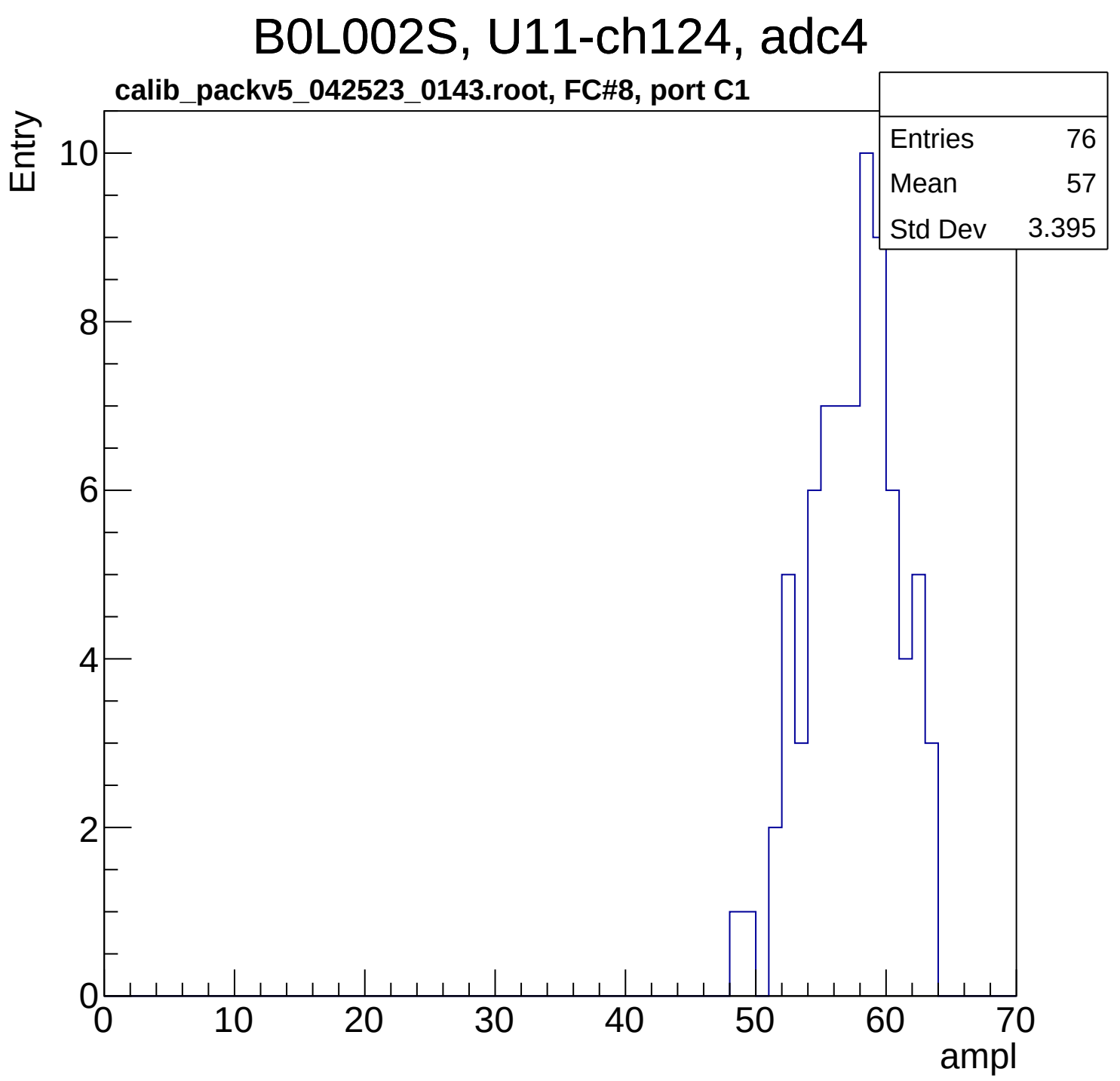
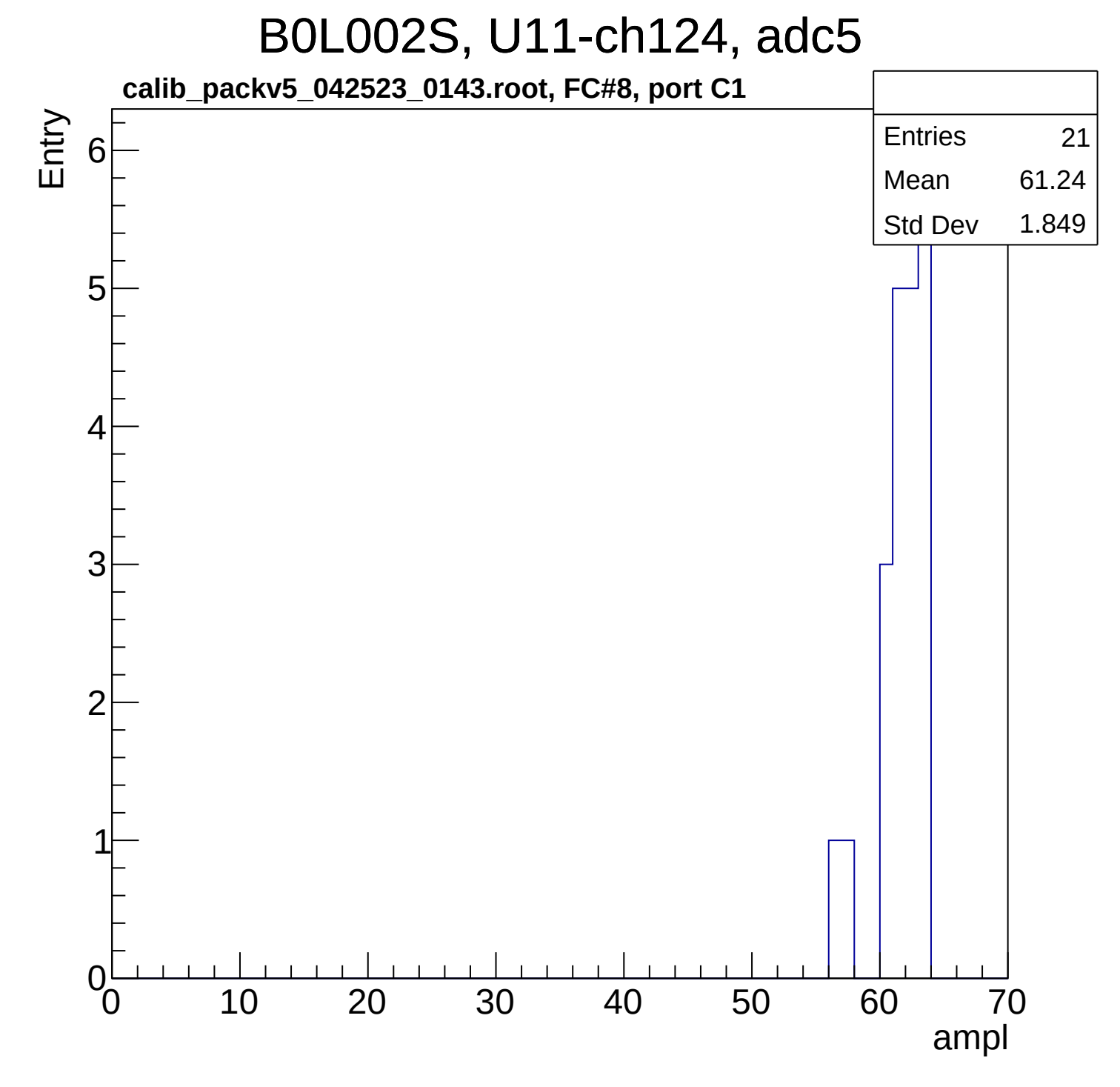

B0L002S, U11-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 57
Std Dev 3.395
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.24
Std Dev 1.849
Entry
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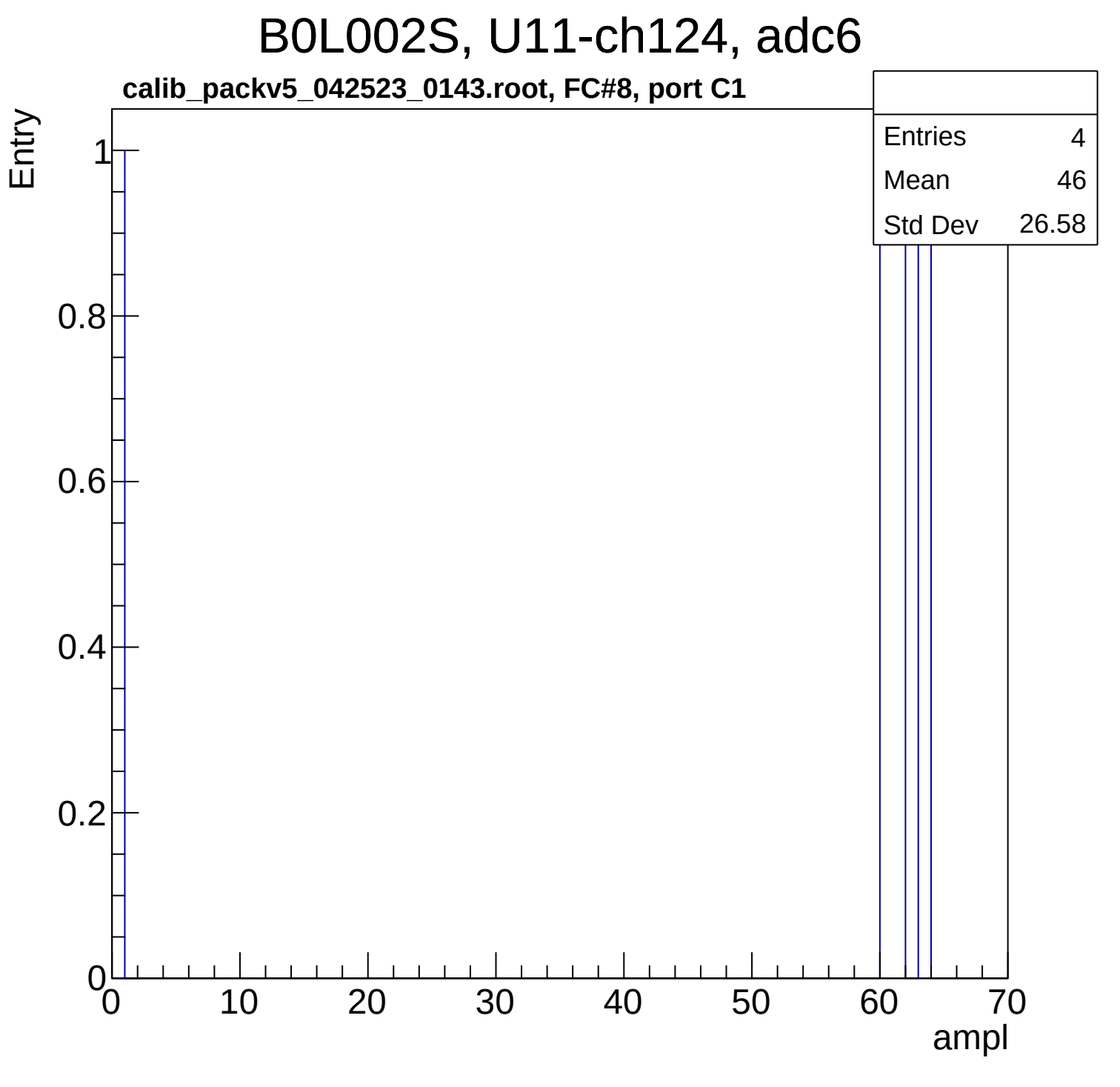
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46
Std Dev 26.58
Entry
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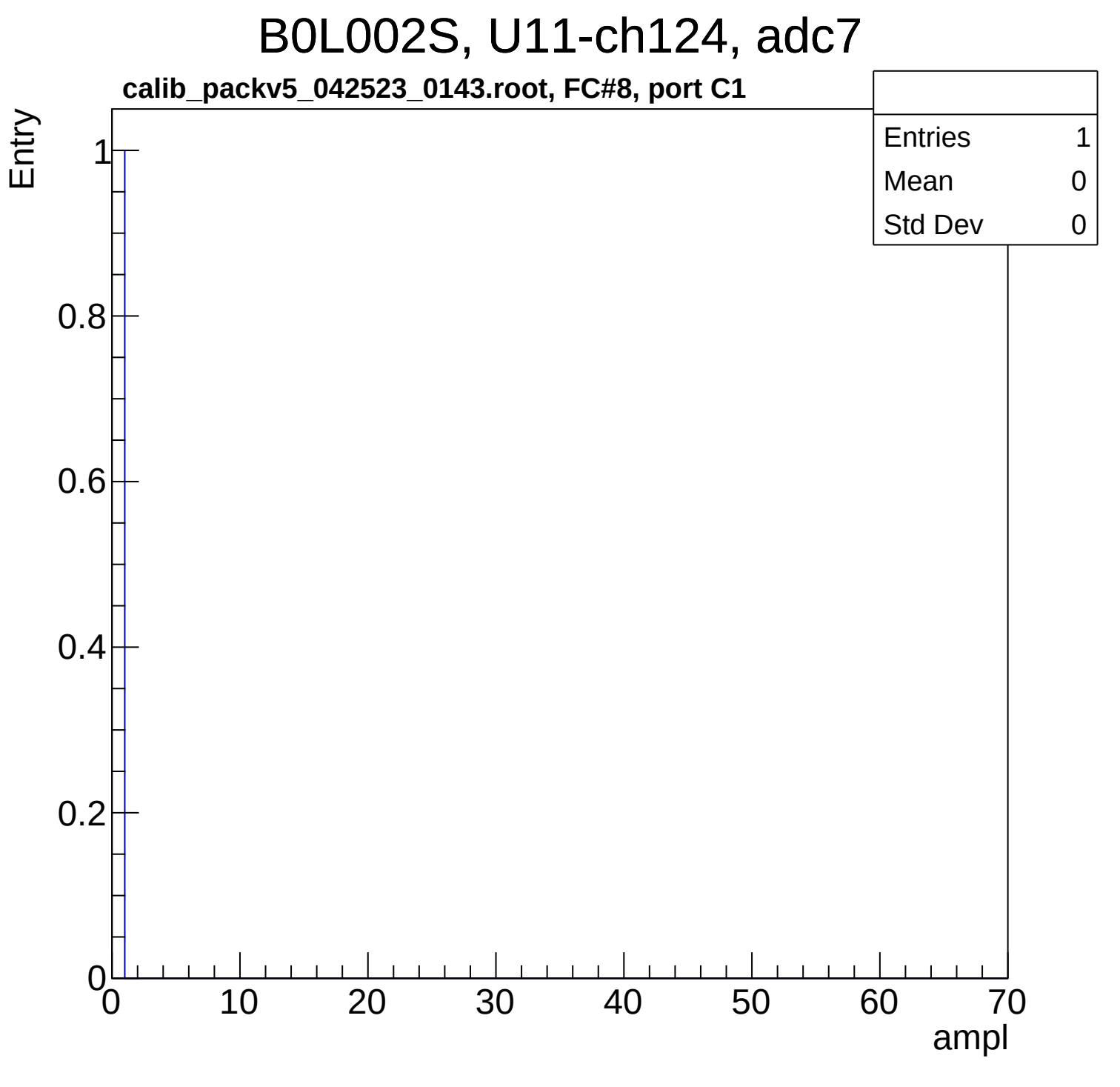
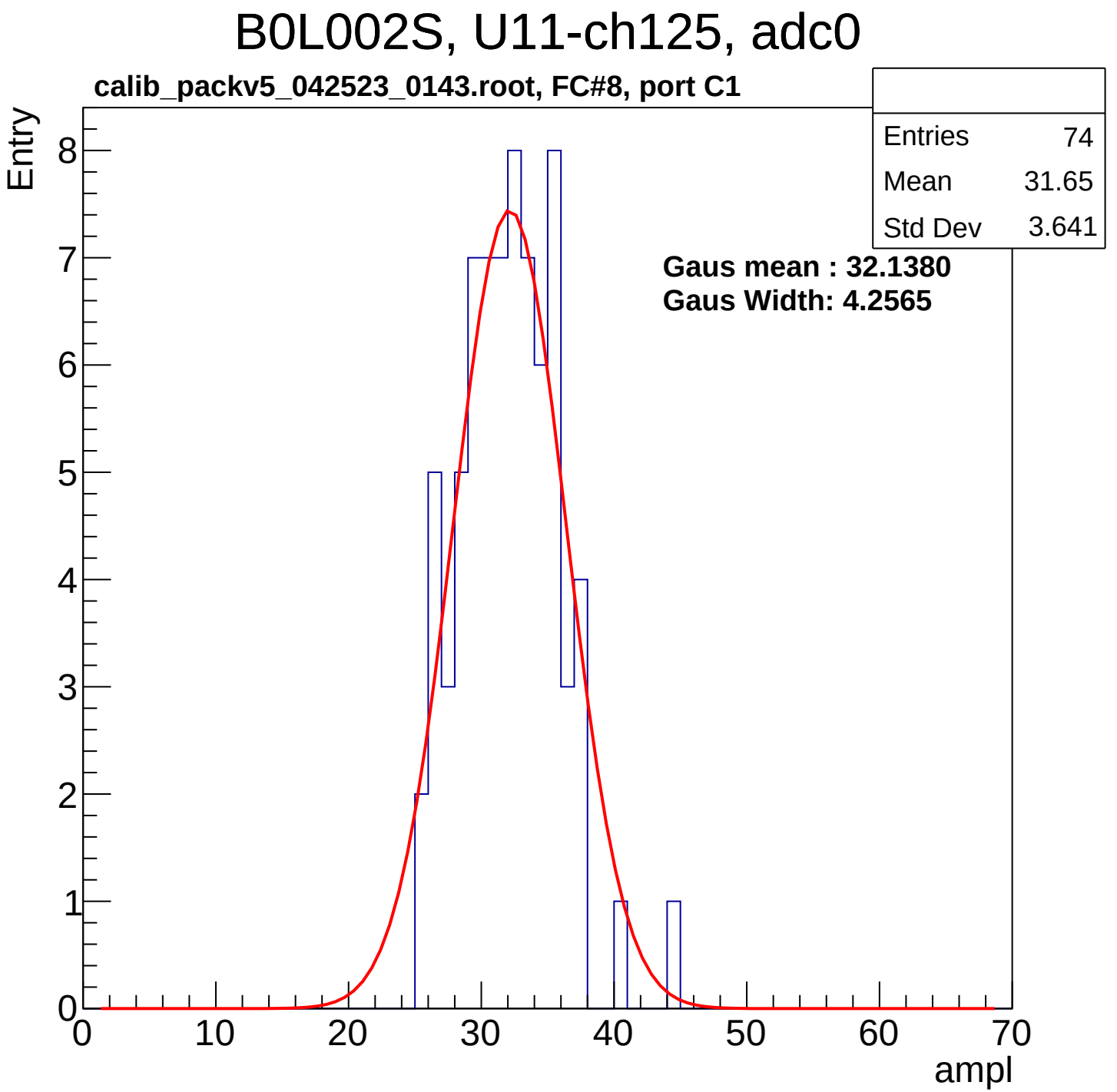
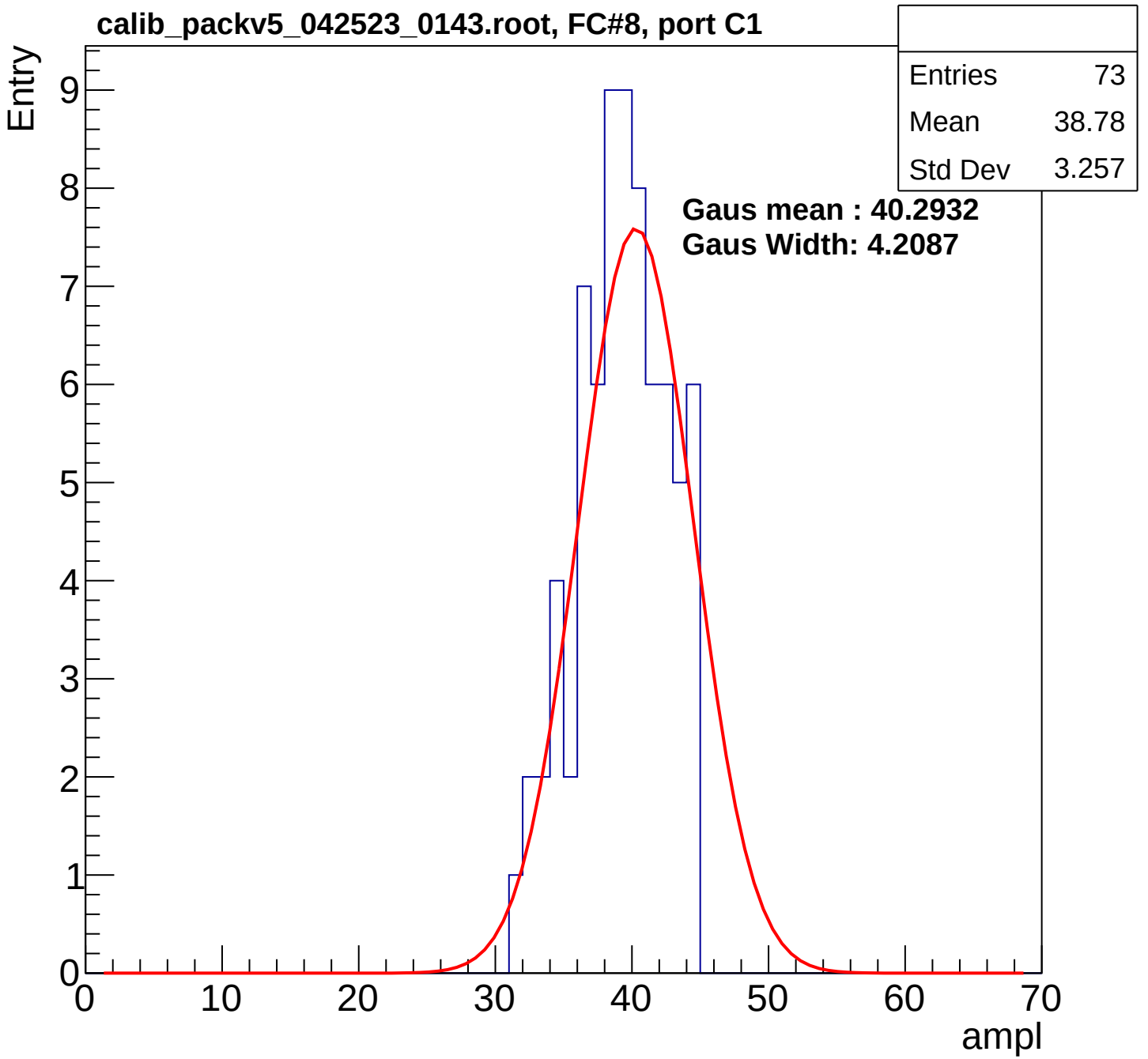
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.65
Std Dev 3.641
Gaus mean : 32.1380
Gaus Width: 4.2565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.78
Std Dev 3.257
Gaus mean : 40.2932
Gaus Width: 4.2087
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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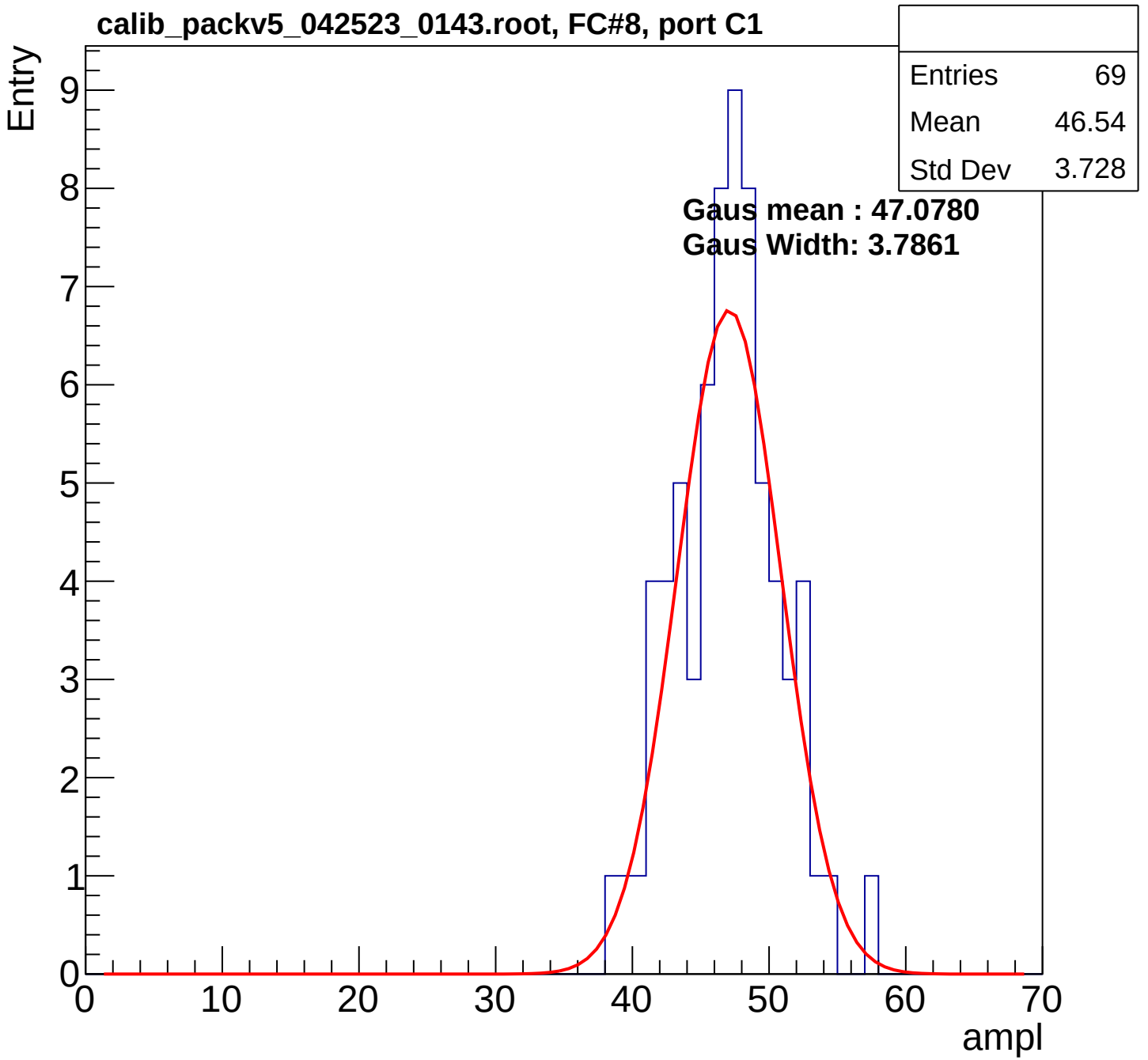
B0L002S, U11-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 46.54
Std Dev 3.728
Gaus mean : 47.0780
Gaus Width: 3.7861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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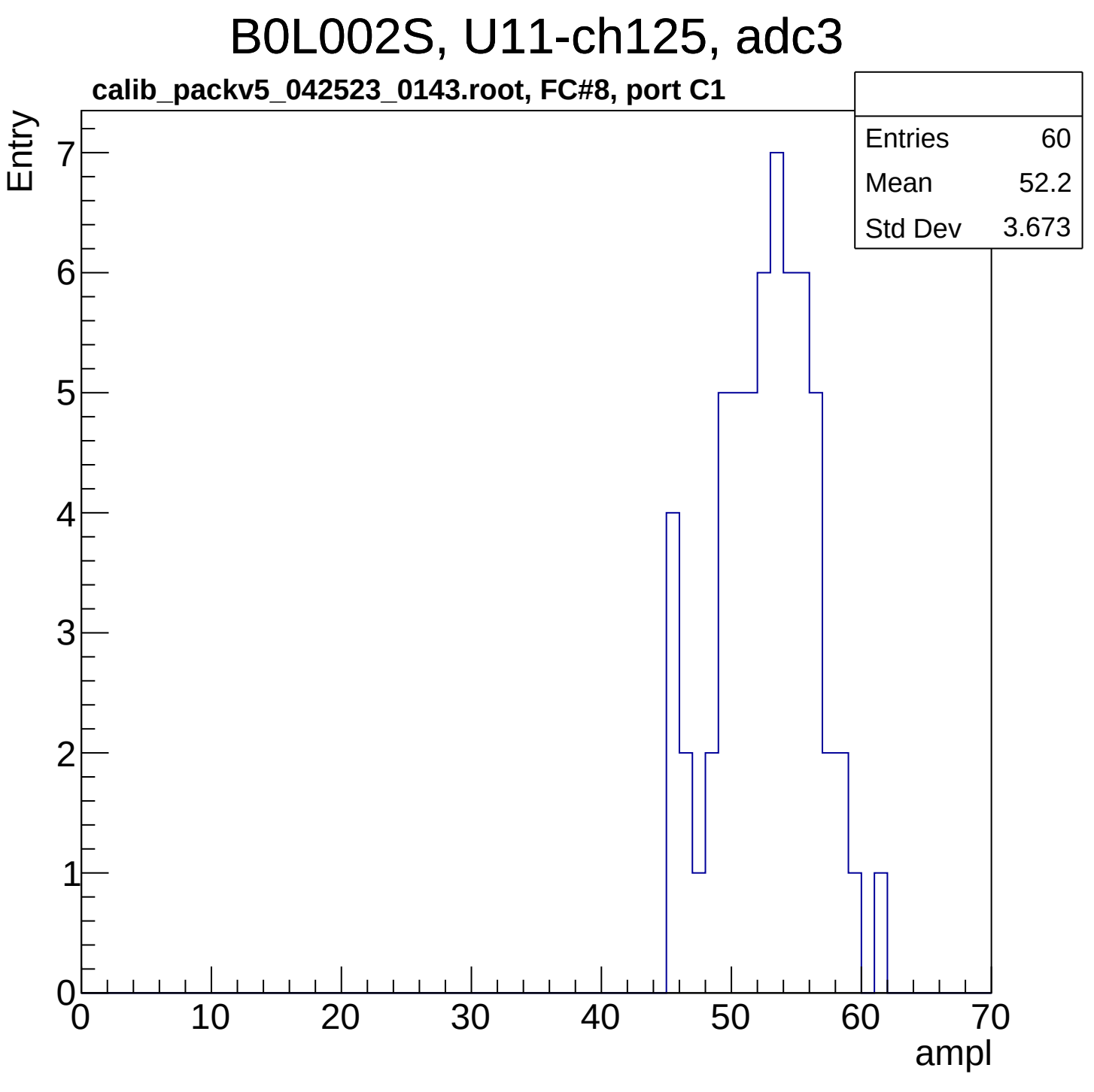
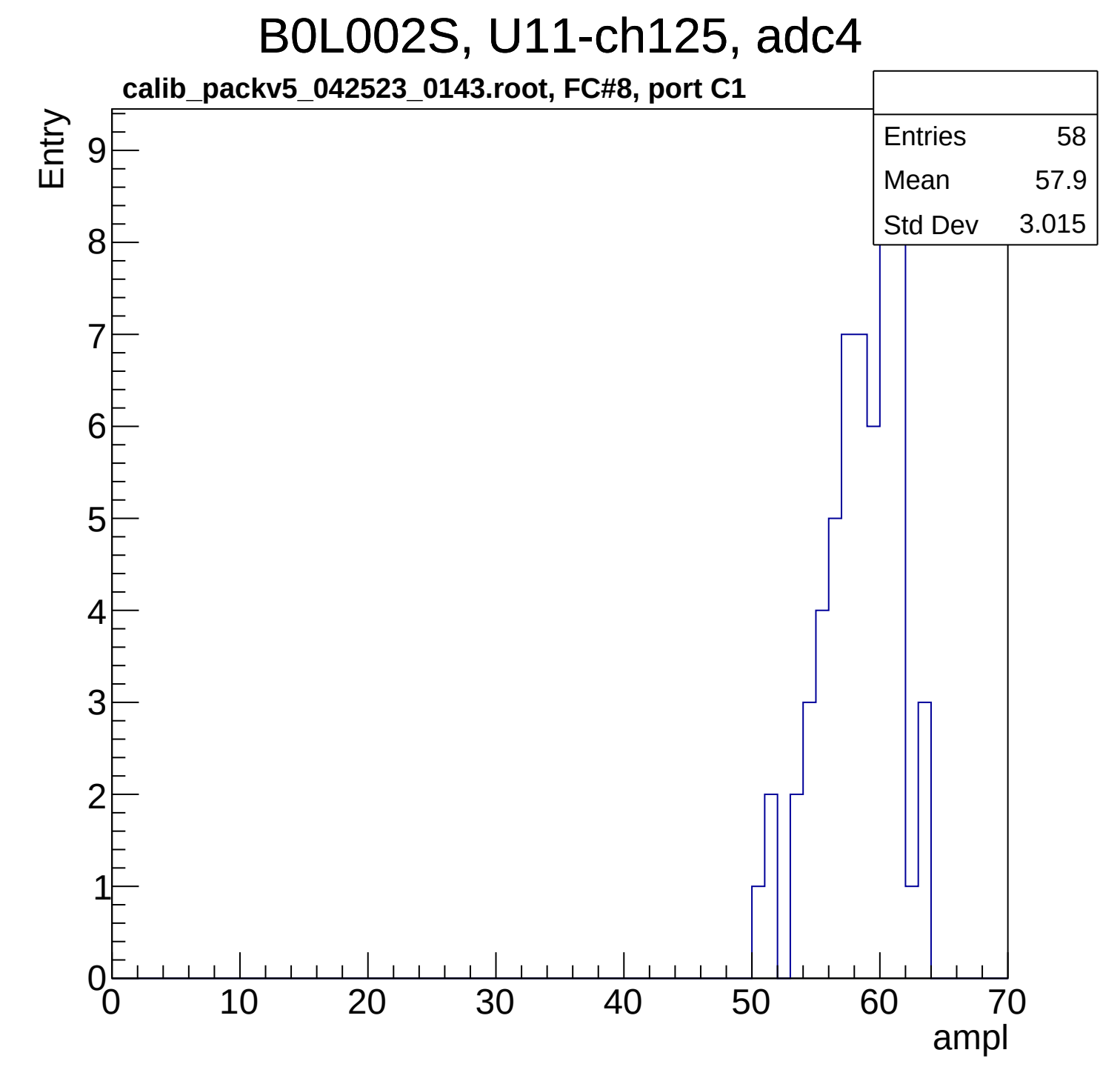
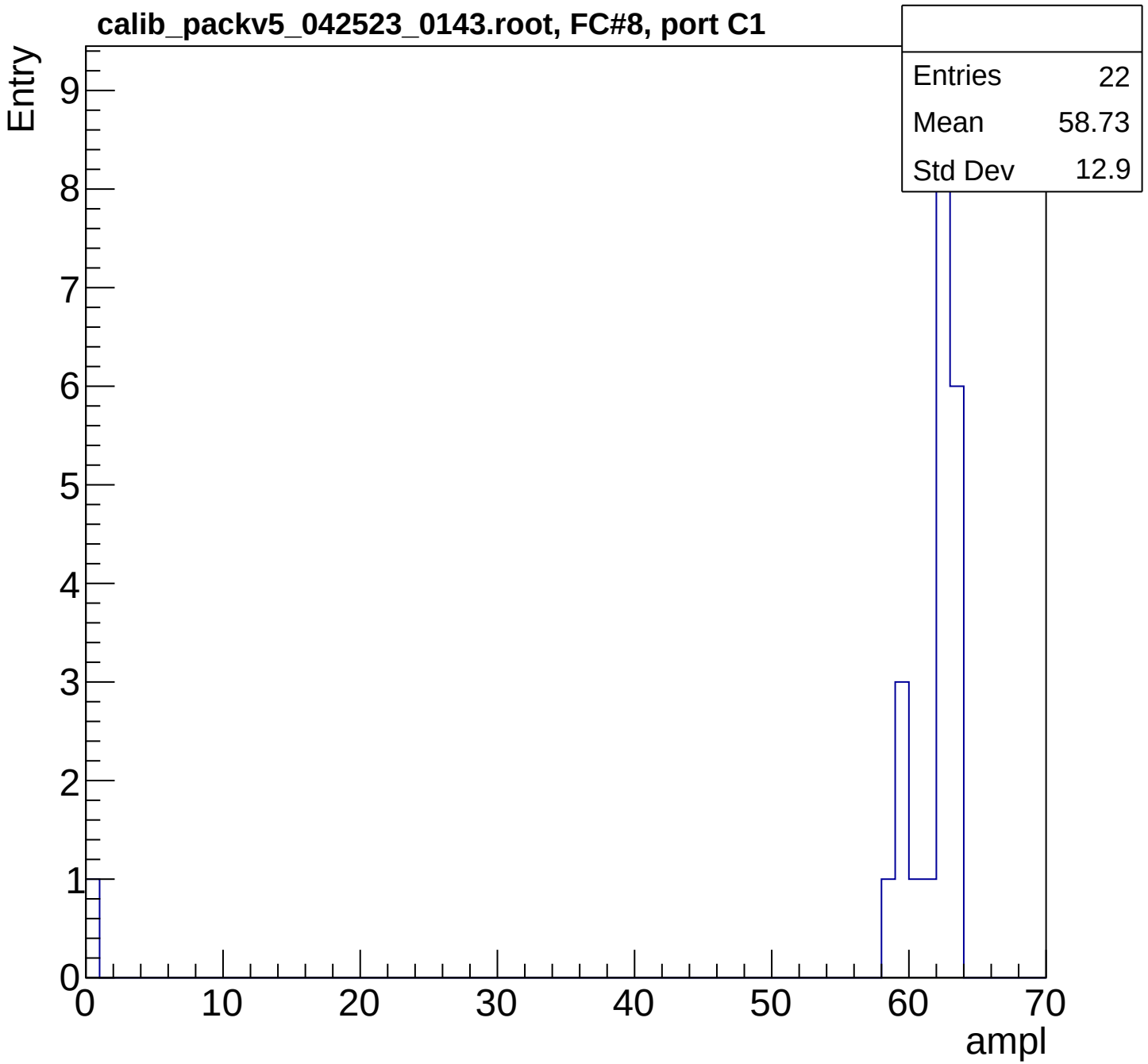
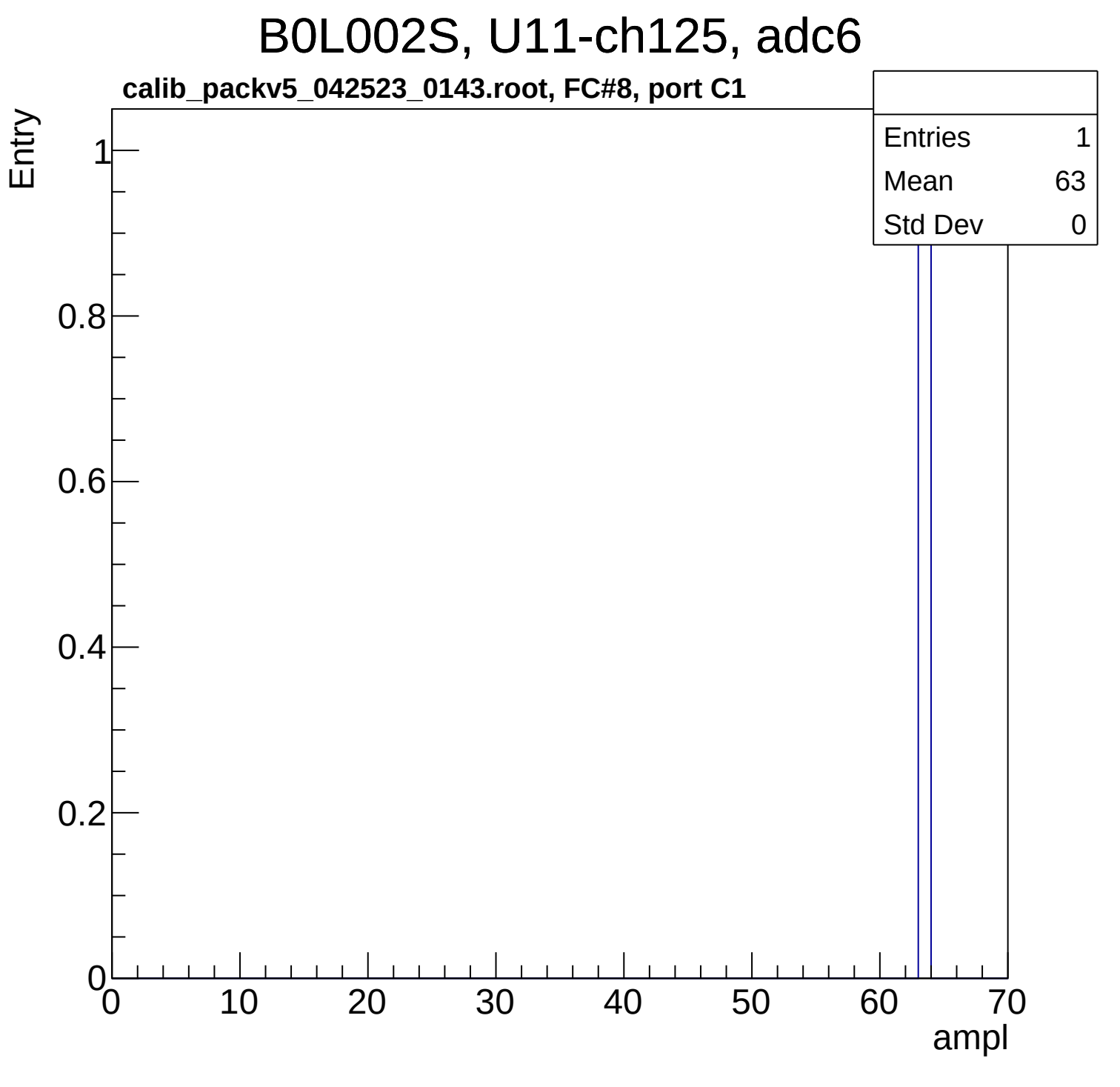

B0L002S, U11-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.2
Std Dev 3.673
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U11-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.9
Std Dev 3.015
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 58.73
Std Dev 12.9
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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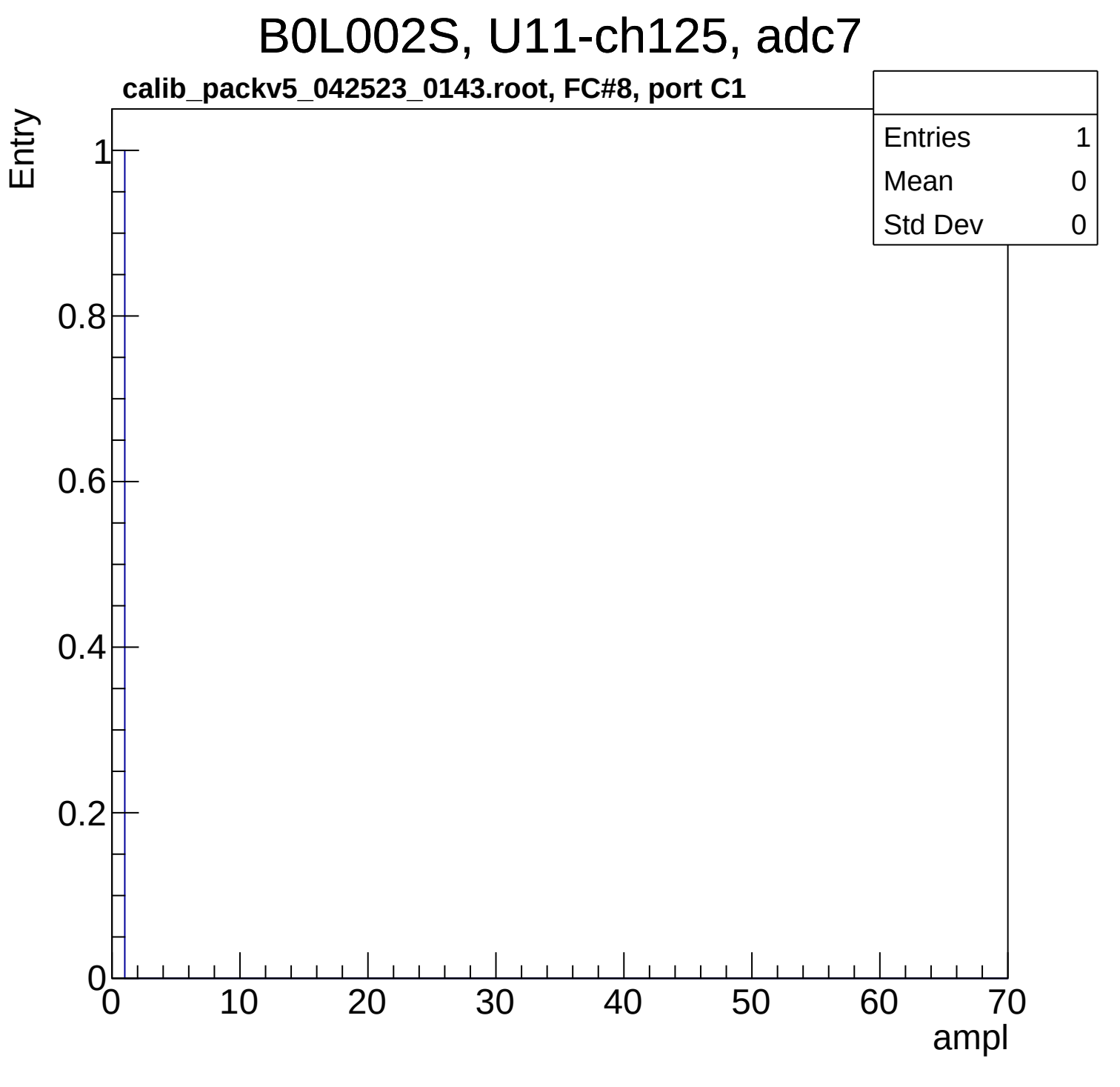
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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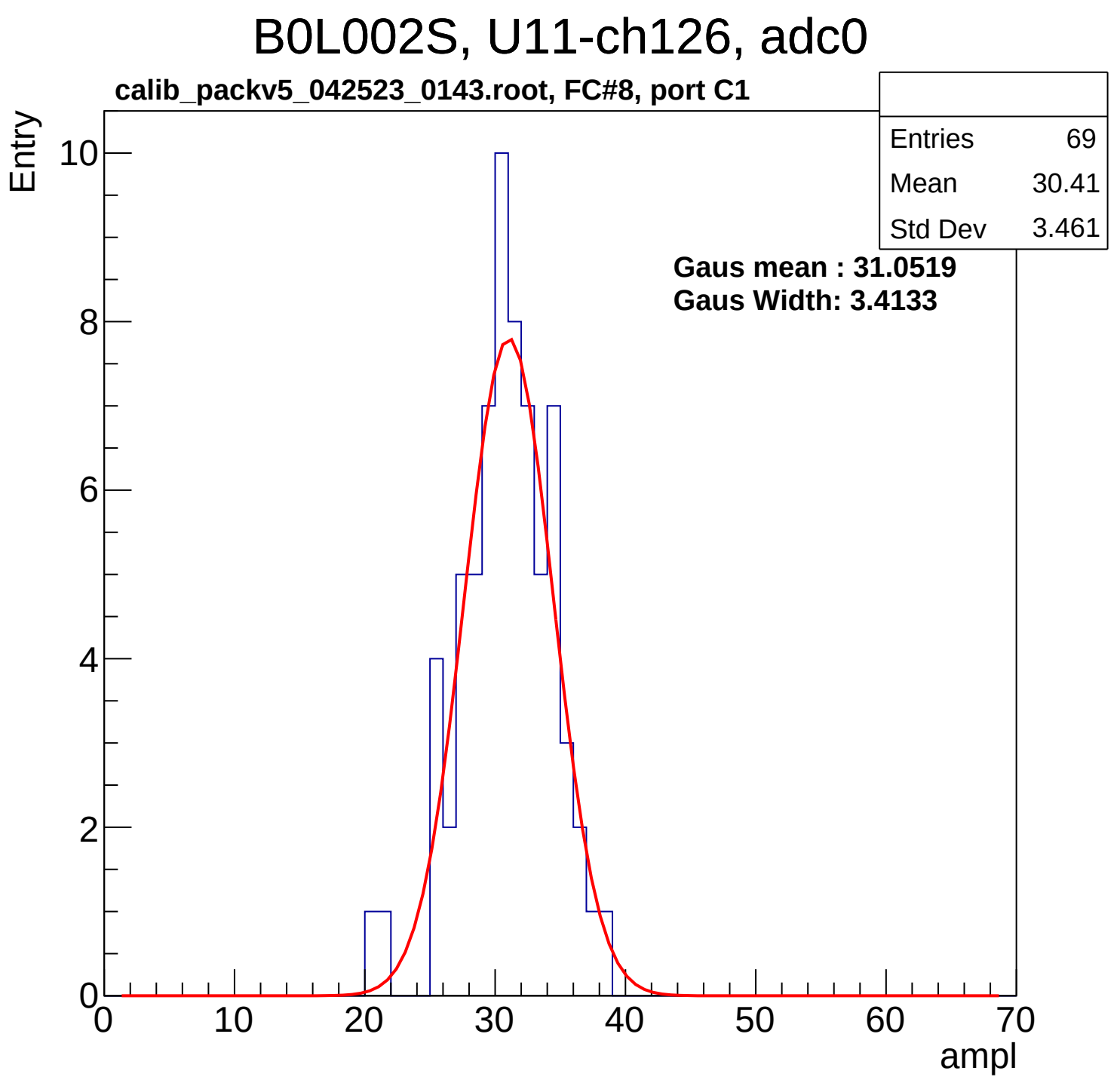
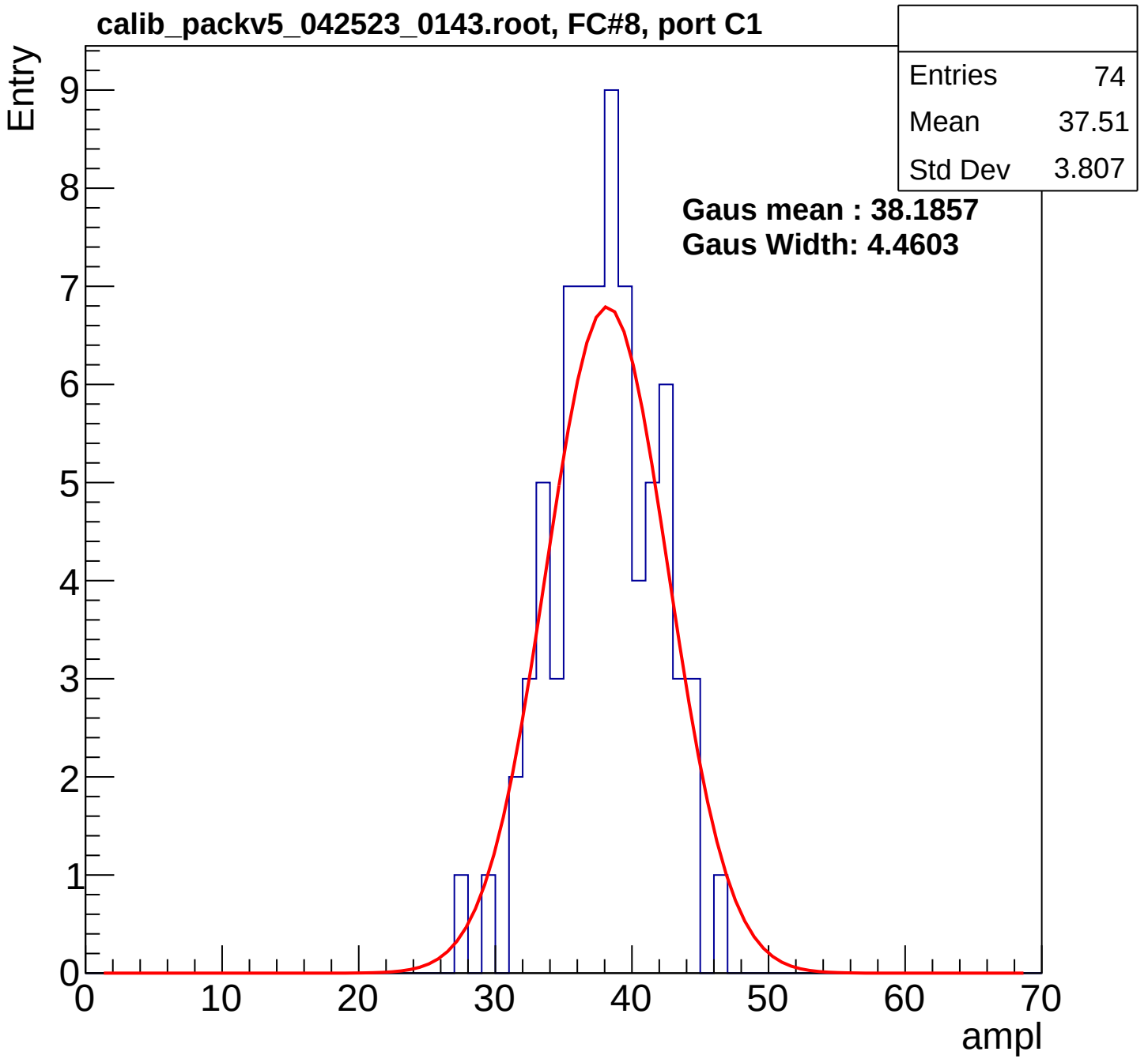
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.41
Std Dev 3.461
Gaus mean : 31.0519
Gaus Width: 3.4133
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U11-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.51
Std Dev 3.807
Gaus mean : 38.1857
Gaus Width: 4.4603
Entry
ampl
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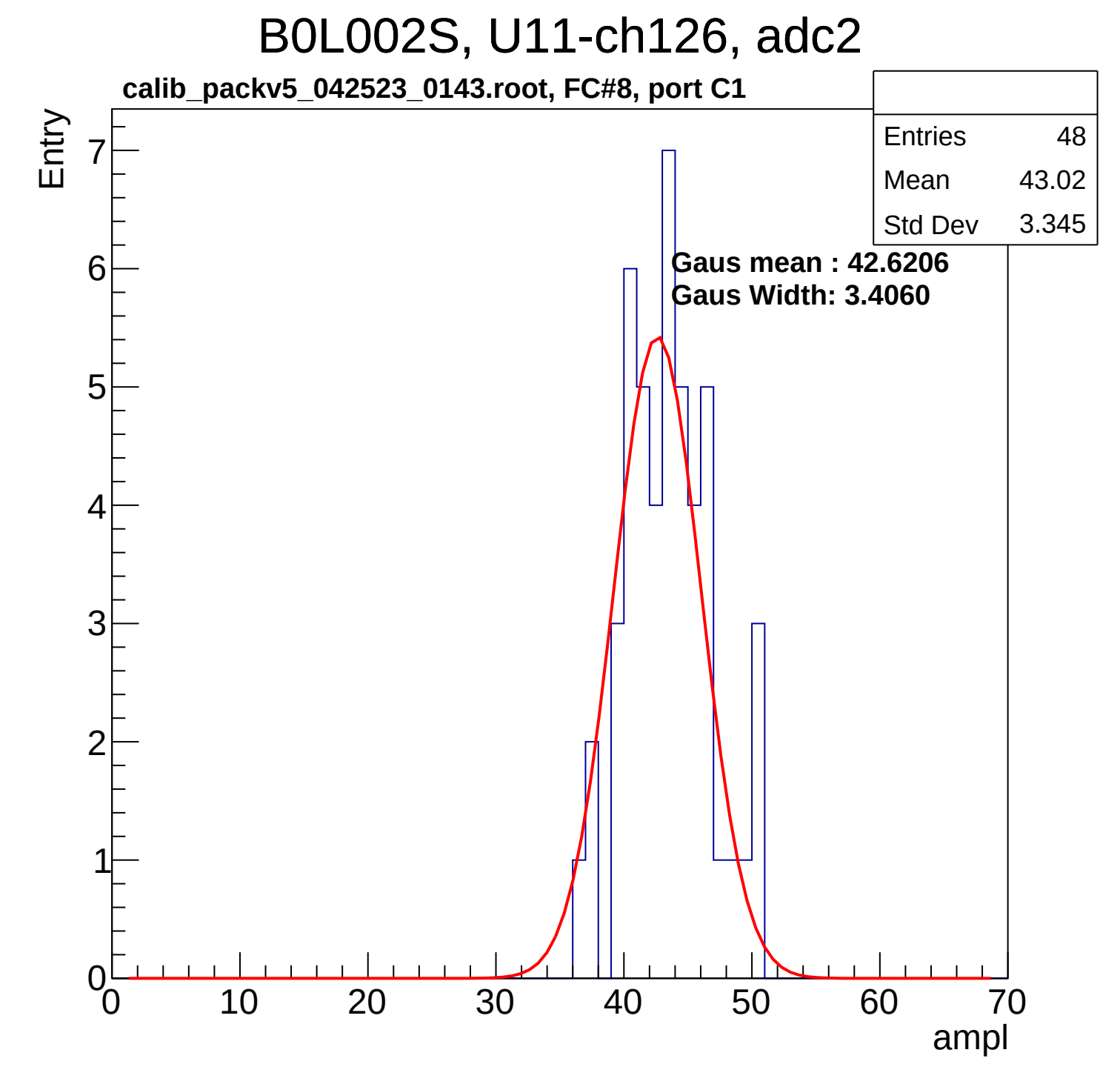
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U11-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 43.02
Std Dev 3.345
Gaus mean : 42.6206
Gaus Width: 3.4060
Entry
ampl
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2
3
4
5
6
7
0
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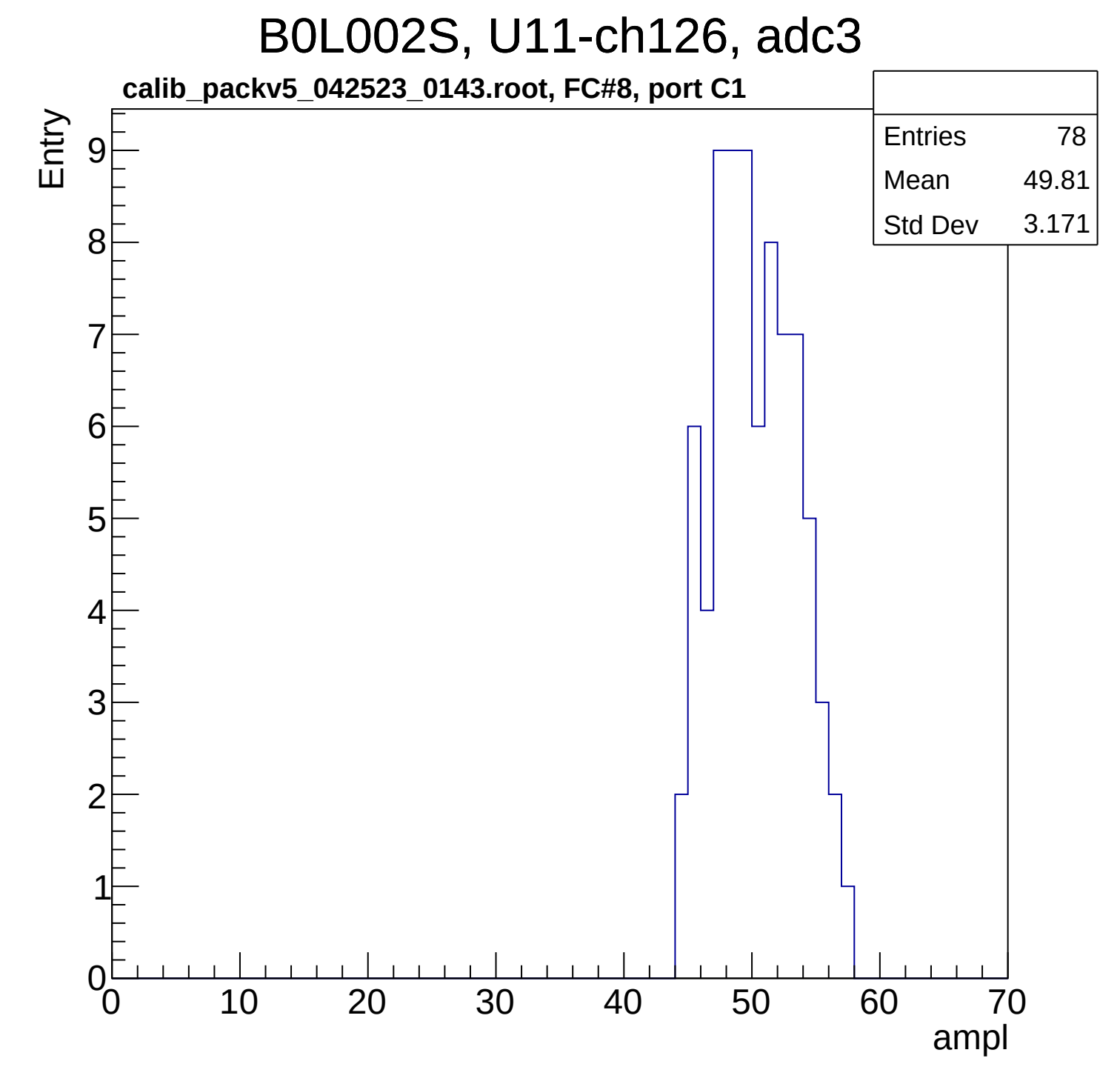
B0L002S, U11-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 49.81
Std Dev 3.171
Entry
ampl
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3
4
5
6
7
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9
0
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60
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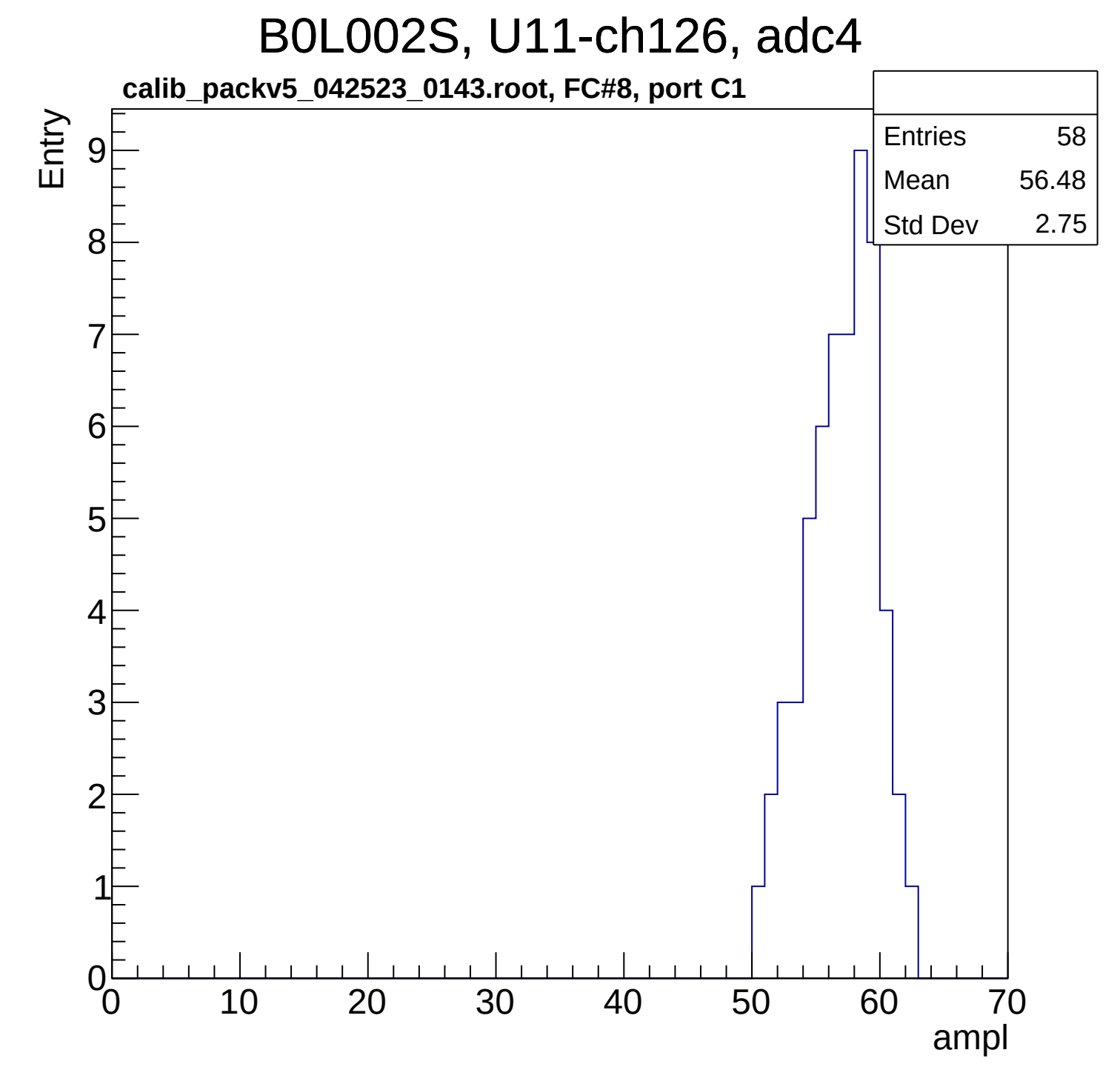
B0L002S, U11-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.48
Std Dev 2.75
Entry
ampl
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2
3
4
5
6
7
8
9
0
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50
60
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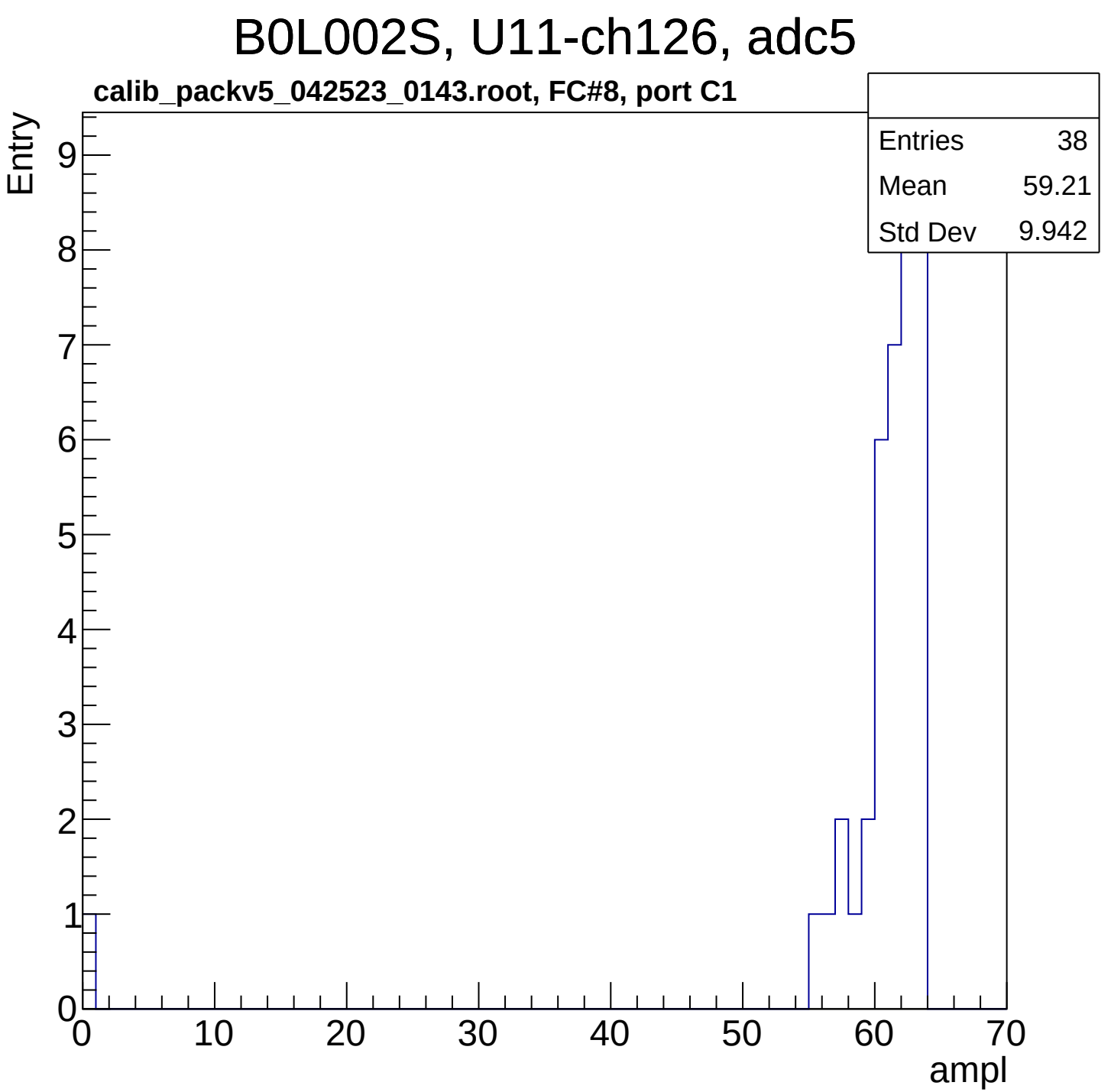
B0L002S, U11-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.21
Std Dev 9.942
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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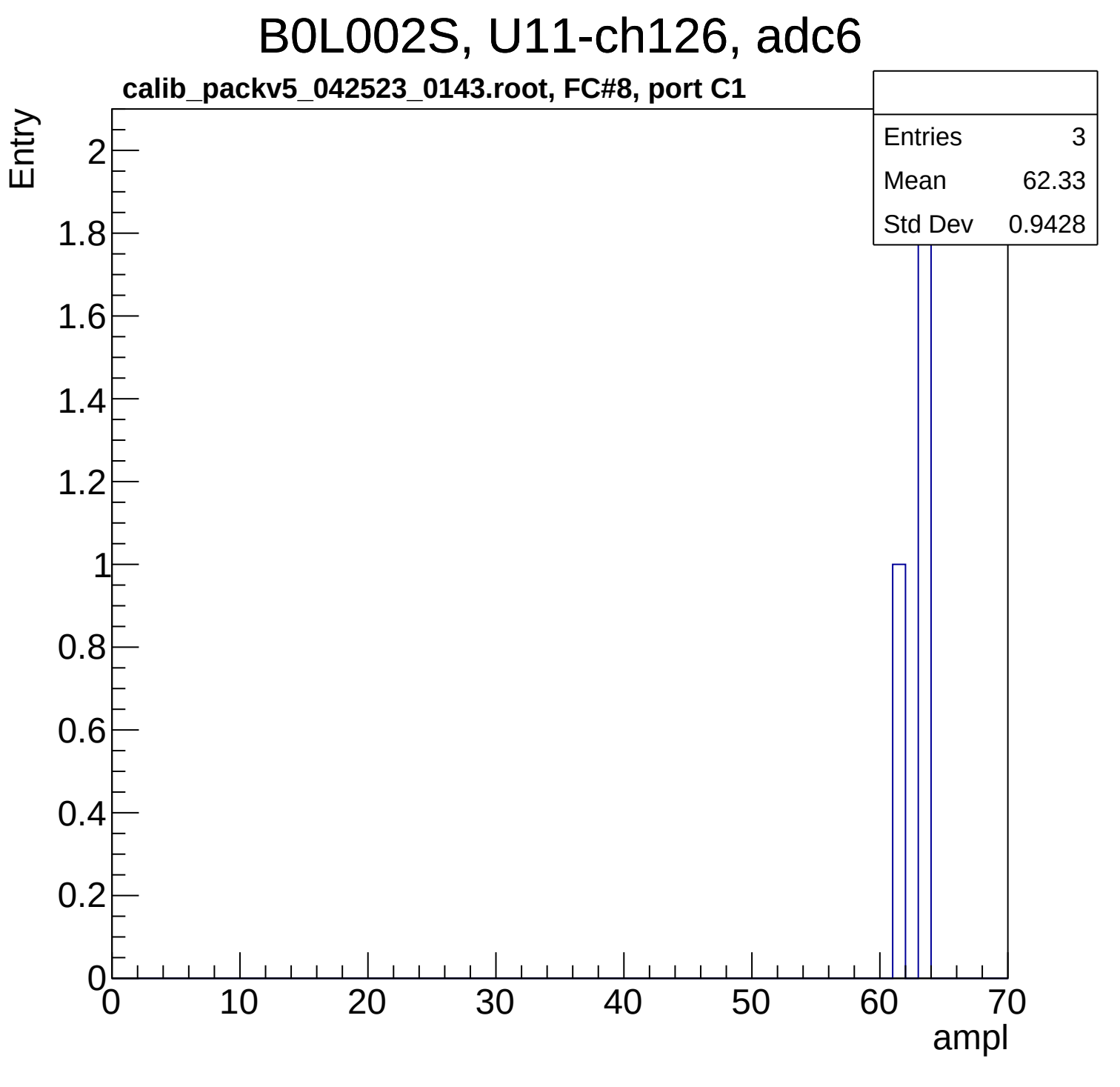
B0L002S, U11-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
50
60
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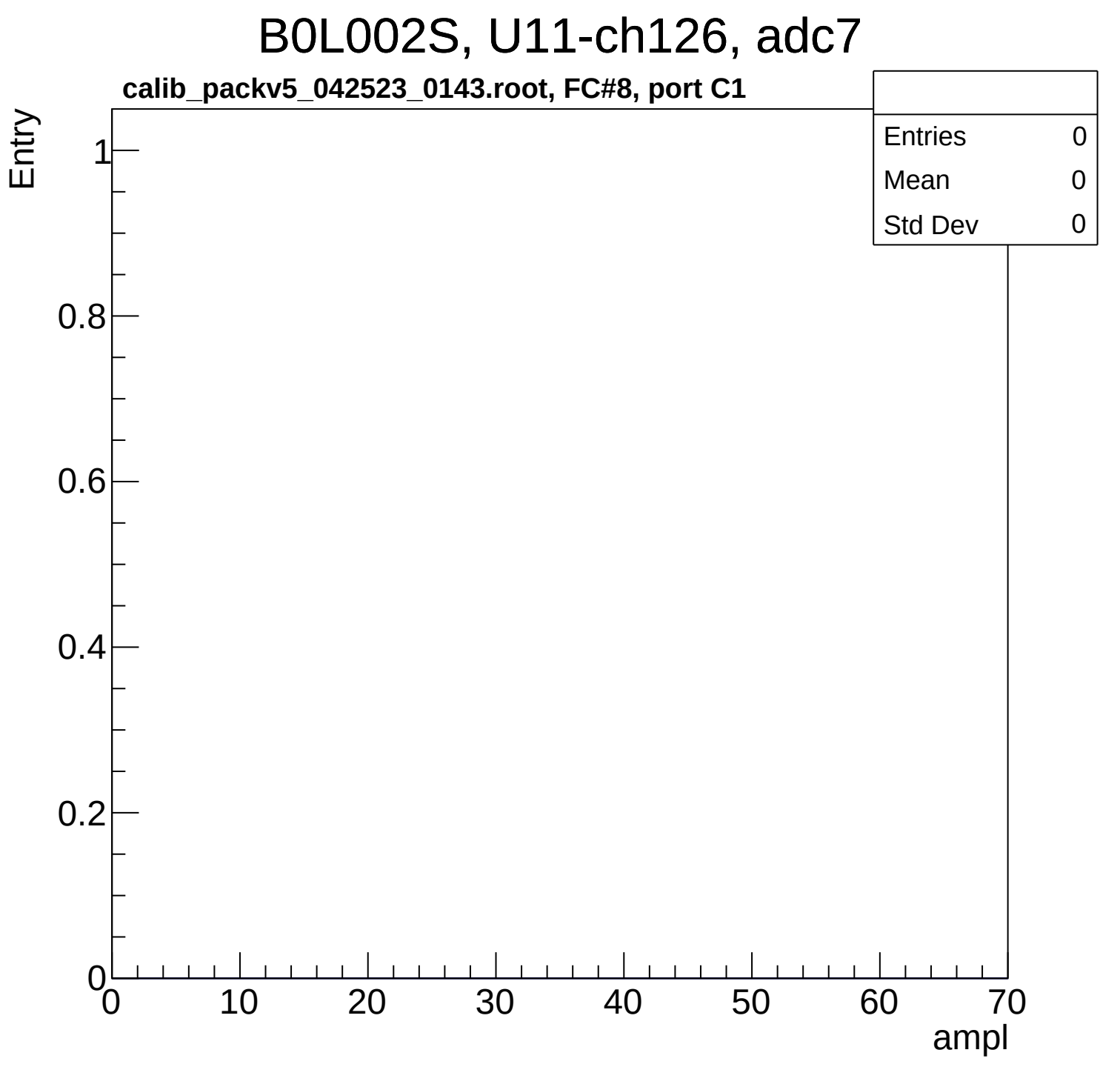
B0L002S, U11-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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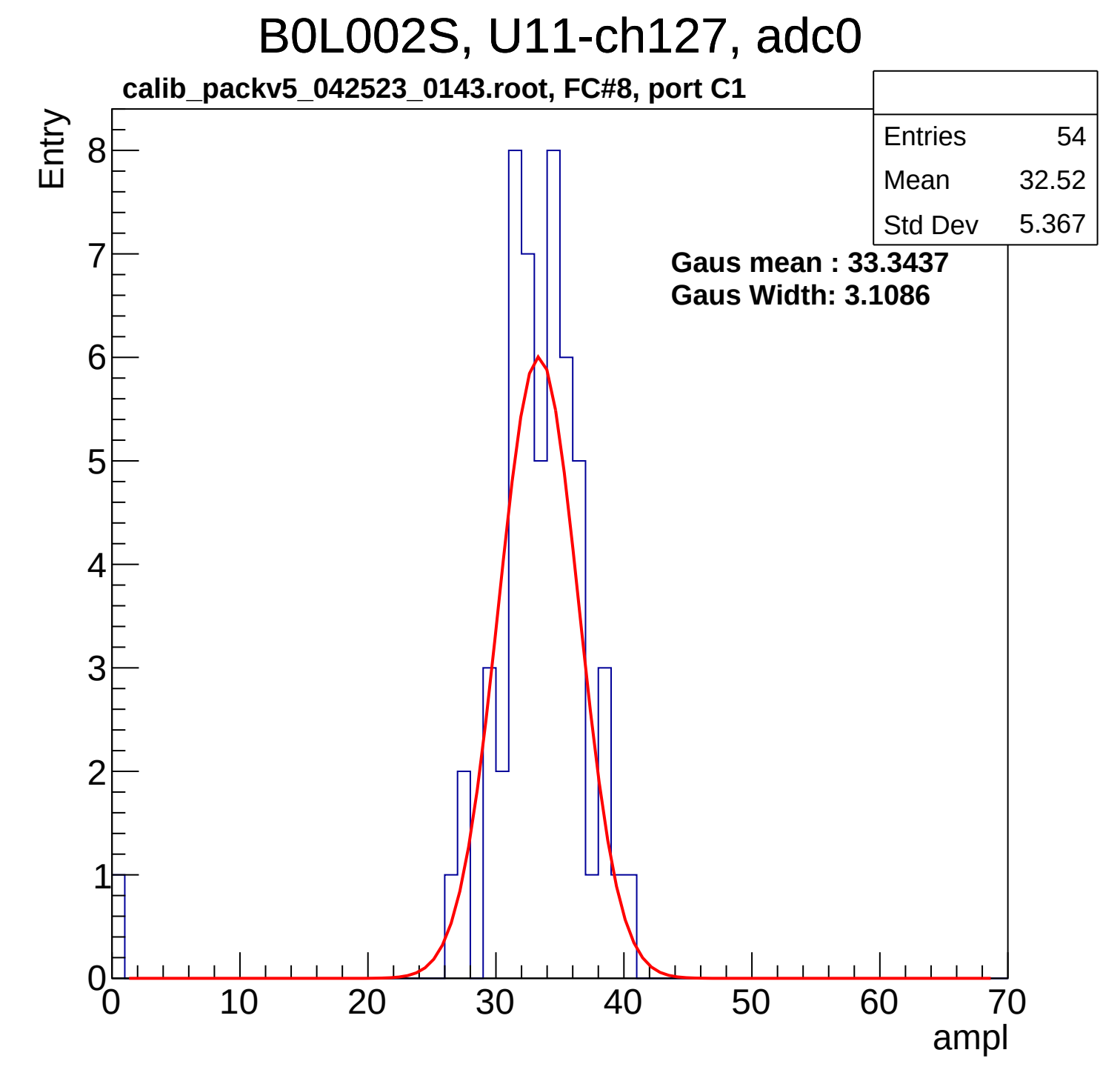
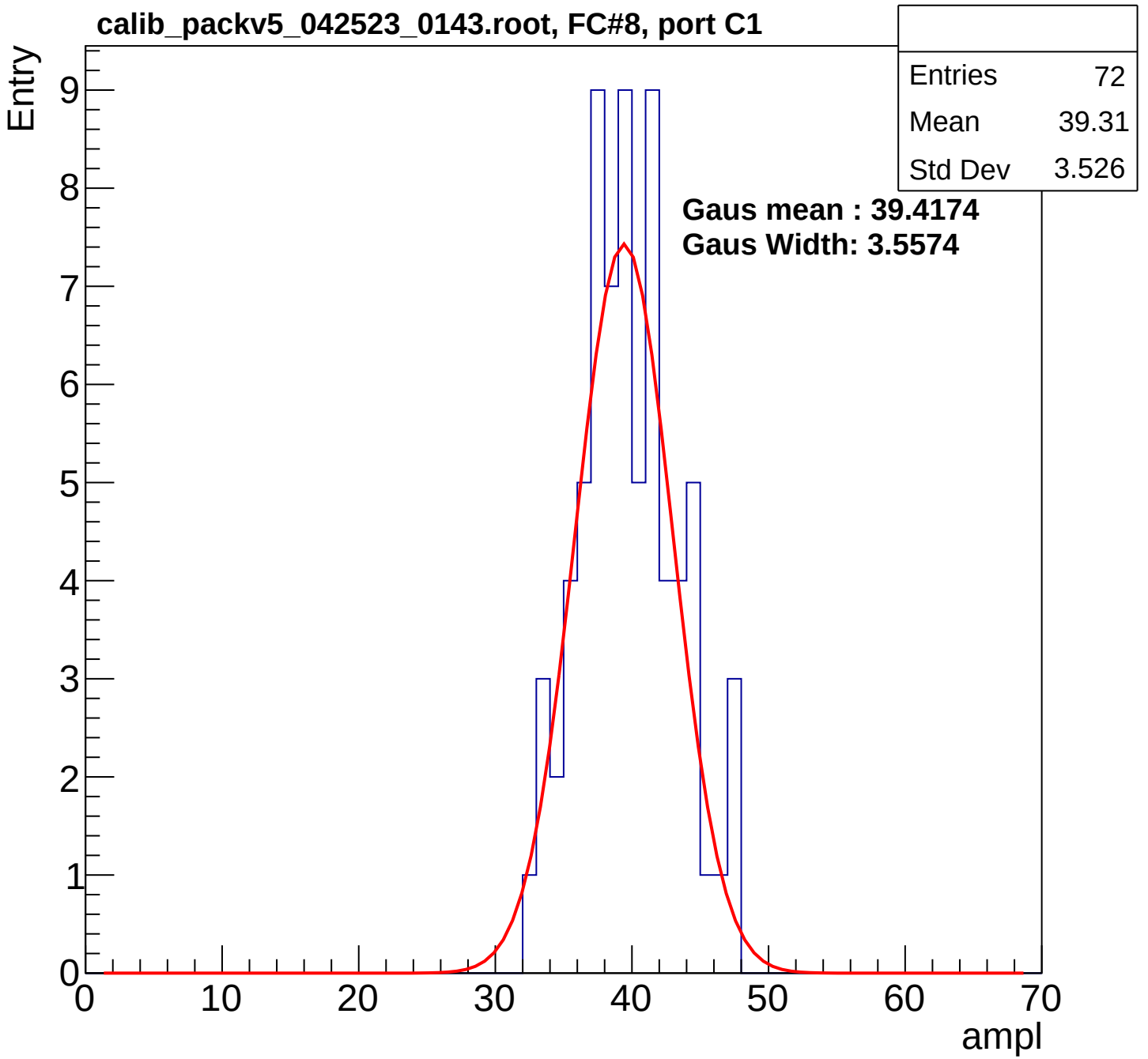
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 32.52
Std Dev 5.367
Gaus mean : 33.3437
Gaus Width: 3.1086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U11-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 39.31
Std Dev 3.526
Gaus mean : 39.4174
Gaus Width: 3.5574
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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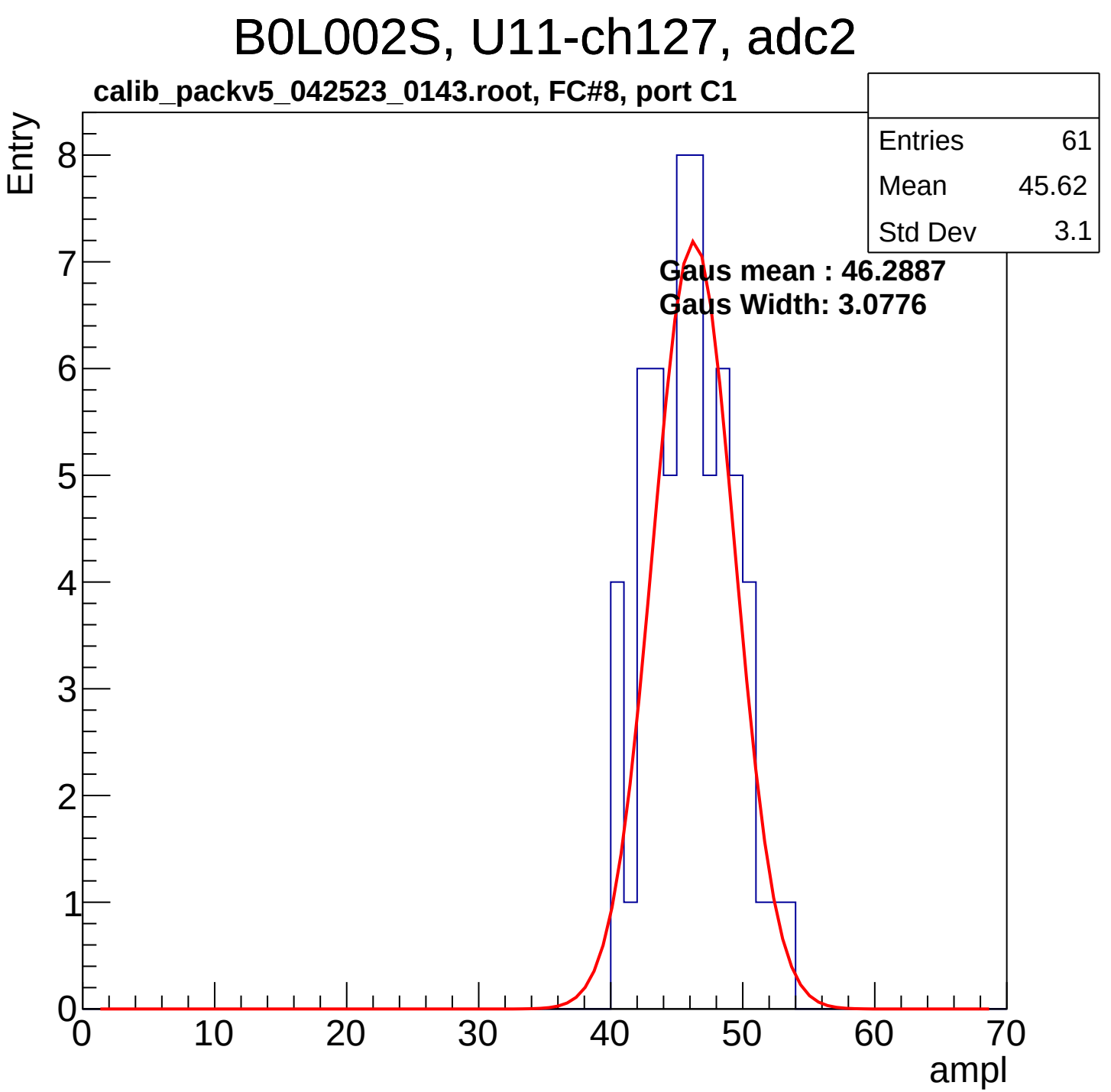
60
70


B0L002S, U11-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 45.62
Std Dev 3.1
Gaus mean : 46.2887
Gaus Width: 3.0776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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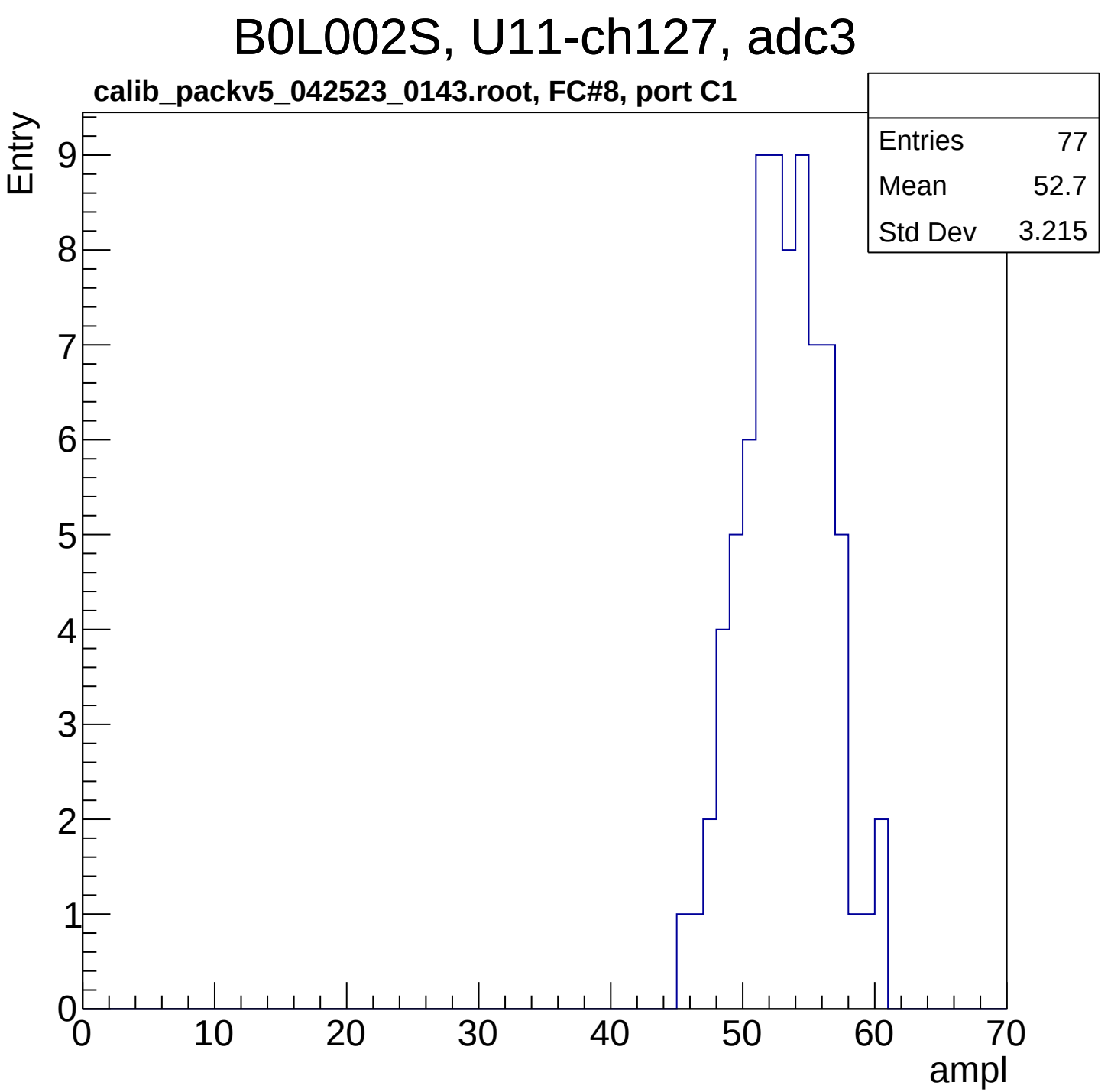
B0L002S, U11-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 52.7
Std Dev 3.215
Entry
ampl
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3
4
5
6
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0
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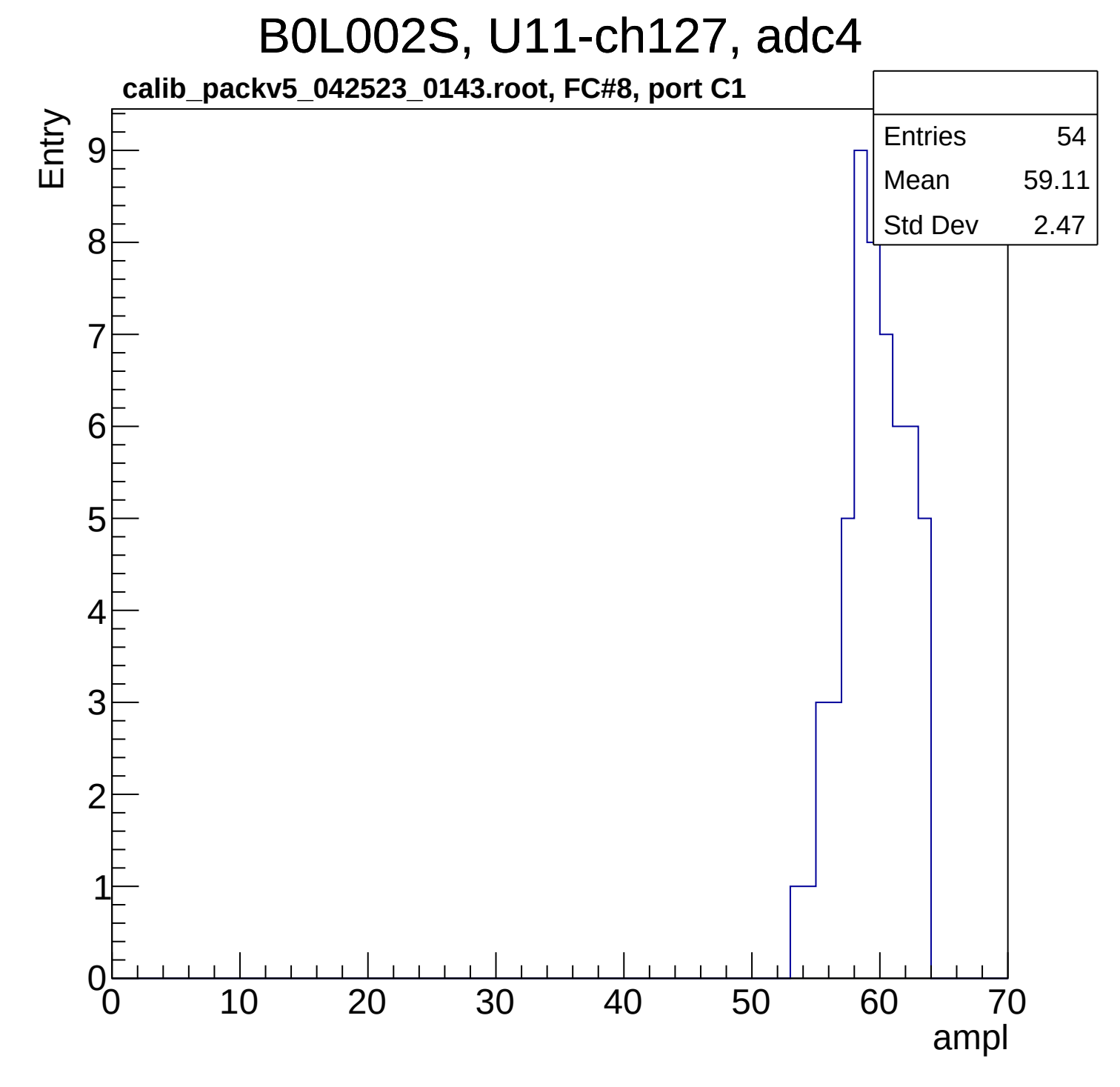
B0L002S, U11-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 59.11
Std Dev 2.47
Entry
ampl
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3
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6
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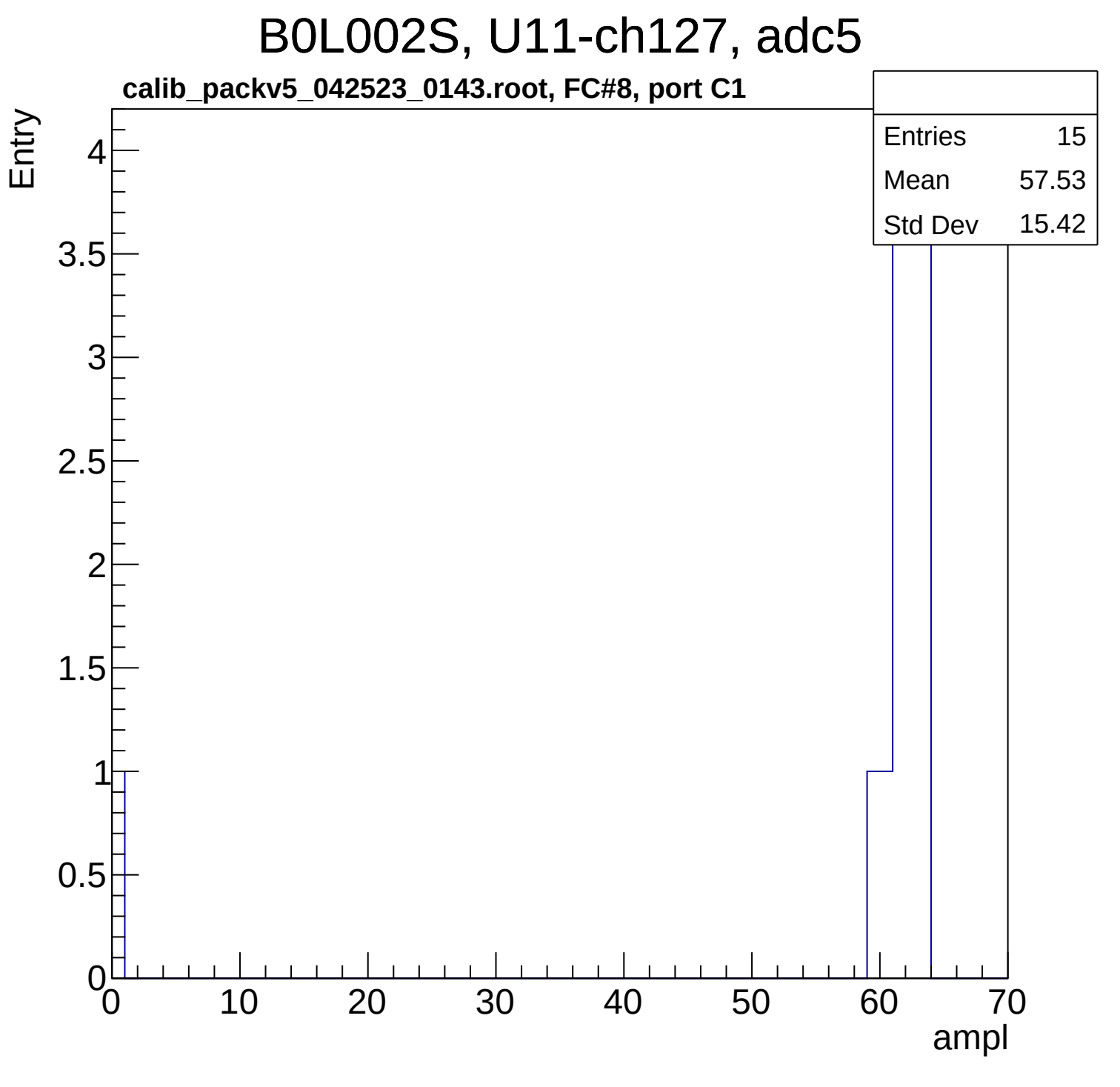
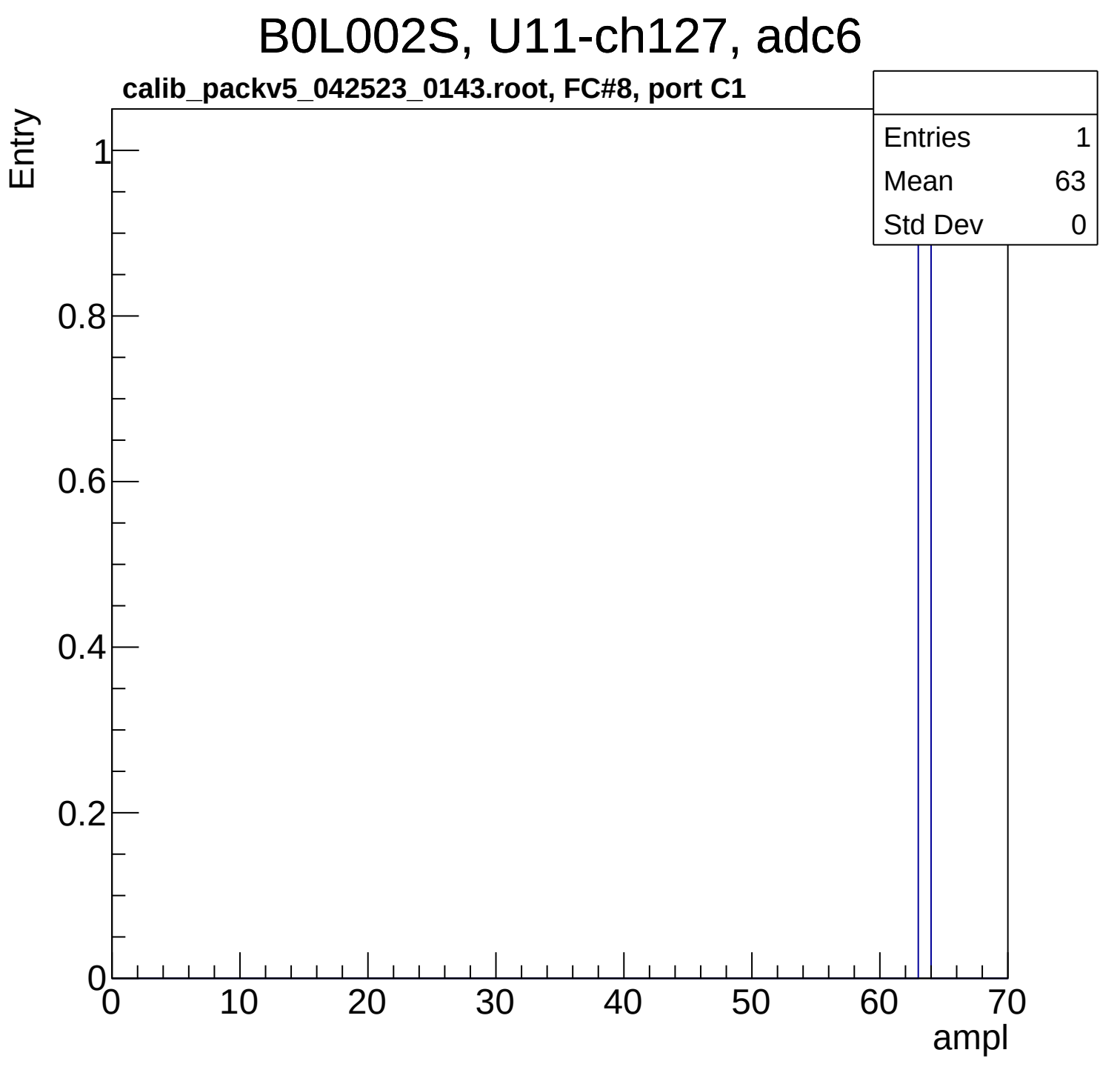
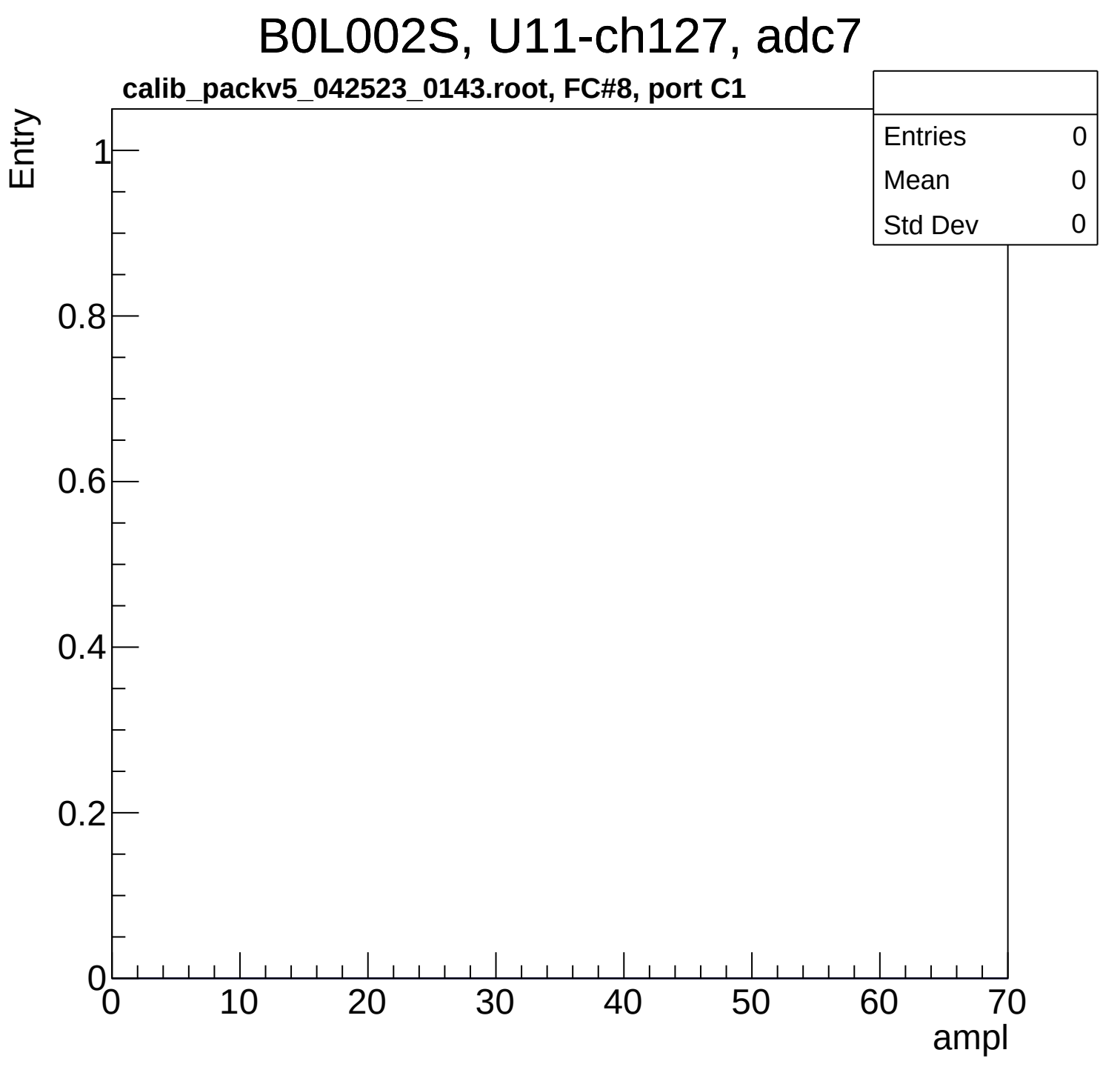
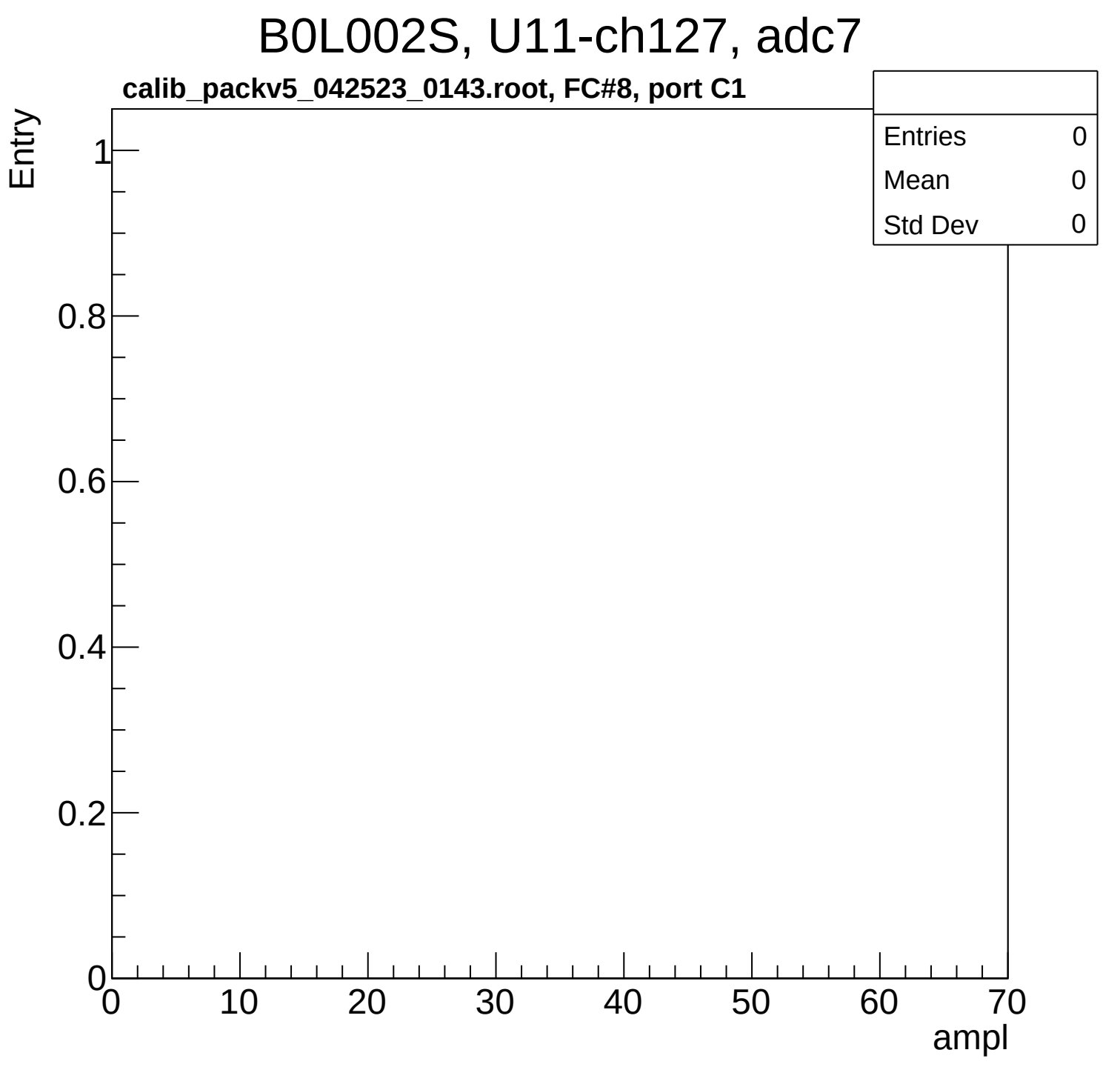

B0L002S, U11-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 57.53
Std Dev 15.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U11-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U11-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl