

B0L001S, U18-ch0, adc0

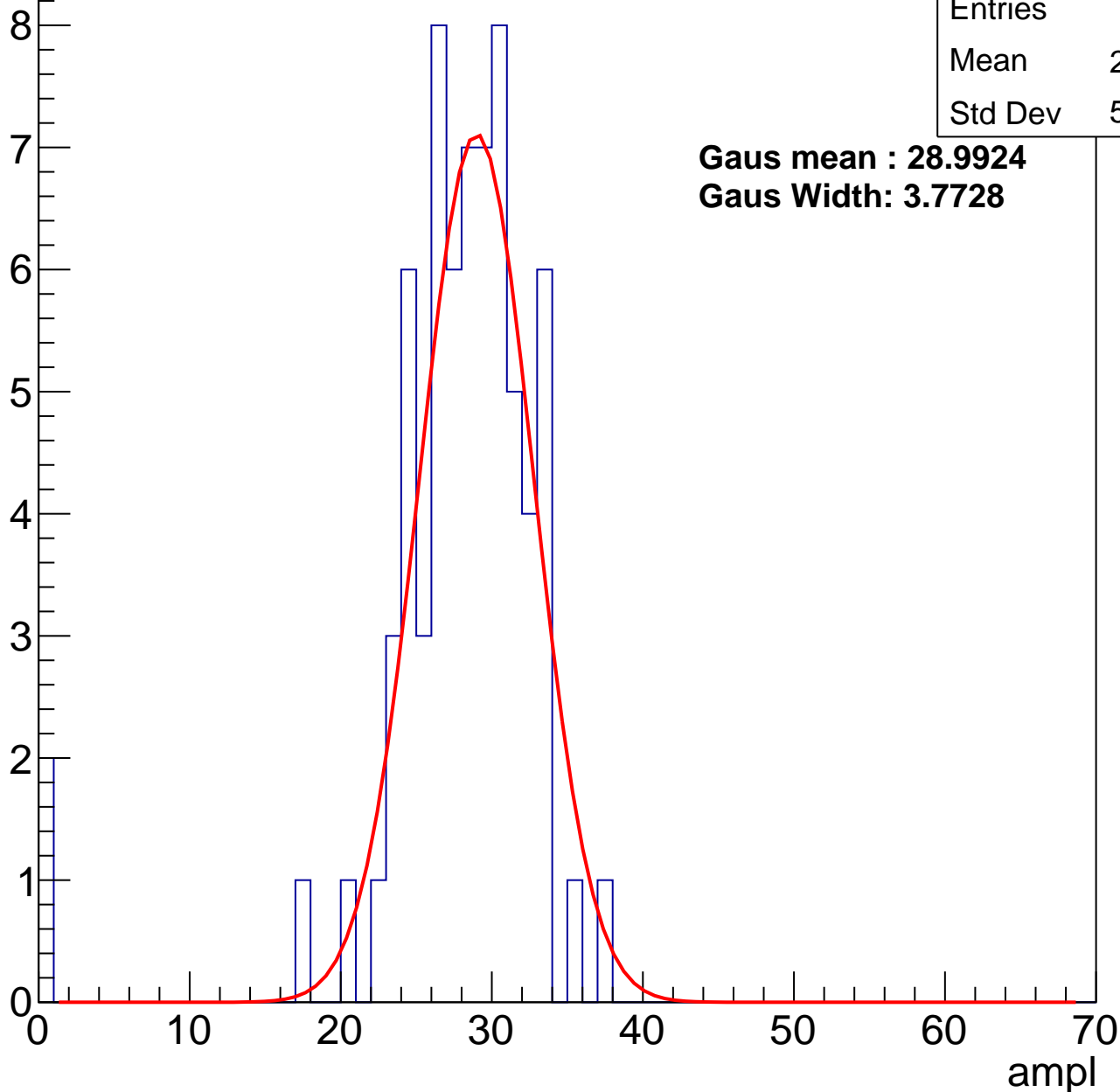
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	27.27
Std Dev	5.875

Gaus mean : 28.9924

Gaus Width: 3.7728



B0L001S, U18-ch0, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	35.23
Std Dev	3.408

Gaus mean : 36.4009

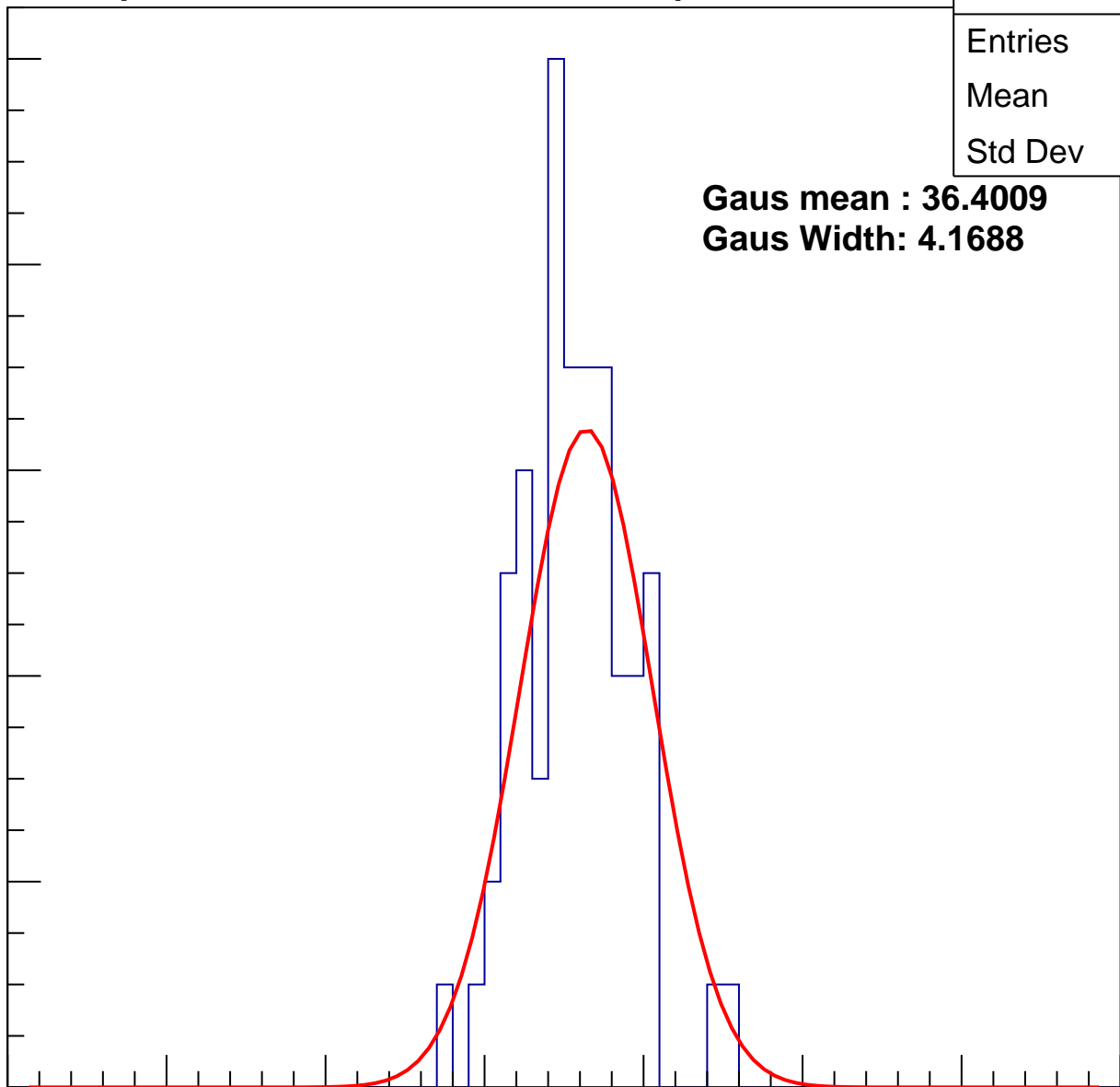
Gaus Width: 4.1688

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch0, adc2

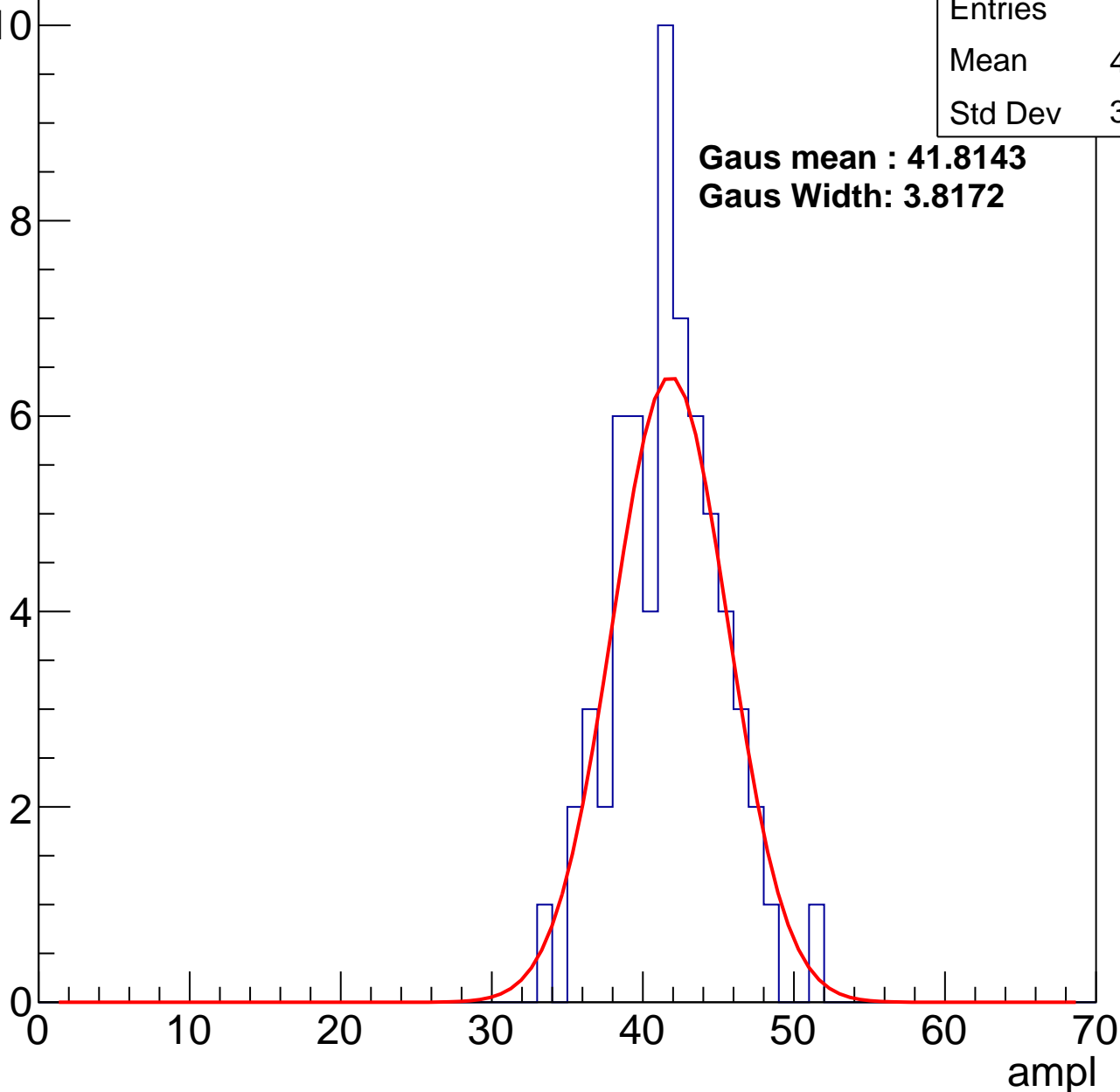
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	41.27
Std Dev	3.465

Gaus mean : 41.8143

Gaus Width: 3.8172

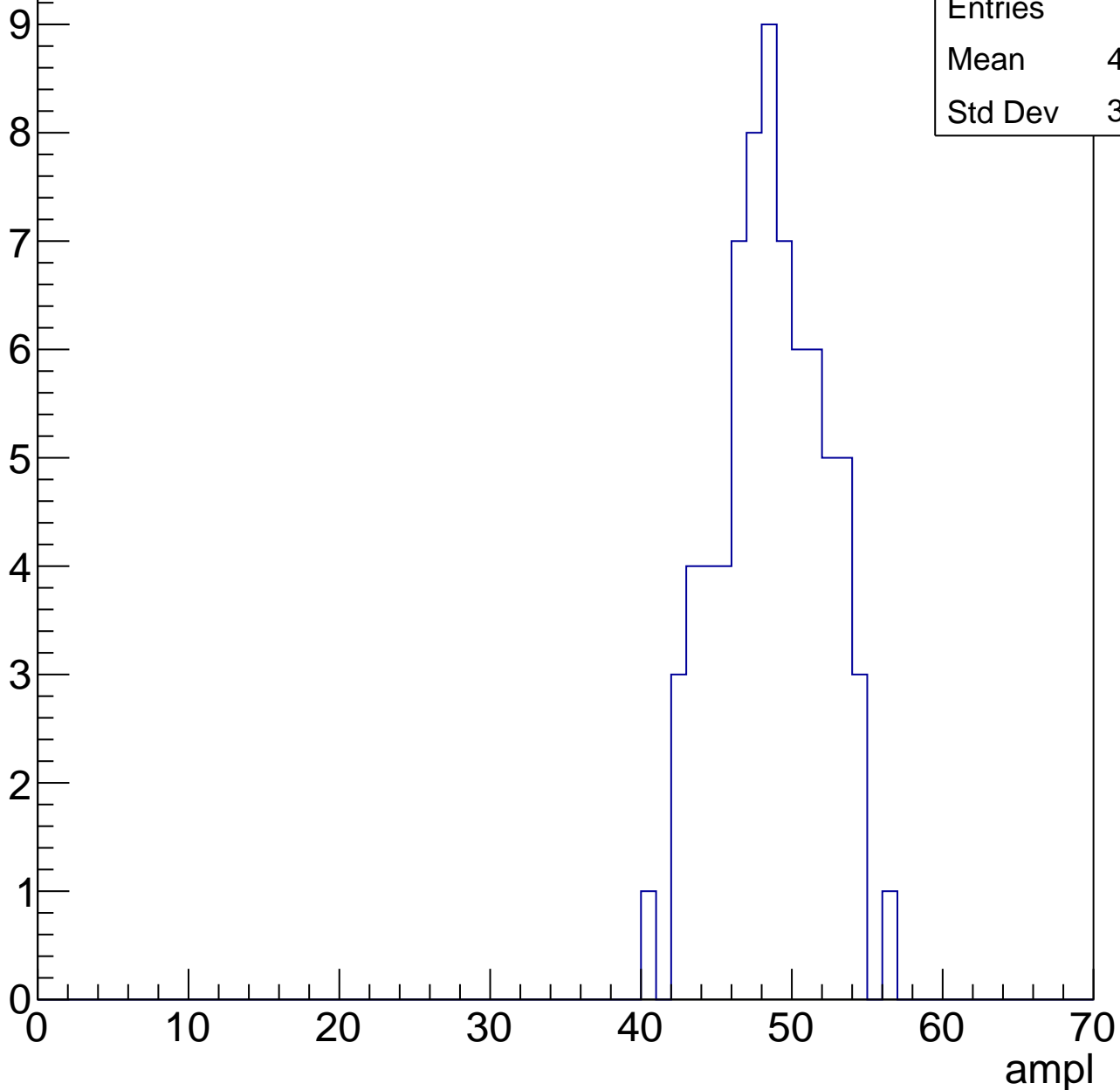


B0L001S, U18-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	48.16
Std Dev	3.448

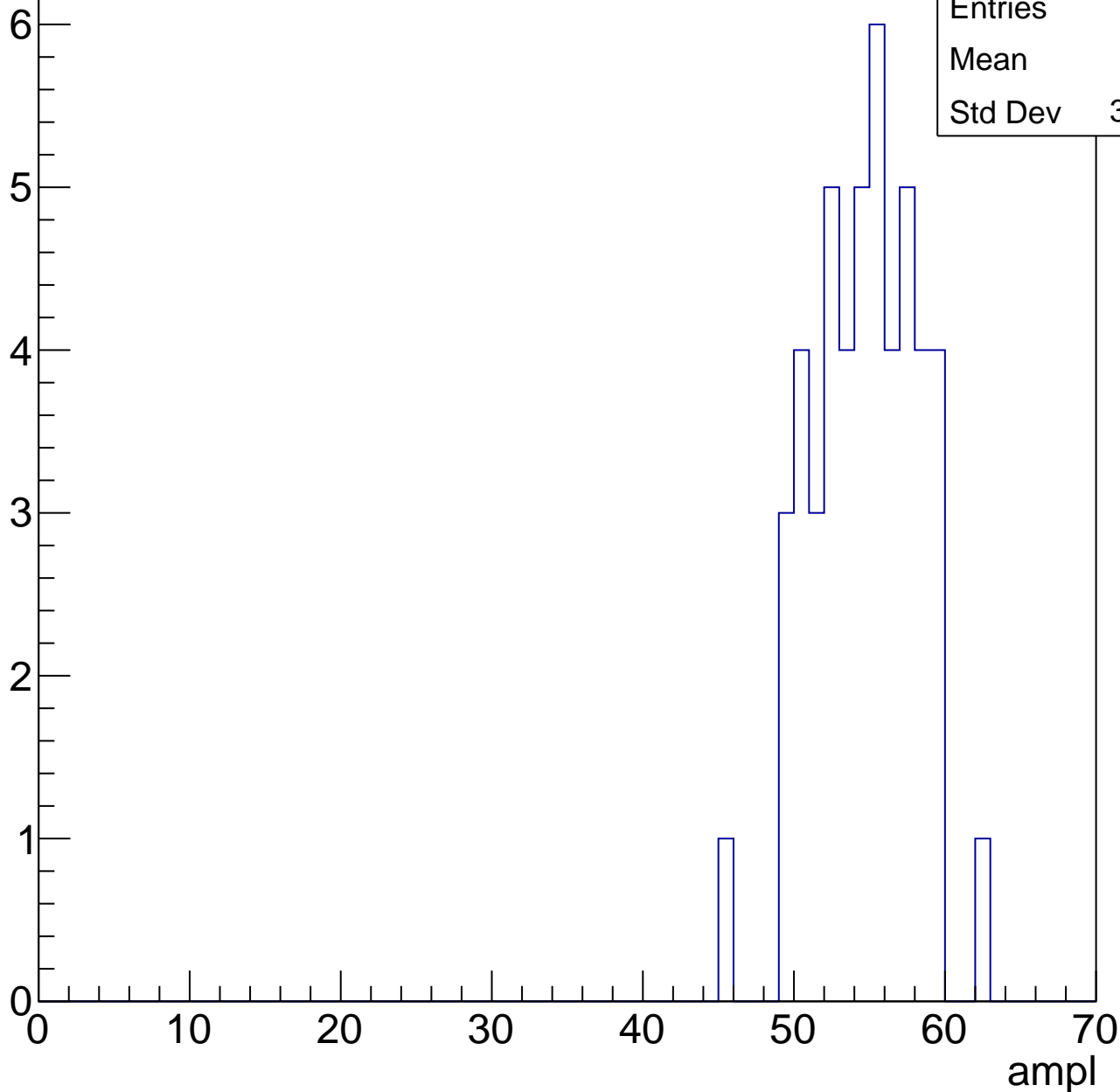


B0L001S, U18-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	54.2
Std Dev	3.393

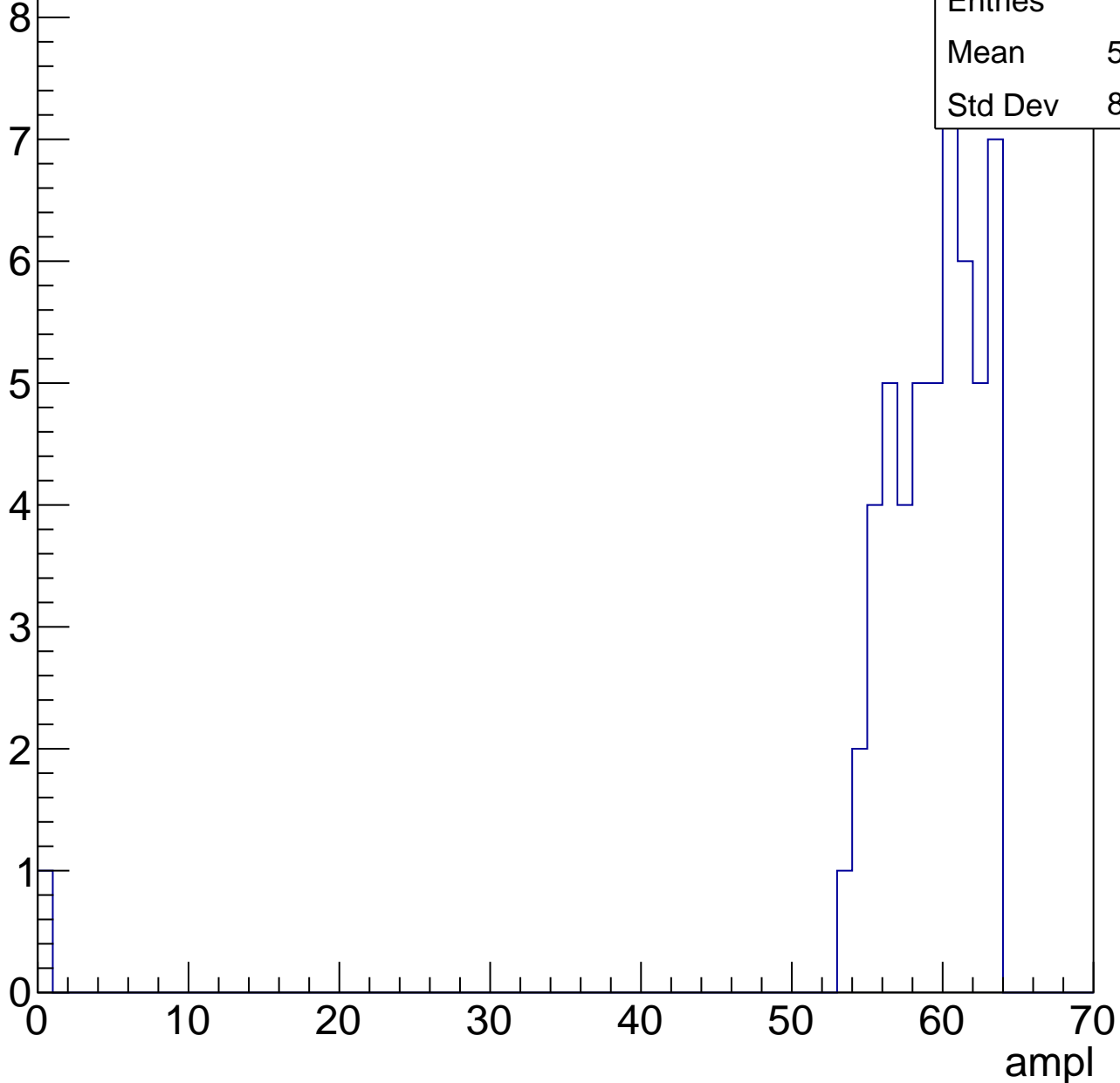


B0L001S, U18-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

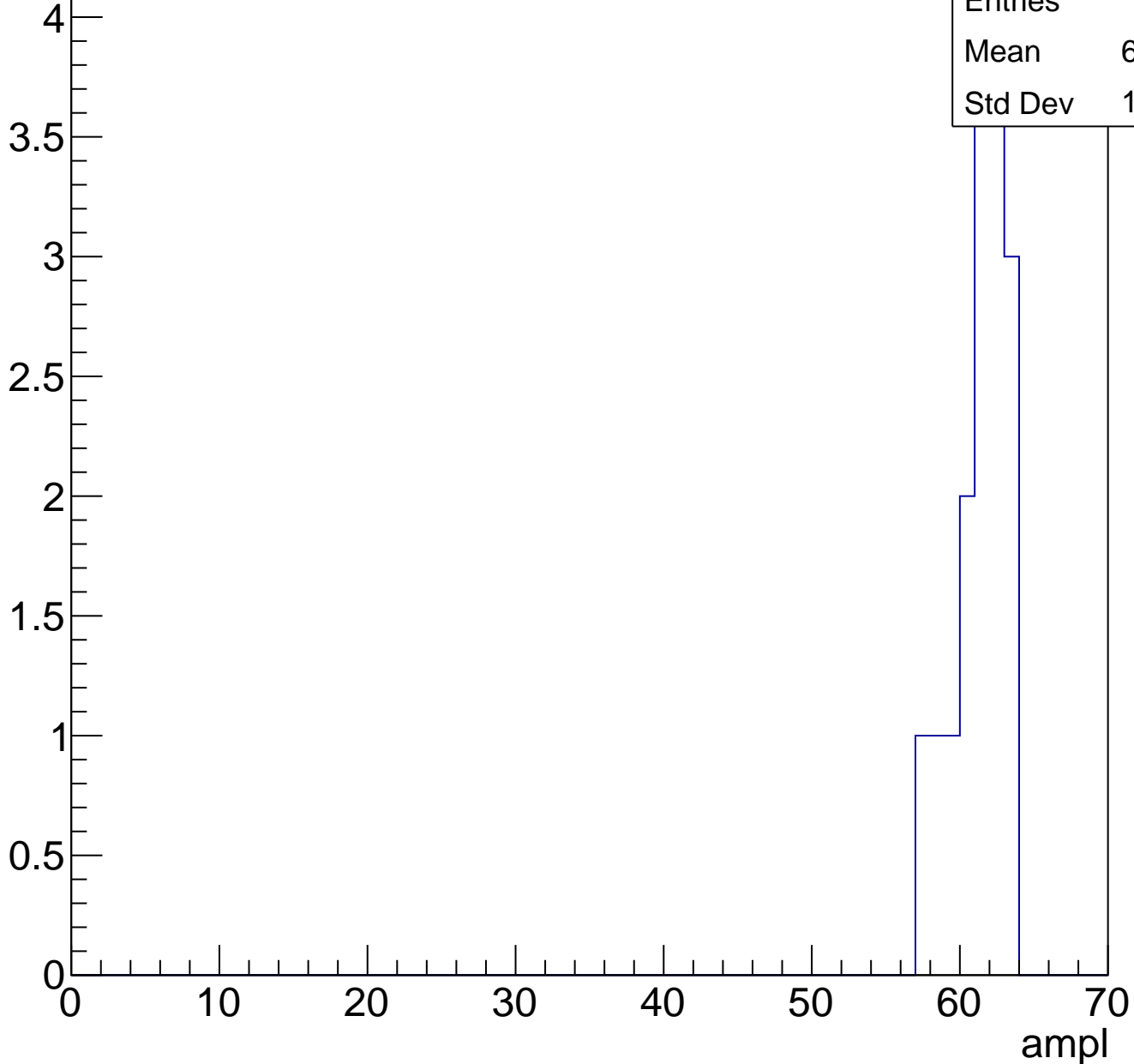
Entries	53
Mean	57.94
Std Dev	8.497



B0L001S, U18-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch1, adc0

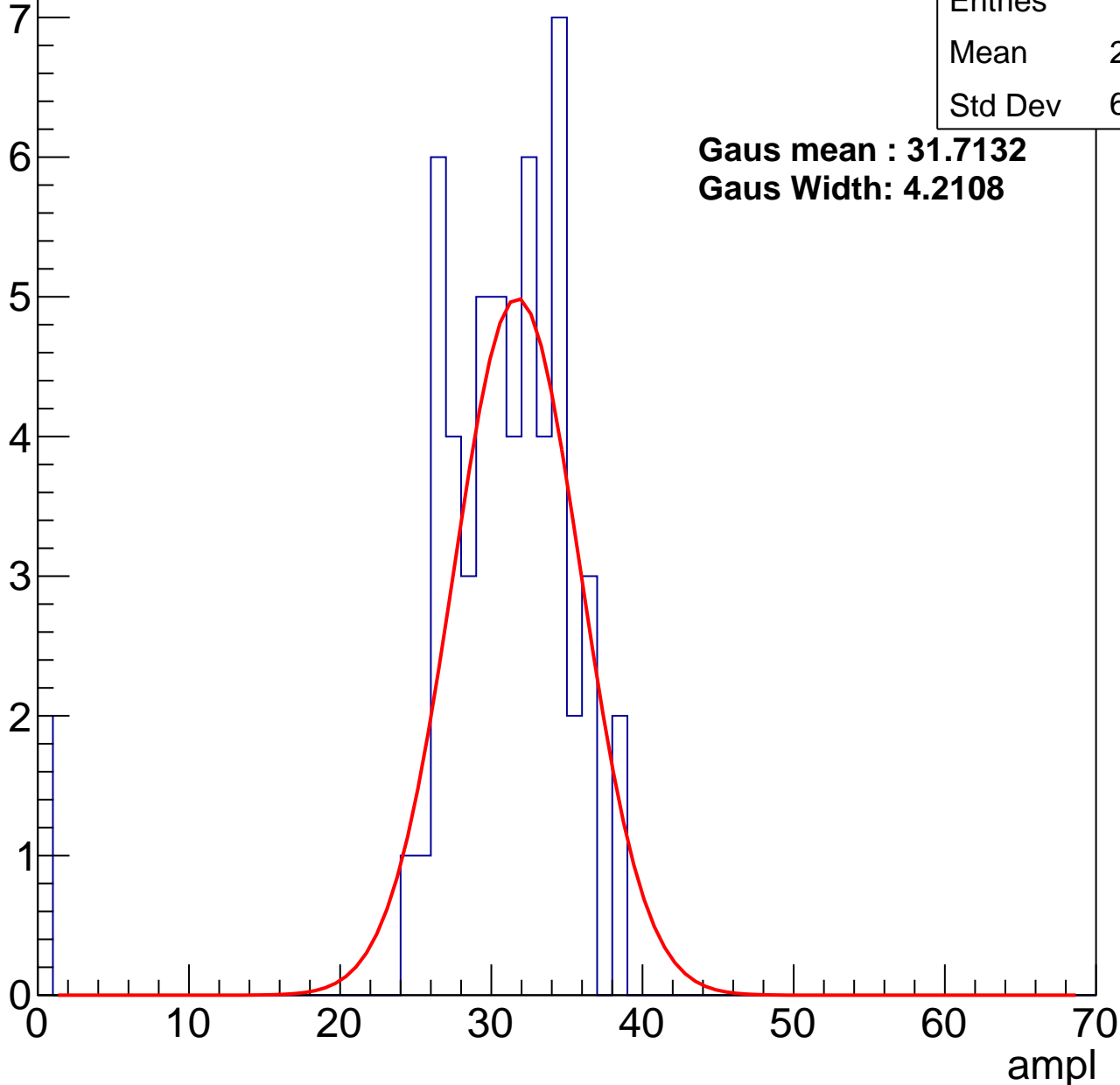
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.67
Std Dev	6.696

Gaus mean : 31.7132

Gaus Width: 4.2108



B0L001S, U18-ch1, adc1

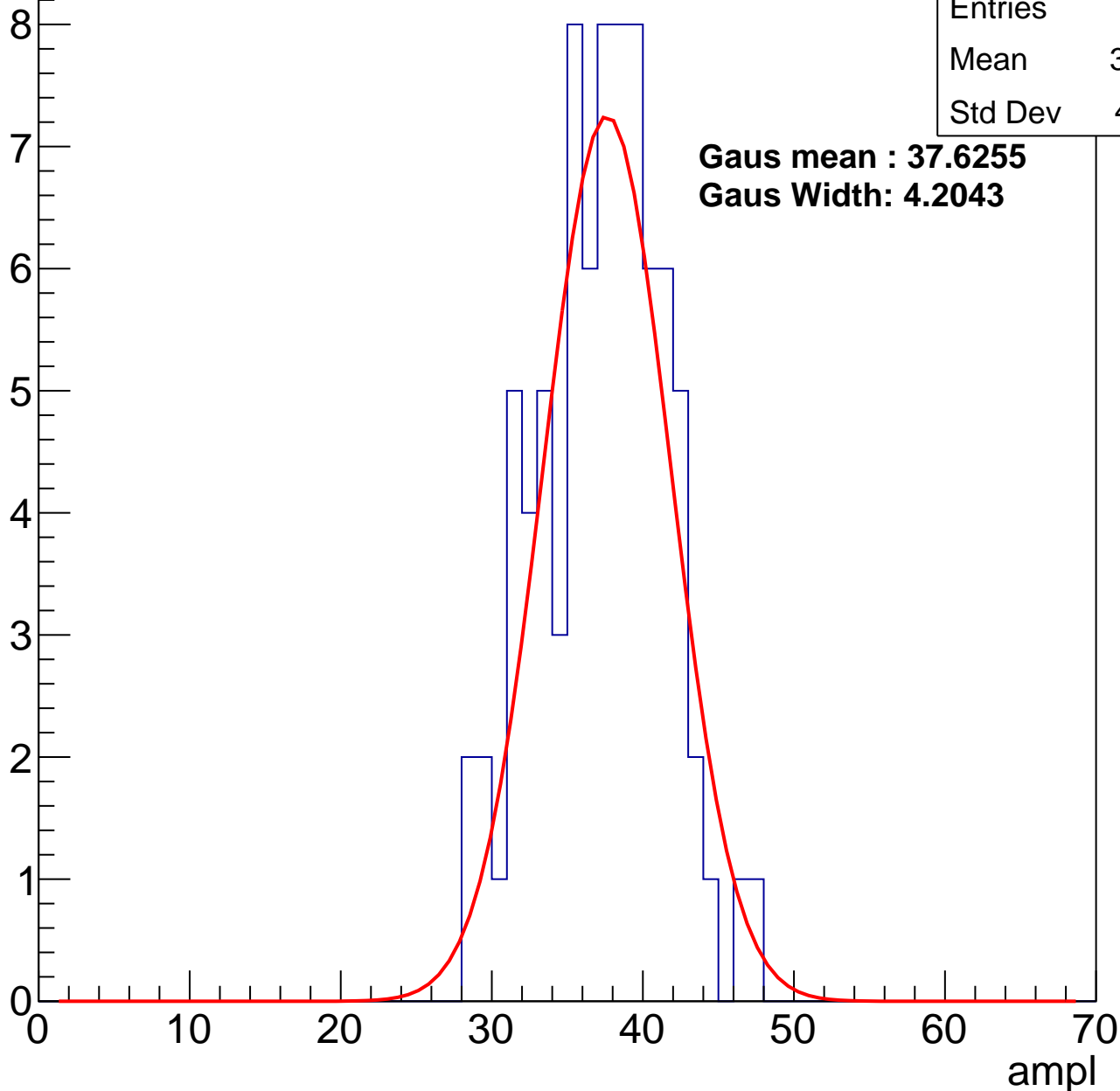
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	36.84
Std Dev	4.101

Gaus mean : 37.6255

Gaus Width: 4.2043



B0L001S, U18-ch1, adc2

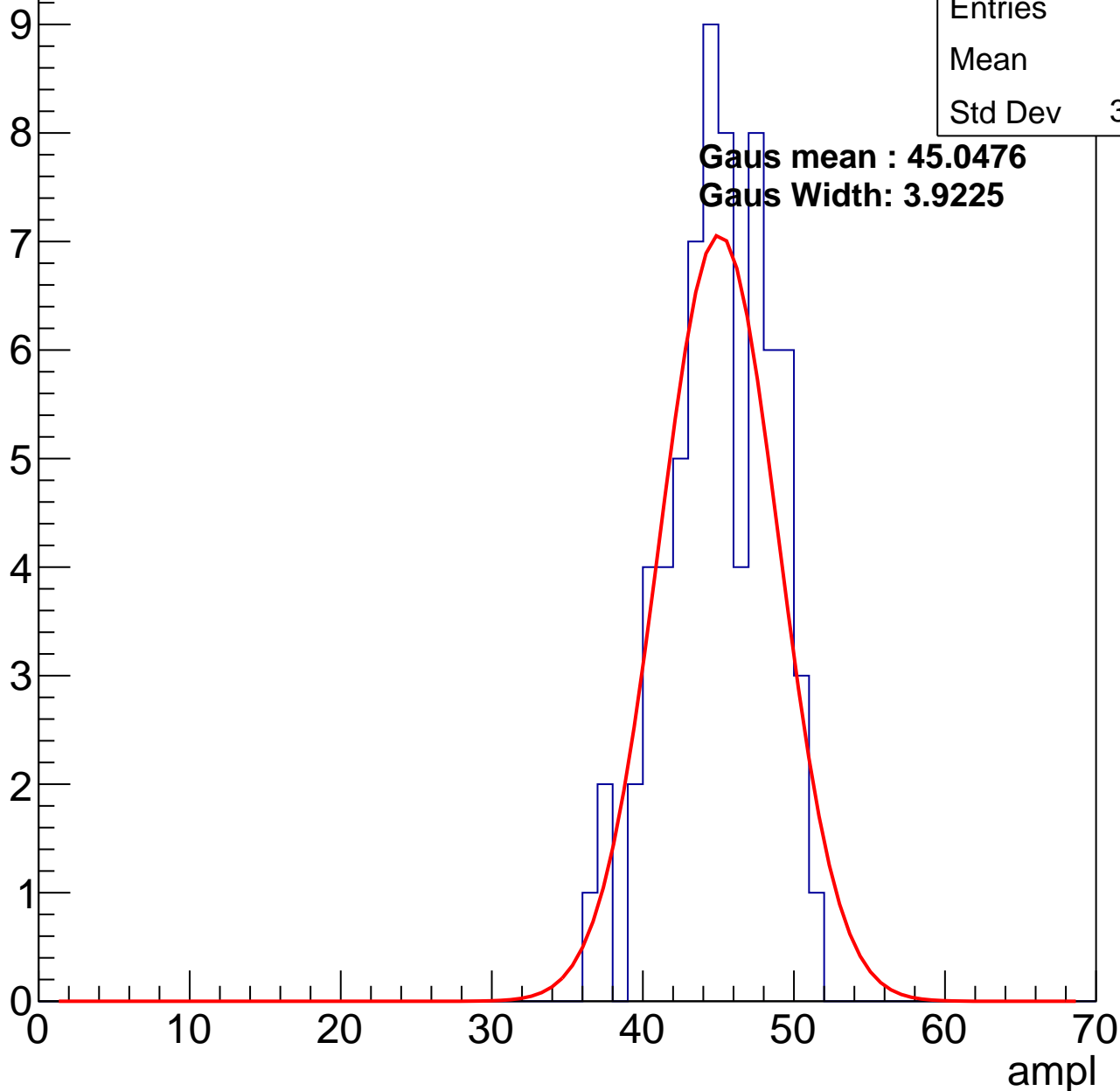
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.6
Std Dev	3.408

Gaus mean : 45.0476

Gaus Width: 3.9225

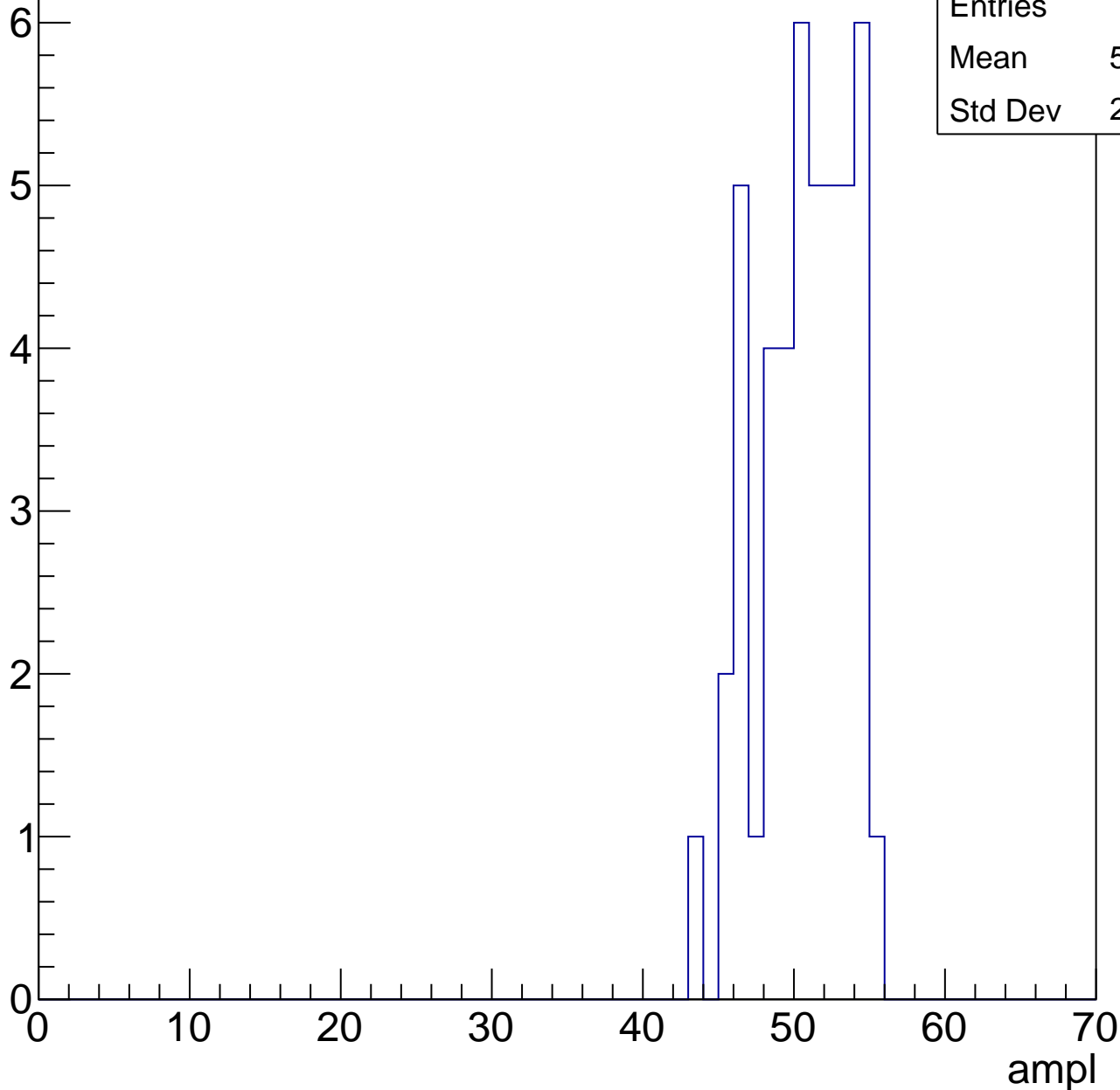


B0L001S, U18-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

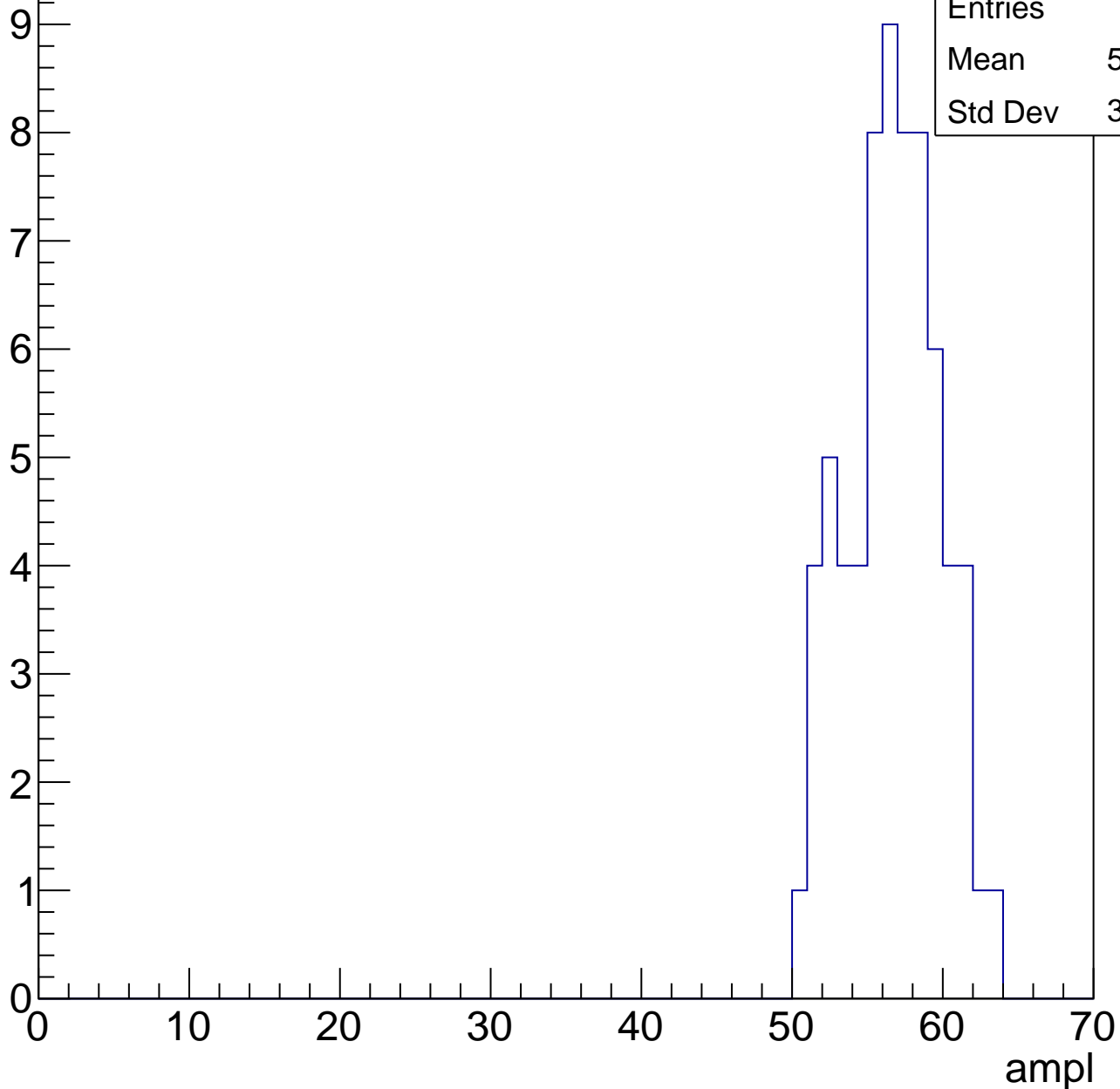
Entries	45
Mean	50.16
Std Dev	2.974



B0L001S, U18-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



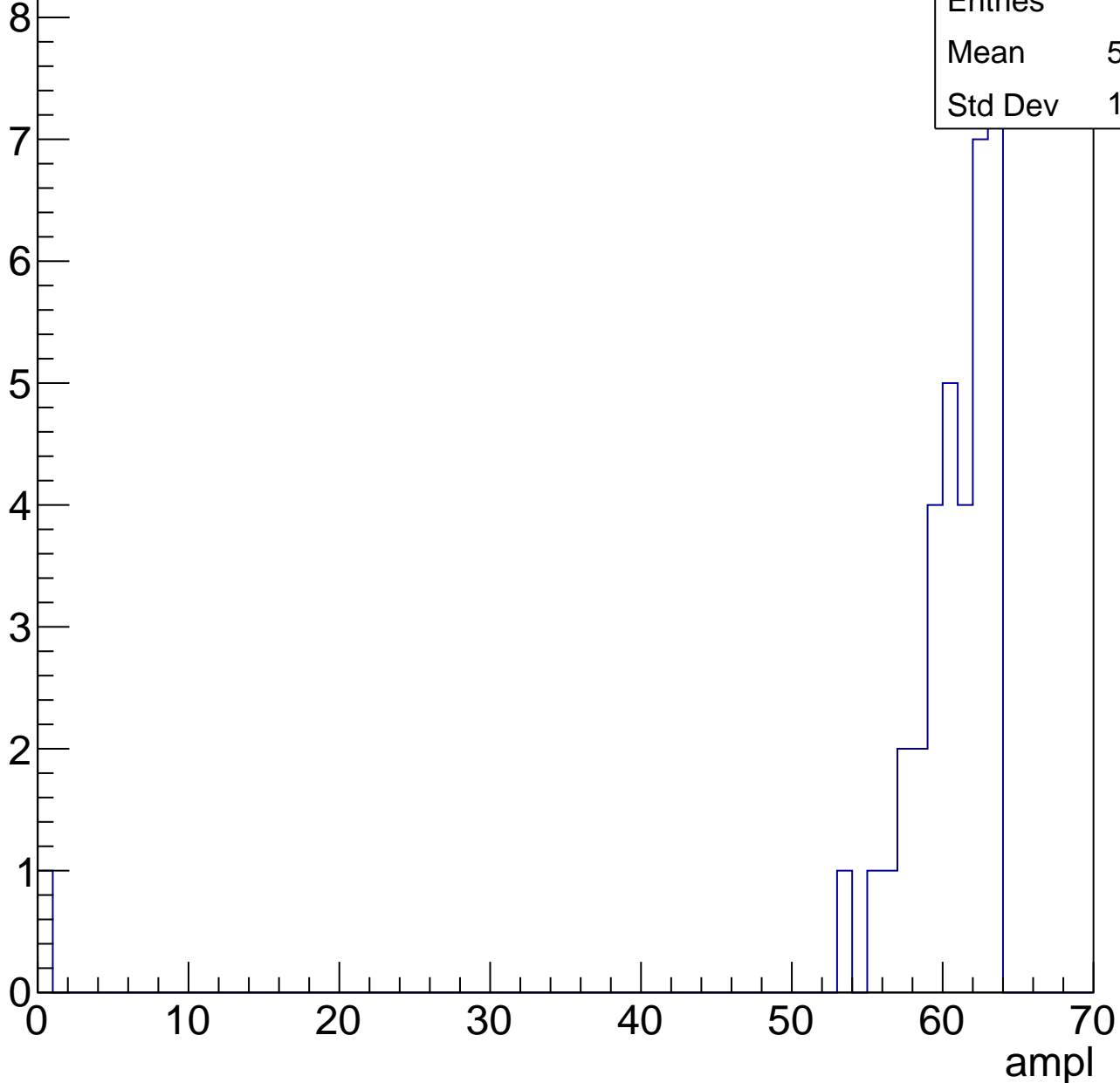
Entries	67
Mean	56.25
Std Dev	3.029

B0L001S, U18-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.67
Std Dev	10.22



B0L001S, U18-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

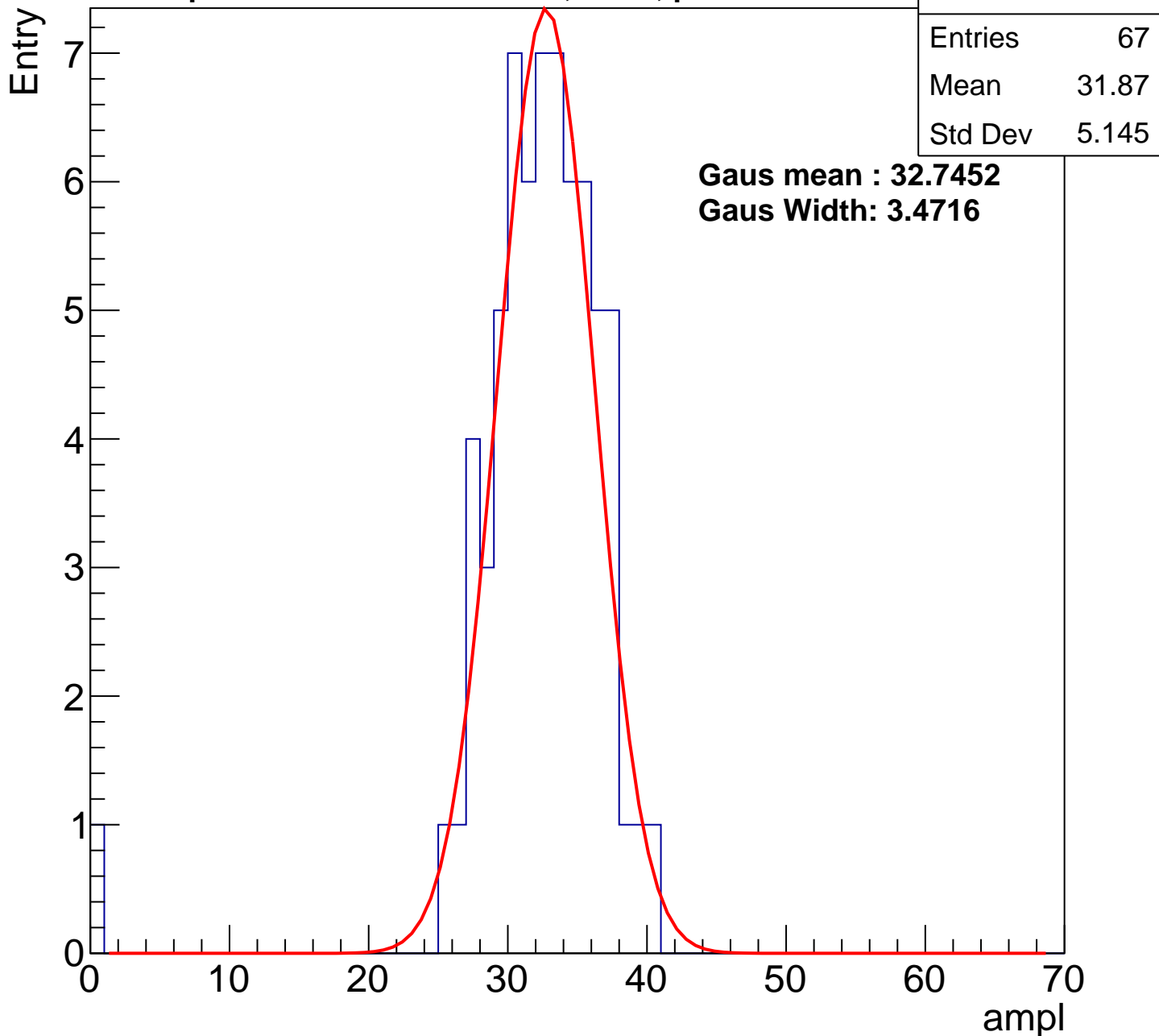
Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch2, adc0

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U18-ch2, adc1

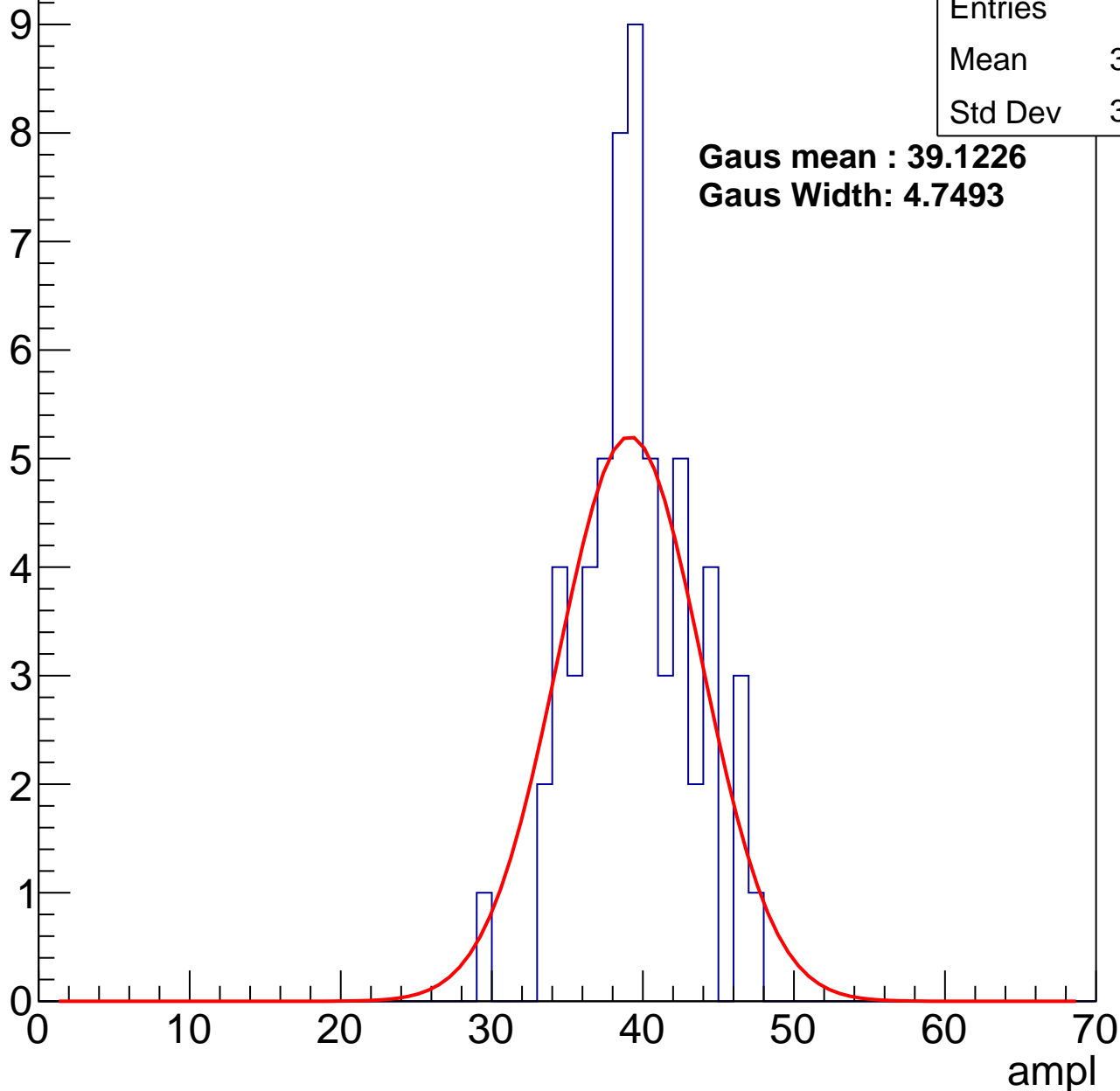
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.98
Std Dev	3.657

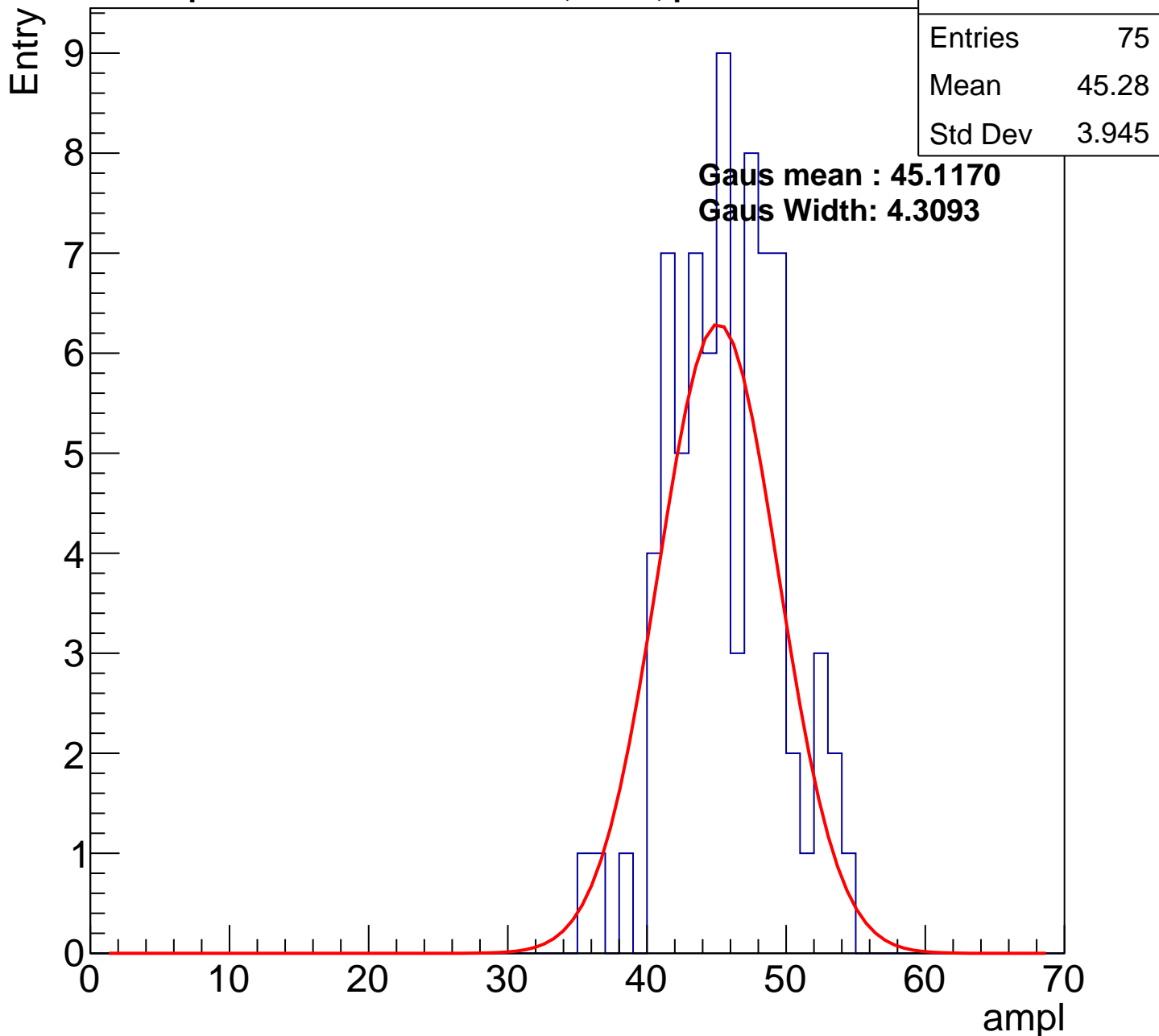
Gaus mean : 39.1226

Gaus Width: 4.7493



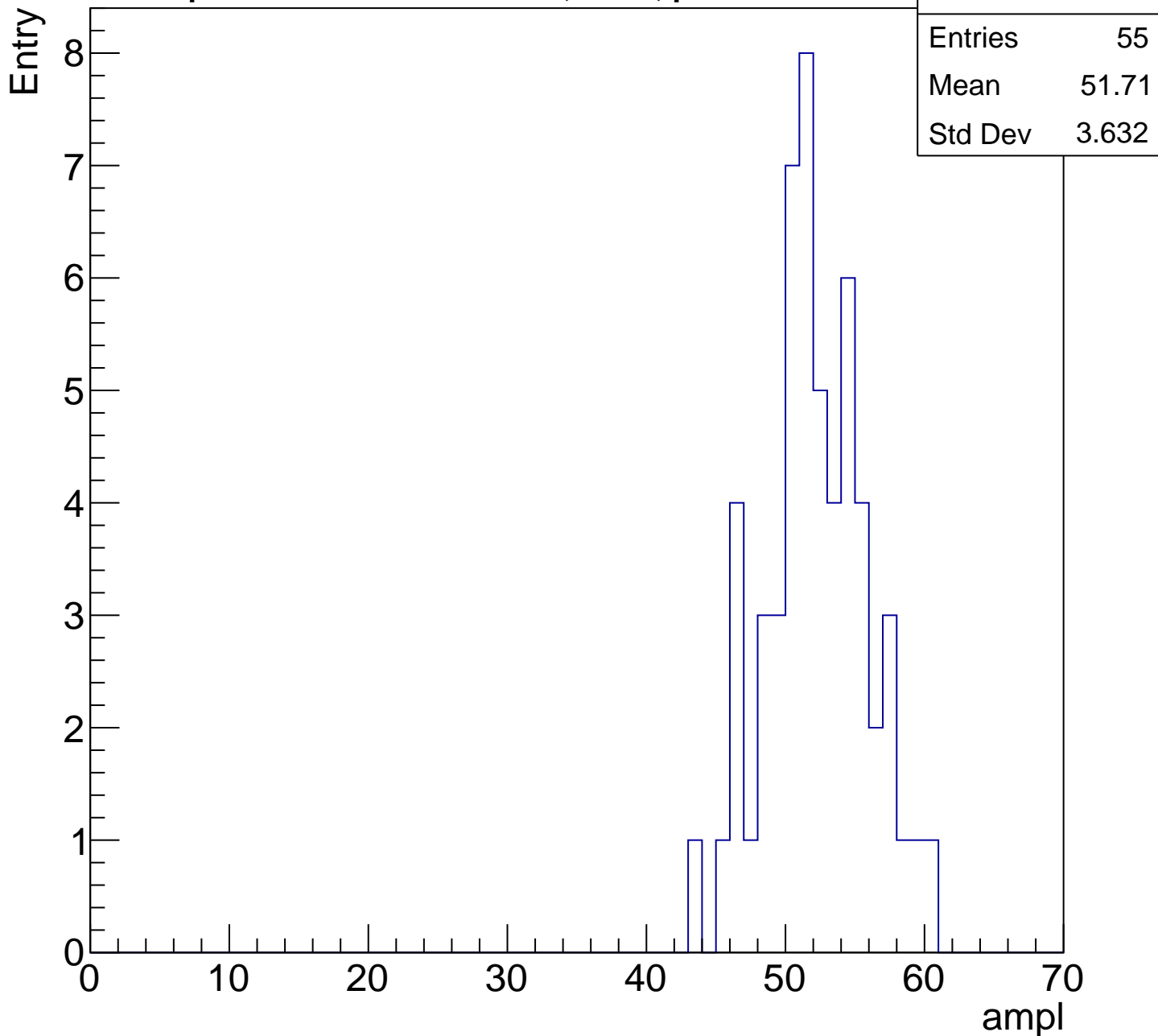
B0L001S, U18-ch2, adc2

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U18-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

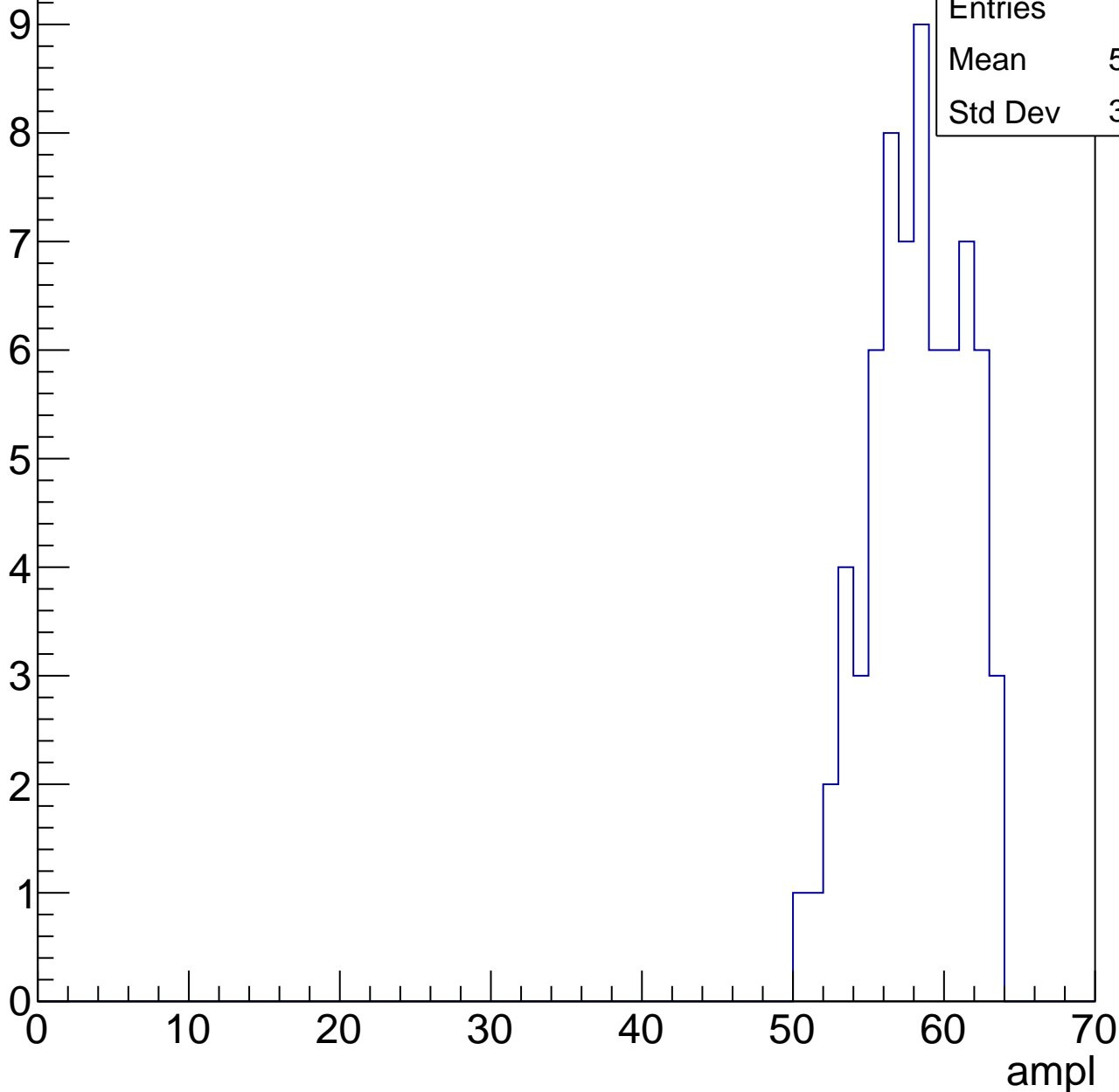


B0L001S, U18-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

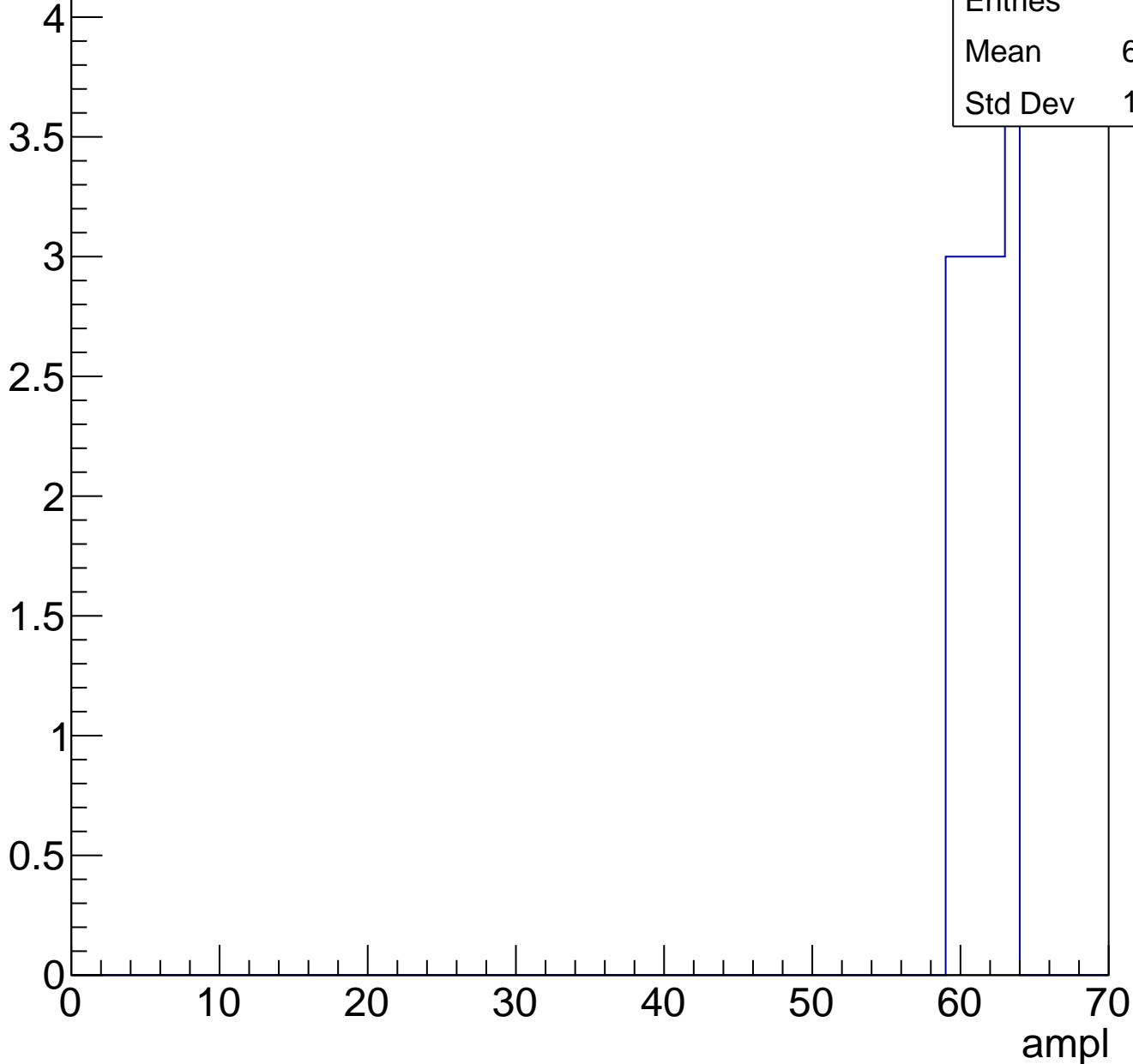
Entries	69
Mean	57.68
Std Dev	3.146



B0L001S, U18-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

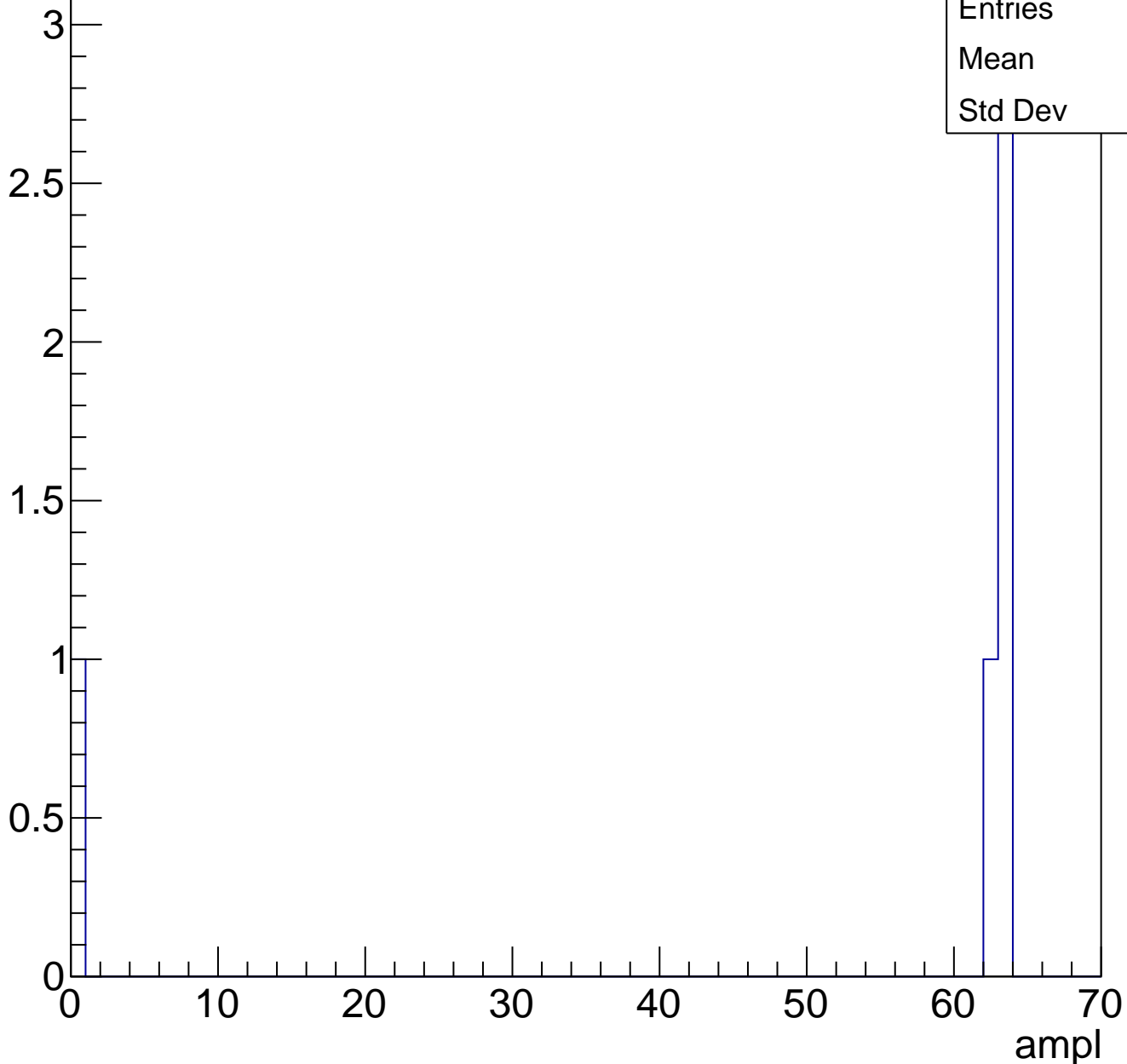
Entry



B0L001S, U18-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	50.2
Std Dev	25.1

B0L001S, U18-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L001S, U18-ch3, adc0

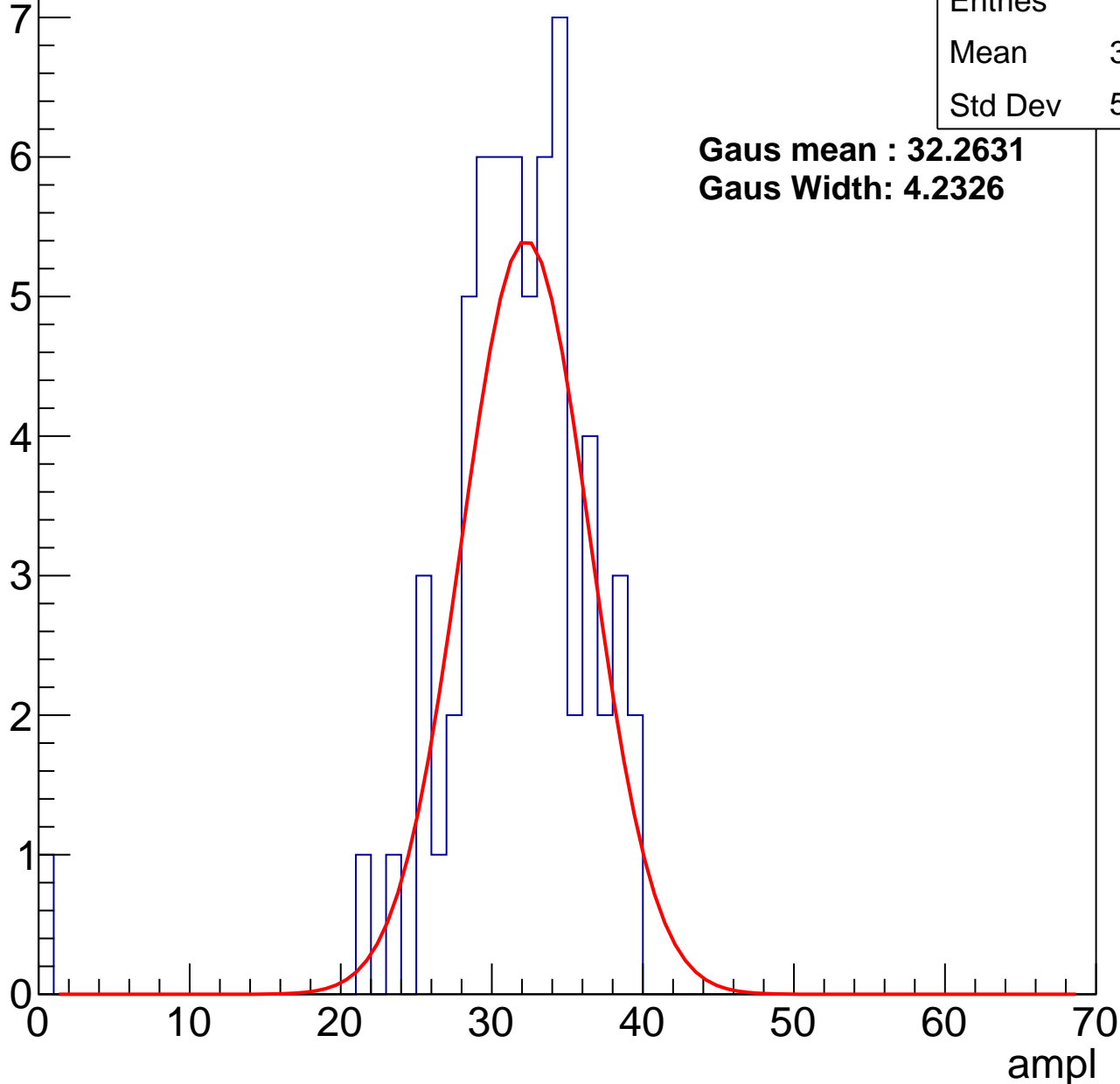
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	31.03
Std Dev	5.543

Gaus mean : 32.2631

Gaus Width: 4.2326



B0L001S, U18-ch3, adc1

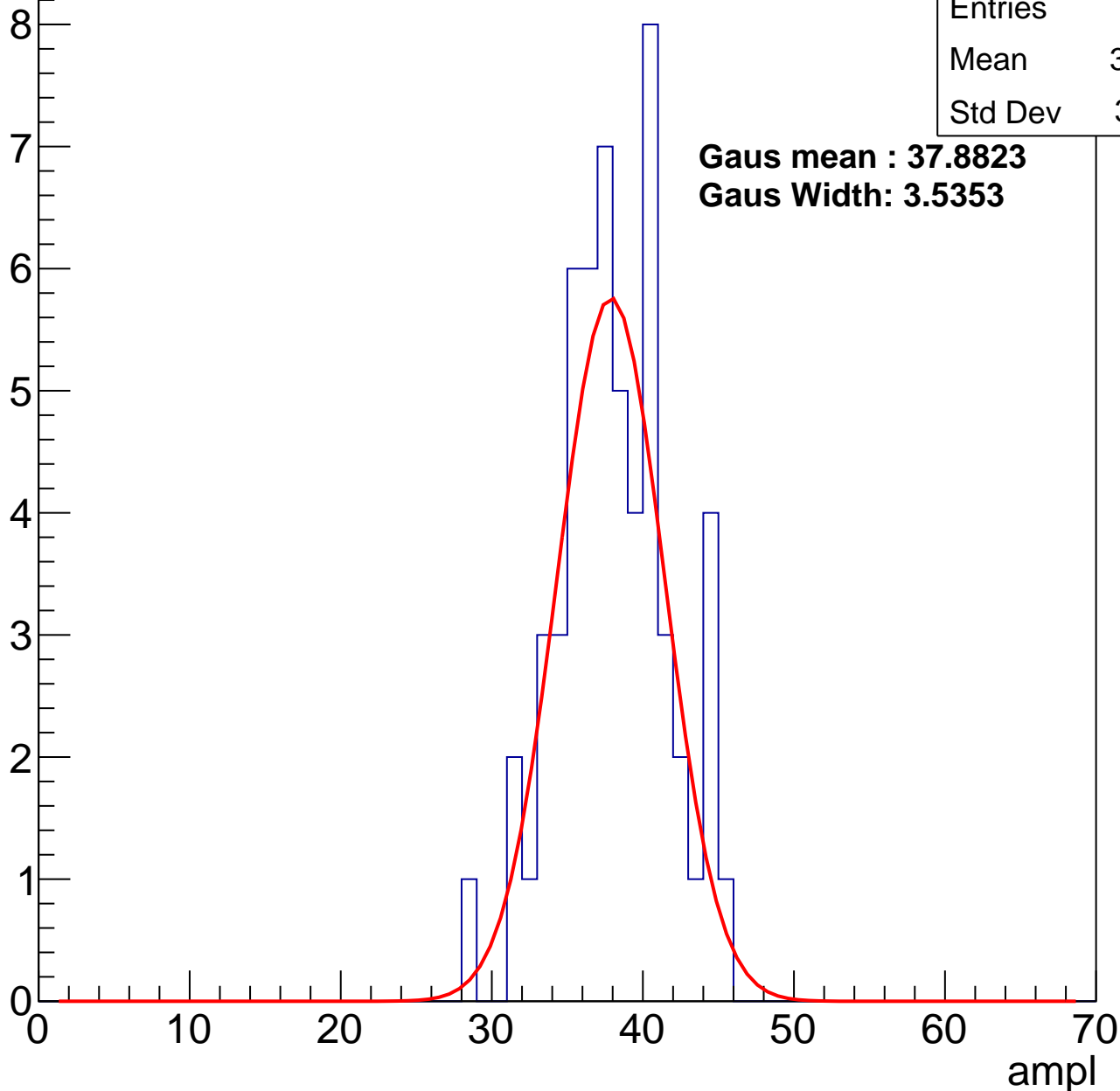
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.63
Std Dev	3.621

Gaus mean : 37.8823

Gaus Width: 3.5353



B0L001S, U18-ch3, adc2

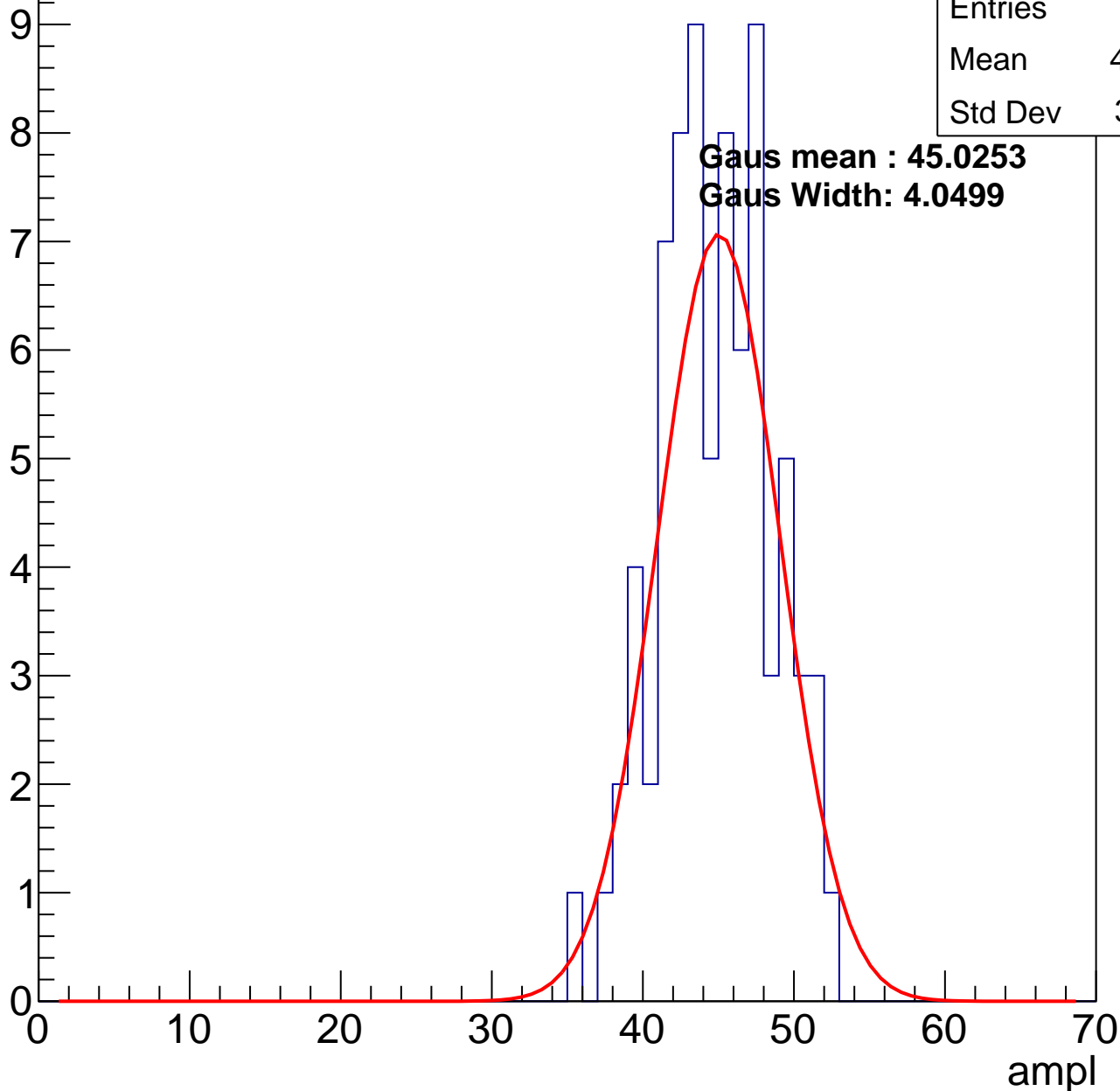
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.38
Std Dev	3.661

Gaus mean : 45.0253

Gaus Width: 4.0499

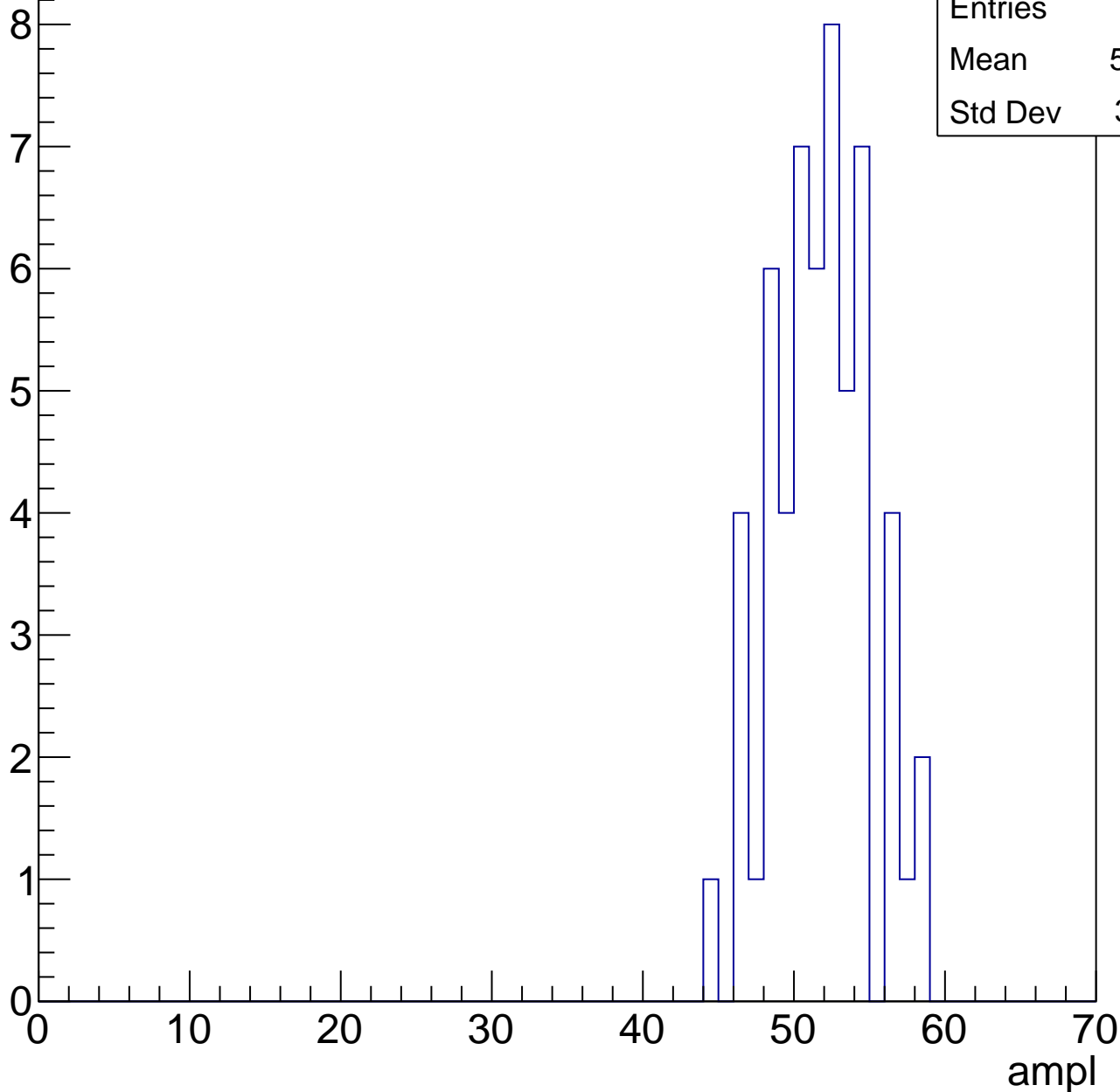


B0L001S, U18-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	51.27
Std Dev	3.171



B0L001S, U18-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	57.07
Std Dev	3.584

Entry

10

8

6

4

2

0

0

10

20

30

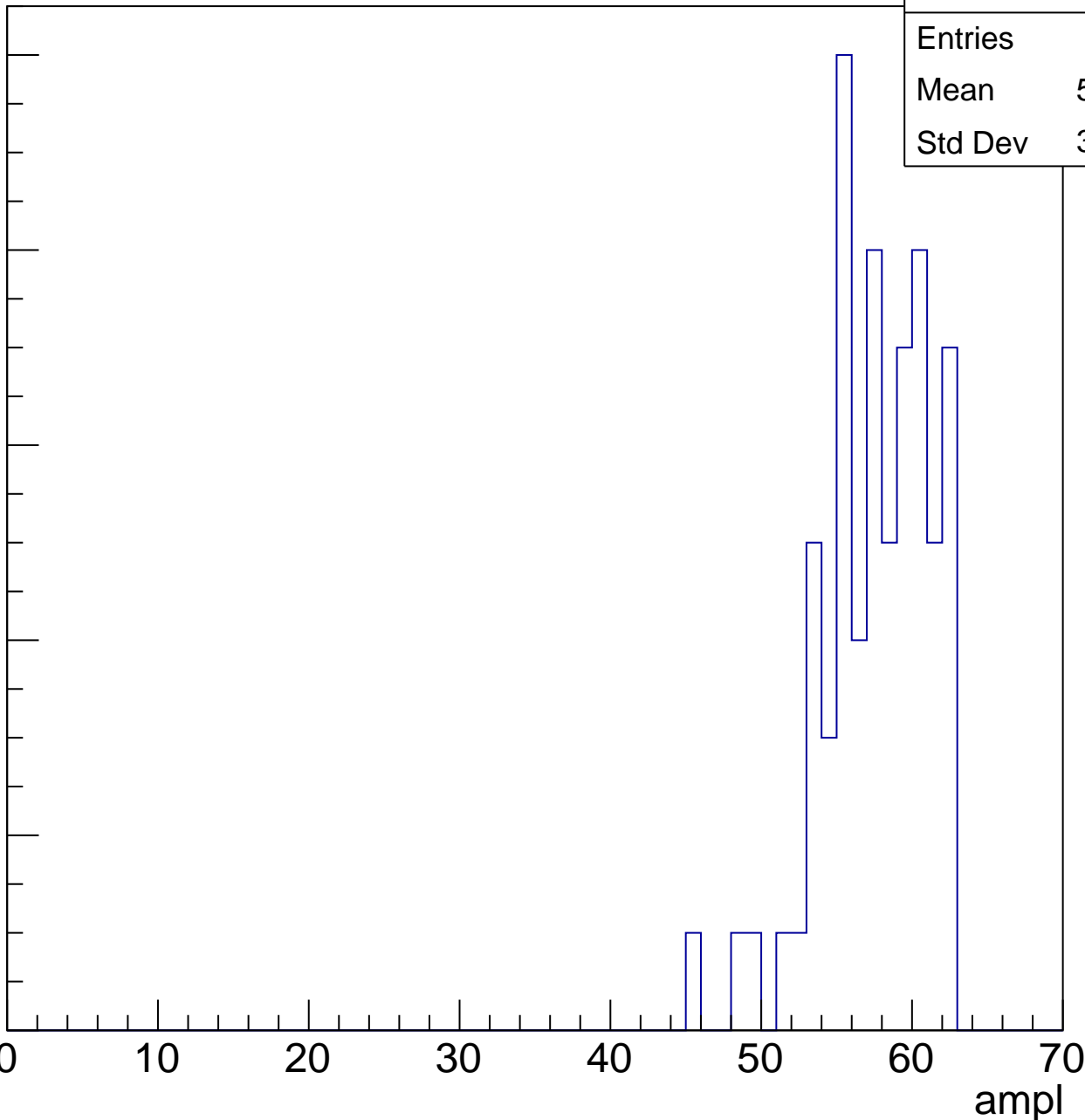
40

50

60

70

ampl

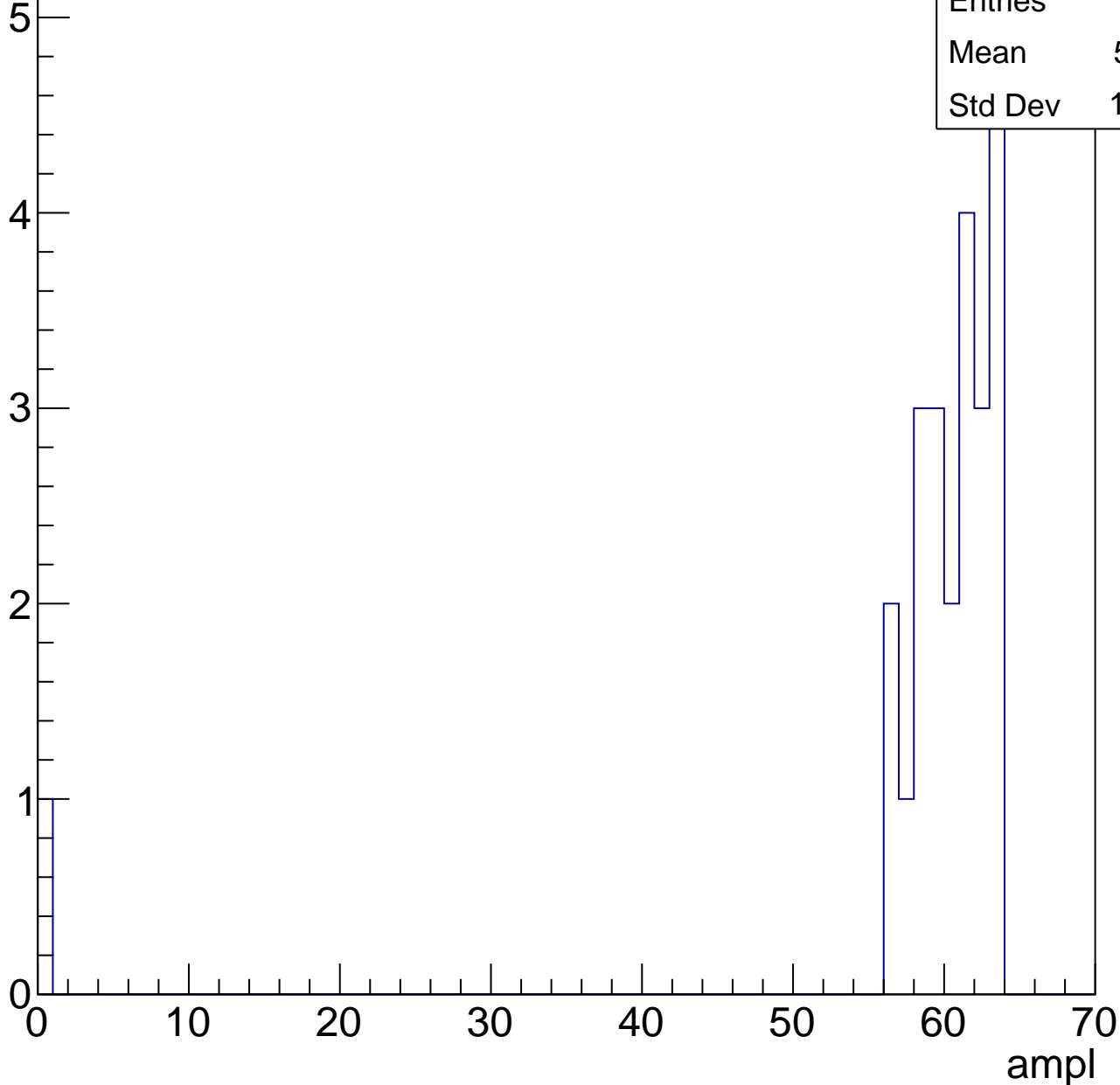


B0L001S, U18-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	57.71
Std Dev	12.23

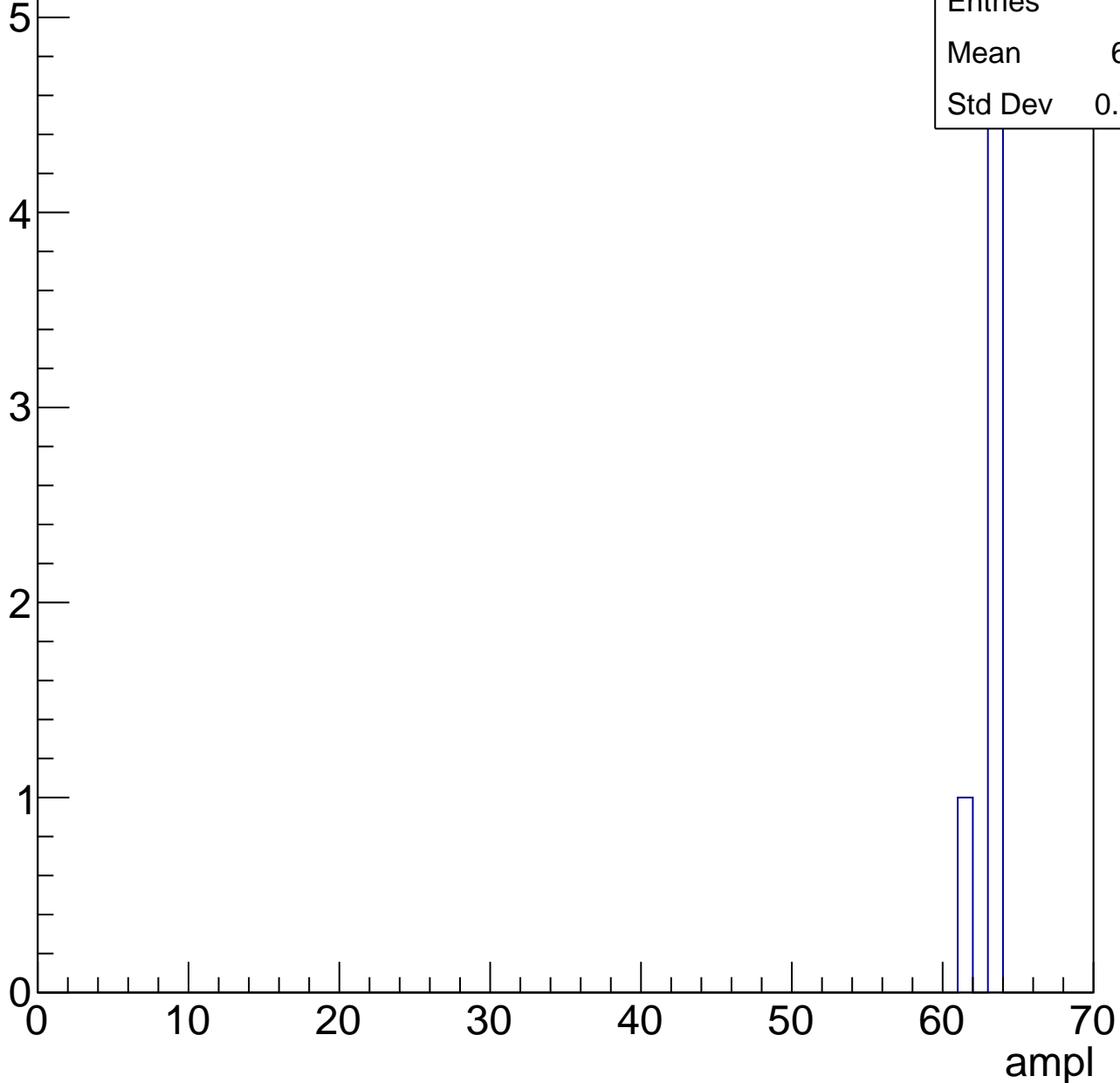


B0L001S, U18-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B0L001S, U18-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch4, adc0

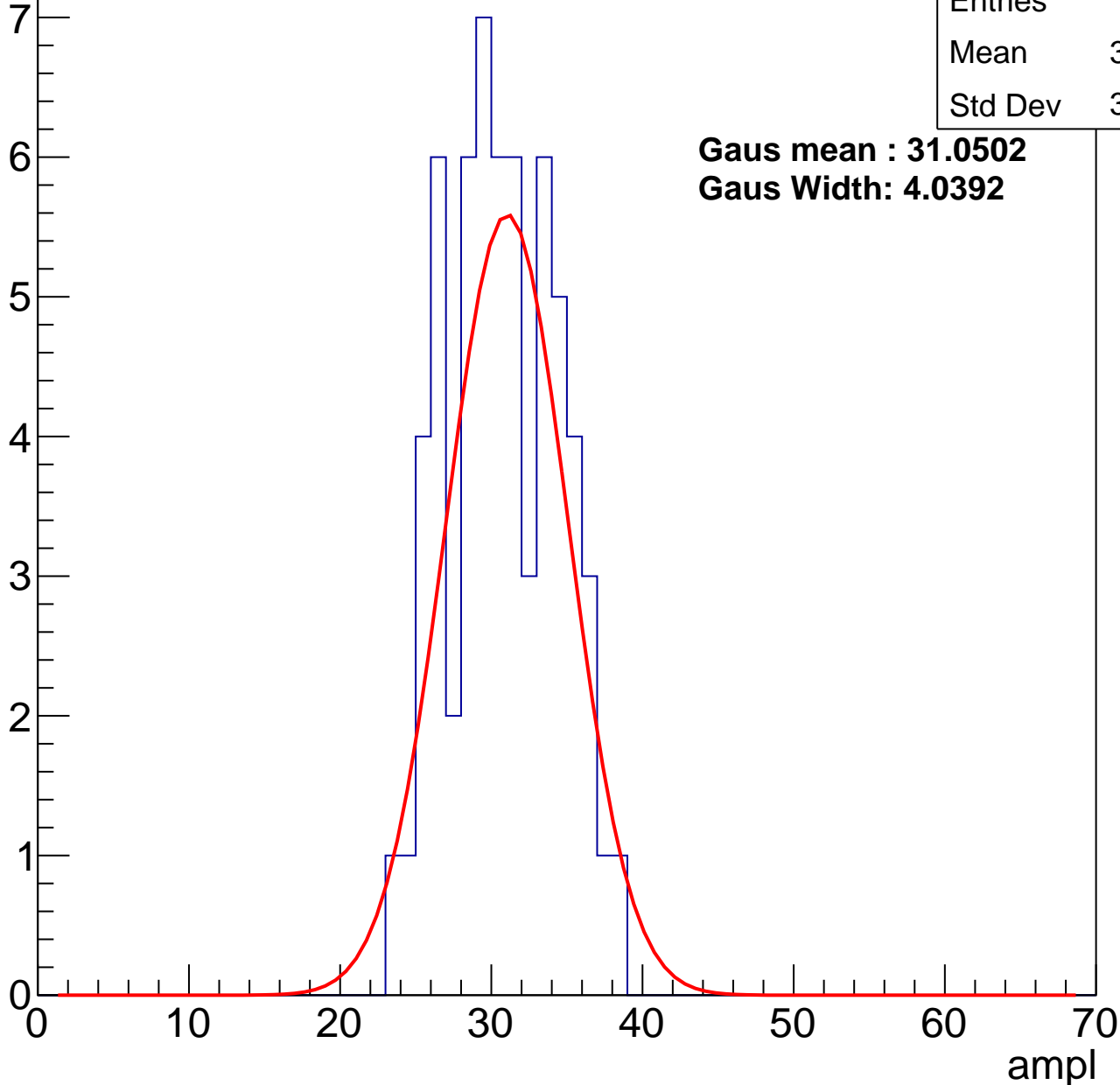
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.34
Std Dev	3.583

Gaus mean : 31.0502

Gaus Width: 4.0392



B0L001S, U18-ch4, adc1

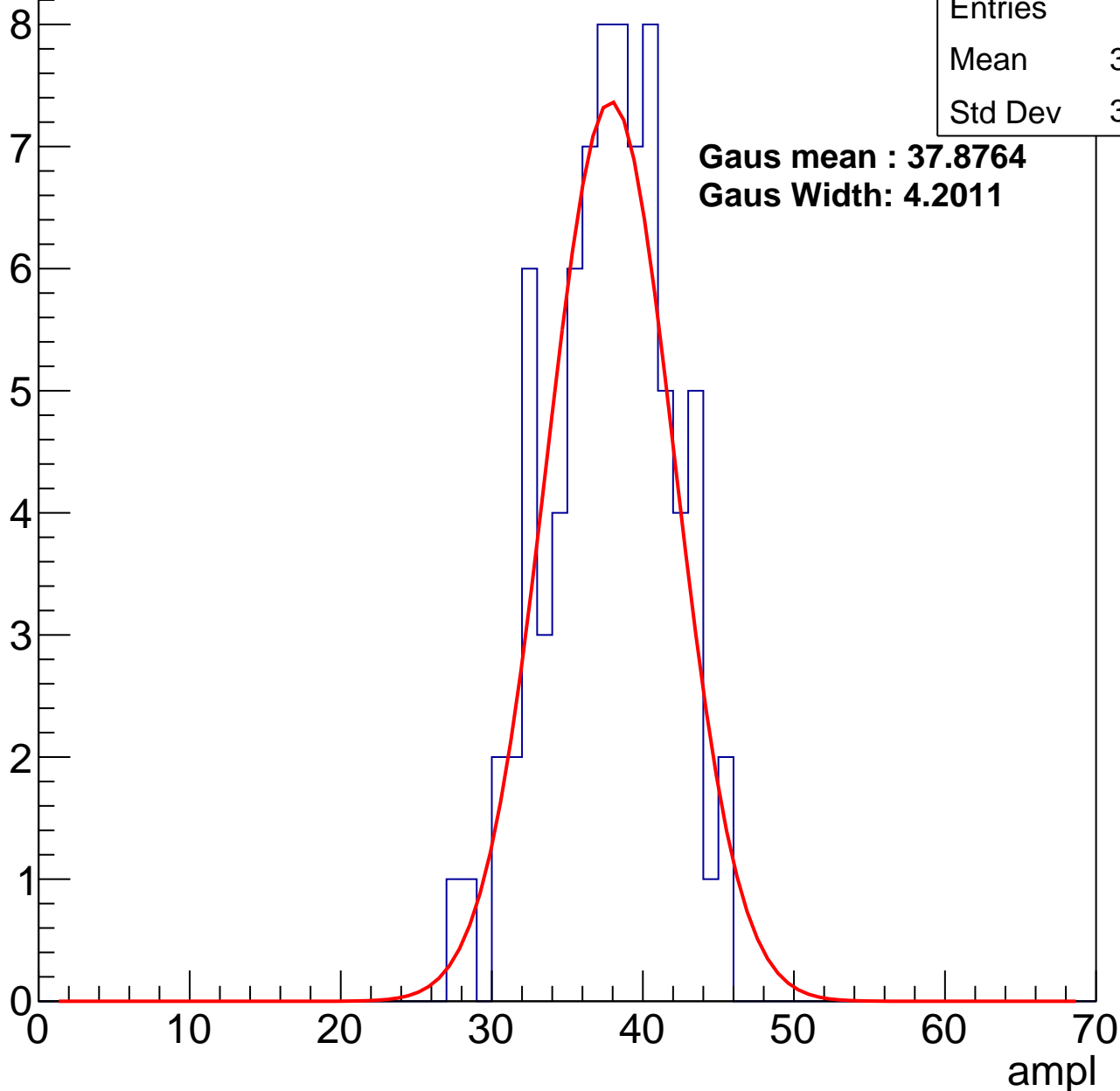
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	37.26
Std Dev	3.962

Gaus mean : 37.8764

Gaus Width: 4.2011



B0L001S, U18-ch4, adc2

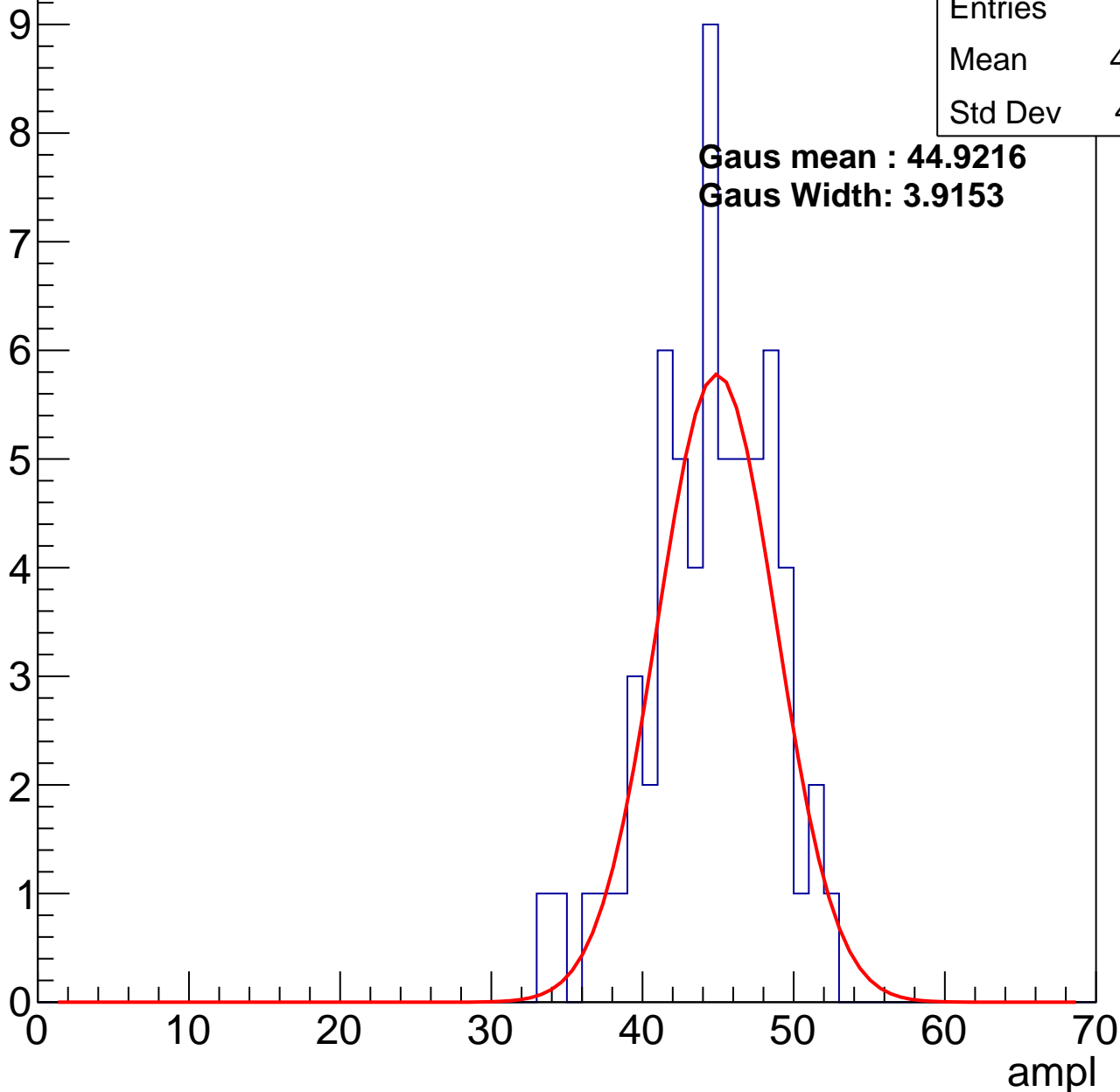
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.08
Std Dev	4.021

Gaus mean : 44.9216

Gaus Width: 3.9153

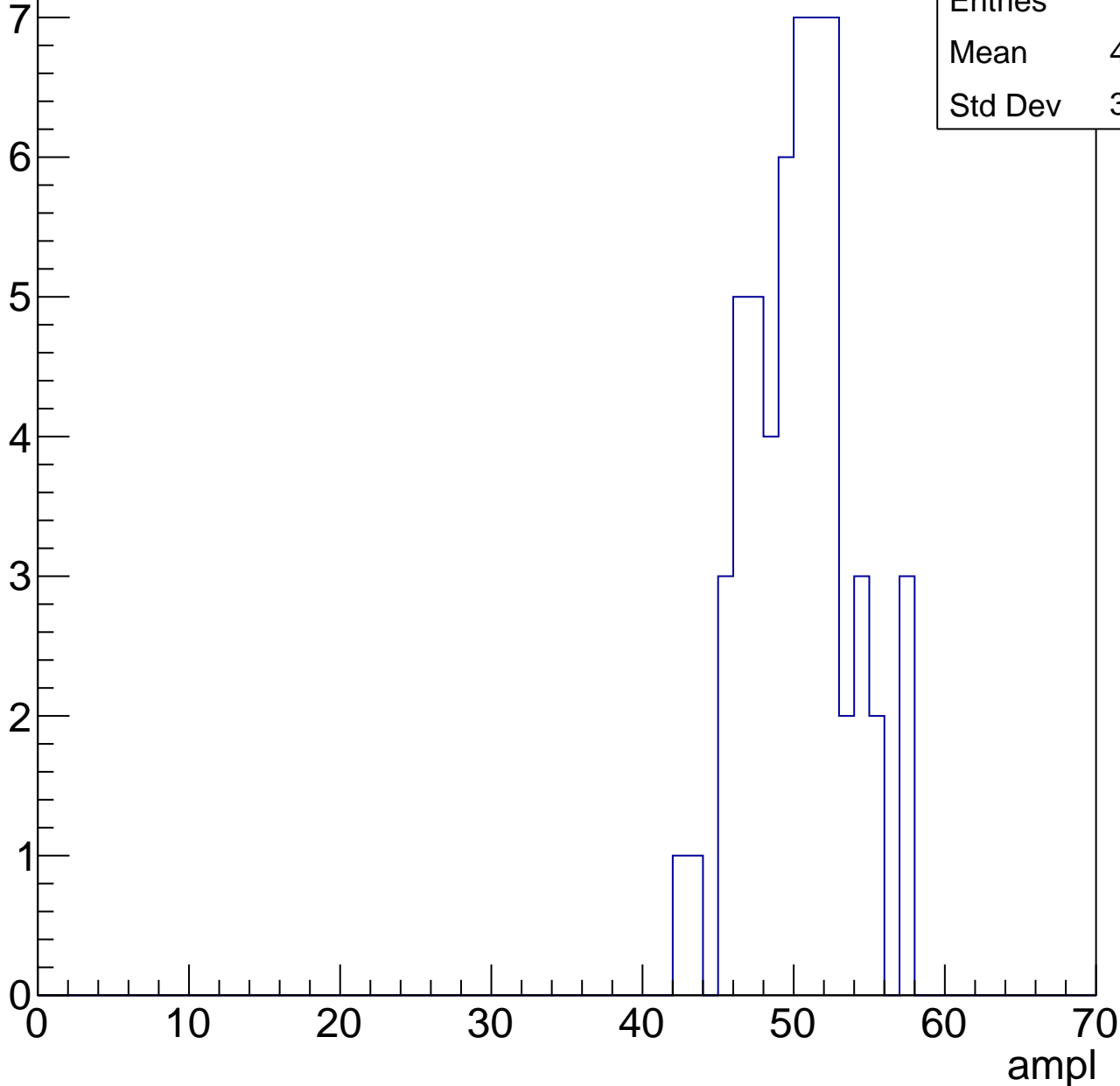


B0L001S, U18-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	49.84
Std Dev	3.358

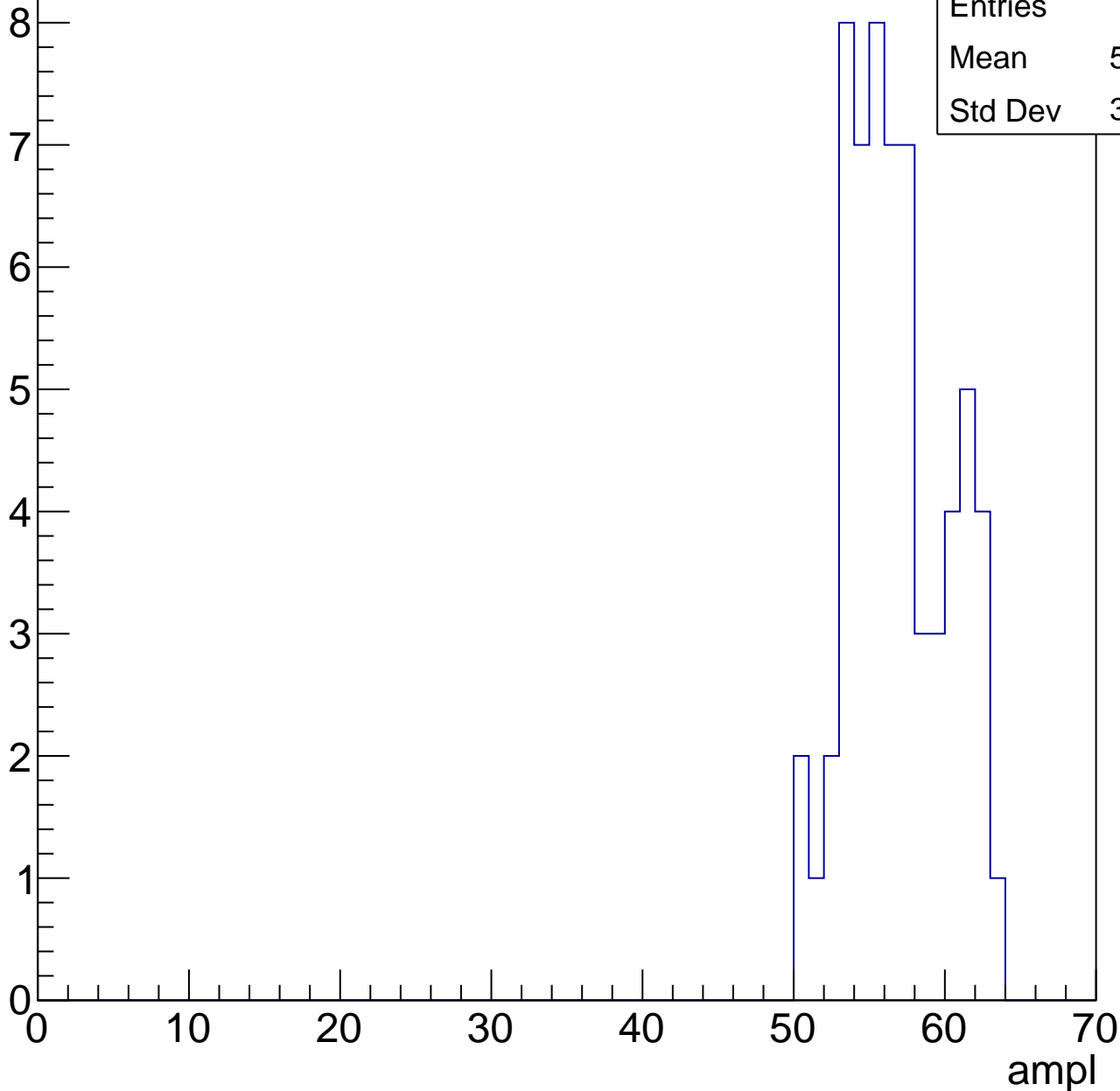


B0L001S, U18-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

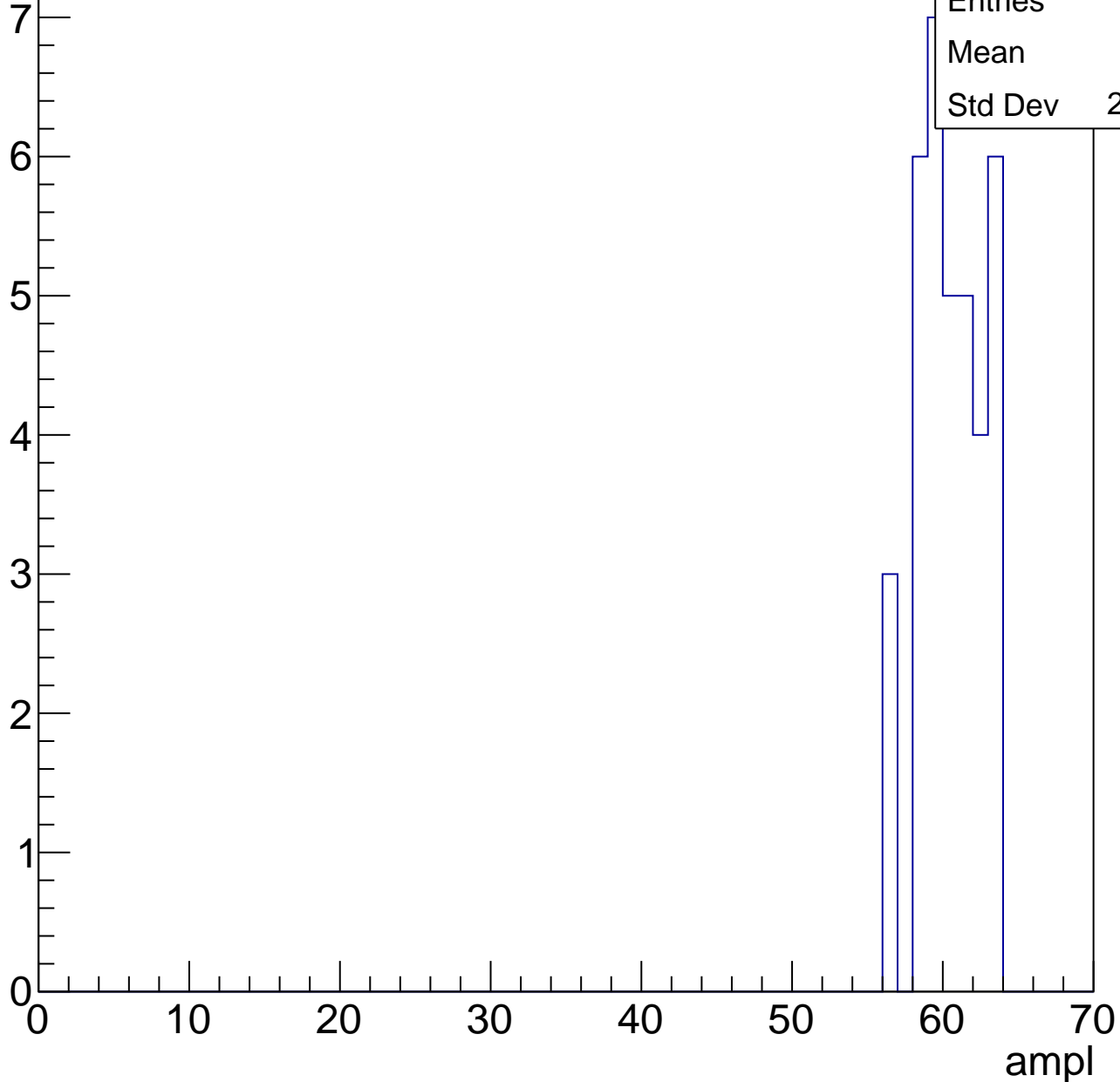
Entries	62
Mean	56.37
Std Dev	3.254



B0L001S, U18-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

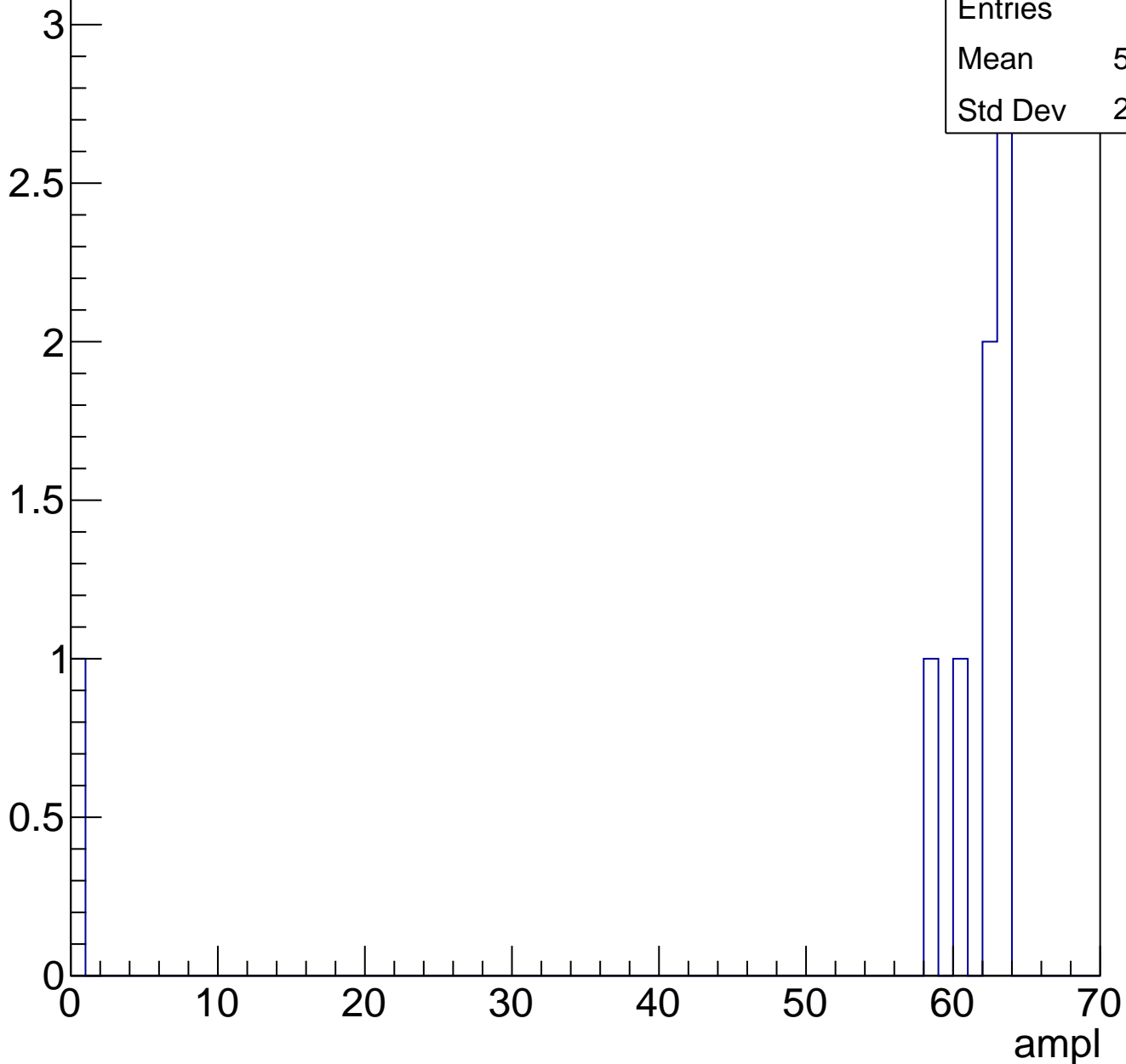
Entry



B0L001S, U18-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch5, adc0

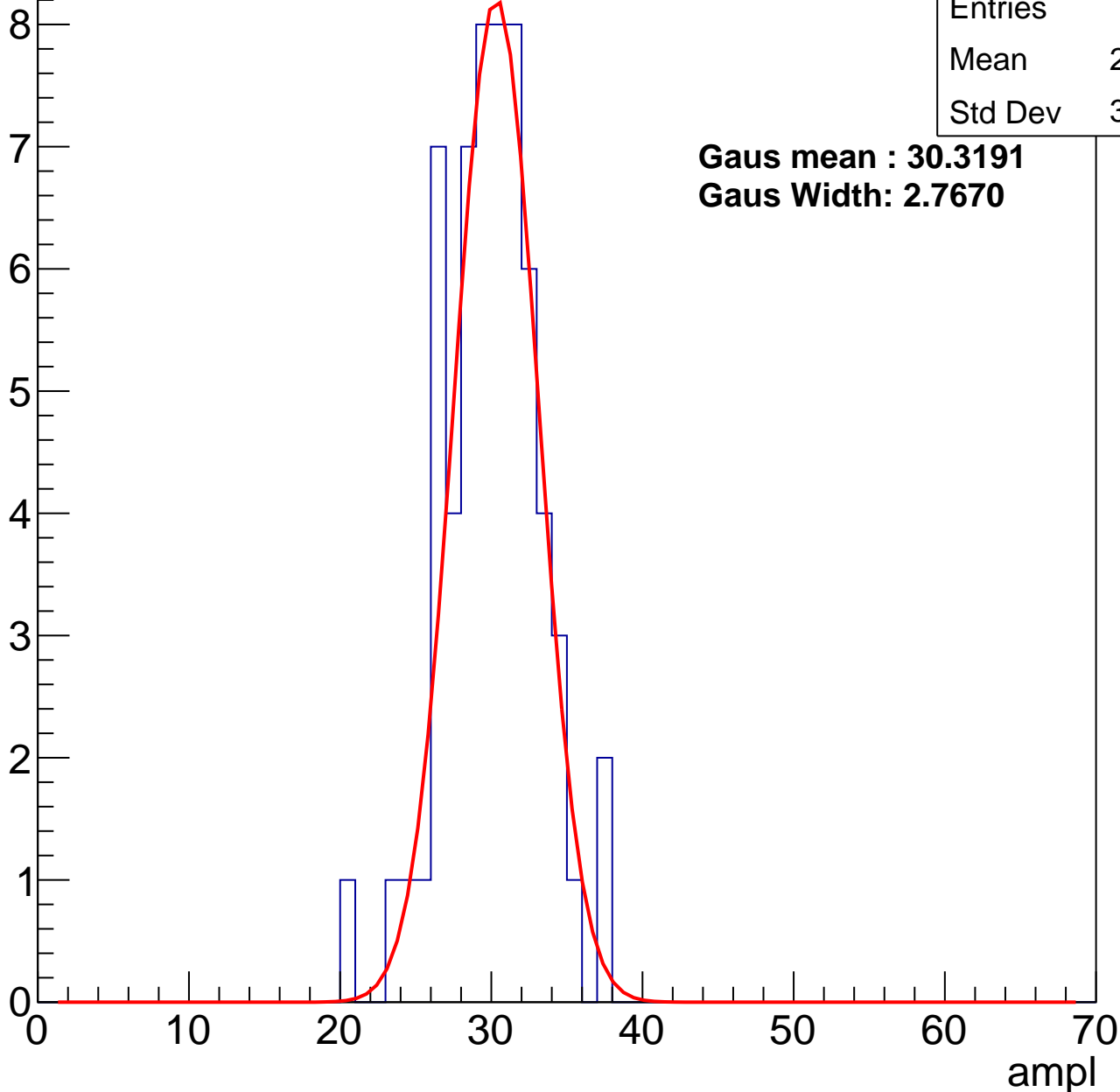
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.56
Std Dev	3.165

Gaus mean : 30.3191

Gaus Width: 2.7670



B0L001S, U18-ch5, adc1

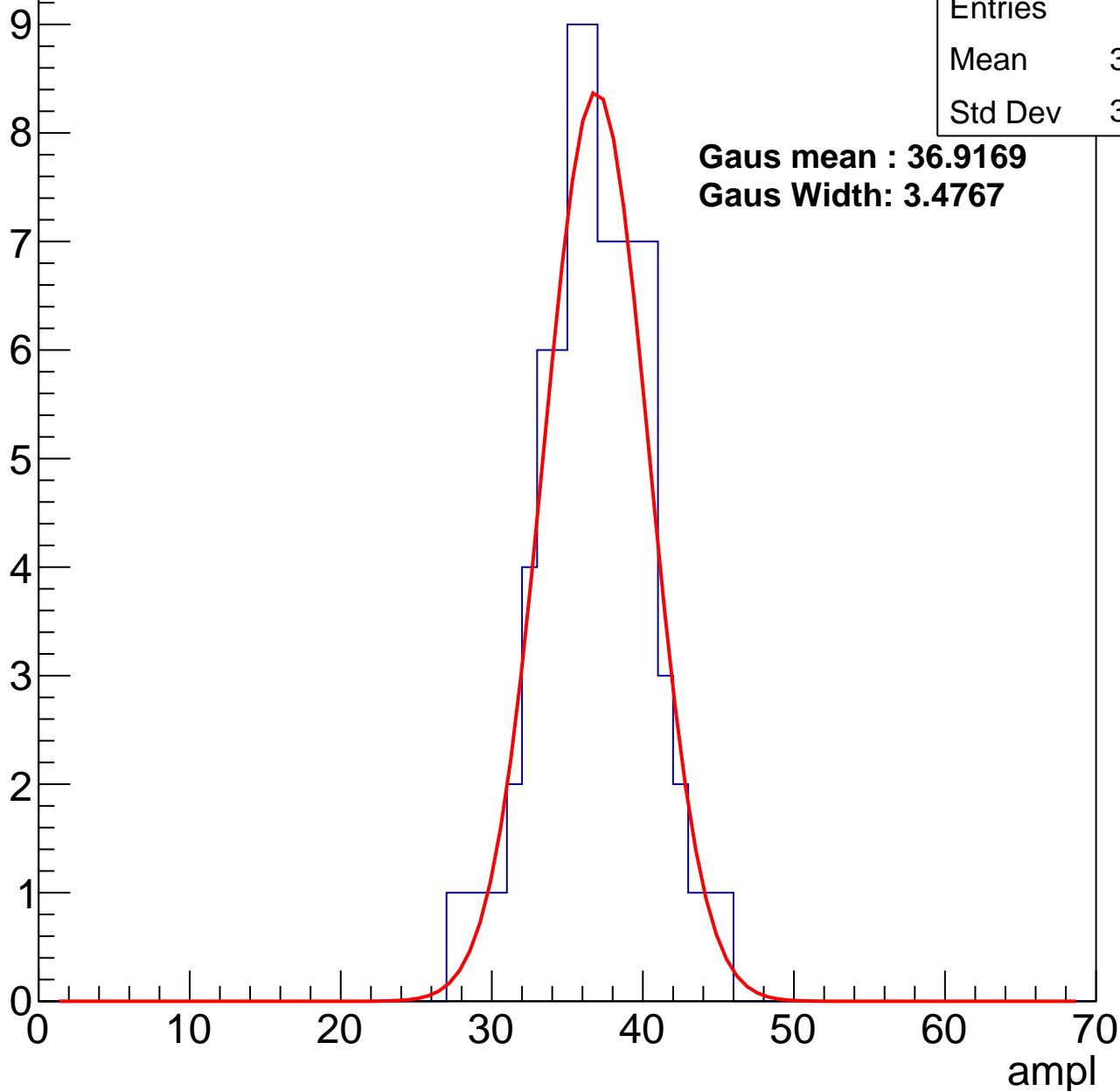
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	36.34
Std Dev	3.578

Gaus mean : 36.9169

Gaus Width: 3.4767



B0L001S, U18-ch5, adc2

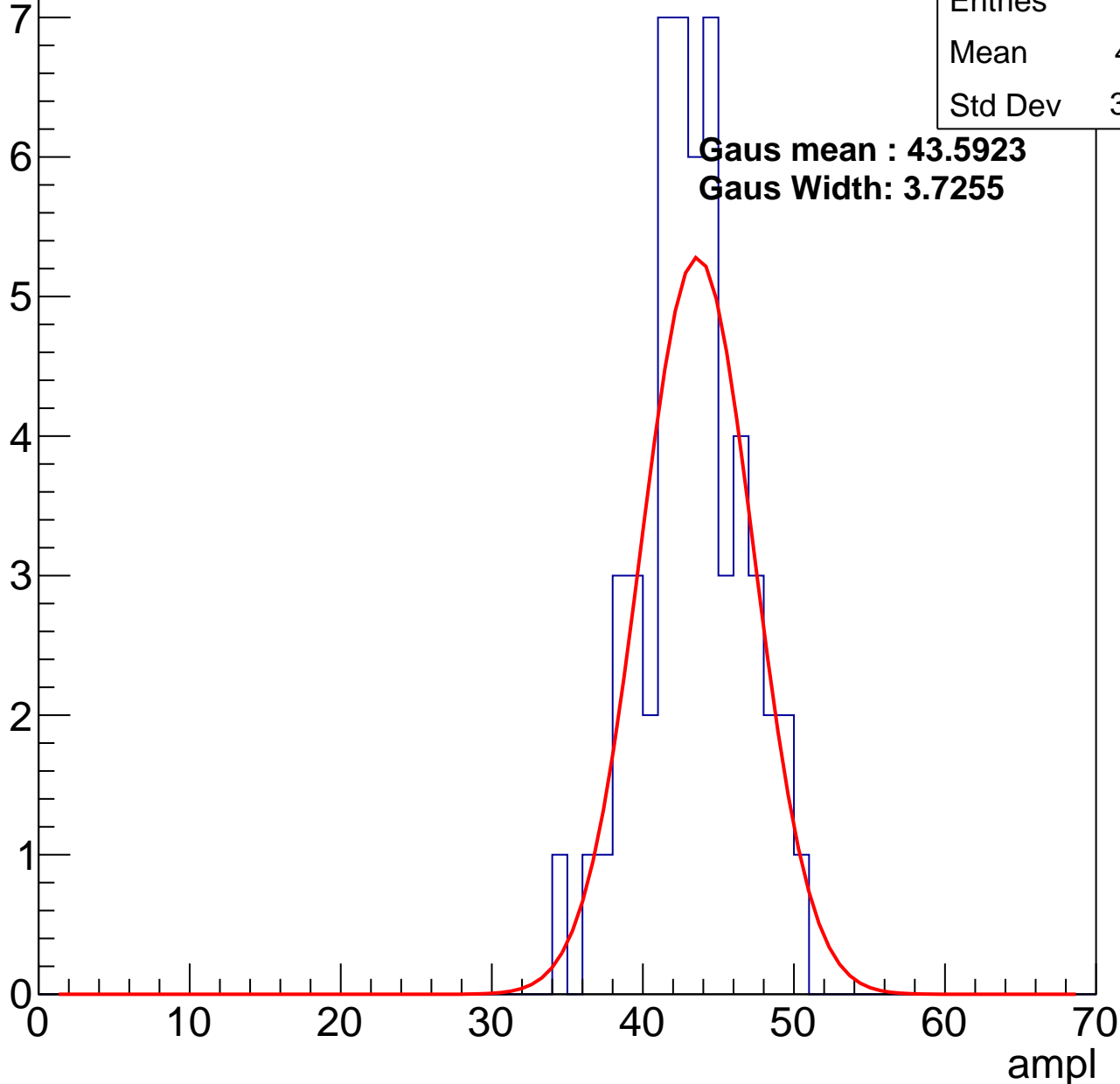
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.81
Std Dev	3.404

Gaus mean : 43.5923

Gaus Width: 3.7255

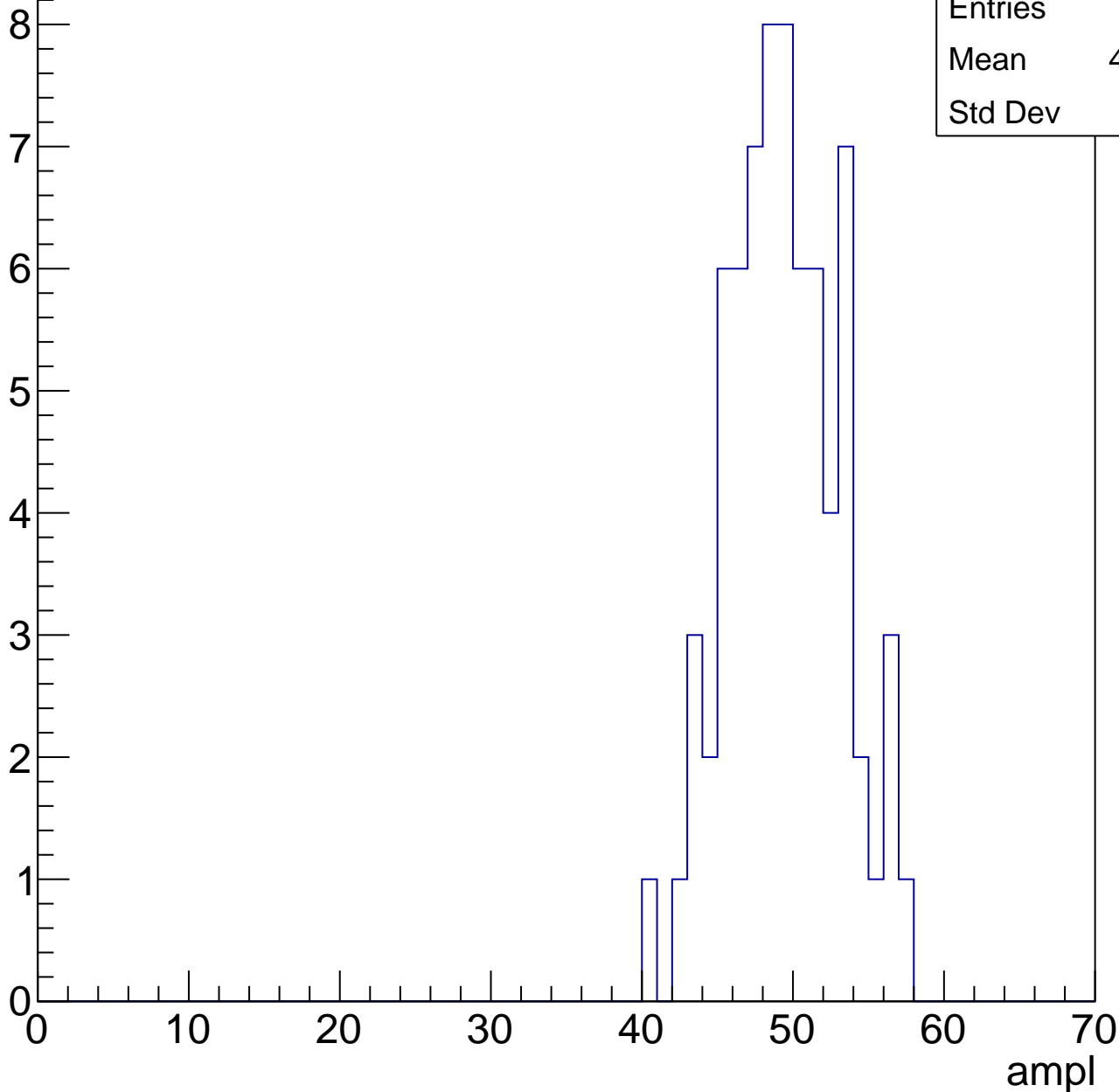


B0L001S, U18-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

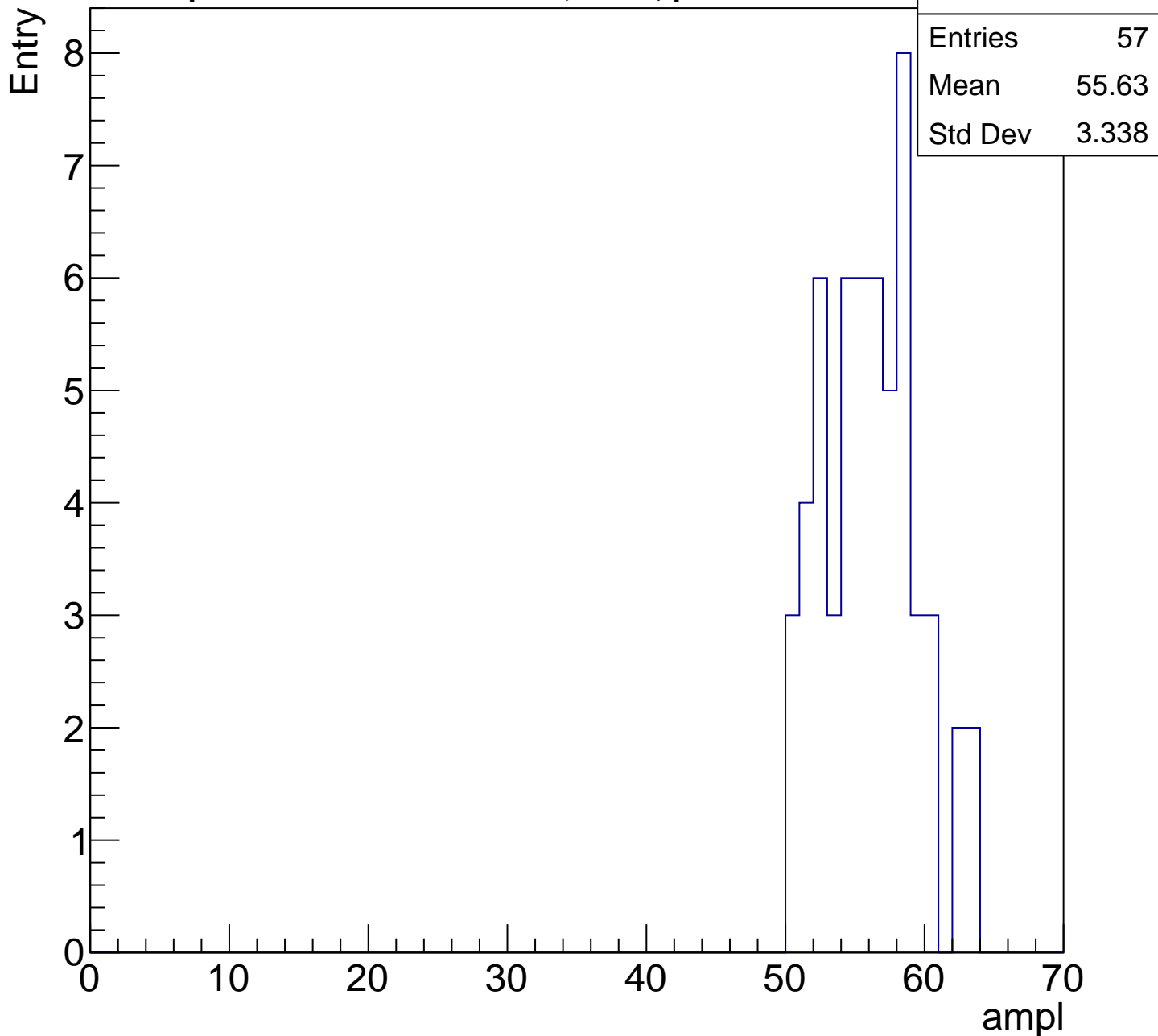
Entry

Entries	72
Mean	48.93
Std Dev	3.63



B0L001S, U18-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

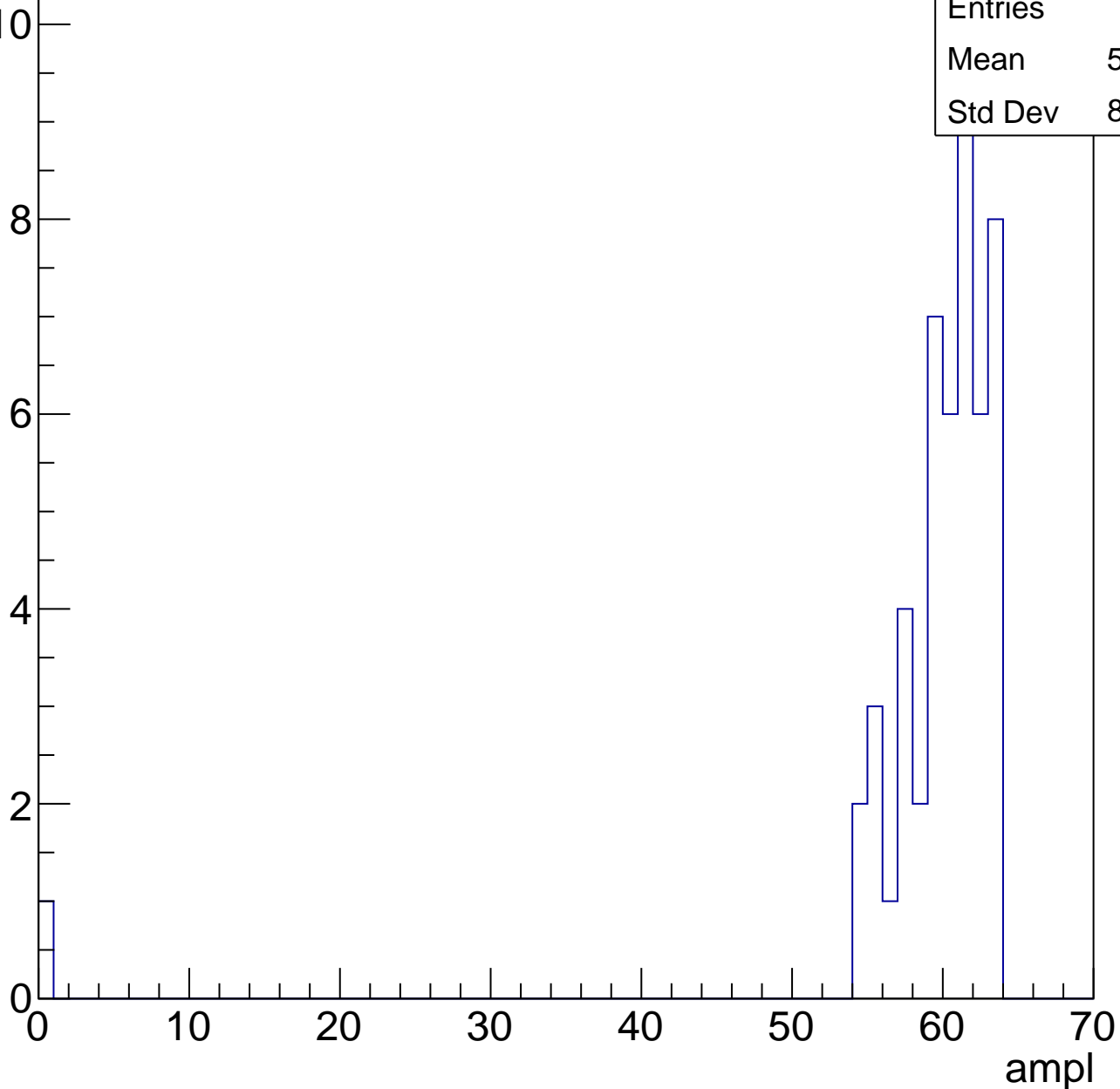


B0L001S, U18-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

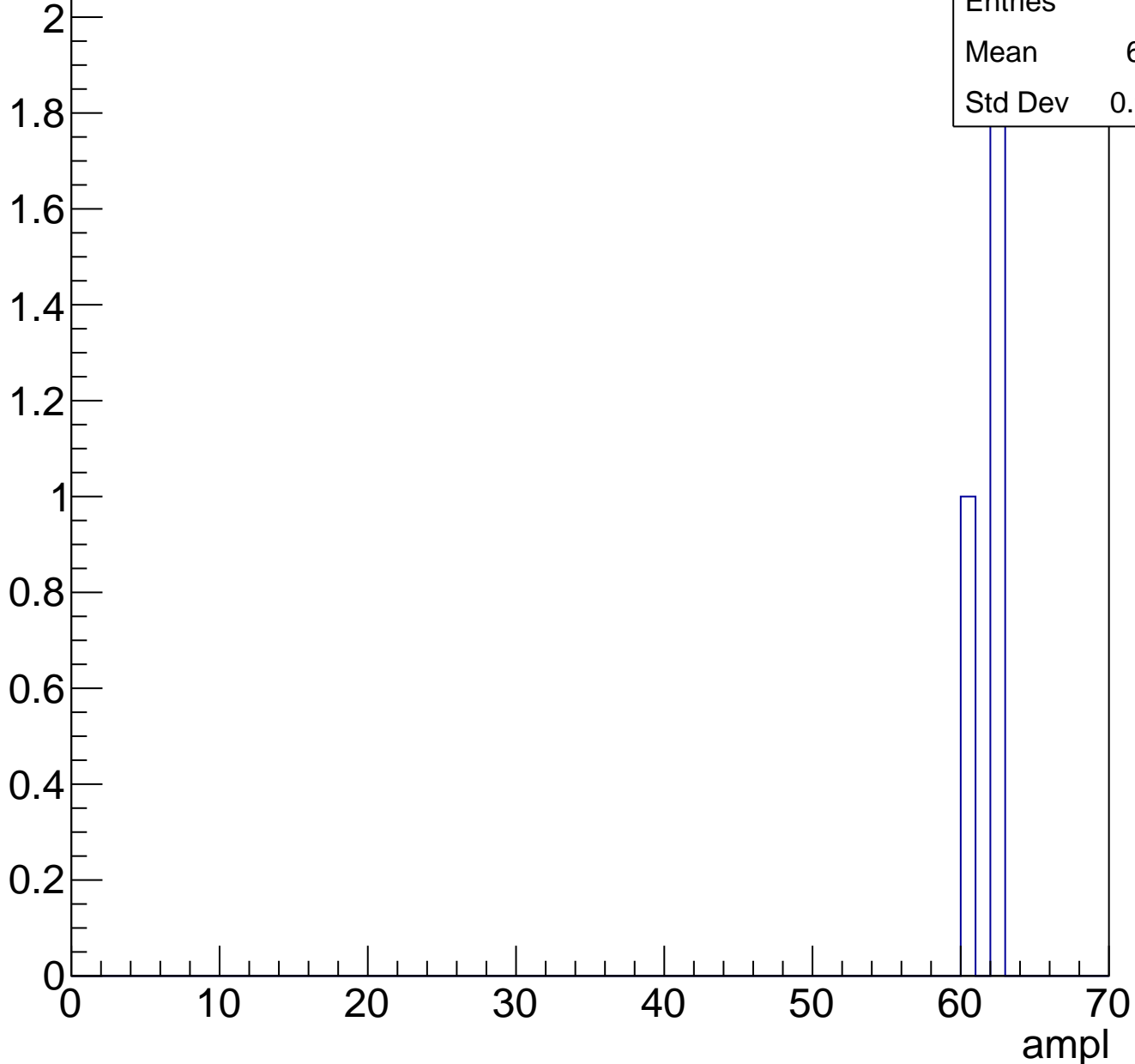
Entries	50
Mean	58.64
Std Dev	8.749



B0L001S, U18-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch6, adc0

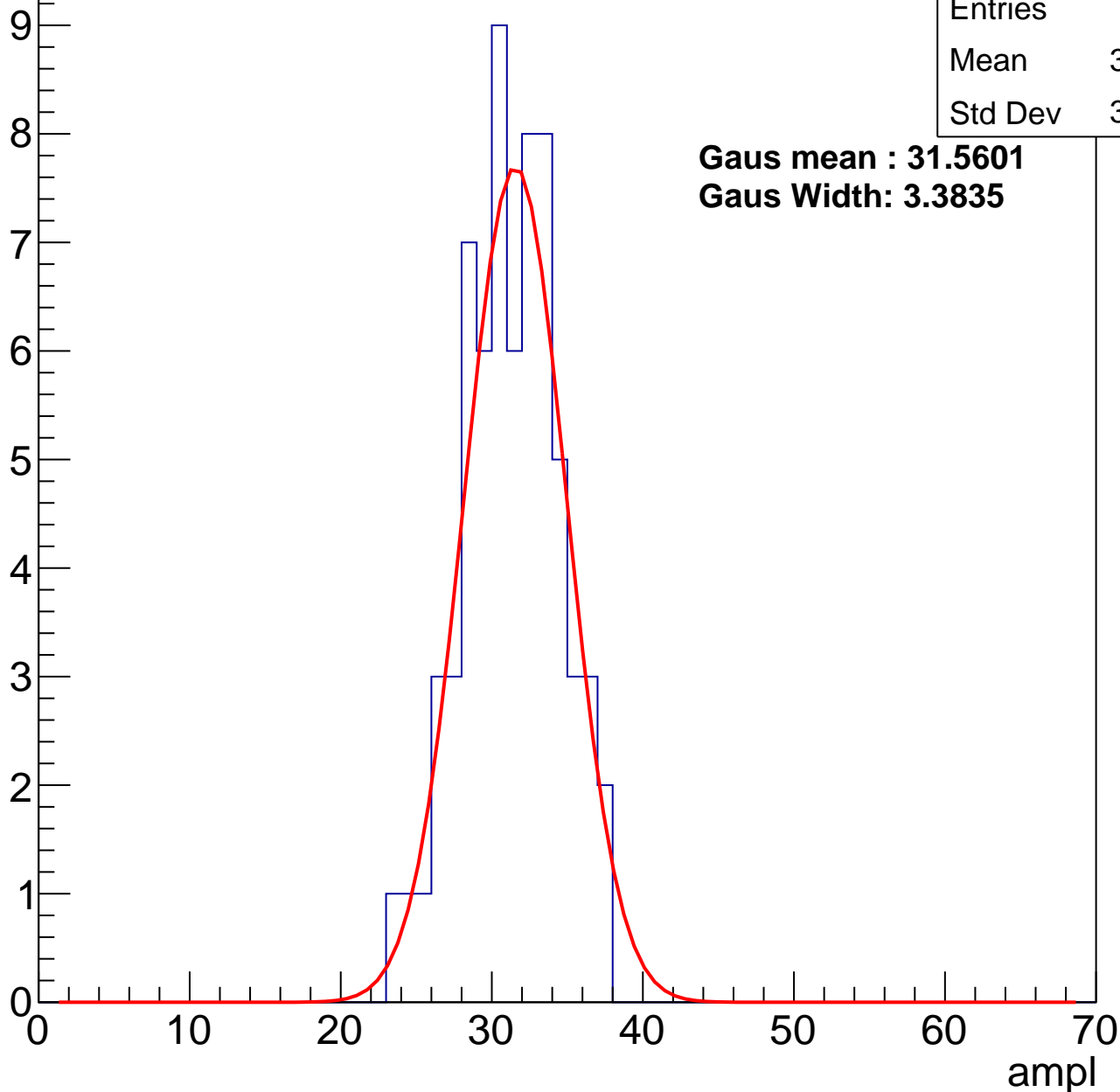
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.82
Std Dev	3.128

Gaus mean : 31.5601

Gaus Width: 3.3835



B0L001S, U18-ch6, adc1

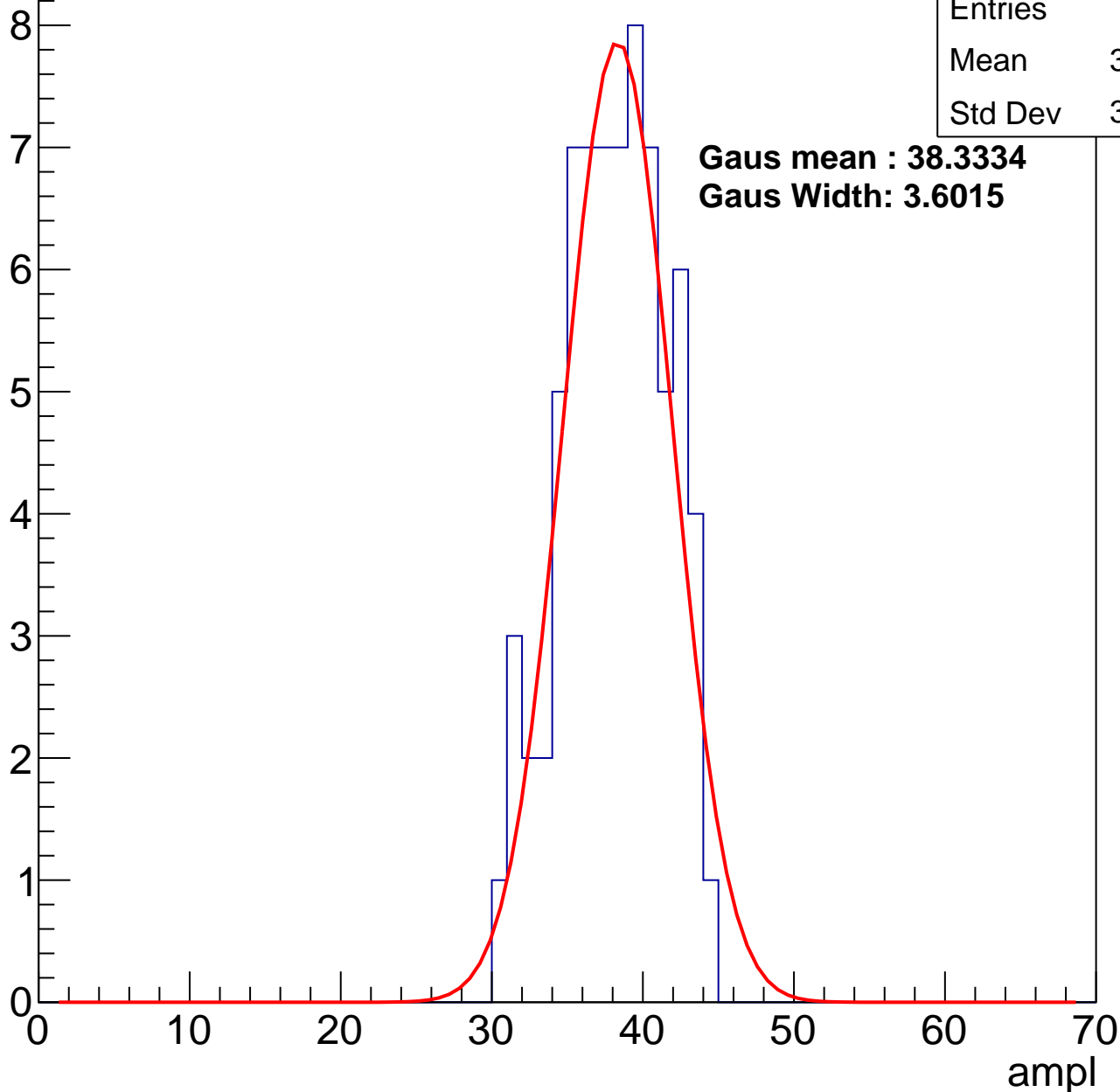
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.64
Std Dev	3.364

Gaus mean : 38.3334

Gaus Width: 3.6015



B0L001S, U18-ch6, adc2

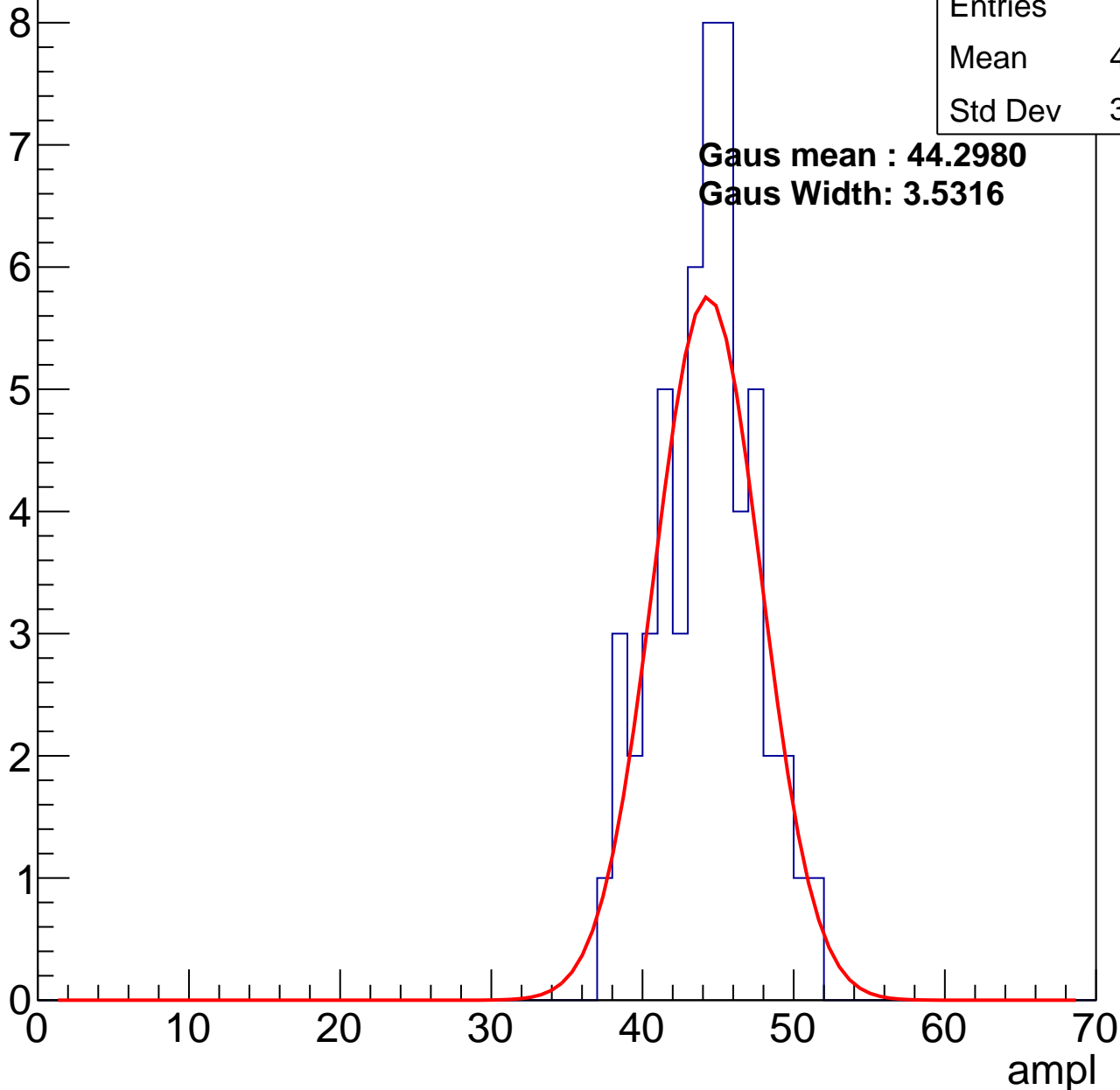
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.78
Std Dev	3.195

Gaus mean : 44.2980

Gaus Width: 3.5316

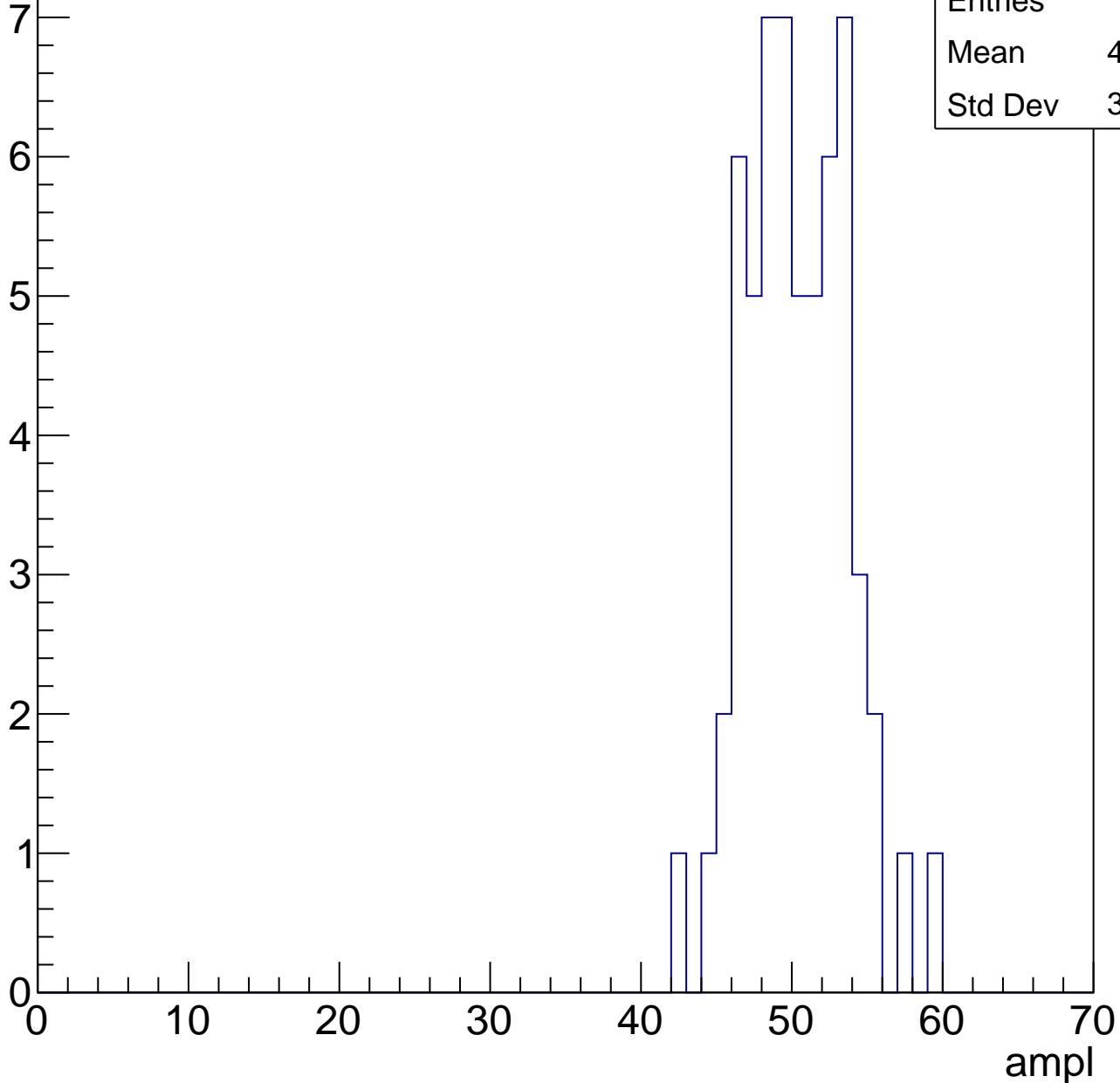


B0L001S, U18-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.86
Std Dev	3.306

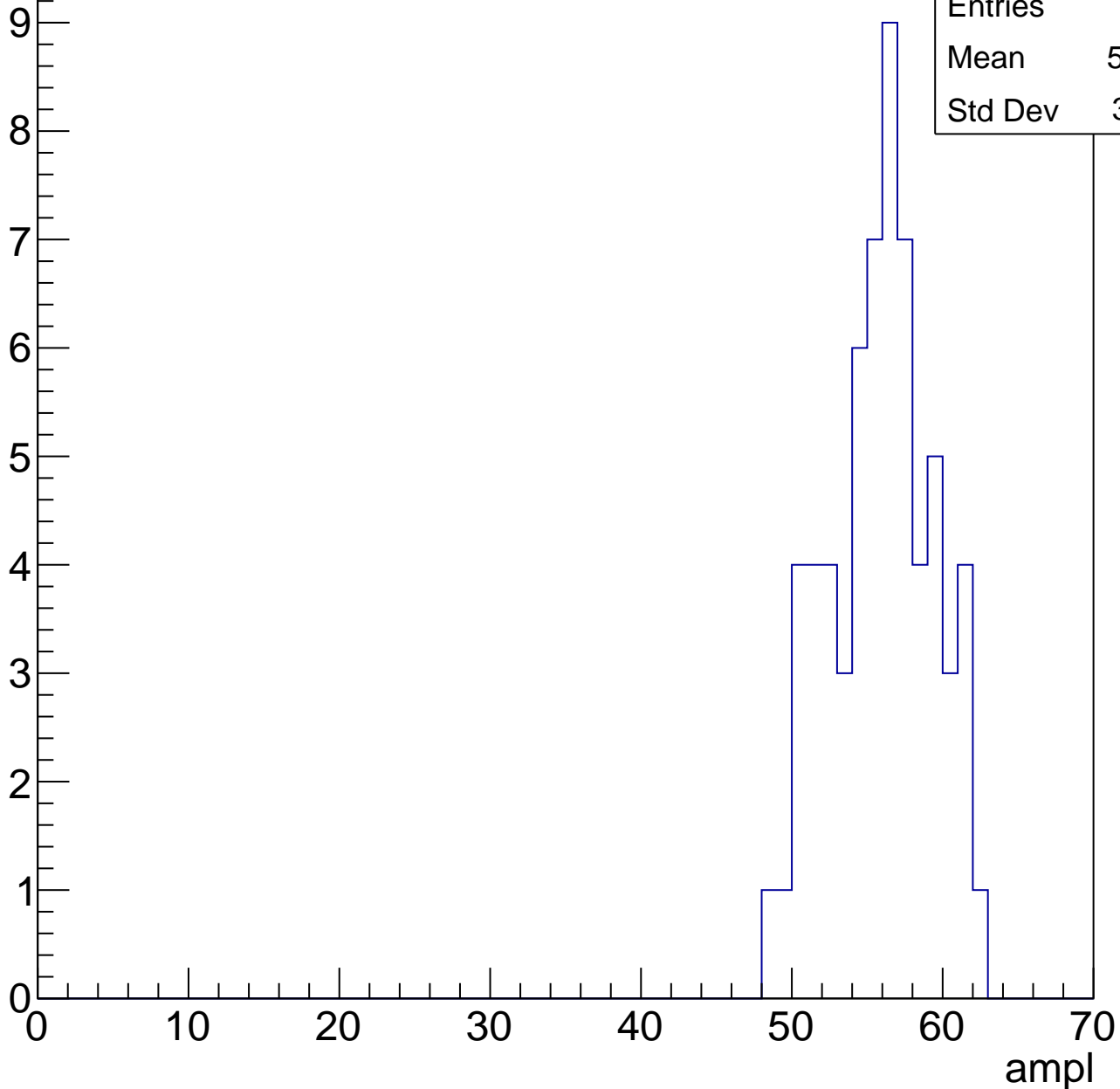


B0L001S, U18-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	55.44
Std Dev	3.361

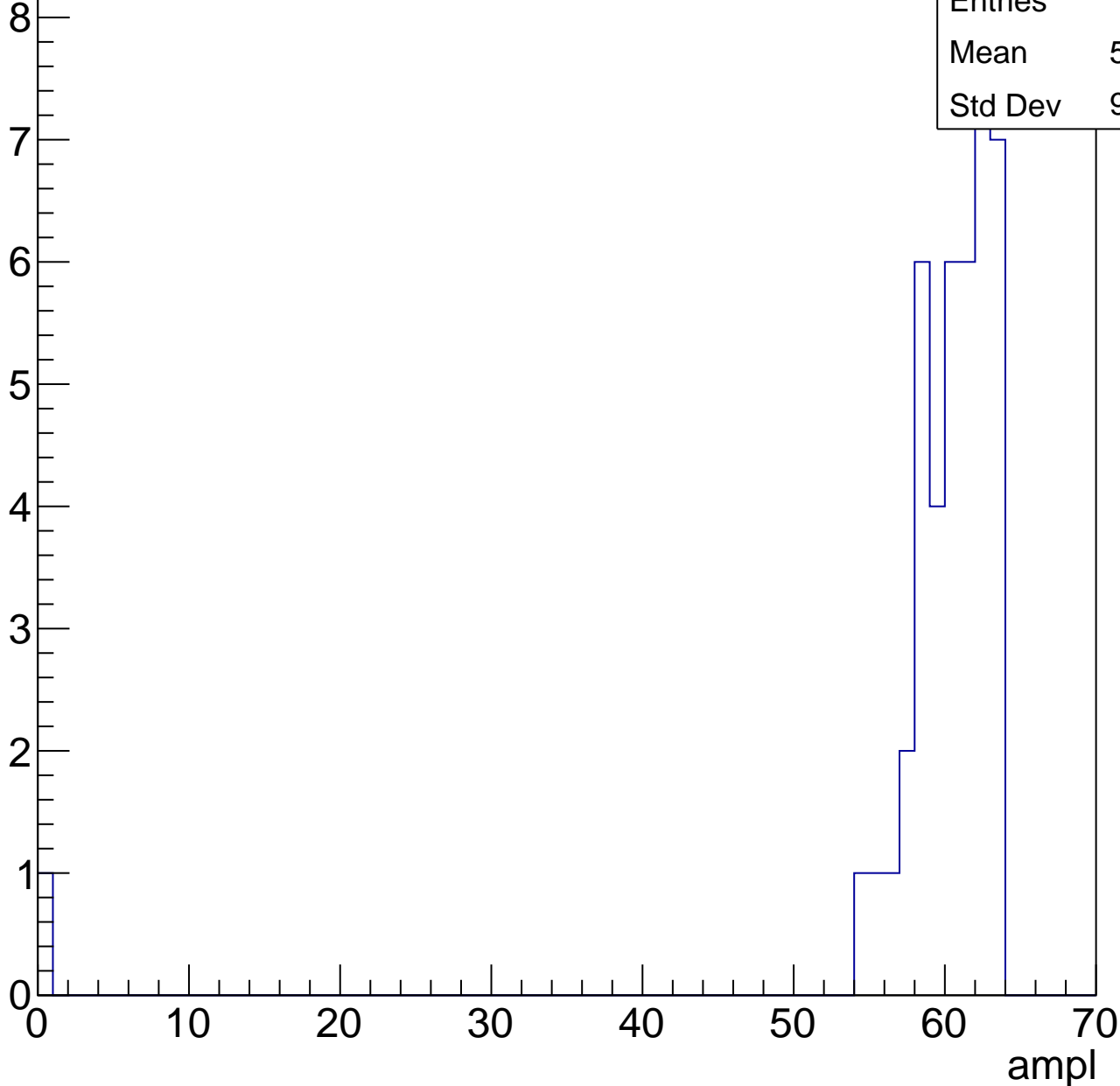


B0L001S, U18-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.74
Std Dev	9.346



B0L001S, U18-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch7, adc0

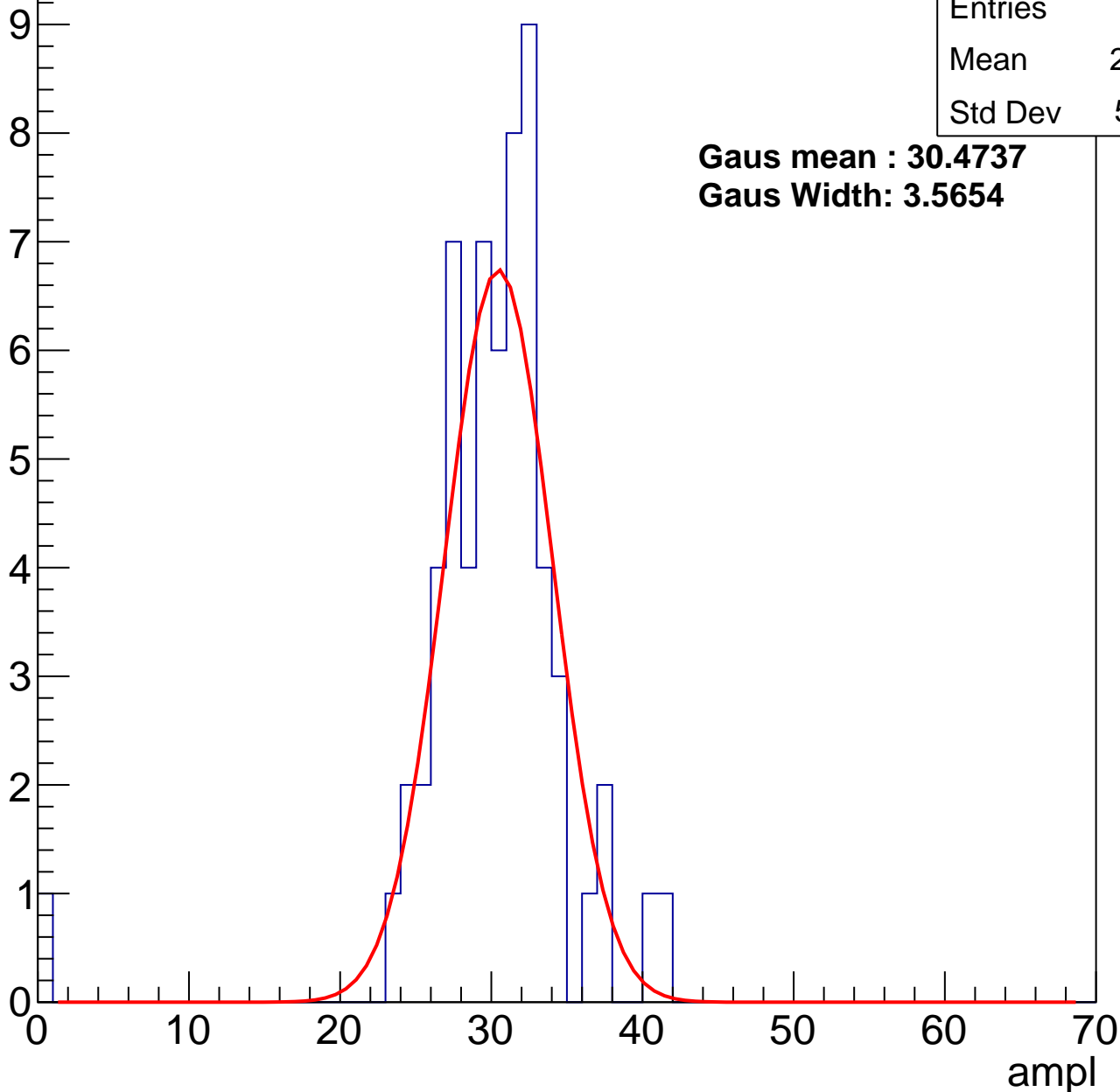
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.68
Std Dev	5.191

Gaus mean : 30.4737

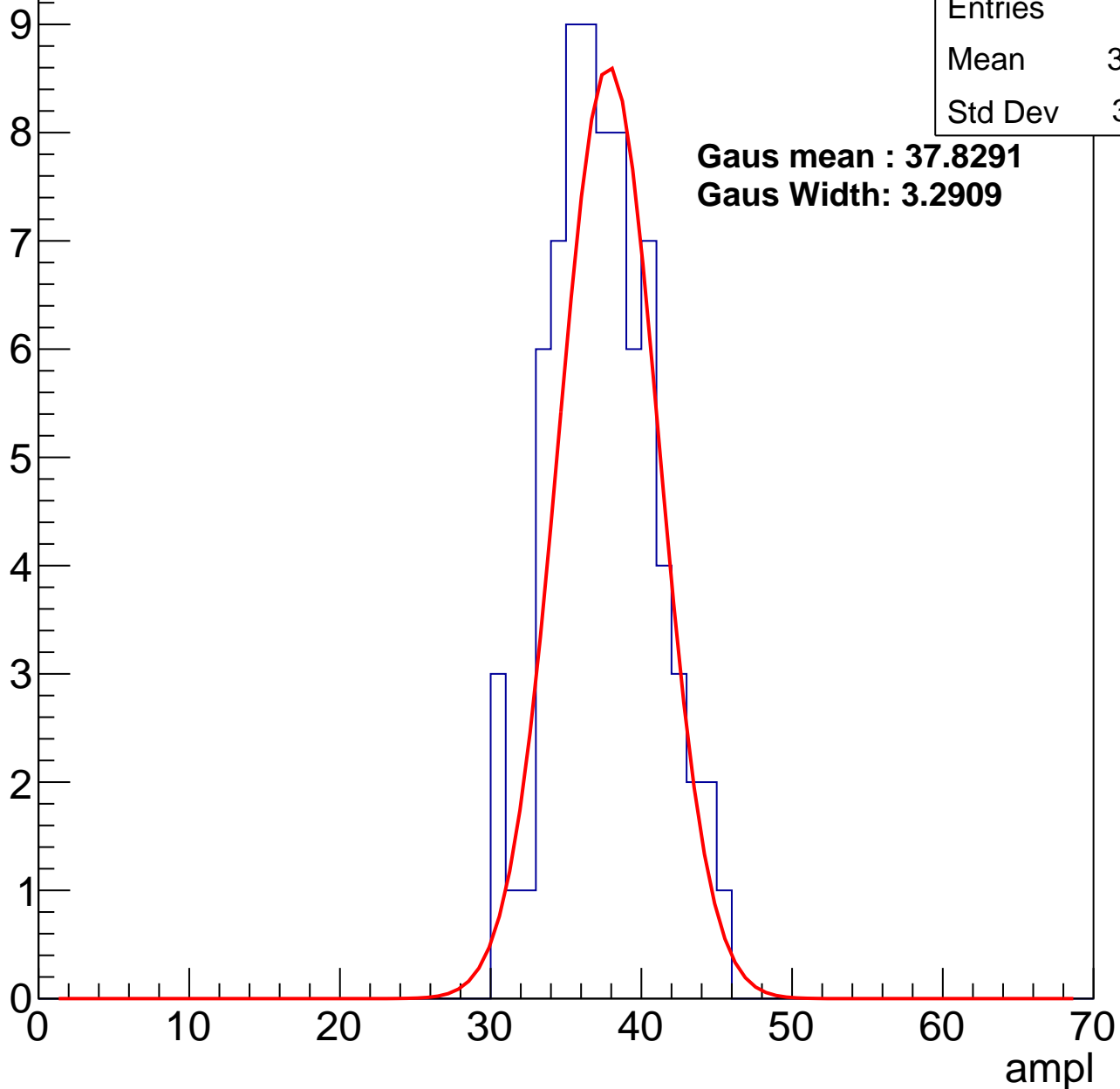
Gaus Width: 3.5654



B0L001S, U18-ch7, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch7, adc2

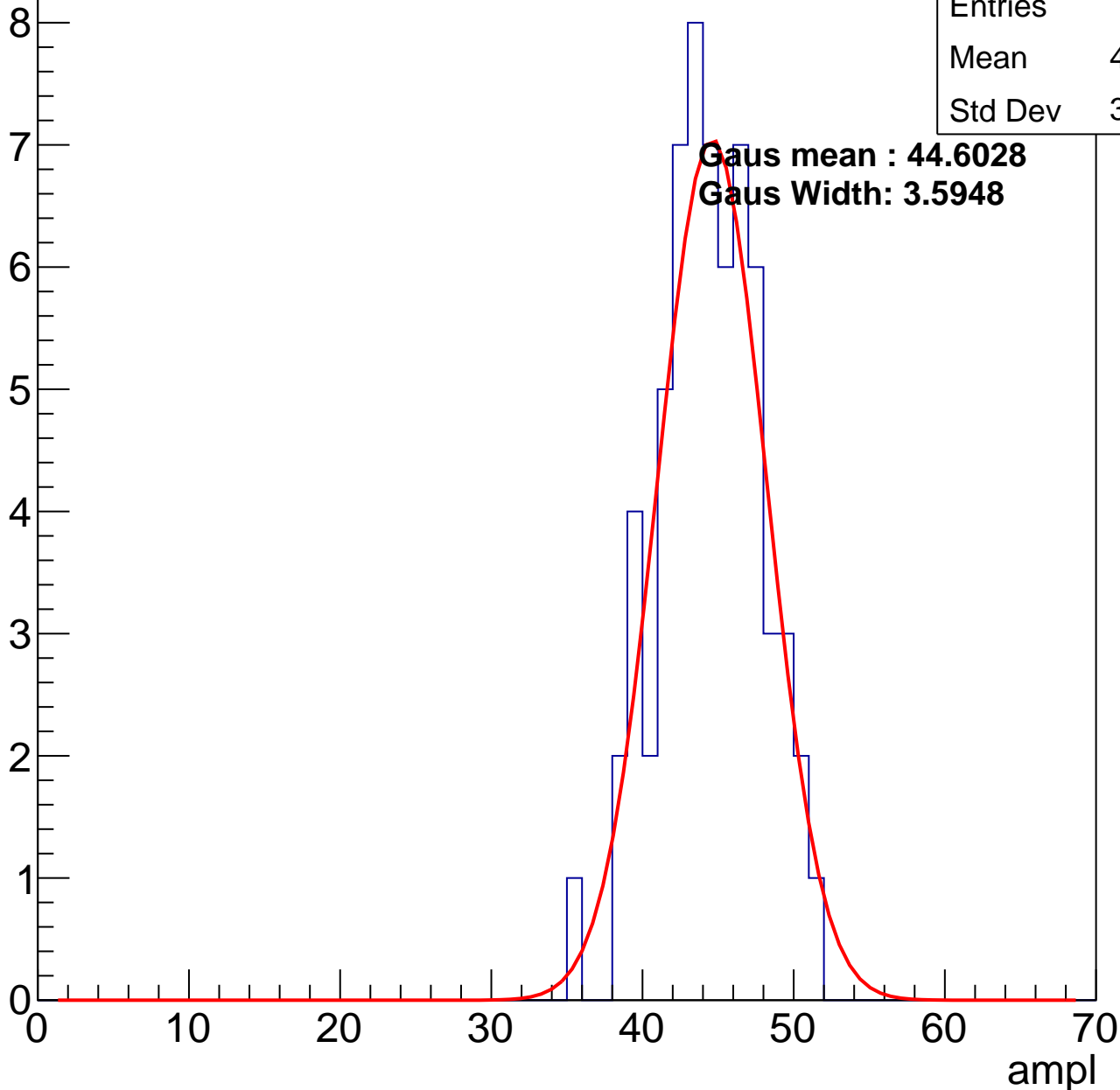
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.97
Std Dev	3.302

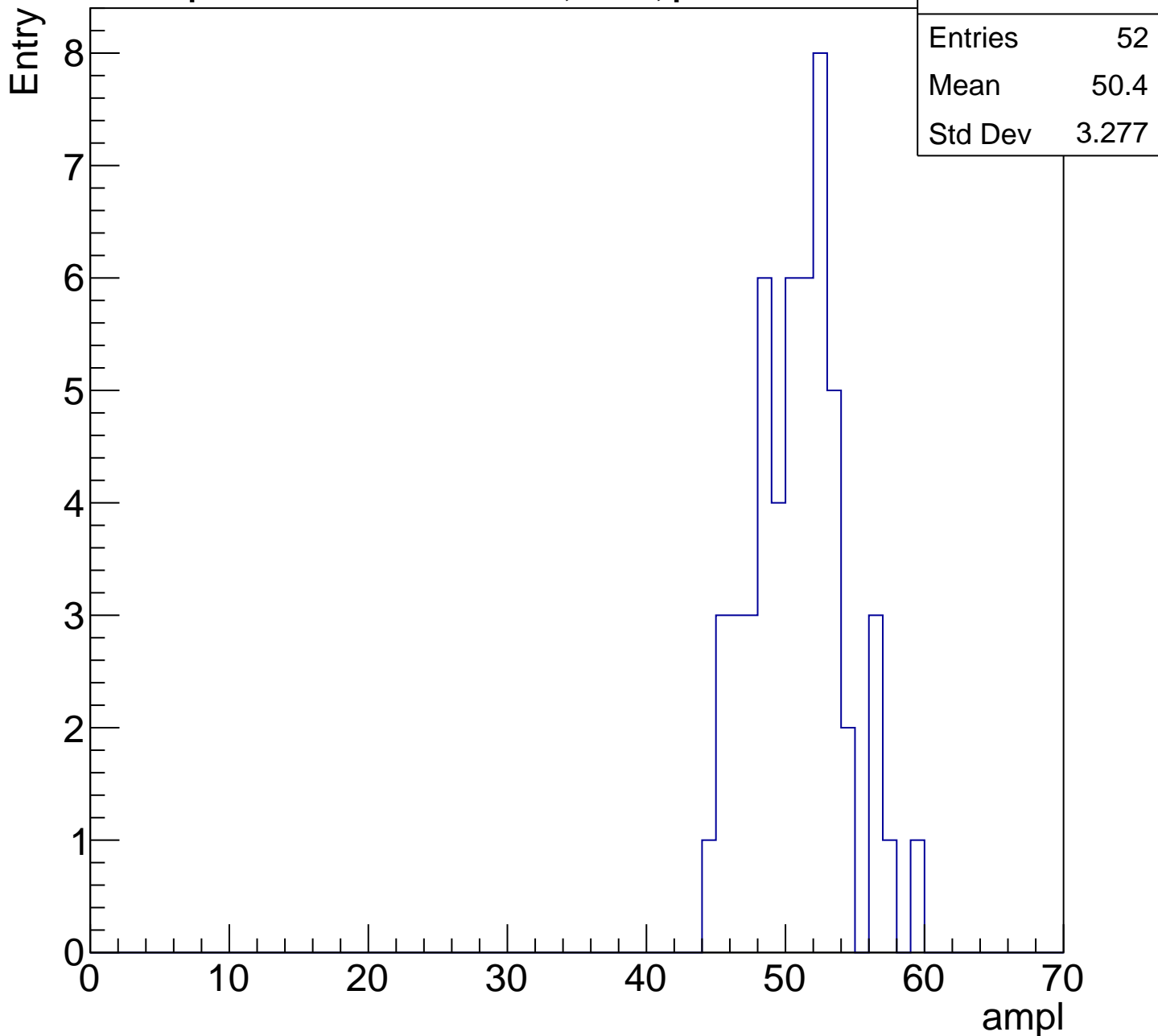
Gaus mean : 44.6028

Gaus Width: 3.5948



B0L001S, U18-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

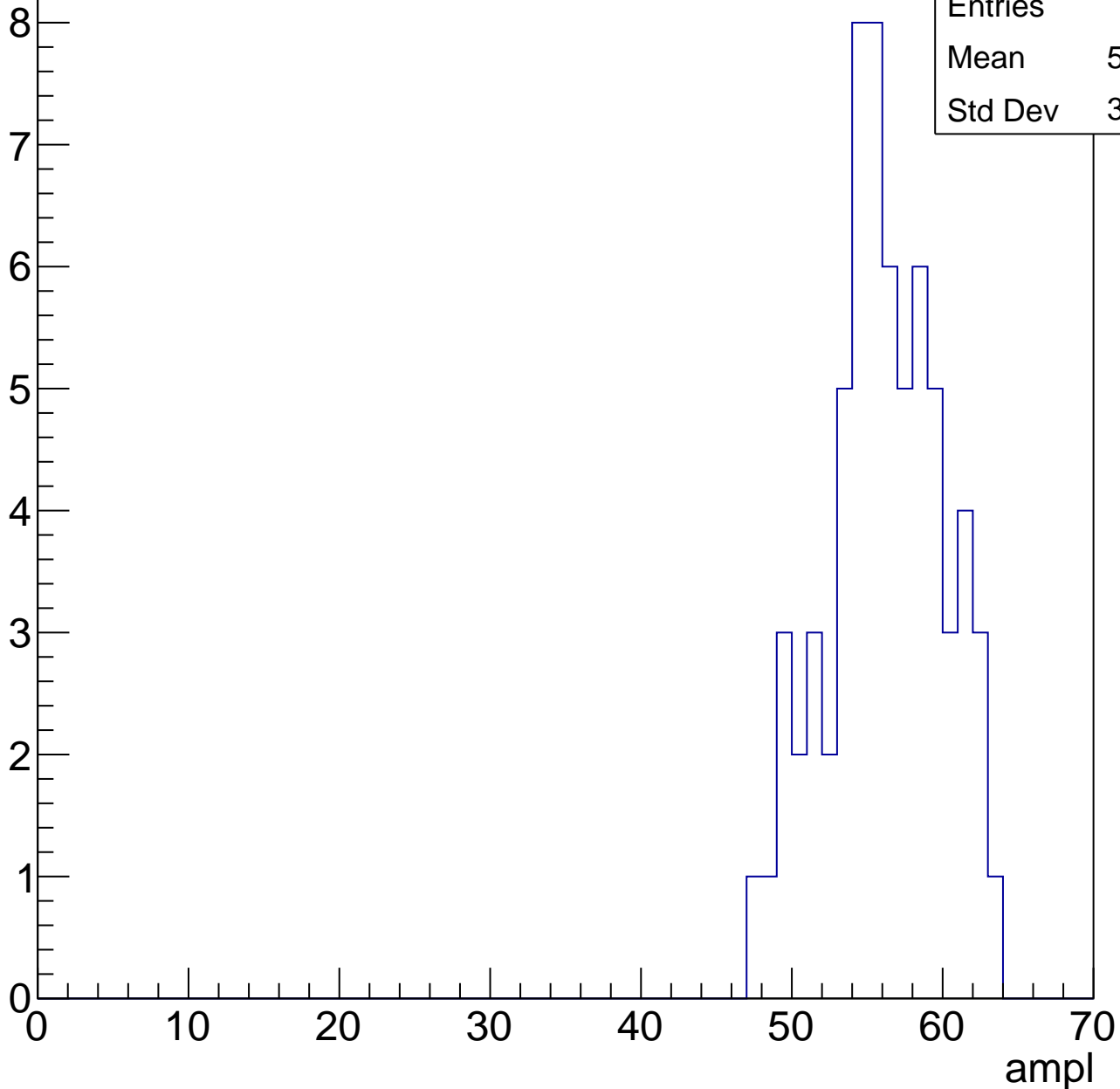


B0L001S, U18-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	55.65
Std Dev	3.748



B0L001S, U18-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	58
Std Dev	9.899

ampl

0

10

20

30

40

50

60

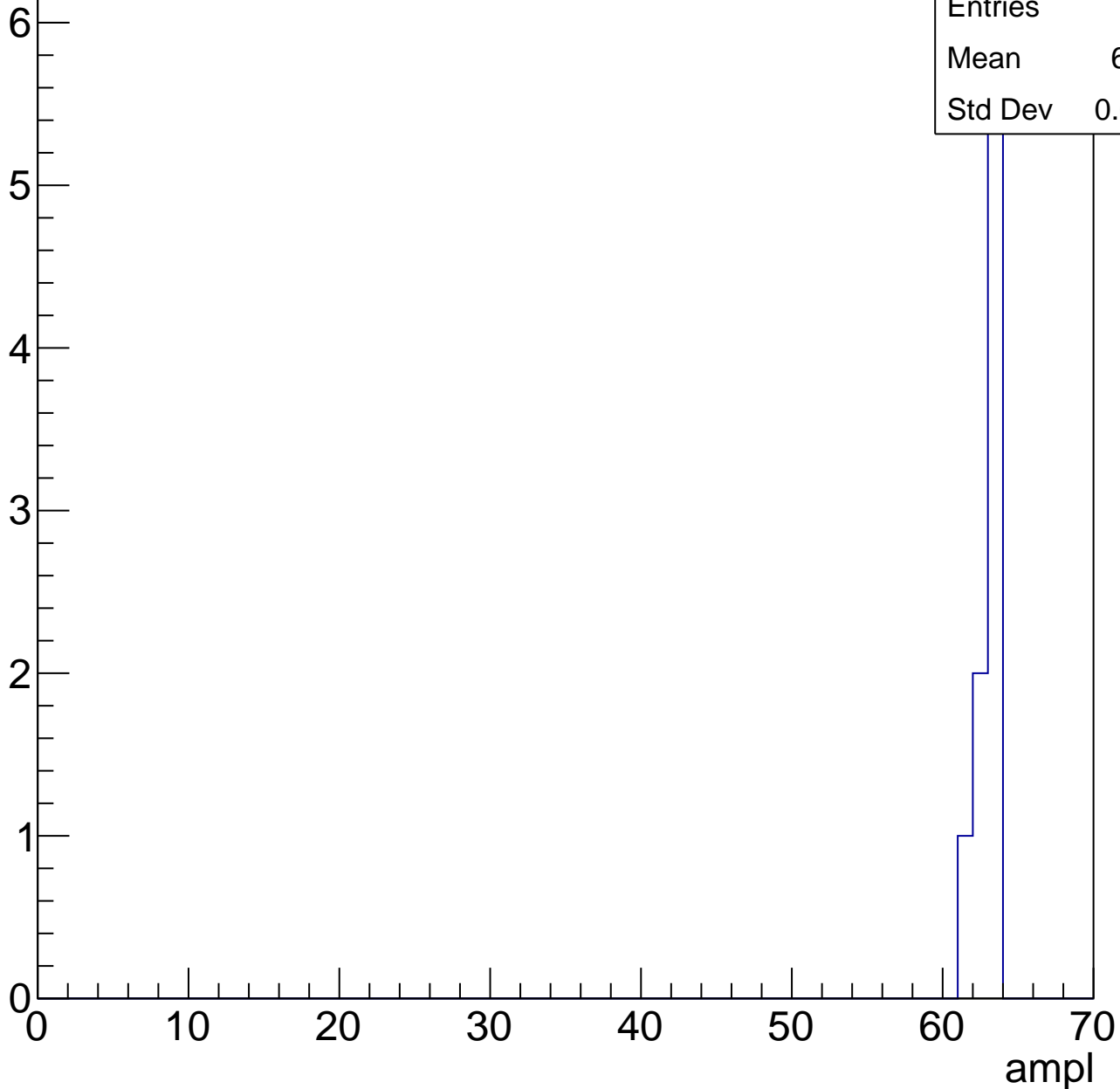
70

B0L001S, U18-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	62.56
Std Dev	0.6849



B0L001S, U18-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch8, adc0

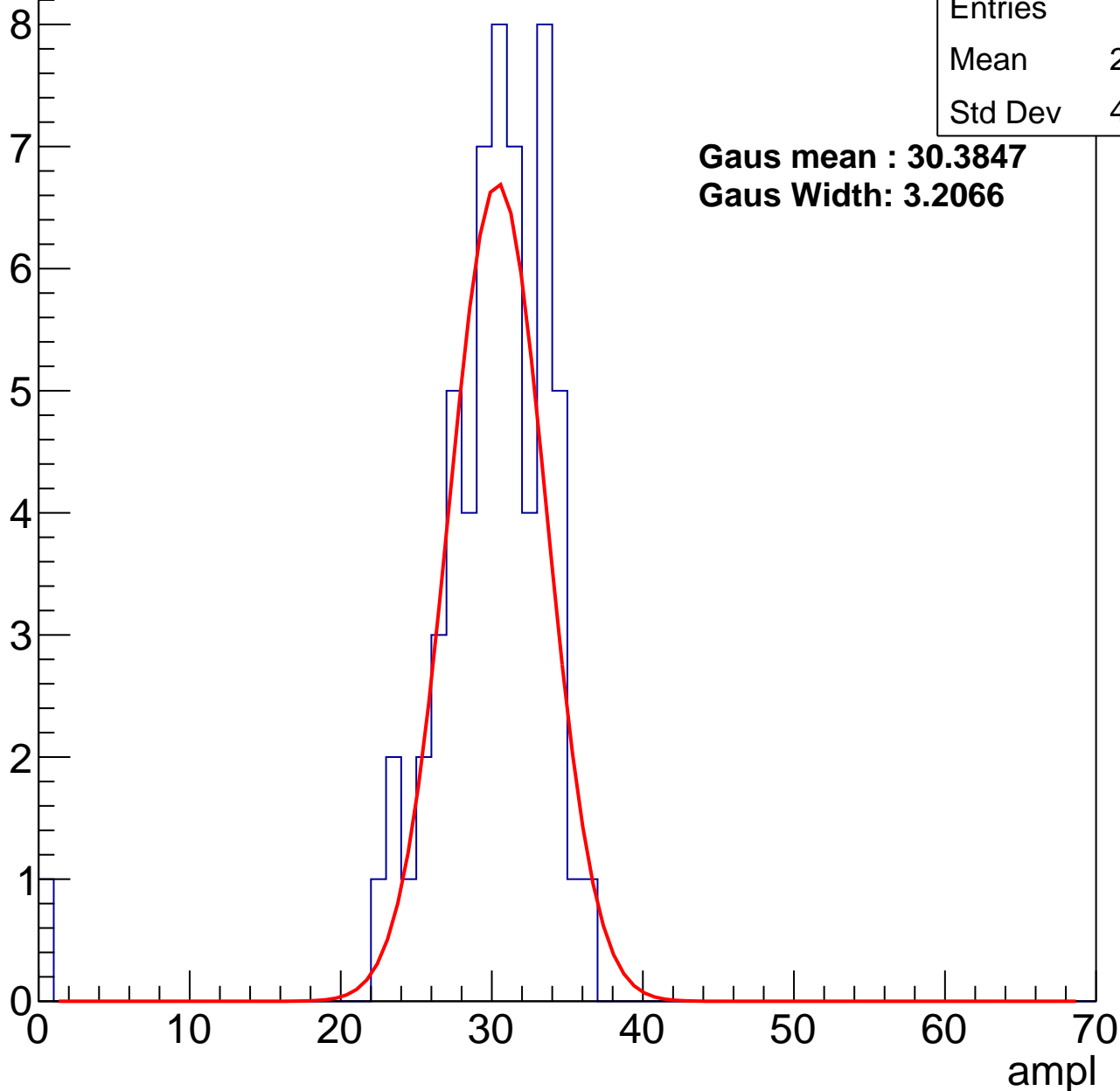
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.33
Std Dev	4.955

Gaus mean : 30.3847

Gaus Width: 3.2066



B0L001S, U18-ch8, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	36.81
Std Dev	3.323

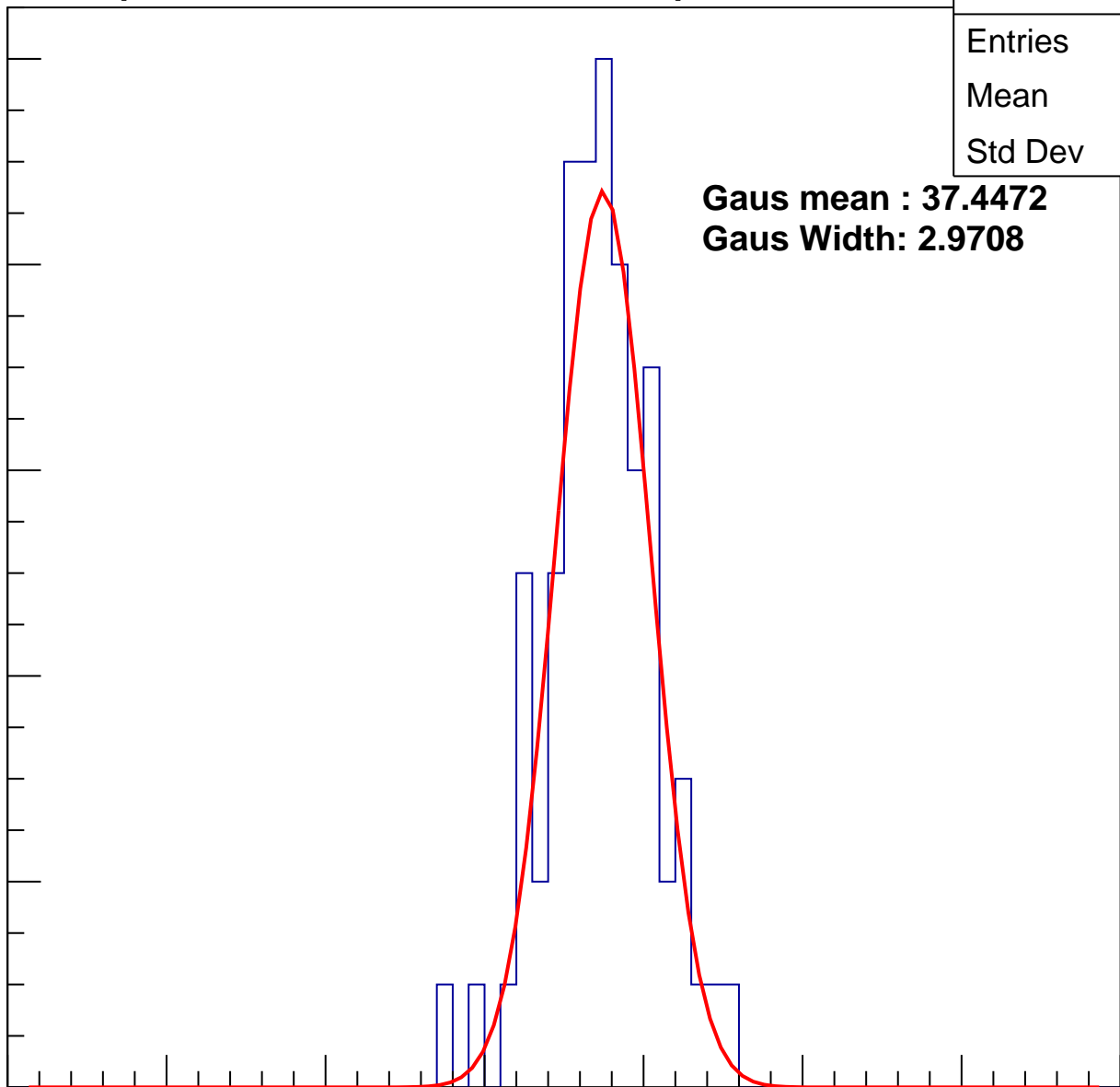
Gaus mean : 37.4472
Gaus Width: 2.9708

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch8, adc2

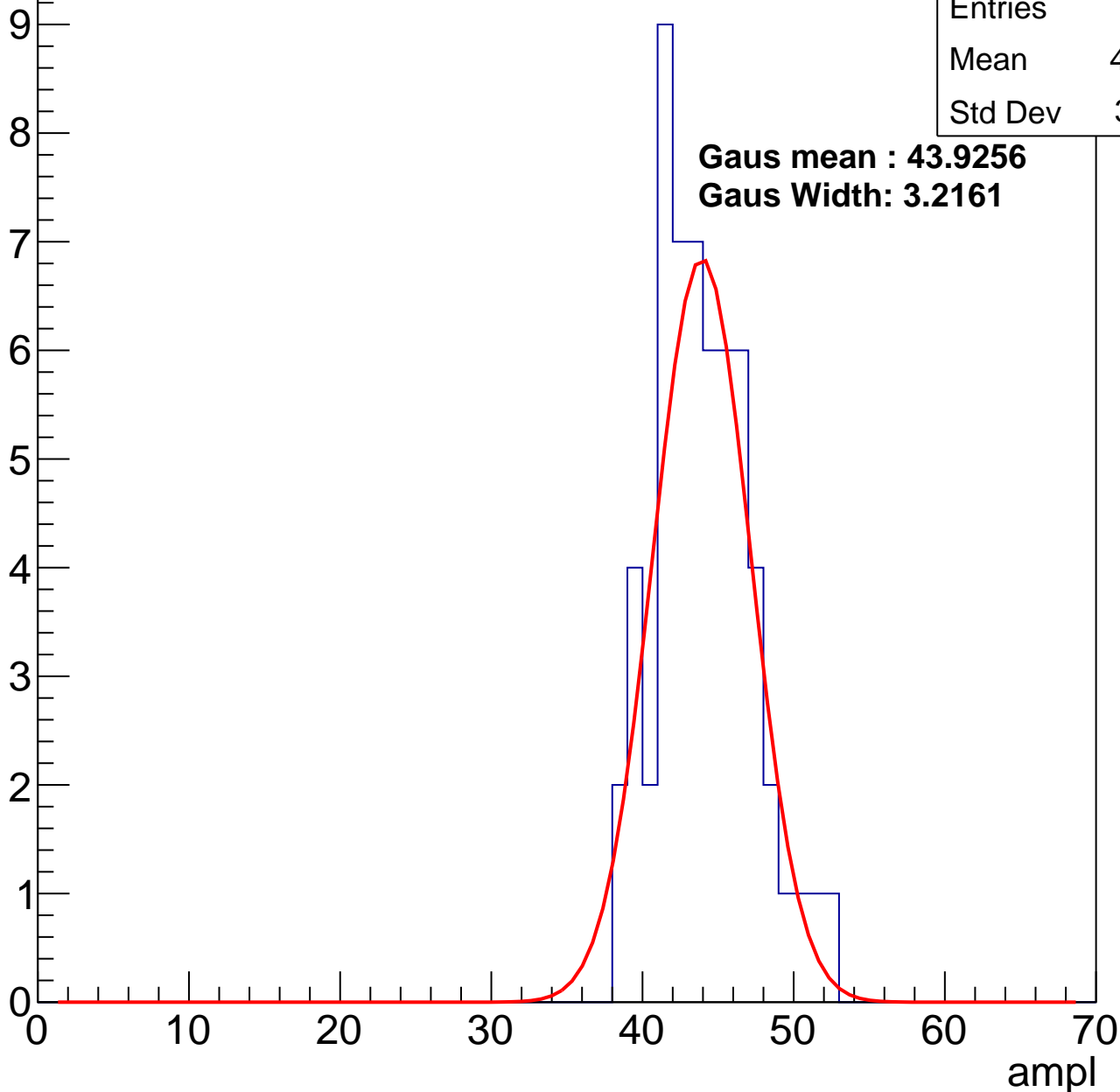
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.59
Std Dev	3.141

Gaus mean : 43.9256

Gaus Width: 3.2161

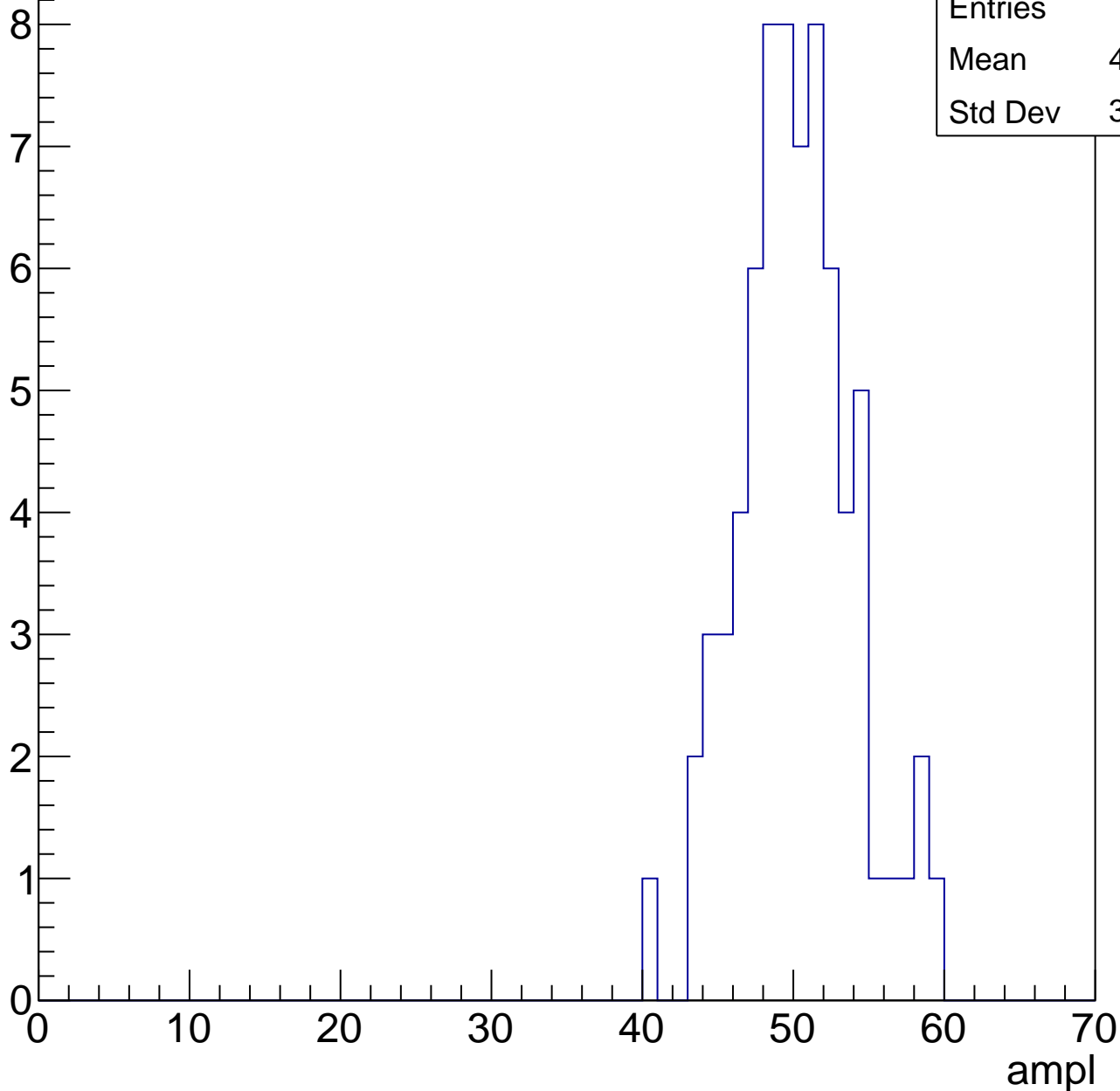


B0L001S, U18-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.72
Std Dev	3.754

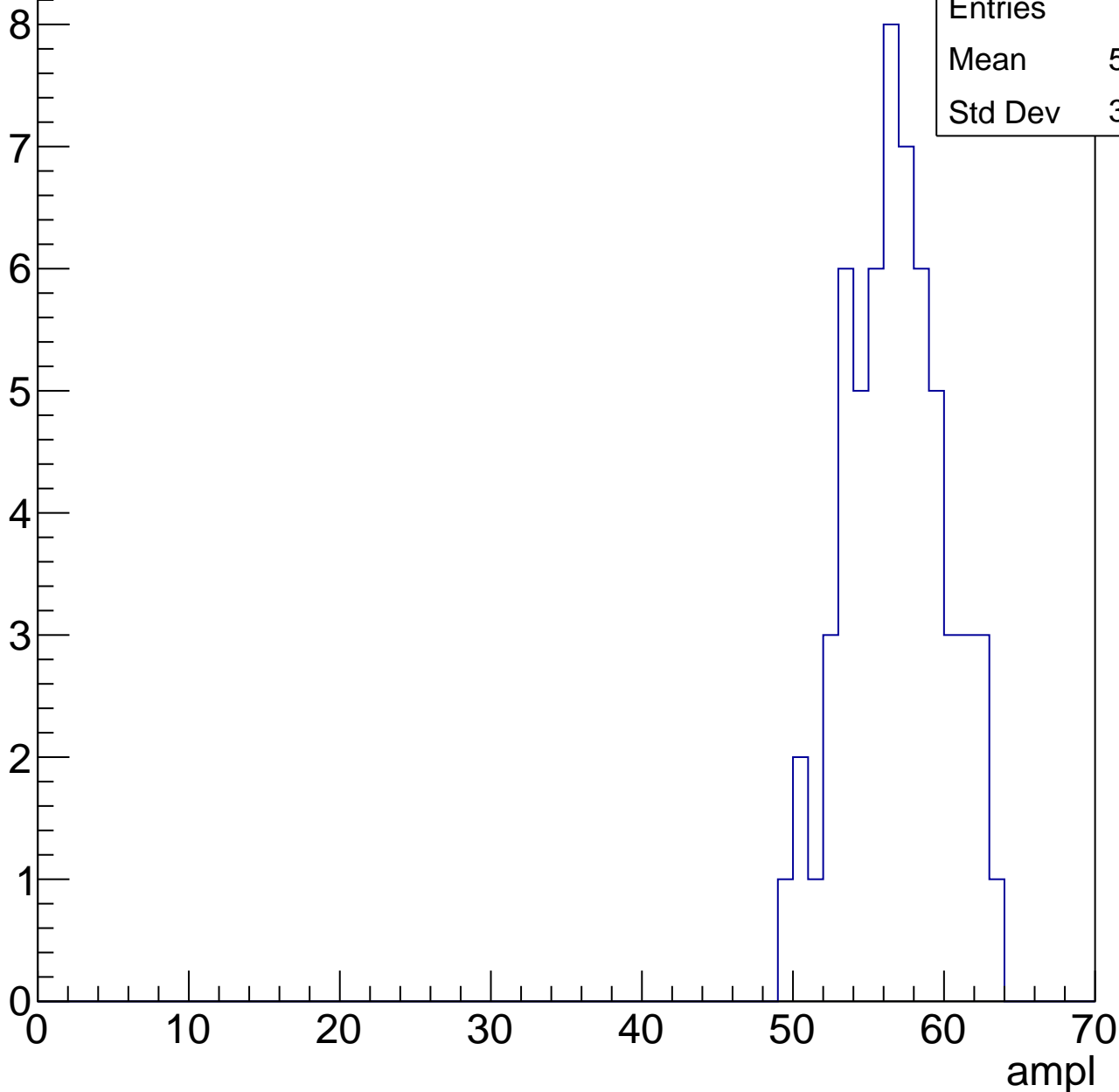


B0L001S, U18-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.27
Std Dev	3.229

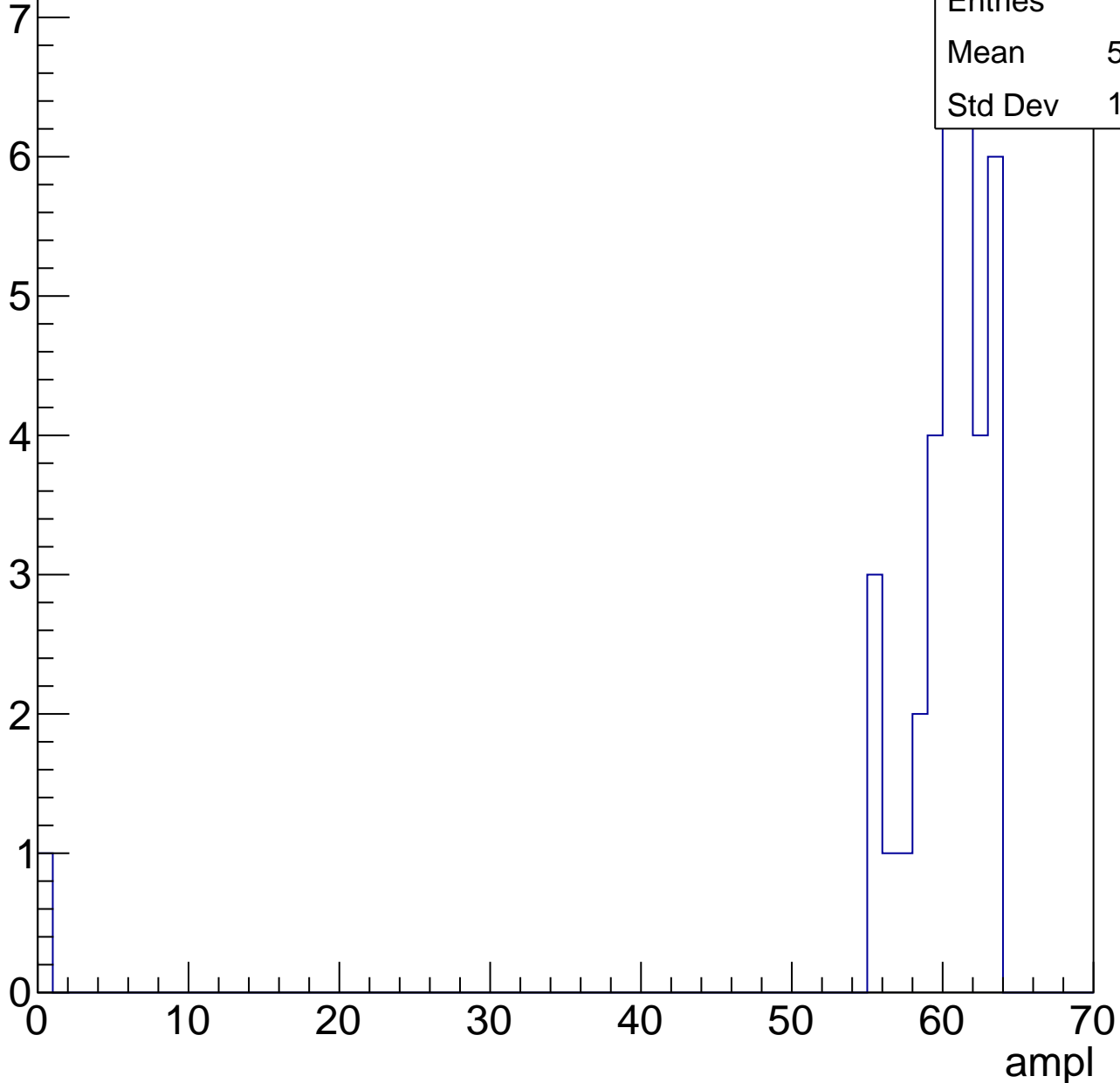


B0L001S, U18-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

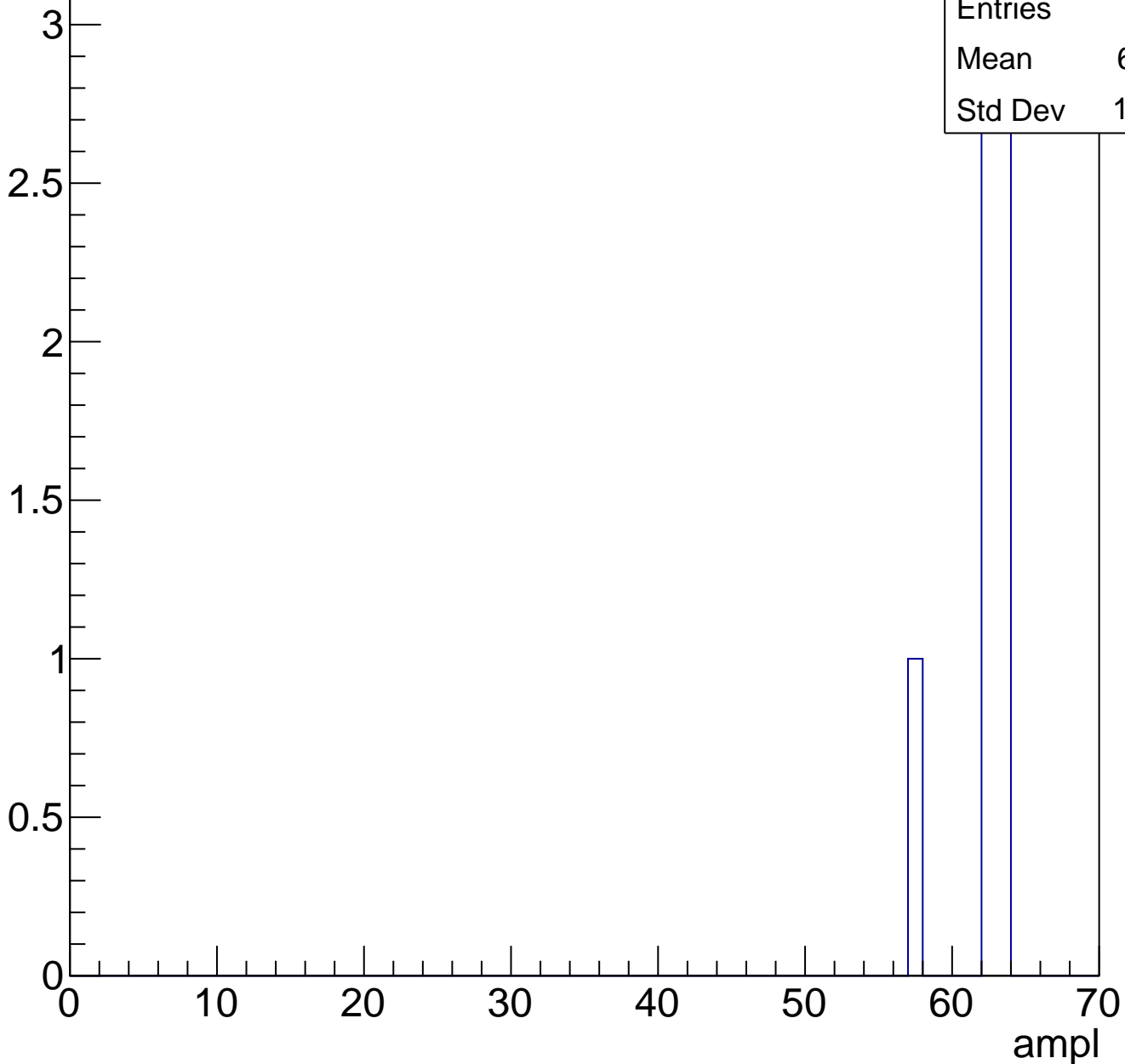
Entries	36
Mean	58.42
Std Dev	10.14



B0L001S, U18-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

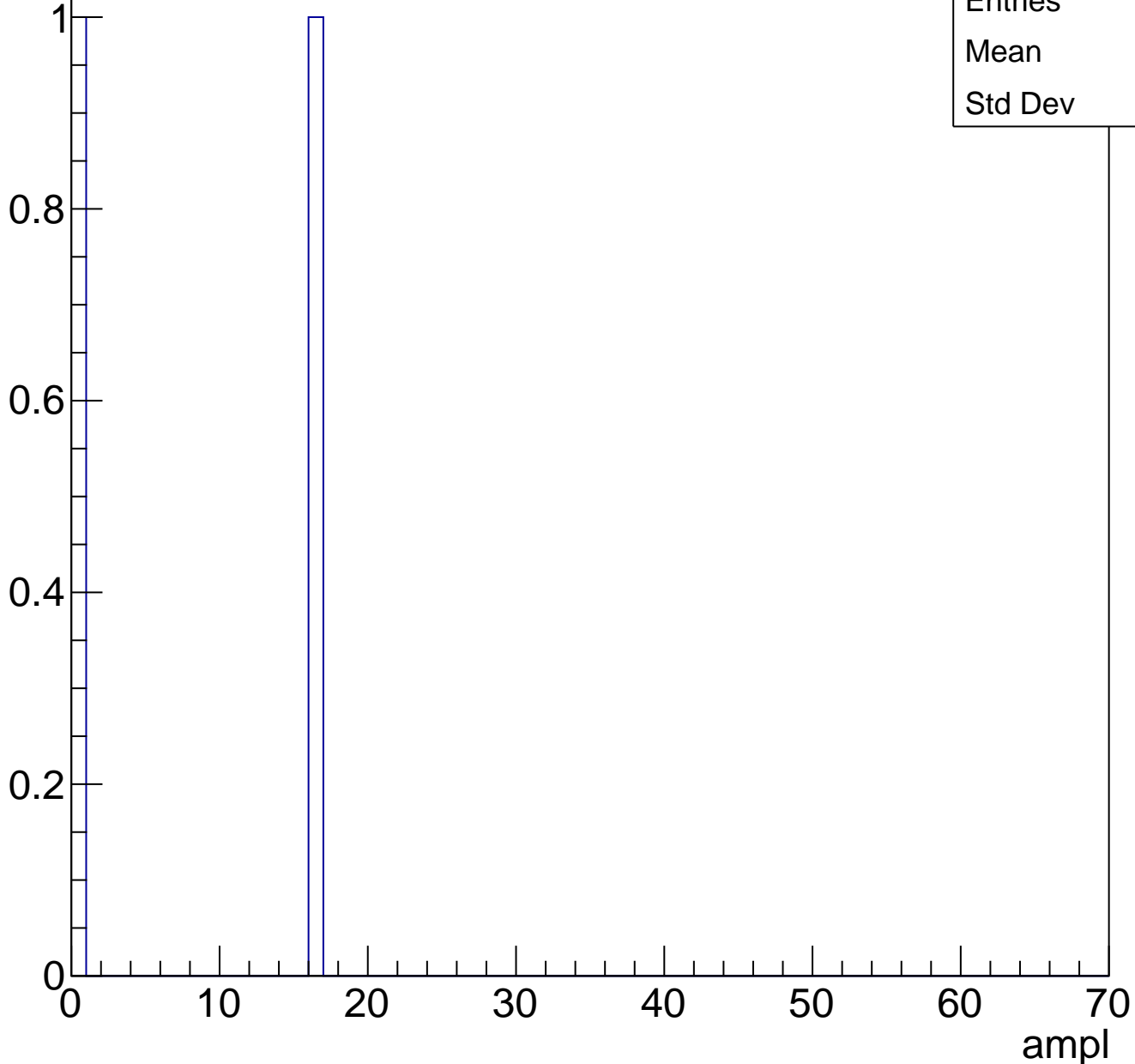
Entry



B0L001S, U18-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	8
Std Dev	8

B0L001S, U18-ch9, adc0

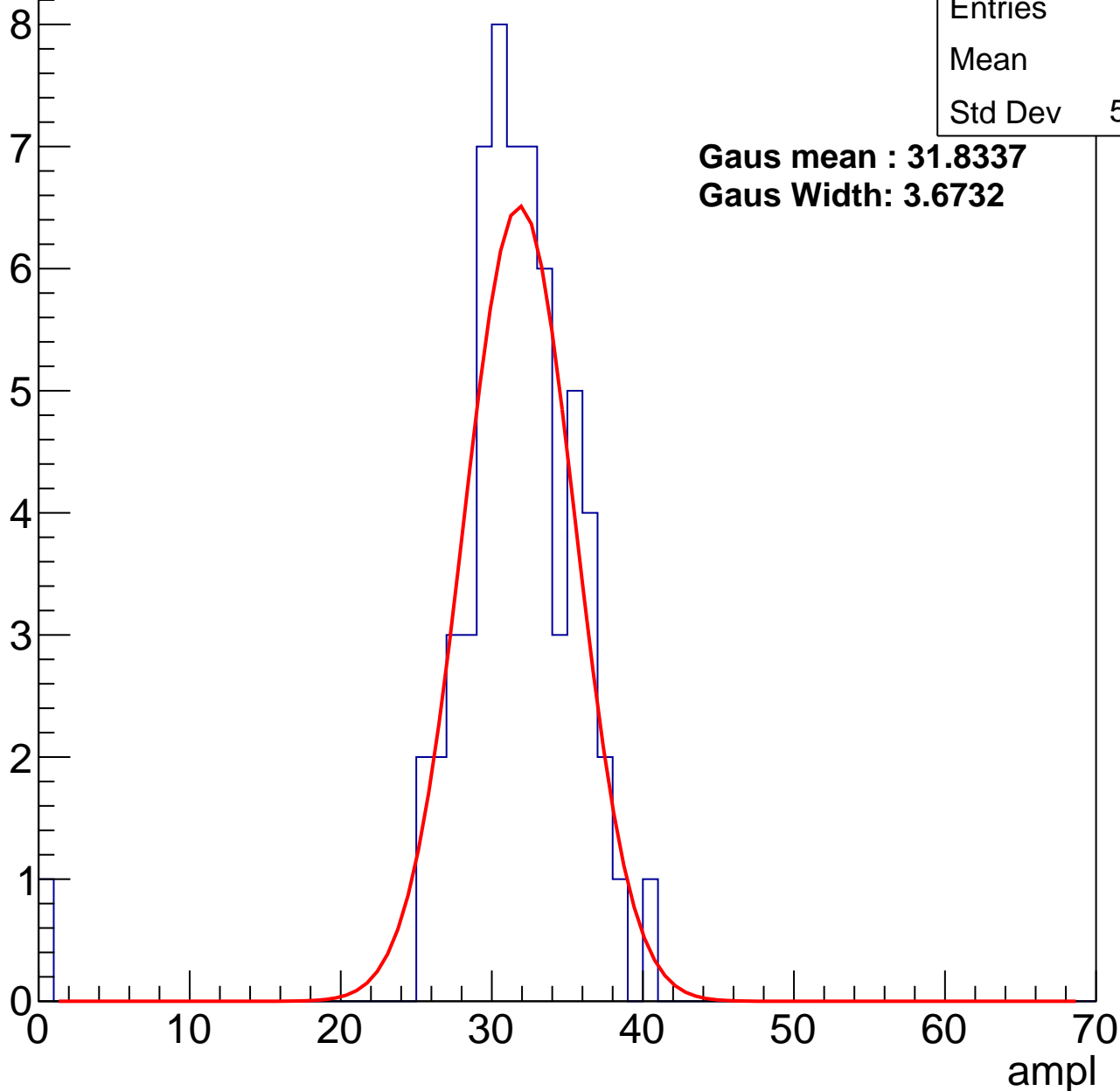
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31
Std Dev	5.134

Gaus mean : 31.8337

Gaus Width: 3.6732



B0L001S, U18-ch9, adc1

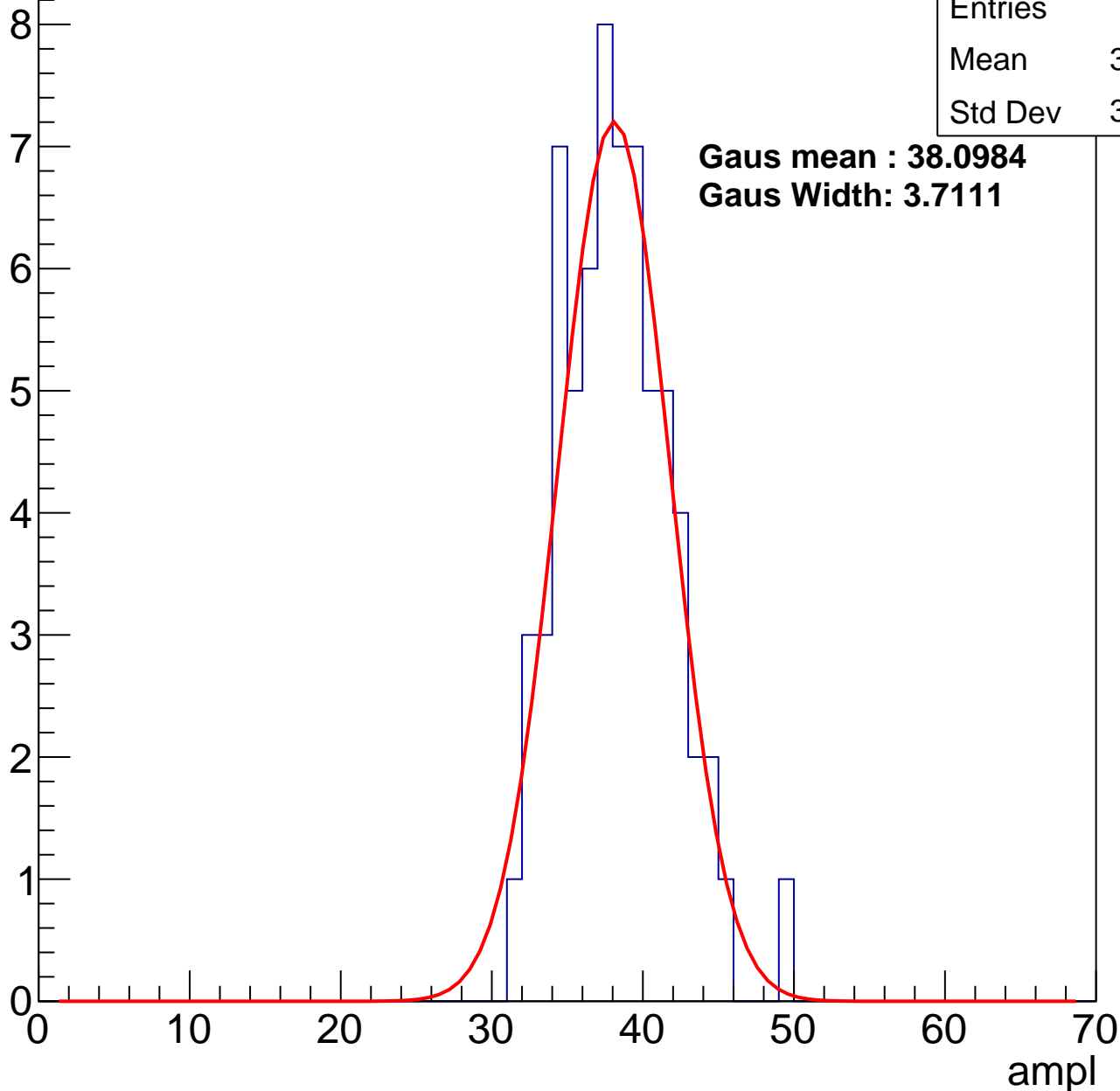
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.78
Std Dev	3.553

Gaus mean : 38.0984

Gaus Width: 3.7111



B0L001S, U18-ch9, adc2

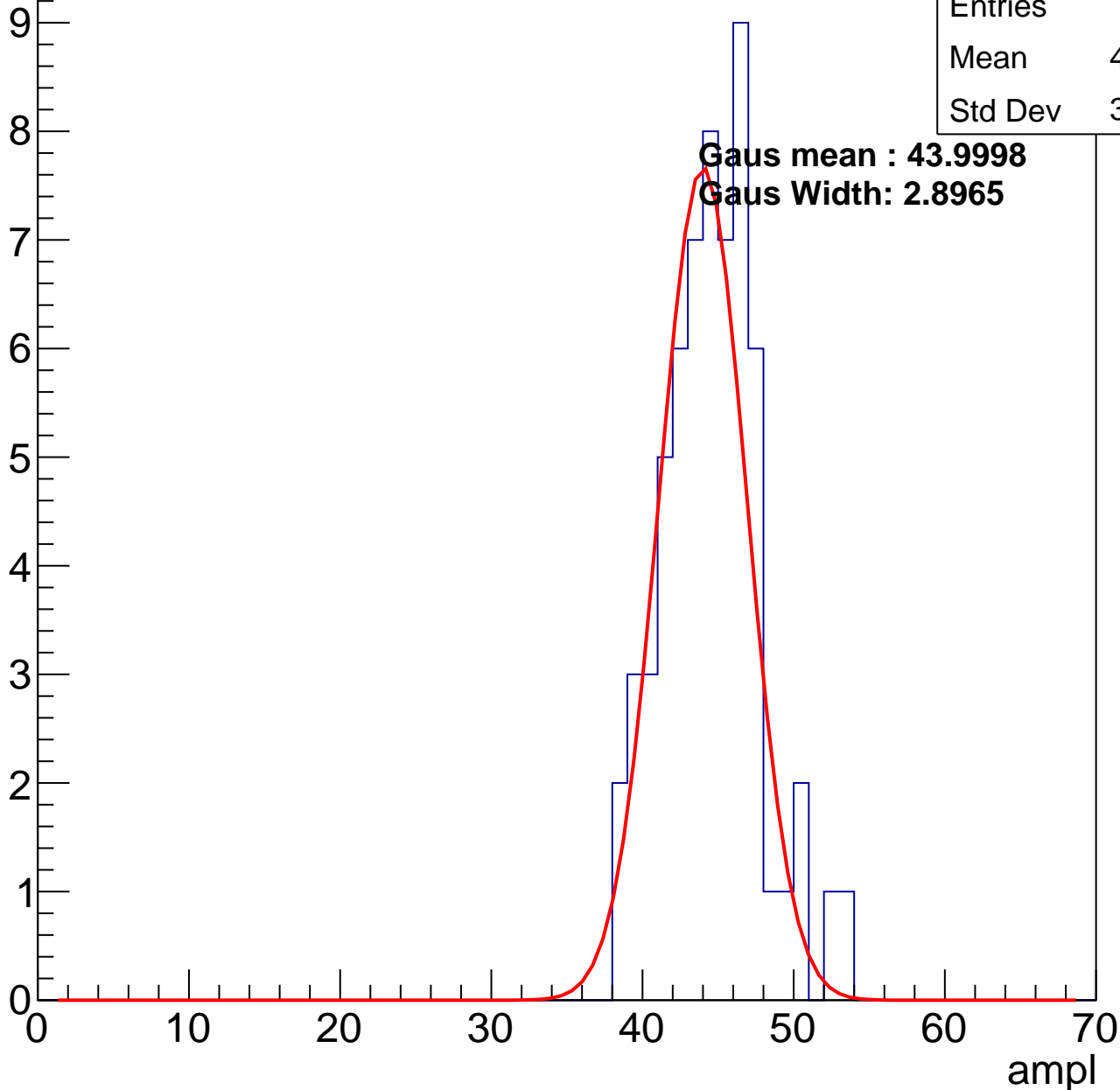
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.13
Std Dev	3.175

Gaus mean : 43.9998

Gaus Width: 2.8965

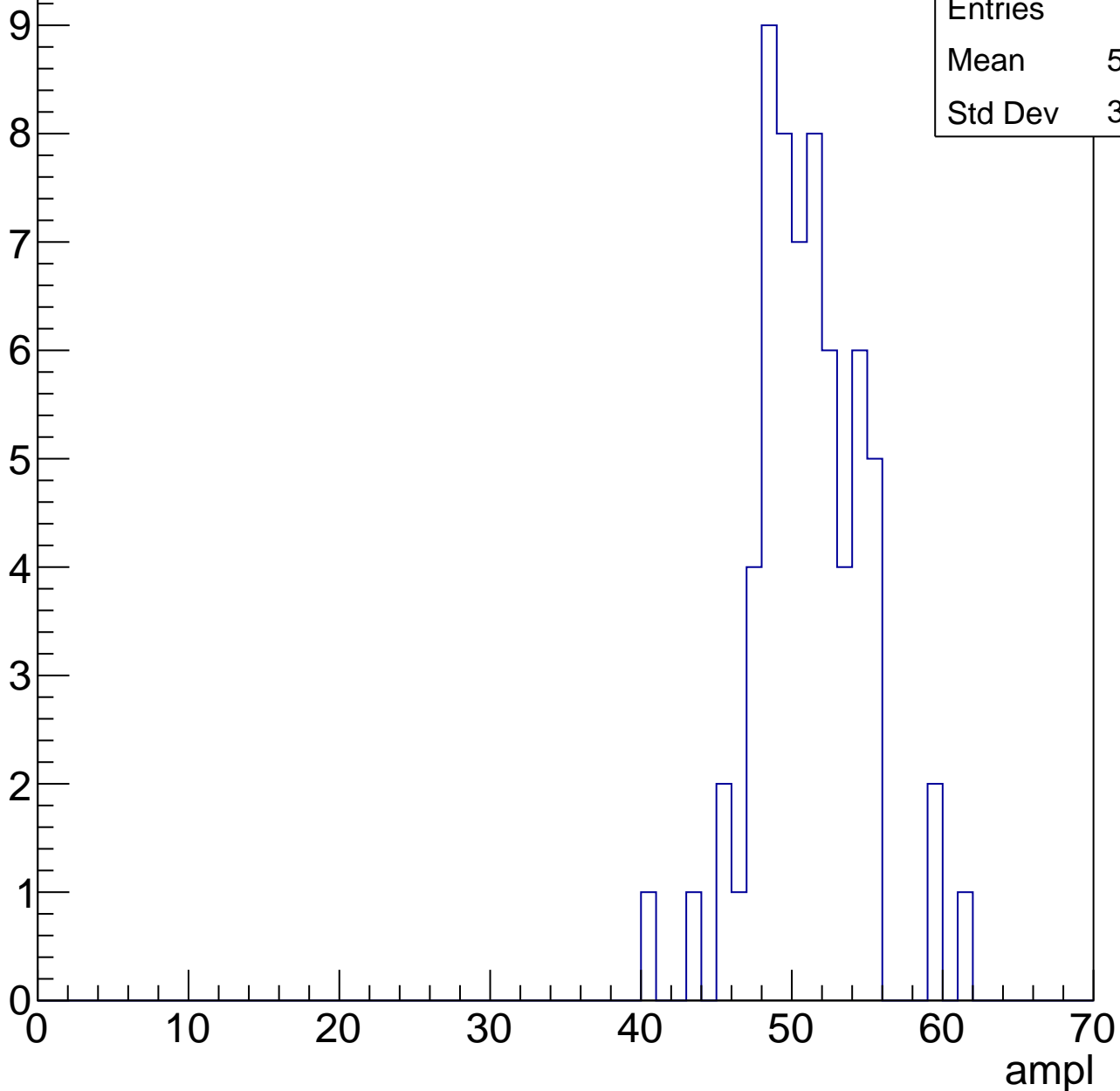


B0L001S, U18-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.63
Std Dev	3.597

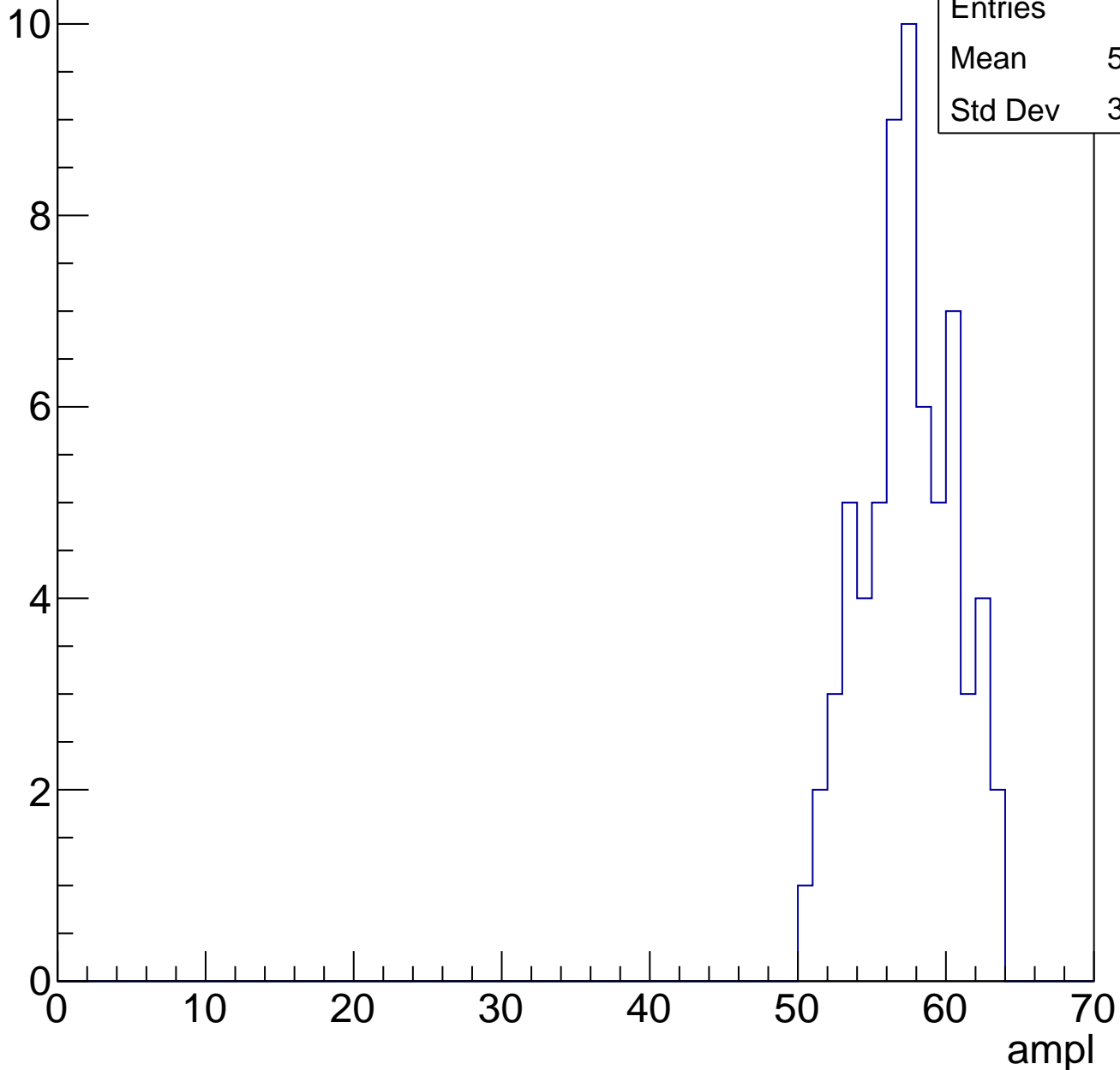


B0L001S, U18-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	56.94
Std Dev	3.138

Entry

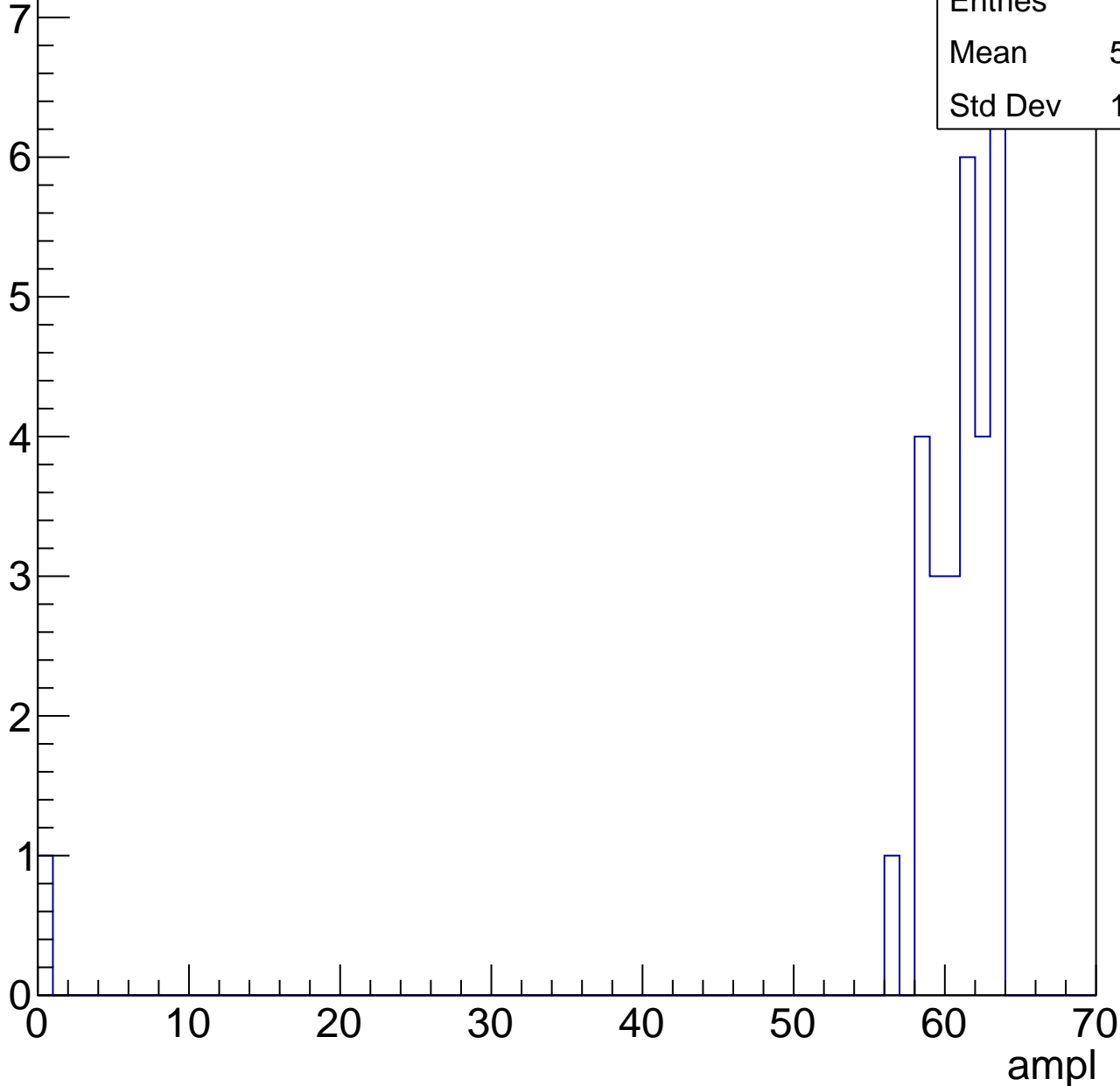


B0L001S, U18-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.62
Std Dev	11.24



B0L001S, U18-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B0L001S, U18-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch10, adc0

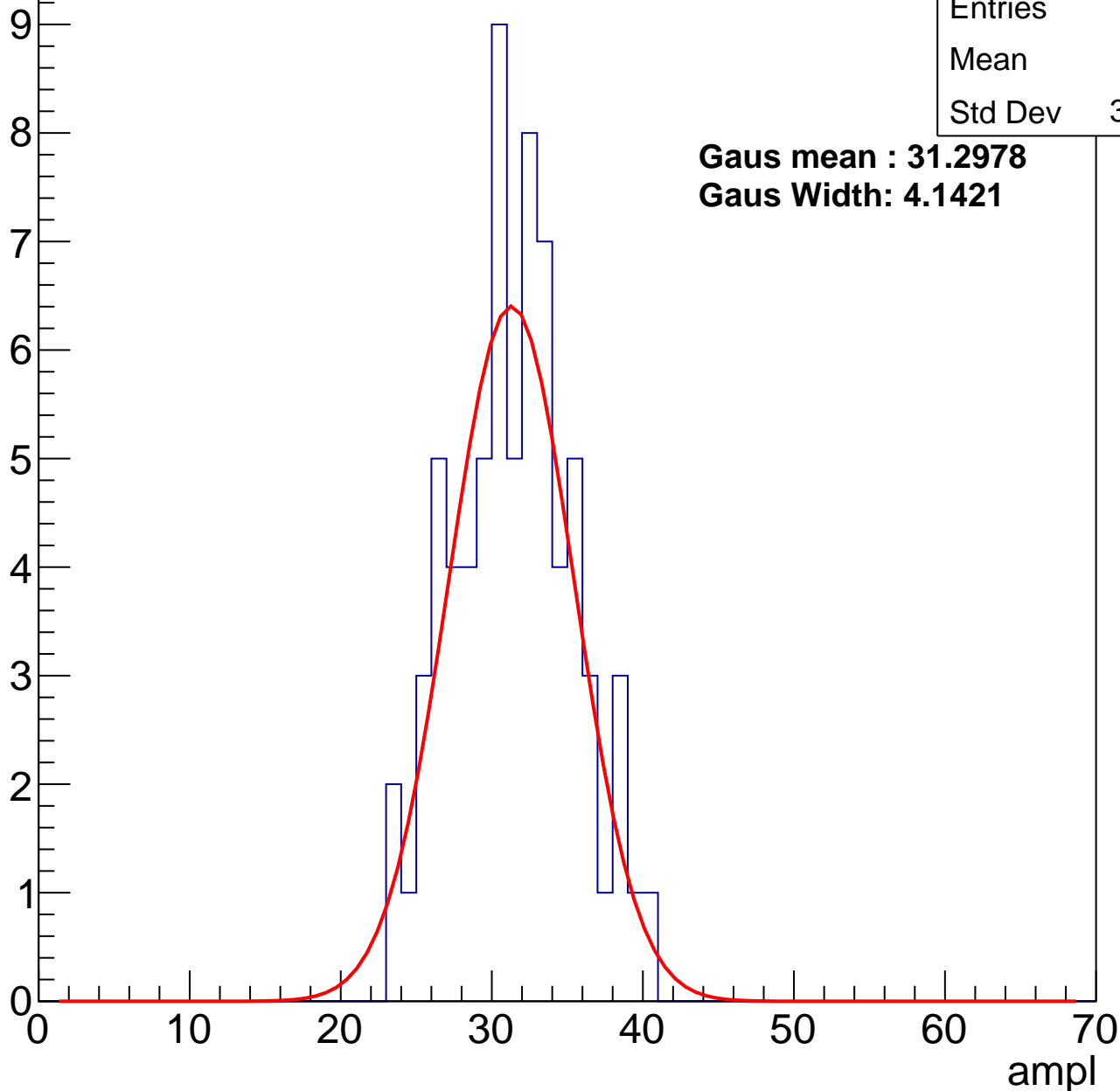
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	31
Std Dev	3.925

Gaus mean : 31.2978

Gaus Width: 4.1421



B0L001S, U18-ch10, adc1

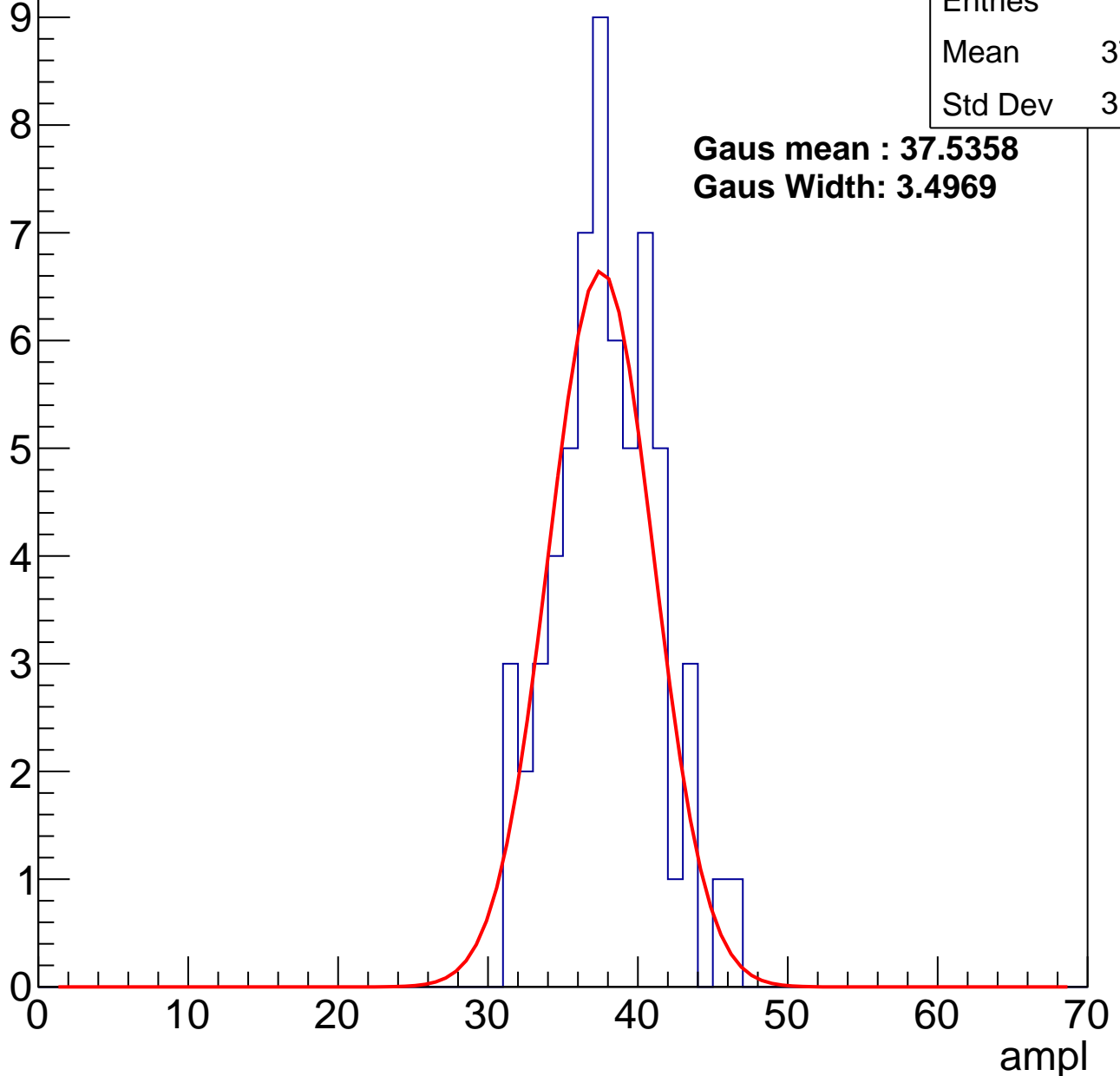
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.45
Std Dev	3.368

Gaus mean : 37.5358

Gaus Width: 3.4969



B0L001S, U18-ch10, adc2

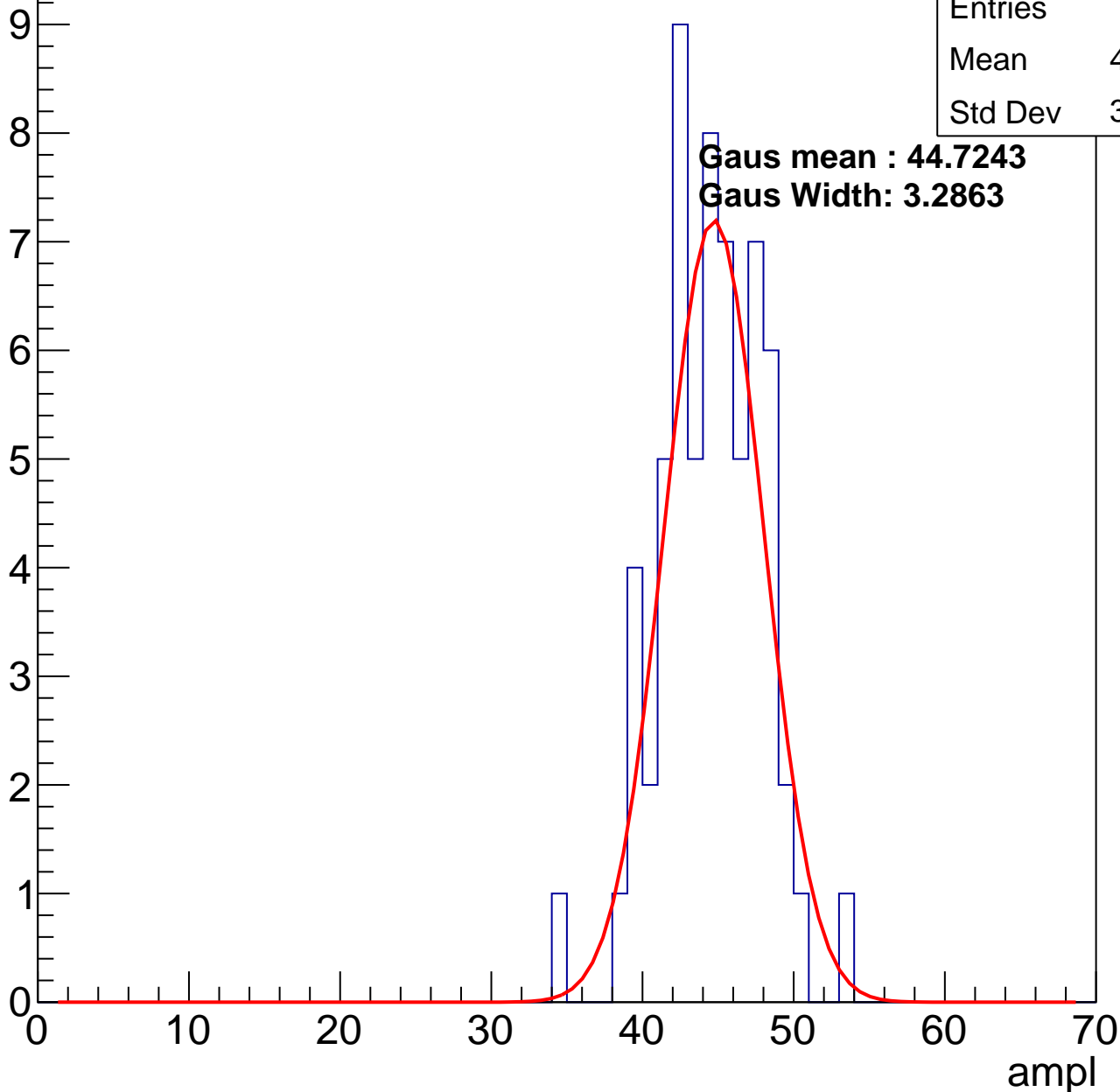
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.08
Std Dev	3.323

Gaus mean : 44.7243

Gaus Width: 3.2863

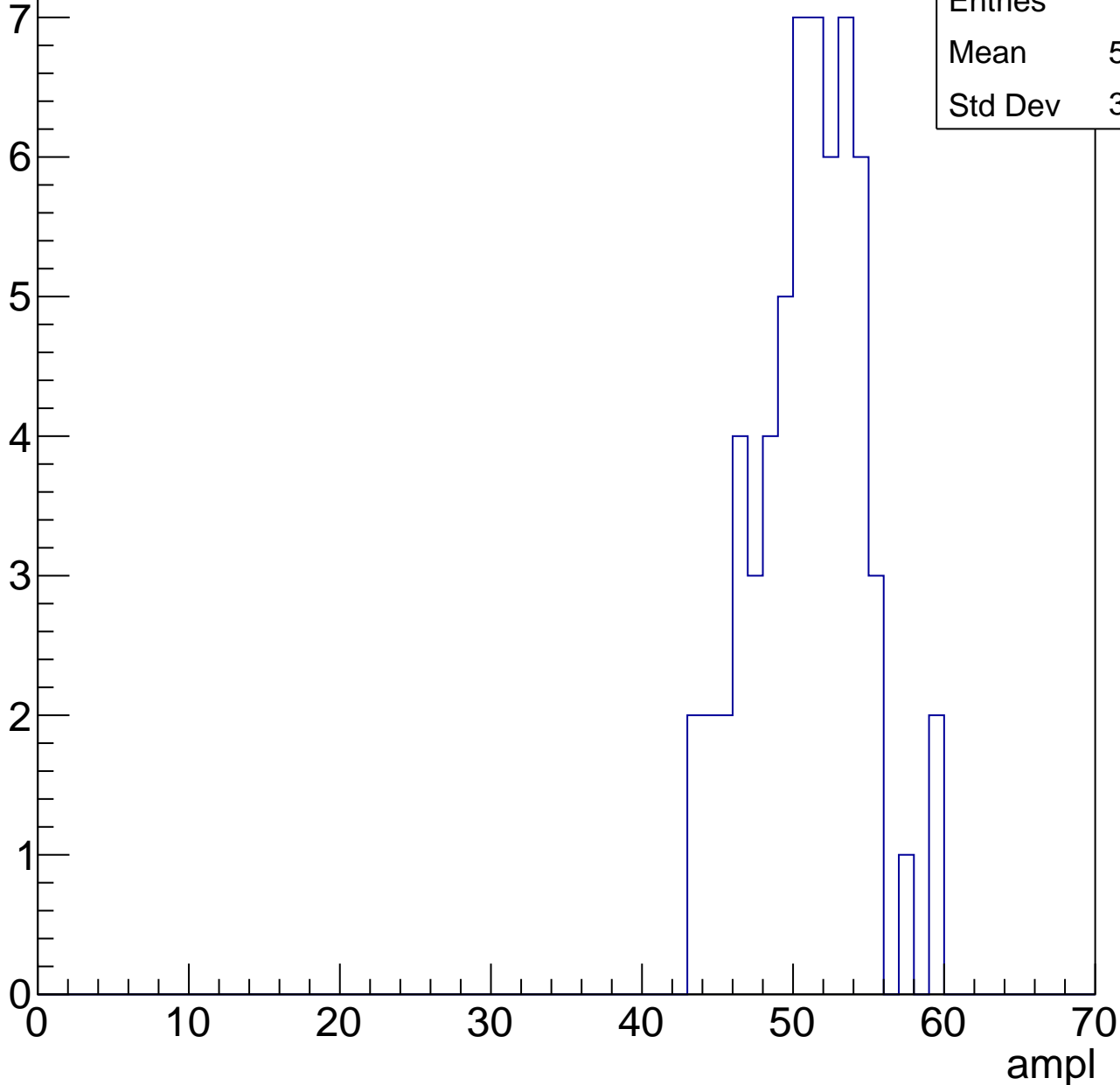


B0L001S, U18-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.49
Std Dev	3.606

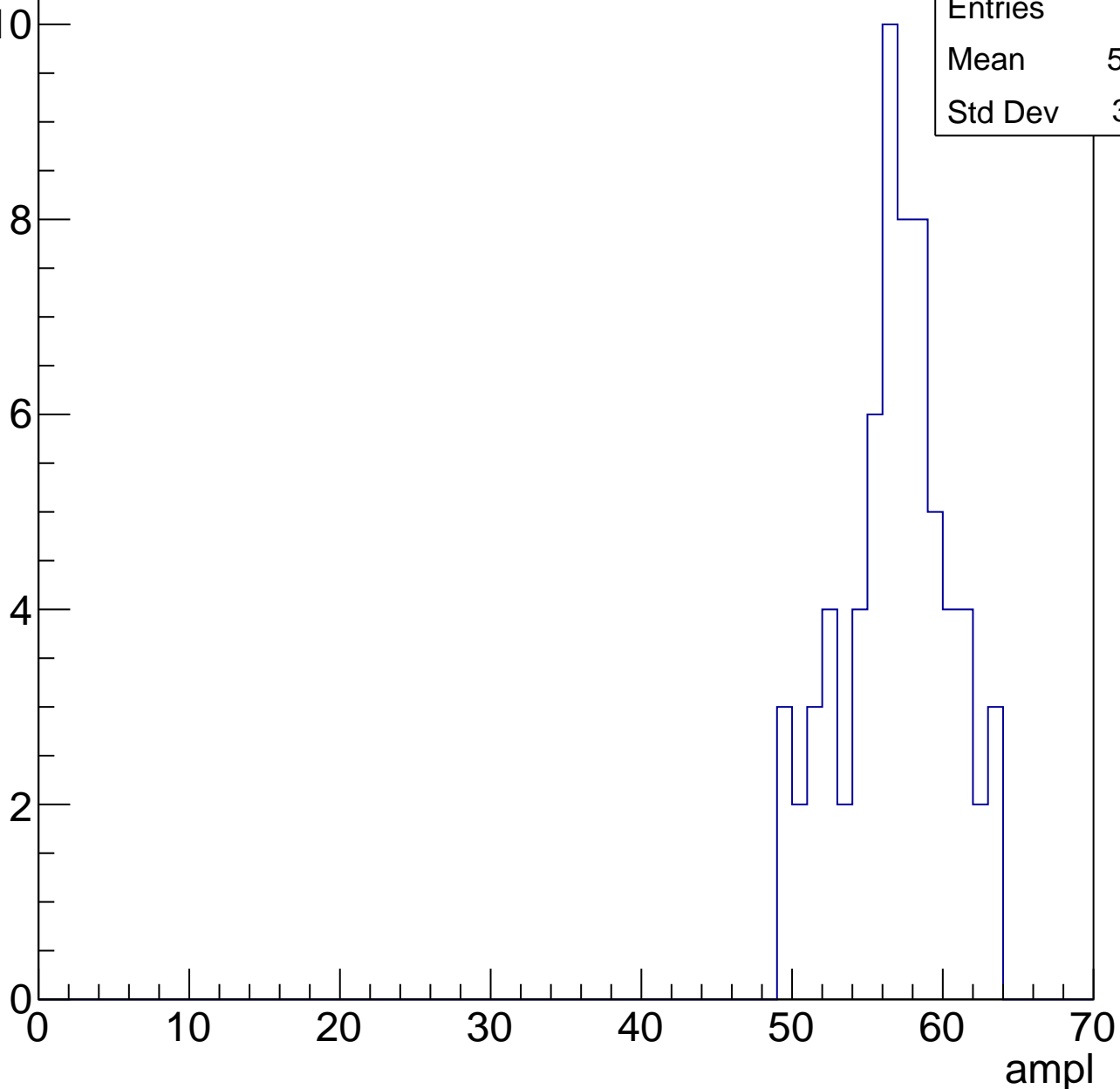


B0L001S, U18-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	56.35
Std Dev	3.551

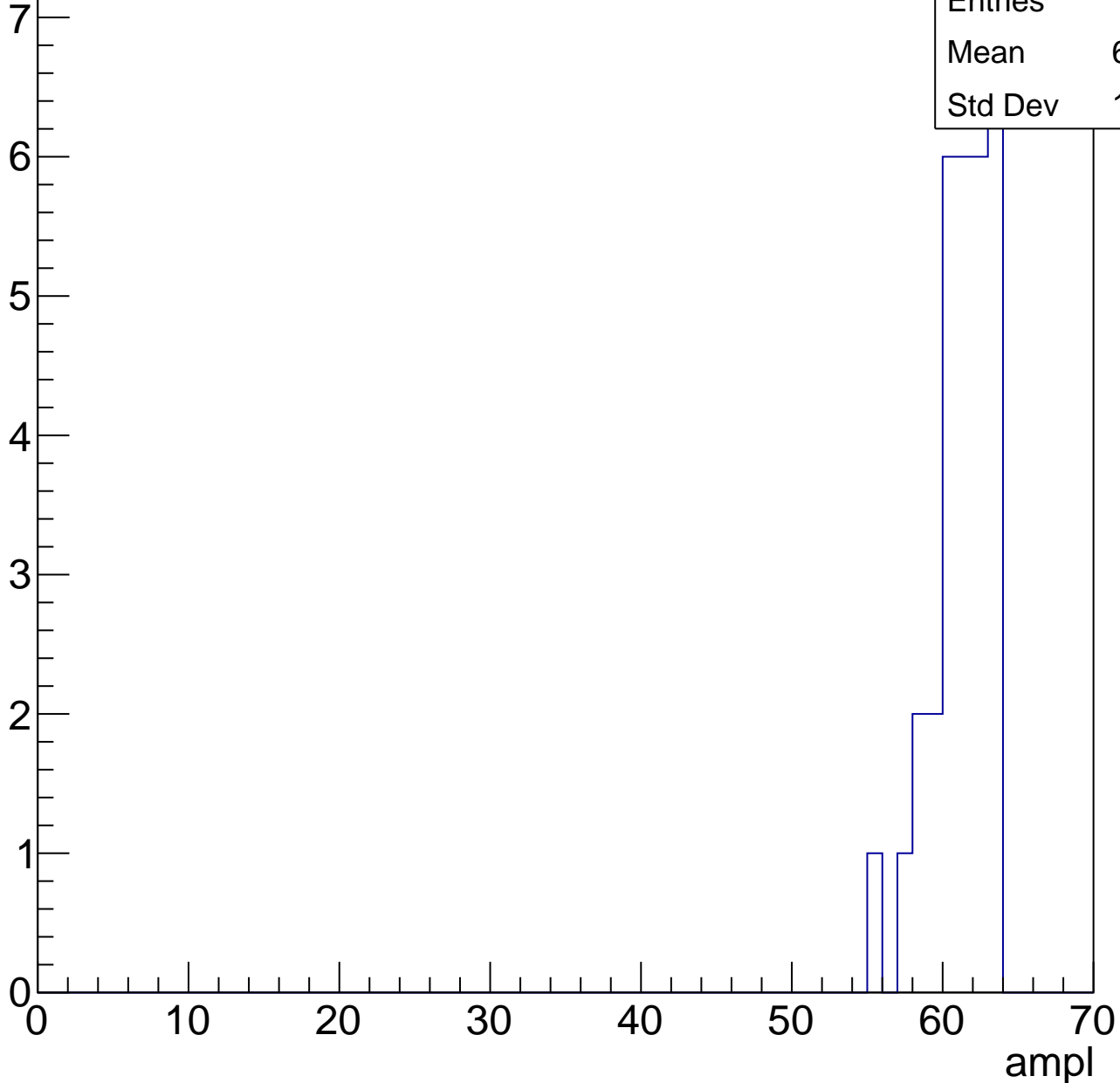


B0L001S, U18-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

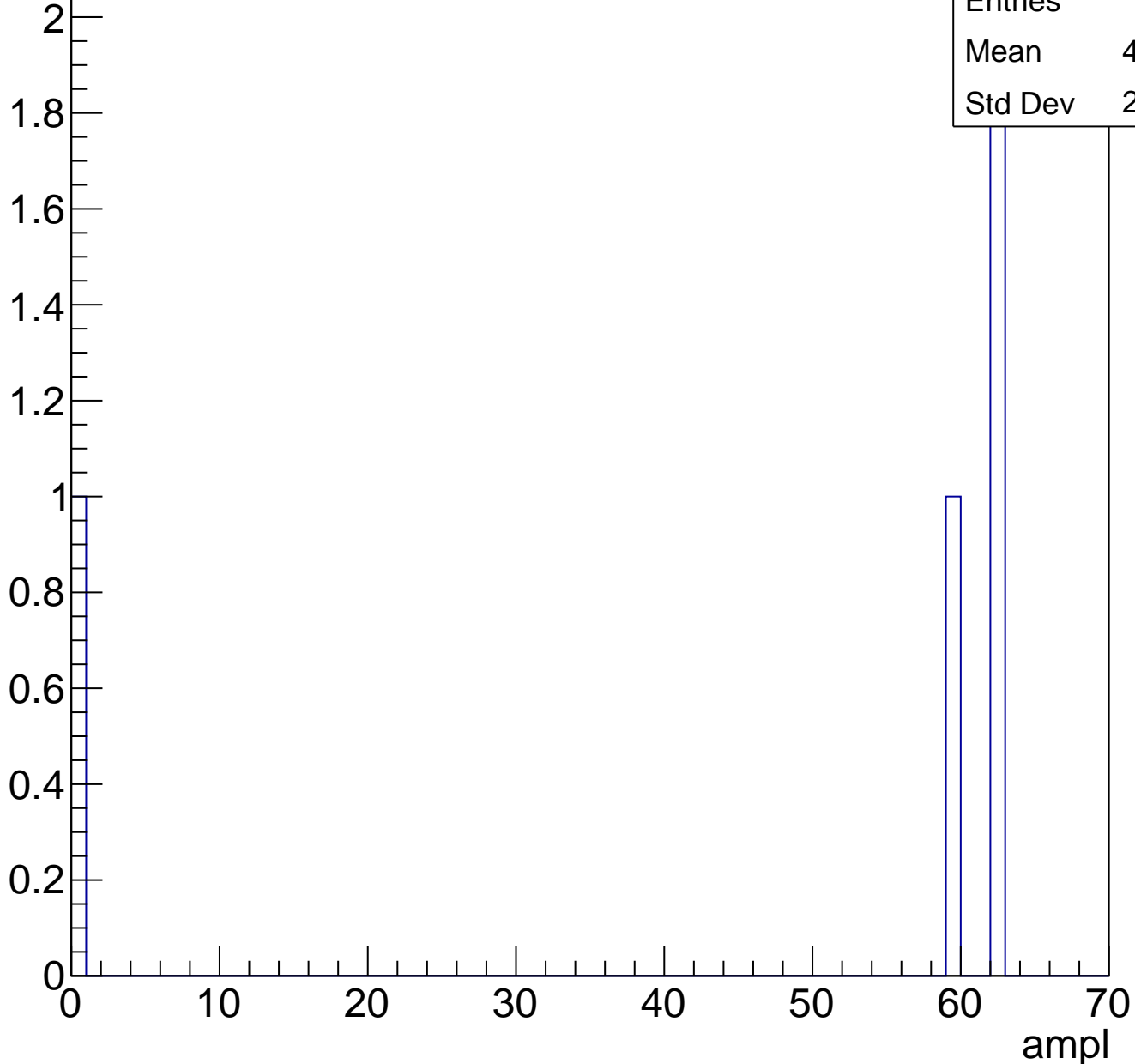
Entries	31
Mean	60.81
Std Dev	1.941



B0L001S, U18-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch11, adc0

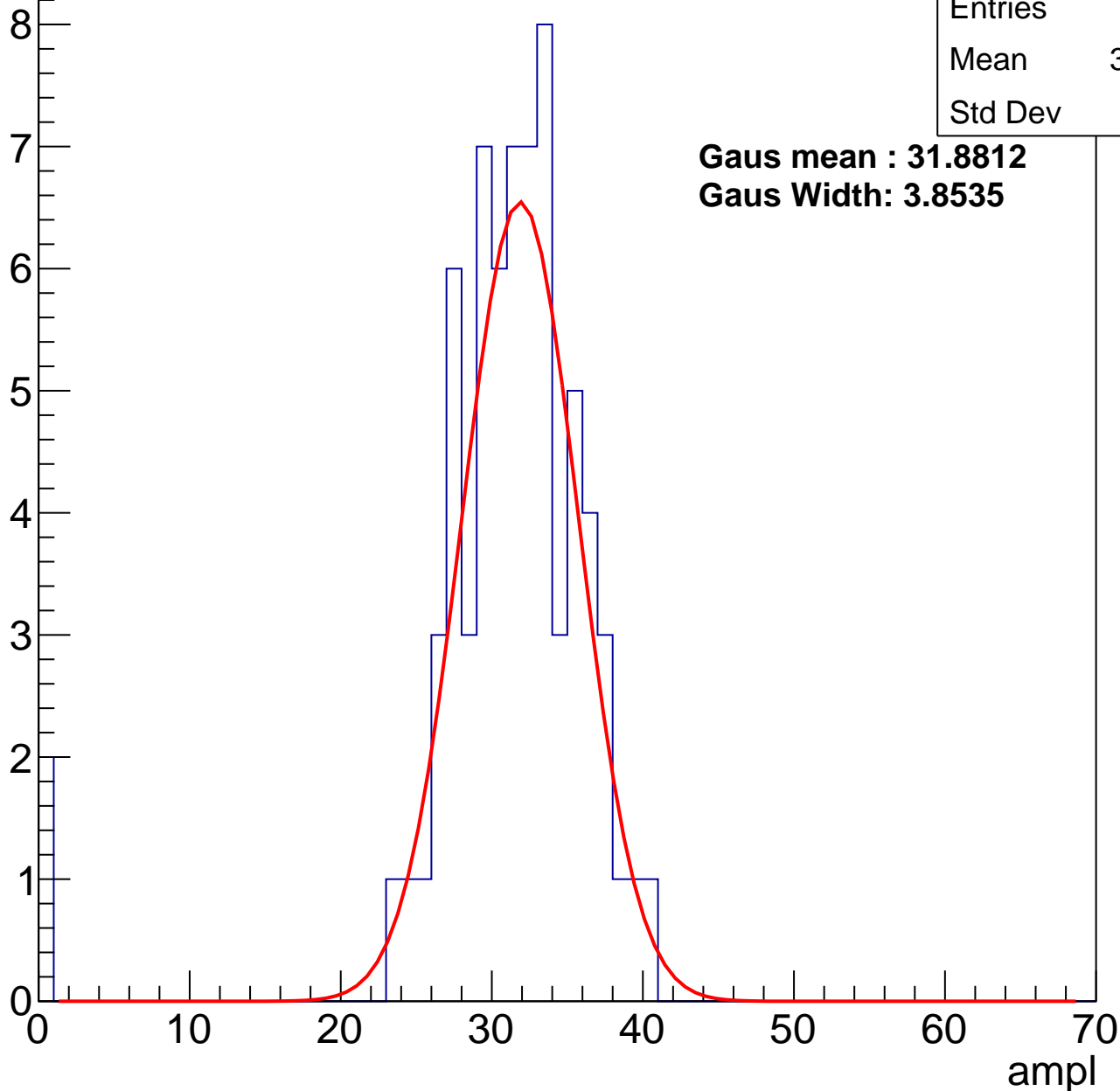
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.47
Std Dev	6.36

Gaus mean : 31.8812

Gaus Width: 3.8535



B0L001S, U18-ch11, adc1

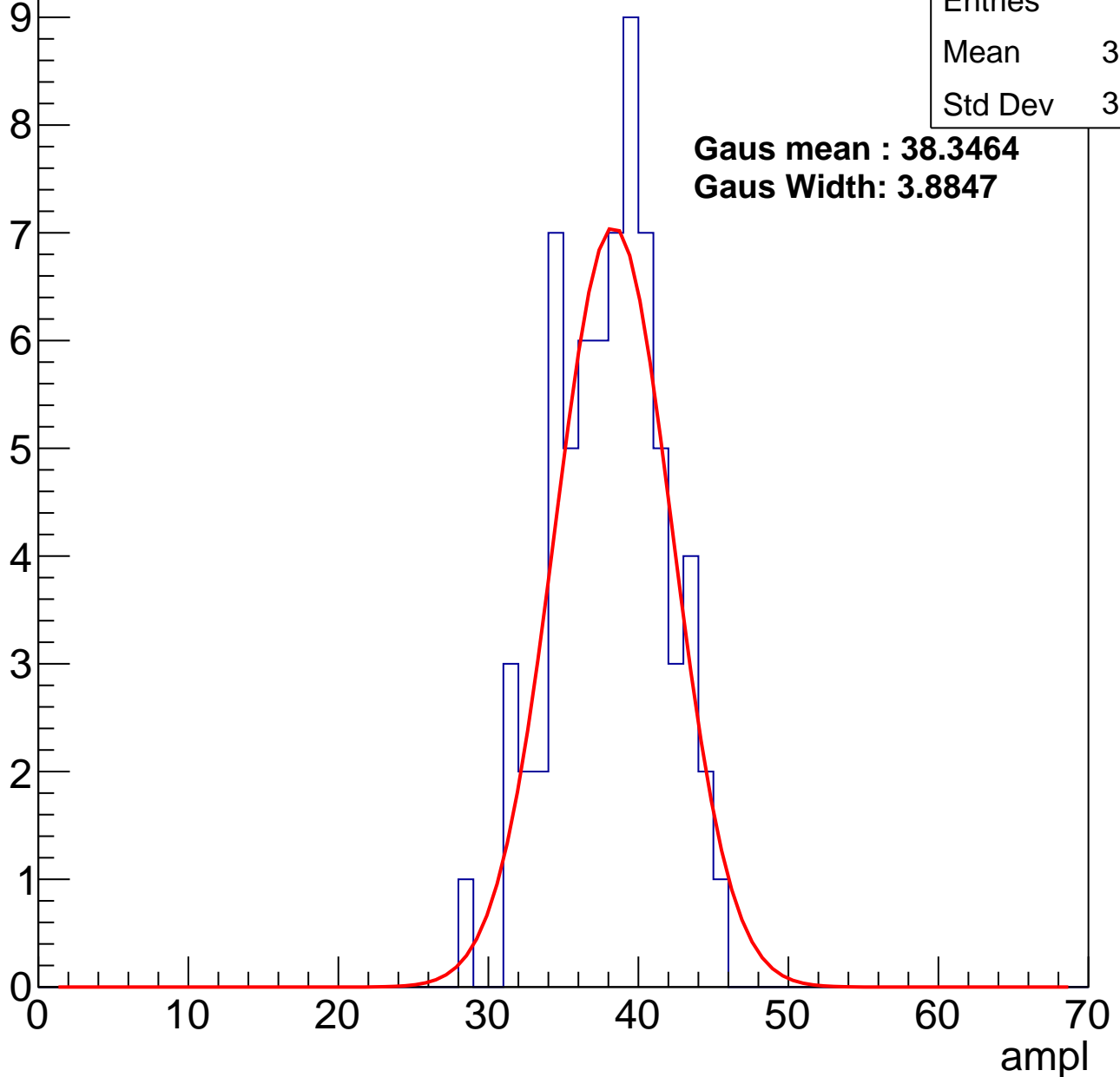
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.64
Std Dev	3.582

Gaus mean : 38.3464

Gaus Width: 3.8847



B0L001S, U18-ch11, adc2

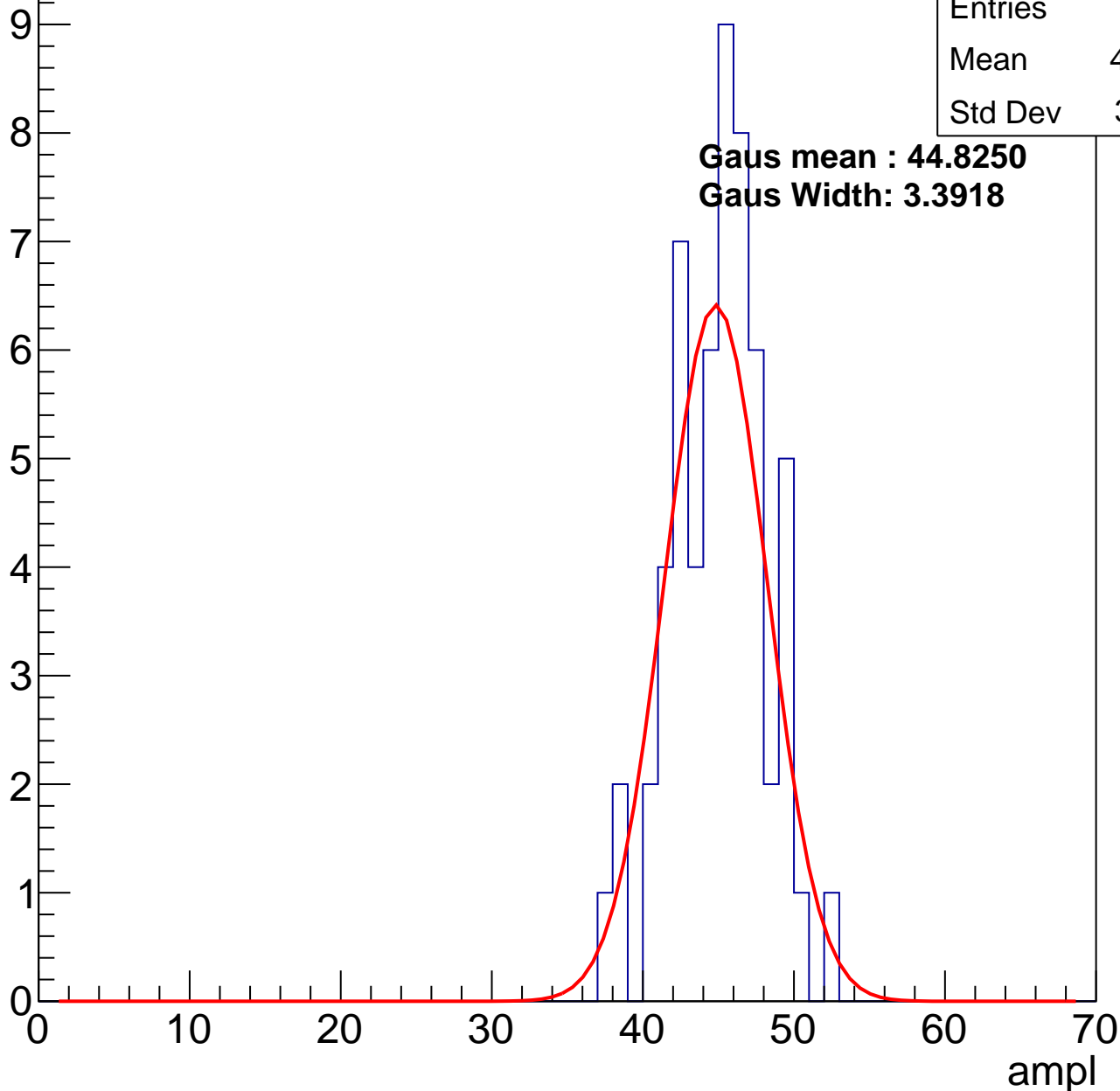
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.57
Std Dev	3.091

Gaus mean : 44.8250

Gaus Width: 3.3918

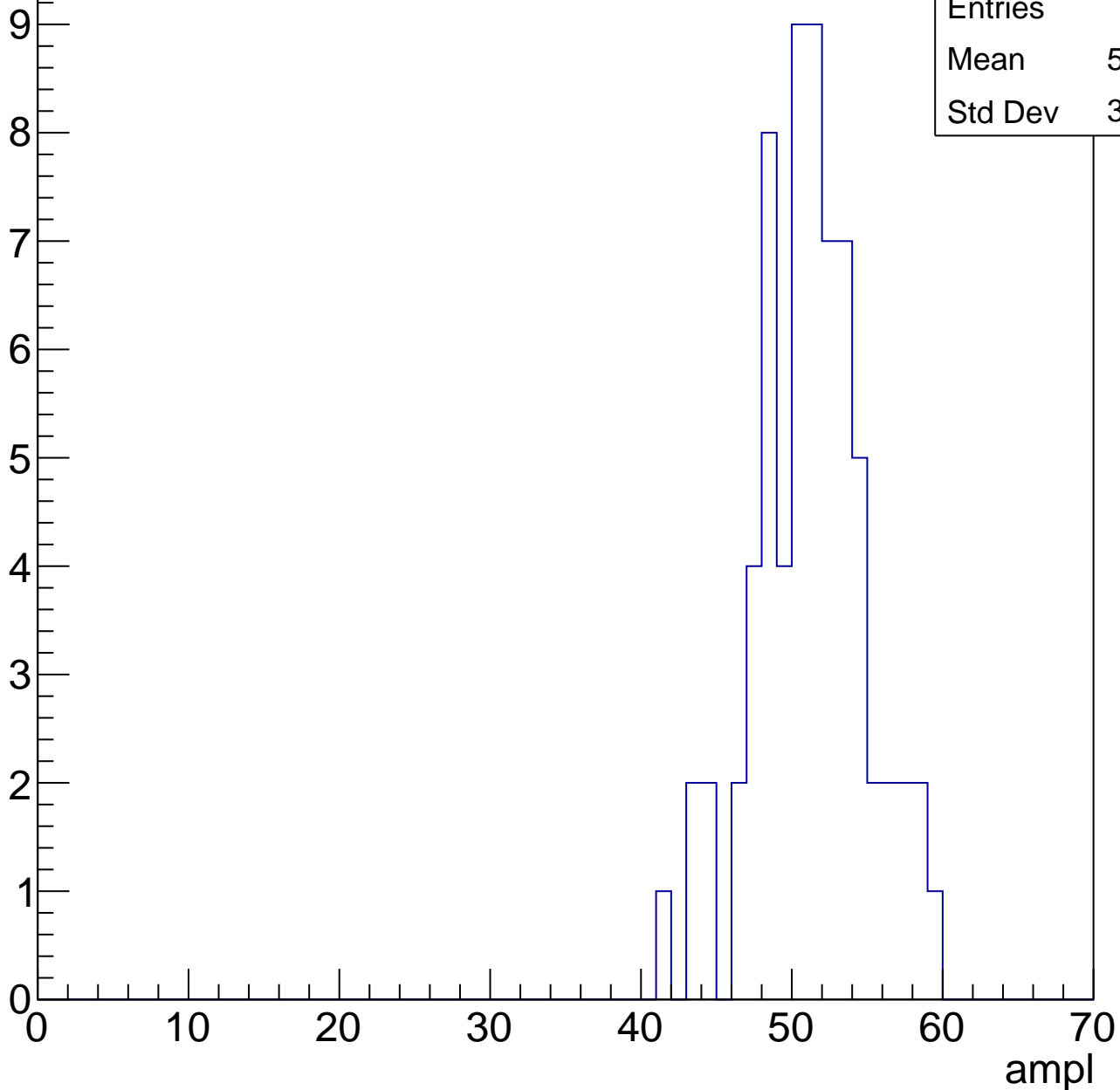


B0L001S, U18-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.72
Std Dev	3.667

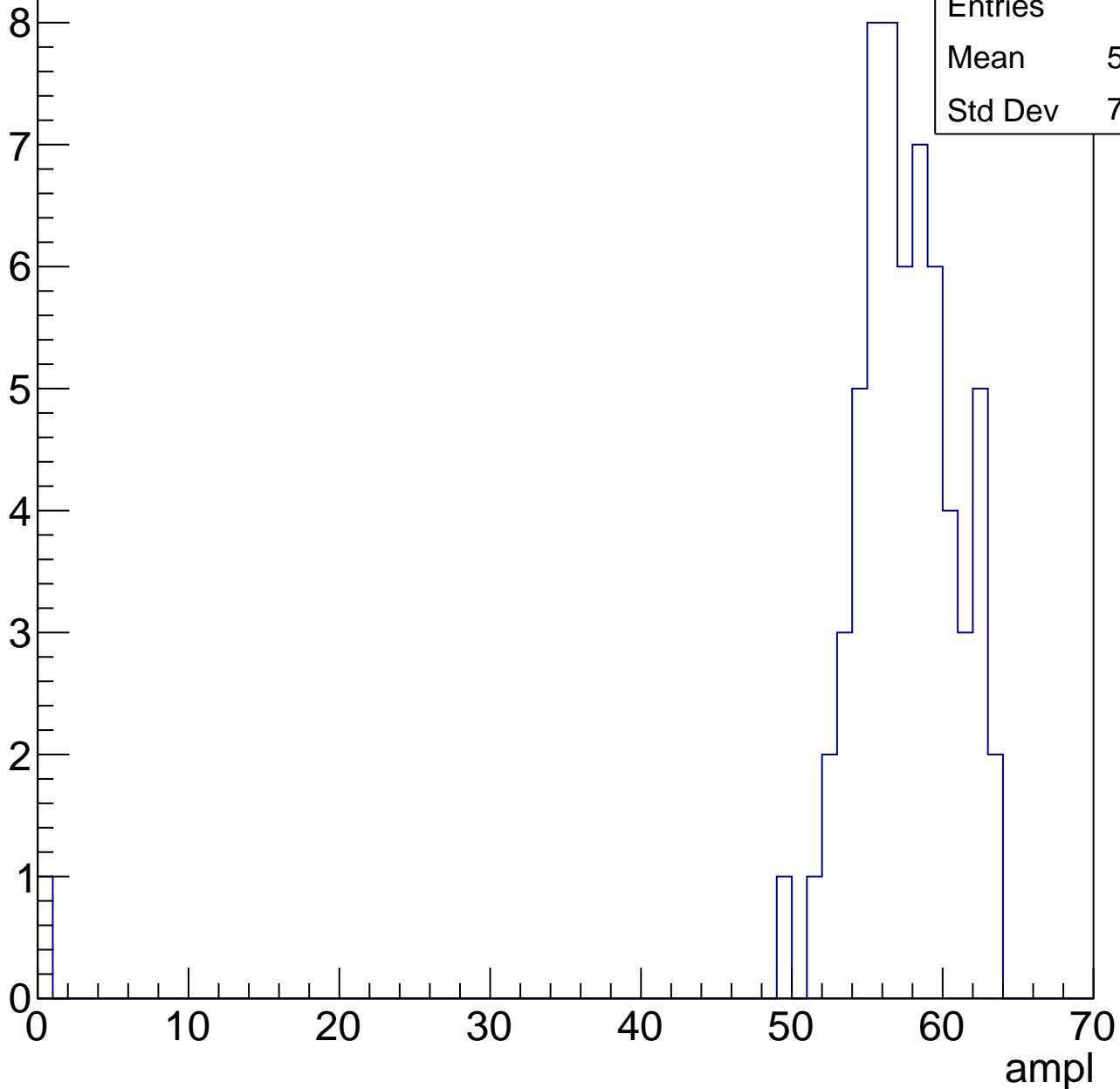


B0L001S, U18-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.16
Std Dev	7.829

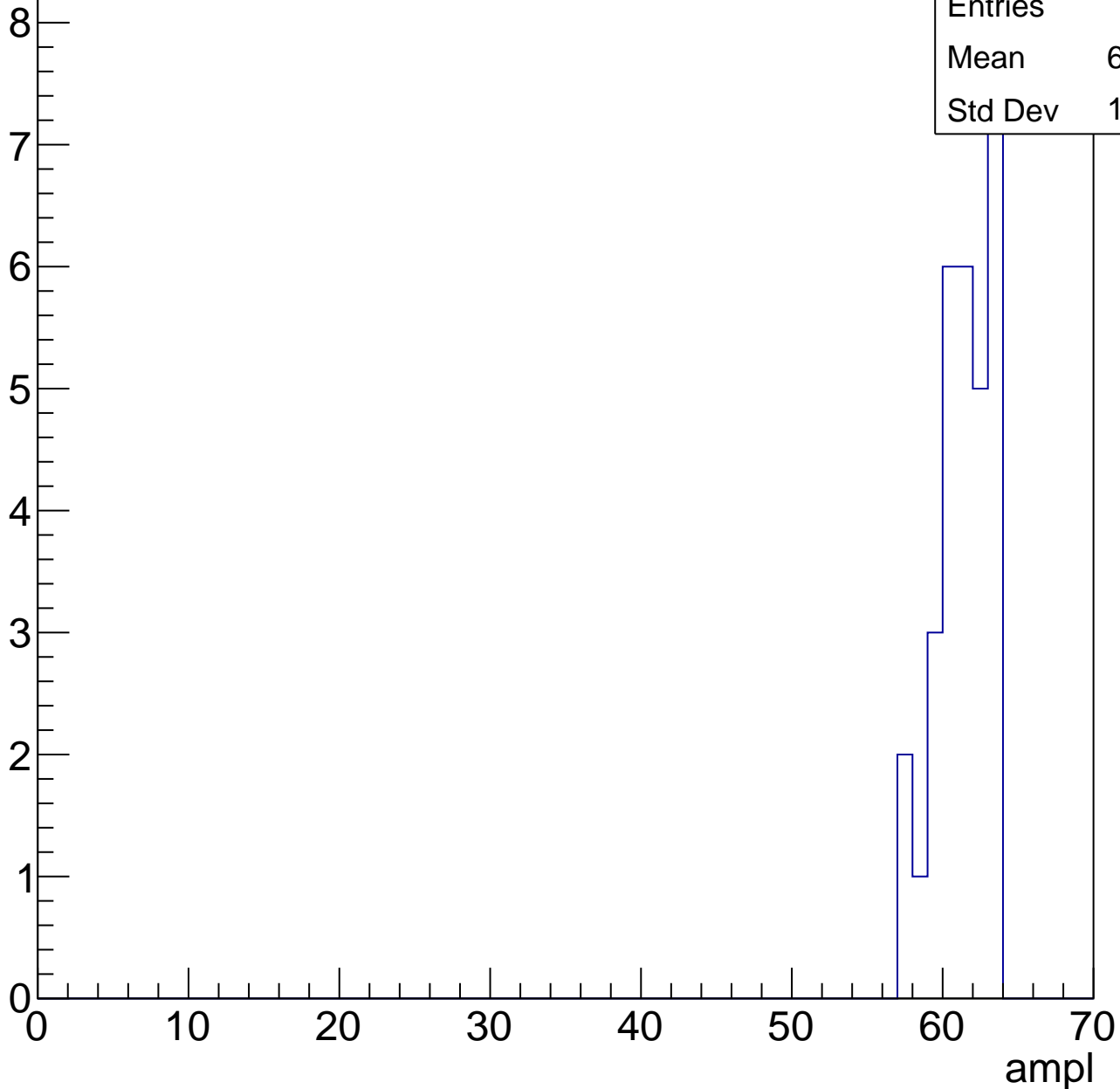


B0L001S, U18-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.94
Std Dev	1.759



B0L001S, U18-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch12, adc0

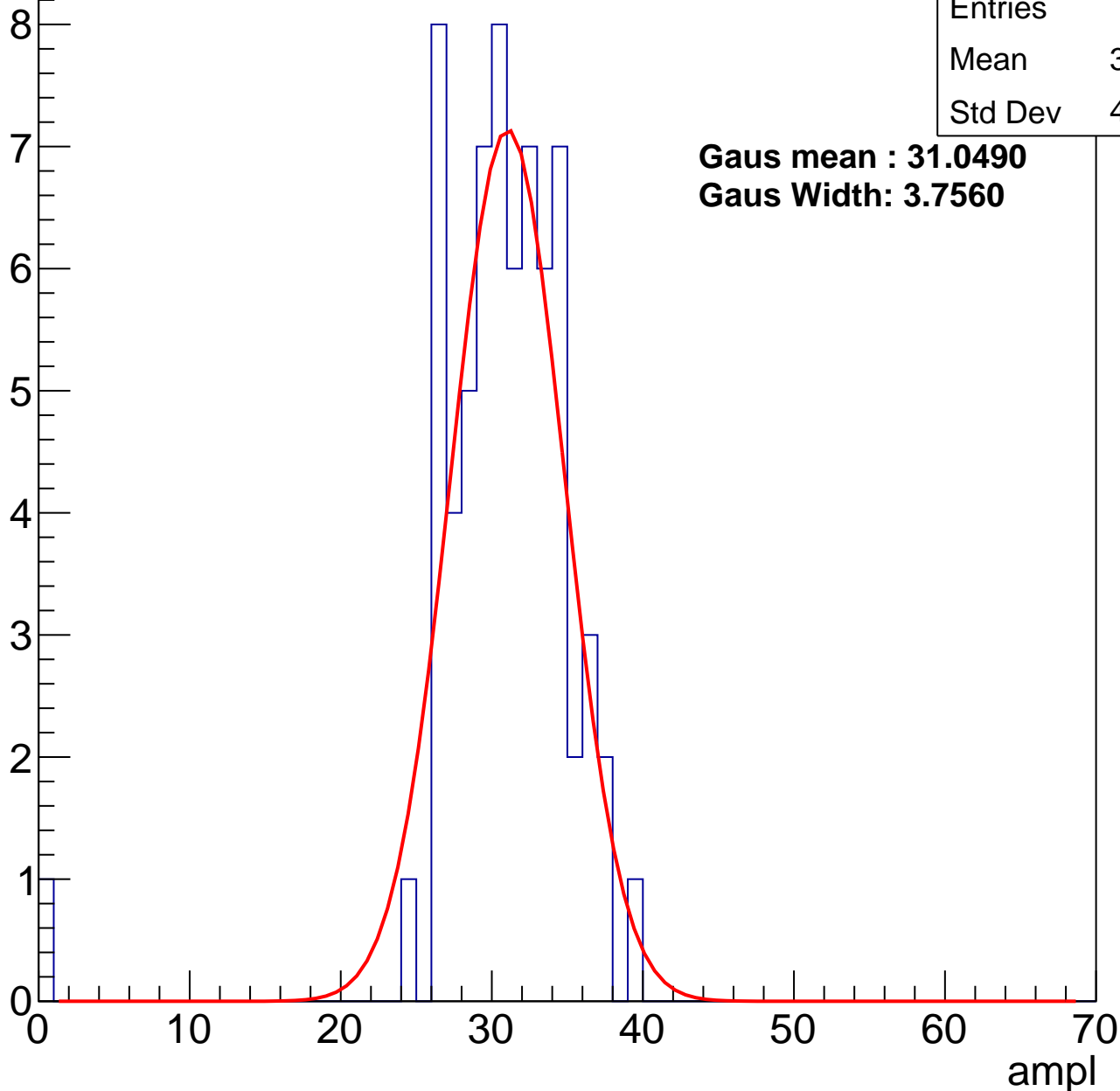
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.29
Std Dev	4.938

Gaus mean : 31.0490

Gaus Width: 3.7560



B0L001S, U18-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	38.03
Std Dev	3.661

Gaus mean : 38.6279

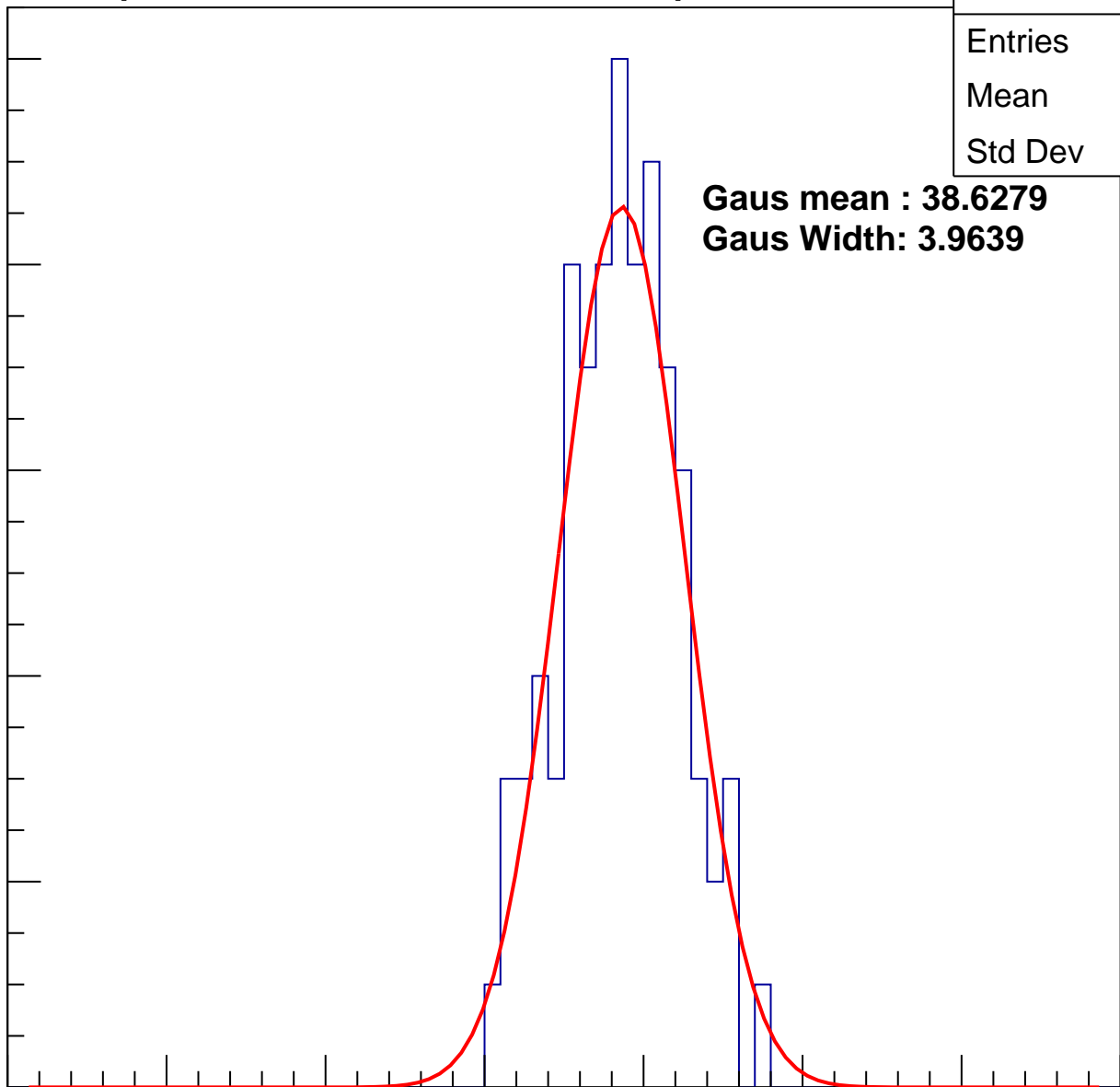
Gaus Width: 3.9639

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

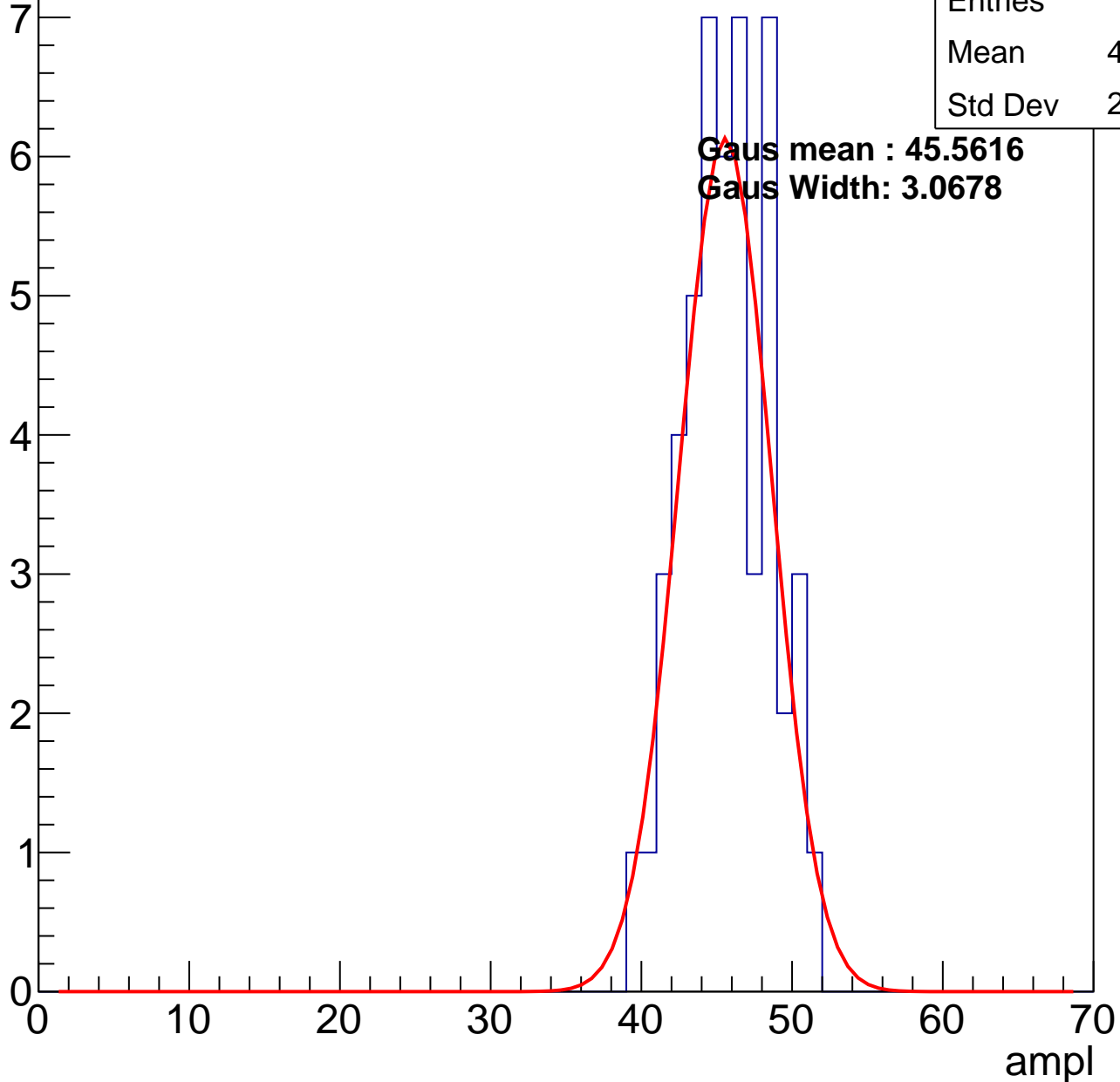


B0L001S, U18-ch12, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

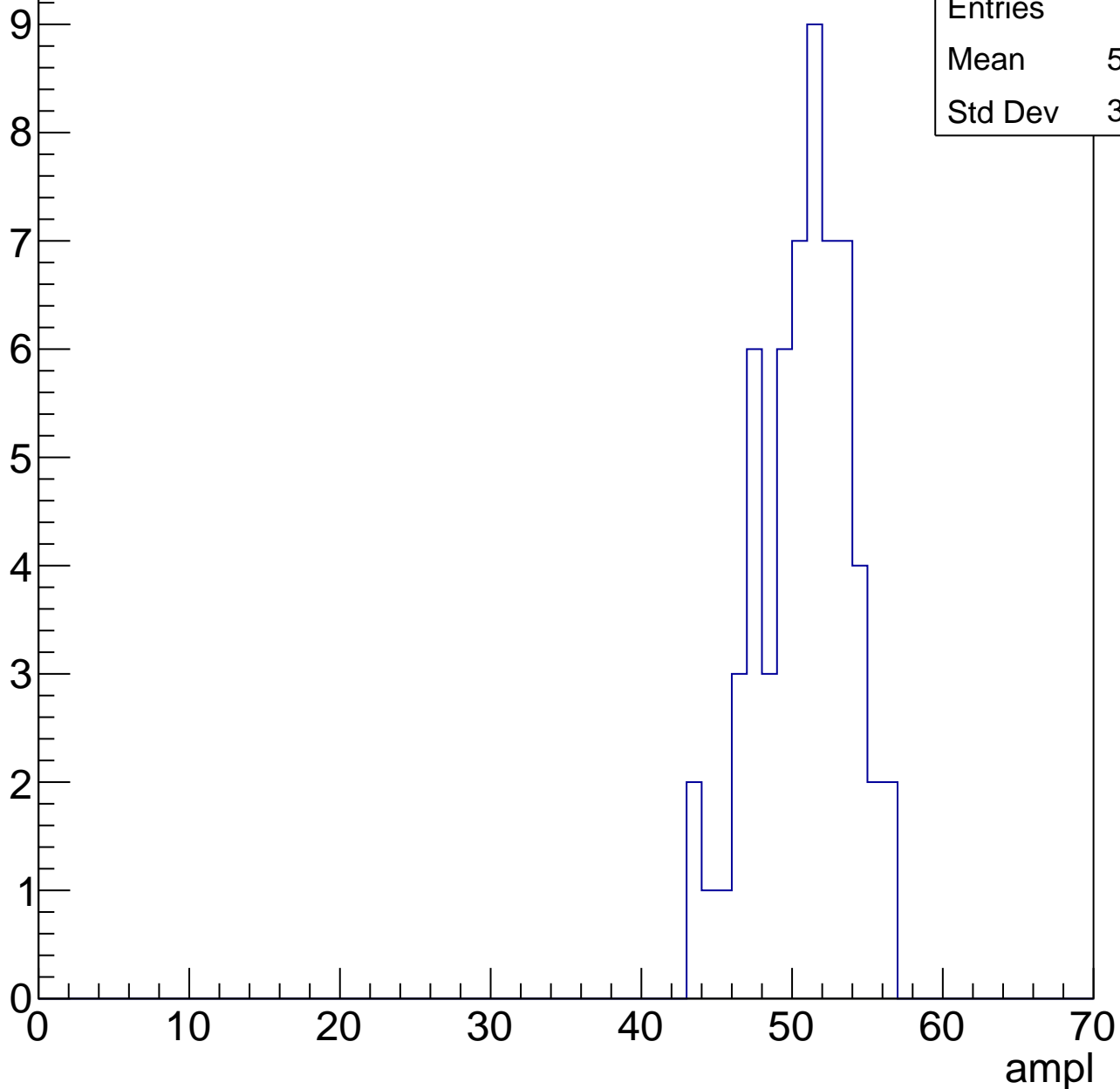
Entries	50
Mean	45.22
Std Dev	2.809



B0L001S, U18-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



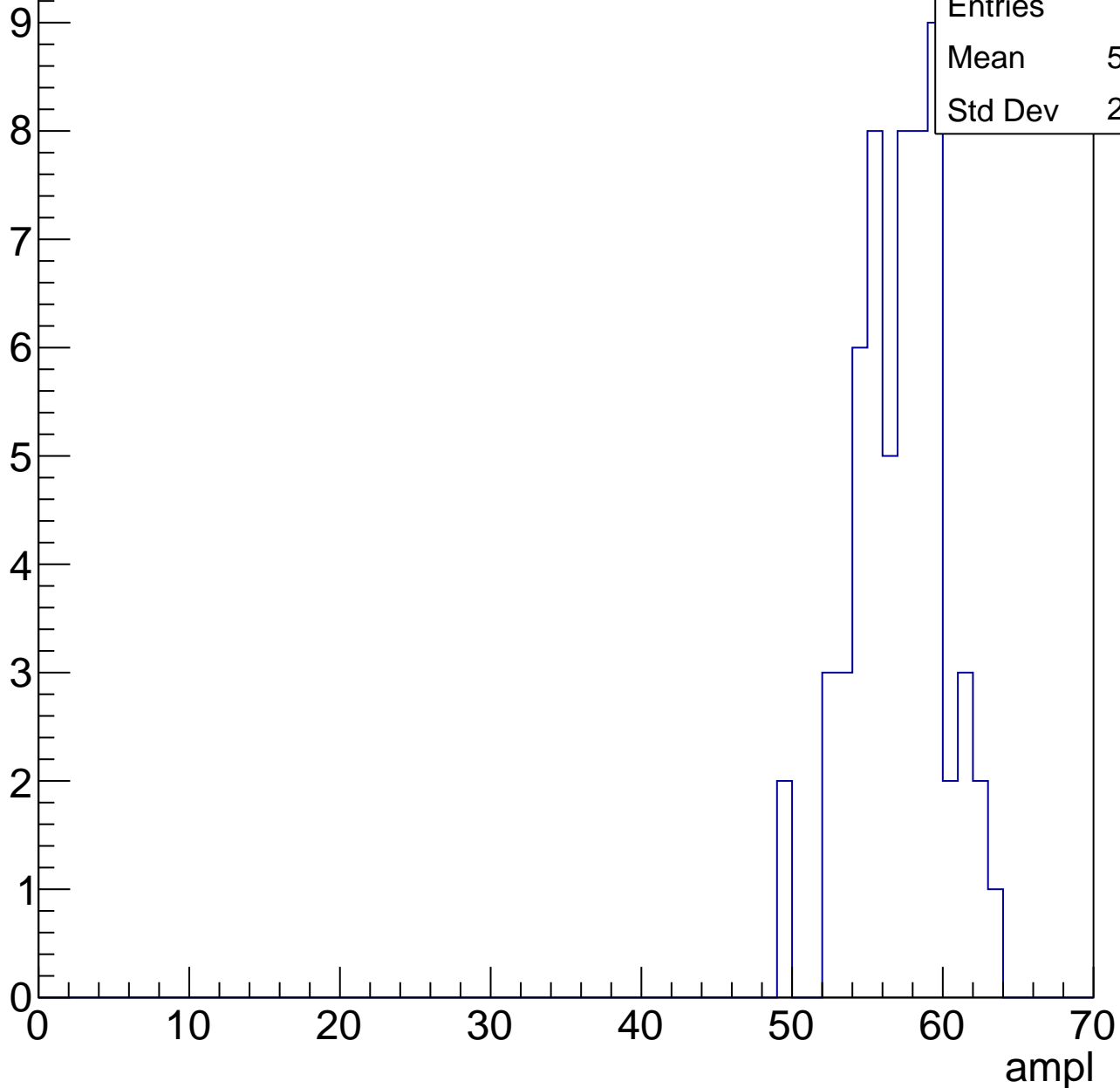
Entries	60
Mean	50.25
Std Dev	3.058

B0L001S, U18-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

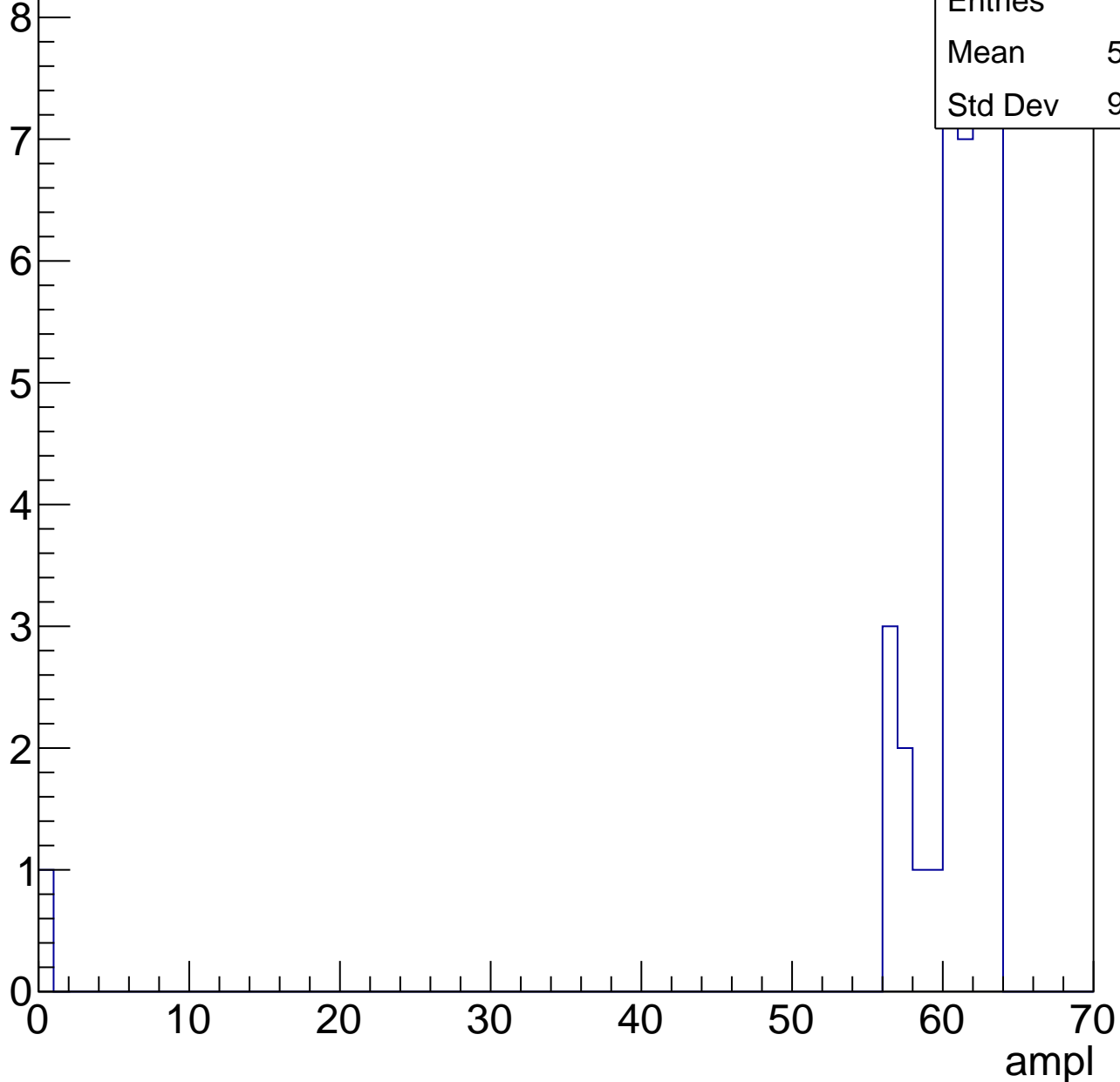
Entries	60
Mean	56.63
Std Dev	2.972



B0L001S, U18-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

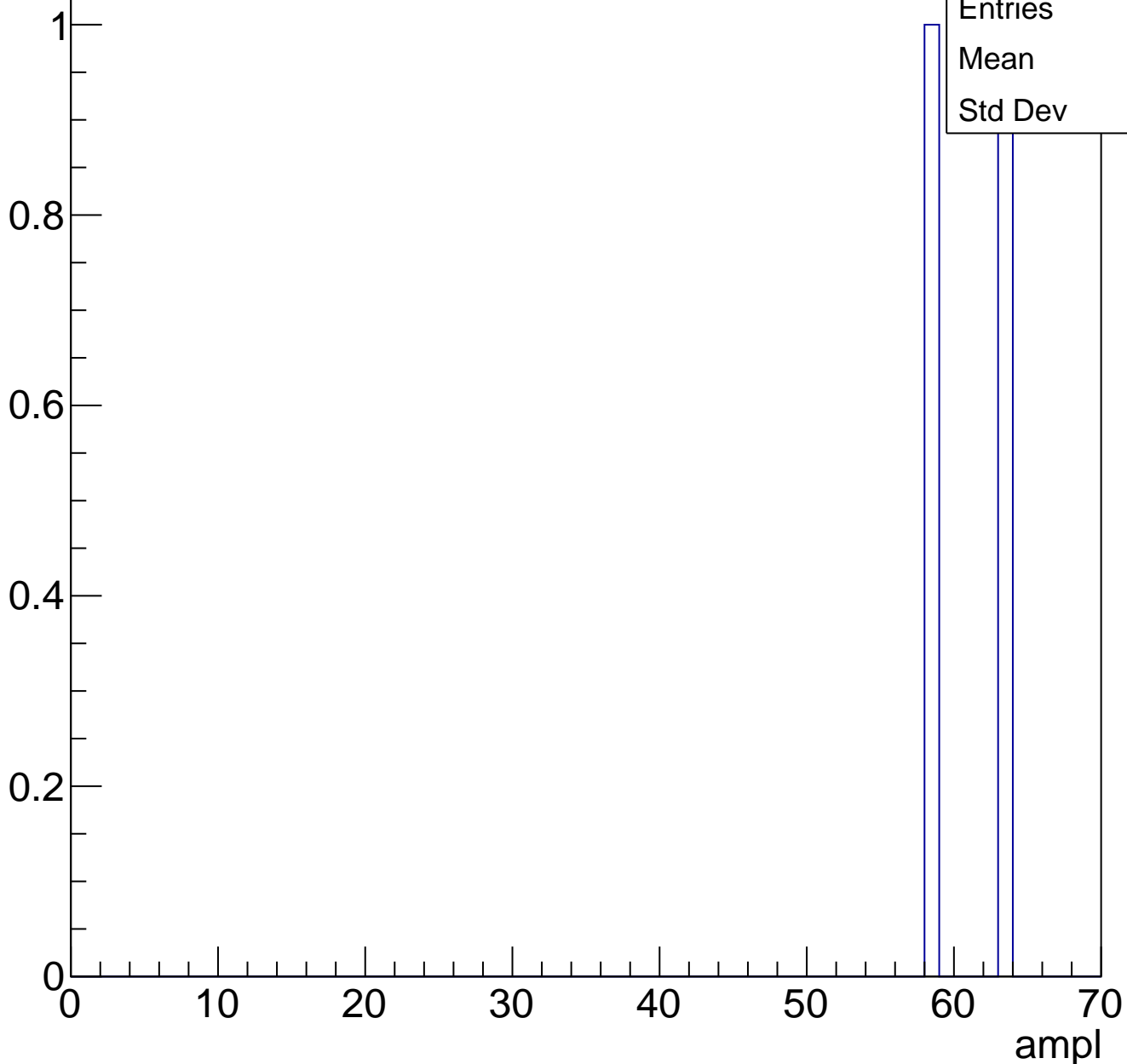
Entry



B0L001S, U18-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch13, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	31.88
Std Dev	3.282

Gaus mean : 32.0054

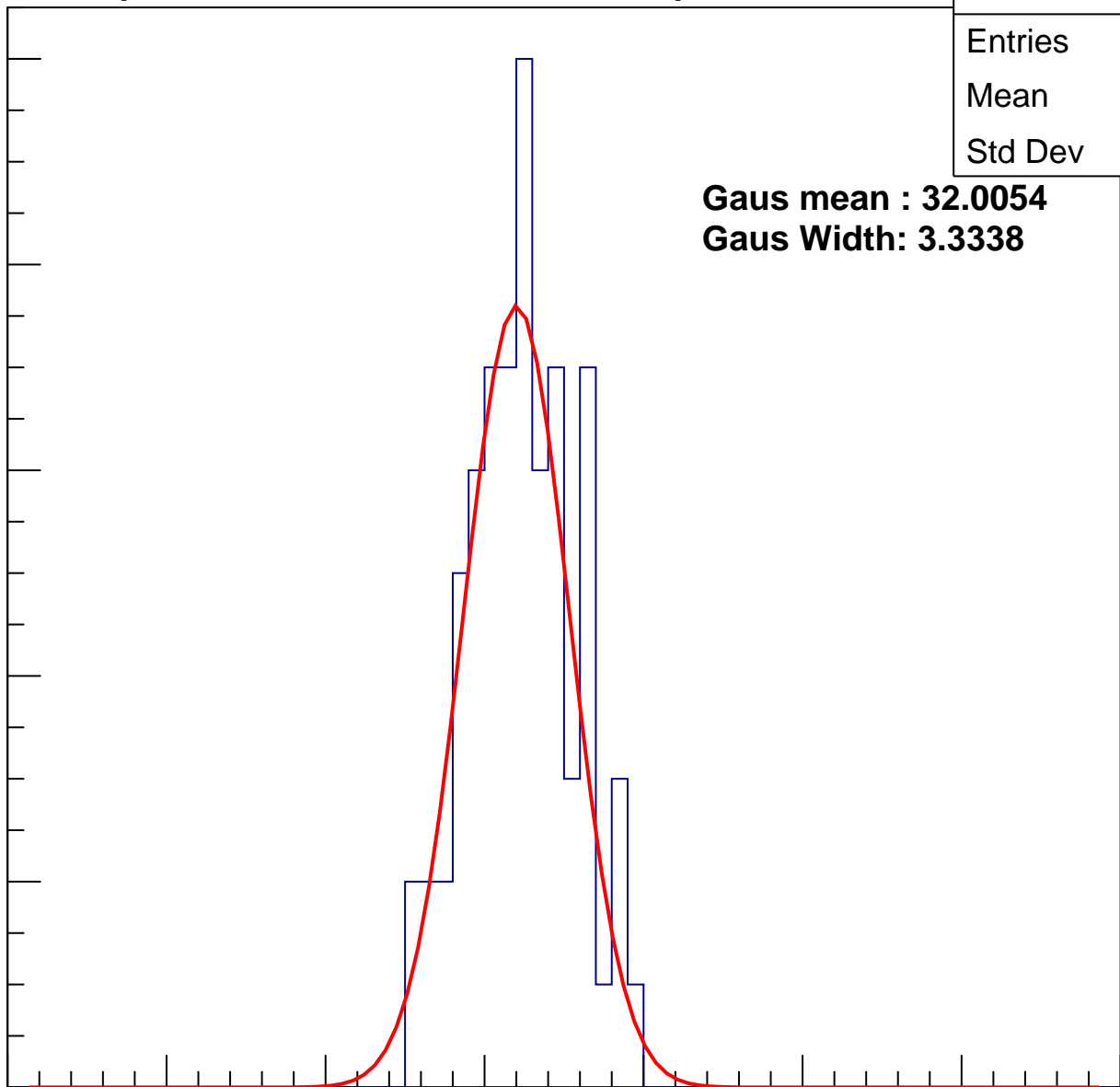
Gaus Width: 3.3338

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch13, adc1

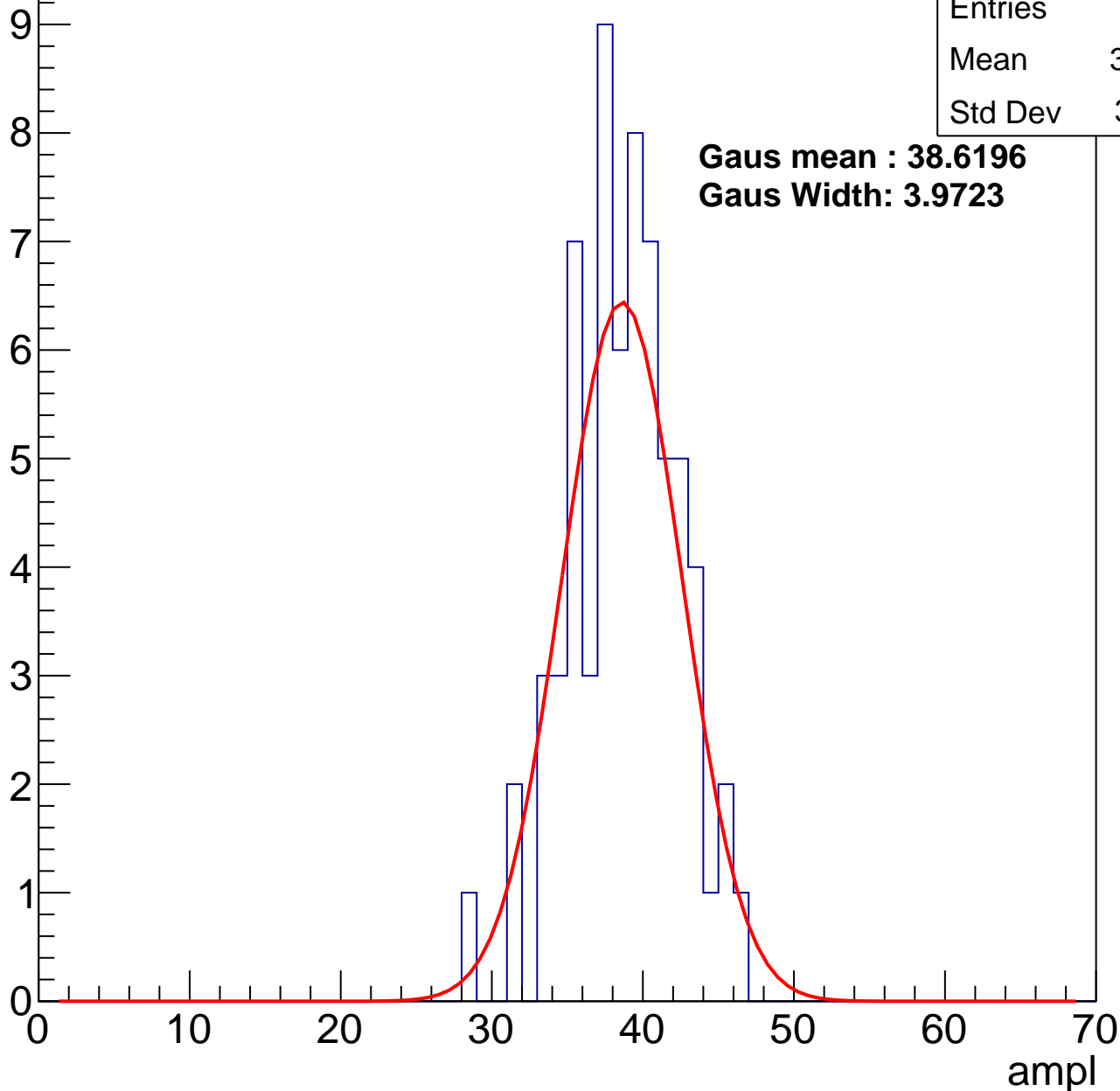
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.27
Std Dev	3.581

Gaus mean : 38.6196

Gaus Width: 3.9723



B0L001S, U18-ch13, adc2

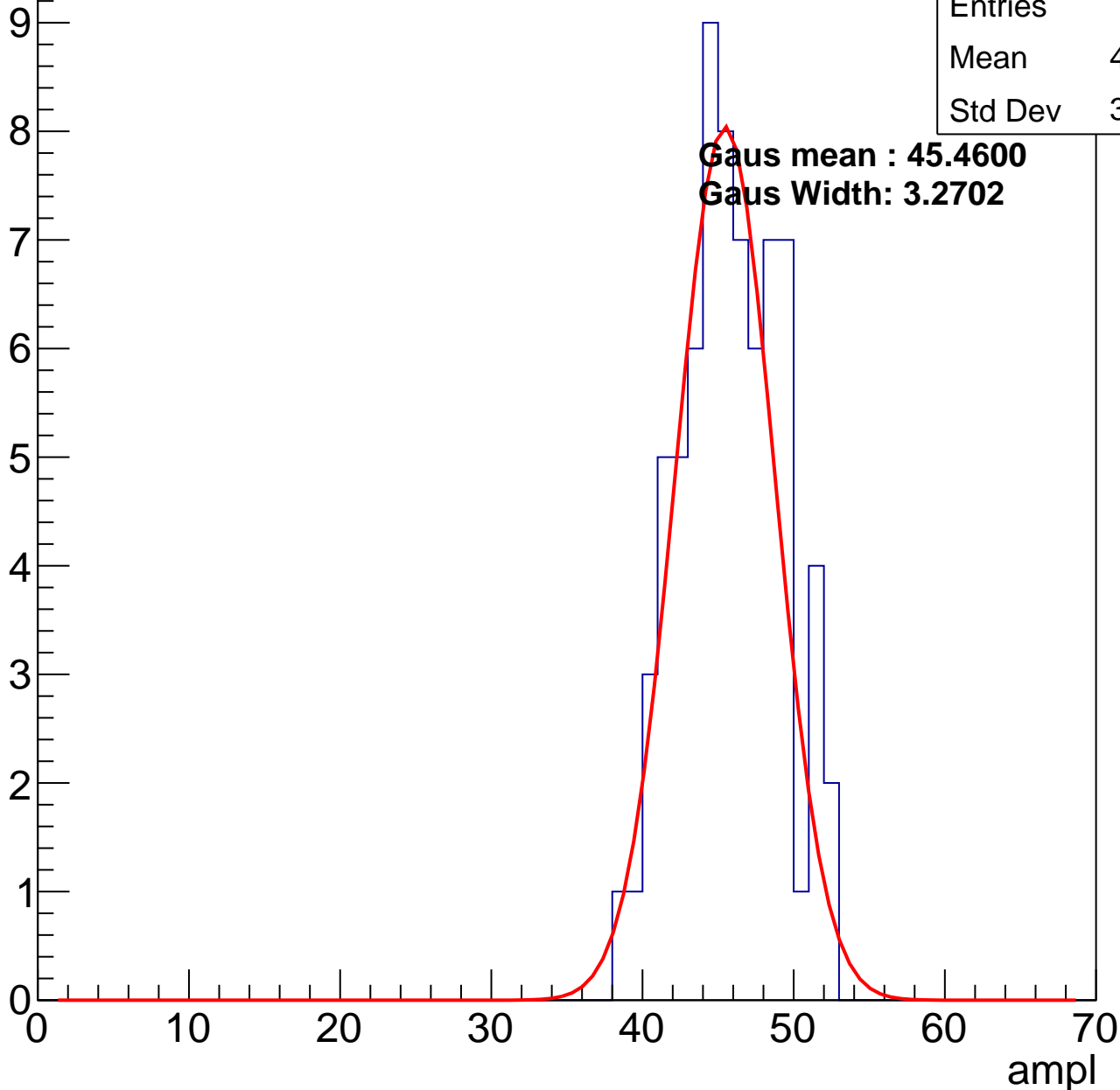
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	45.38
Std Dev	3.293

Gaus mean : 45.4600

Gaus Width: 3.2702

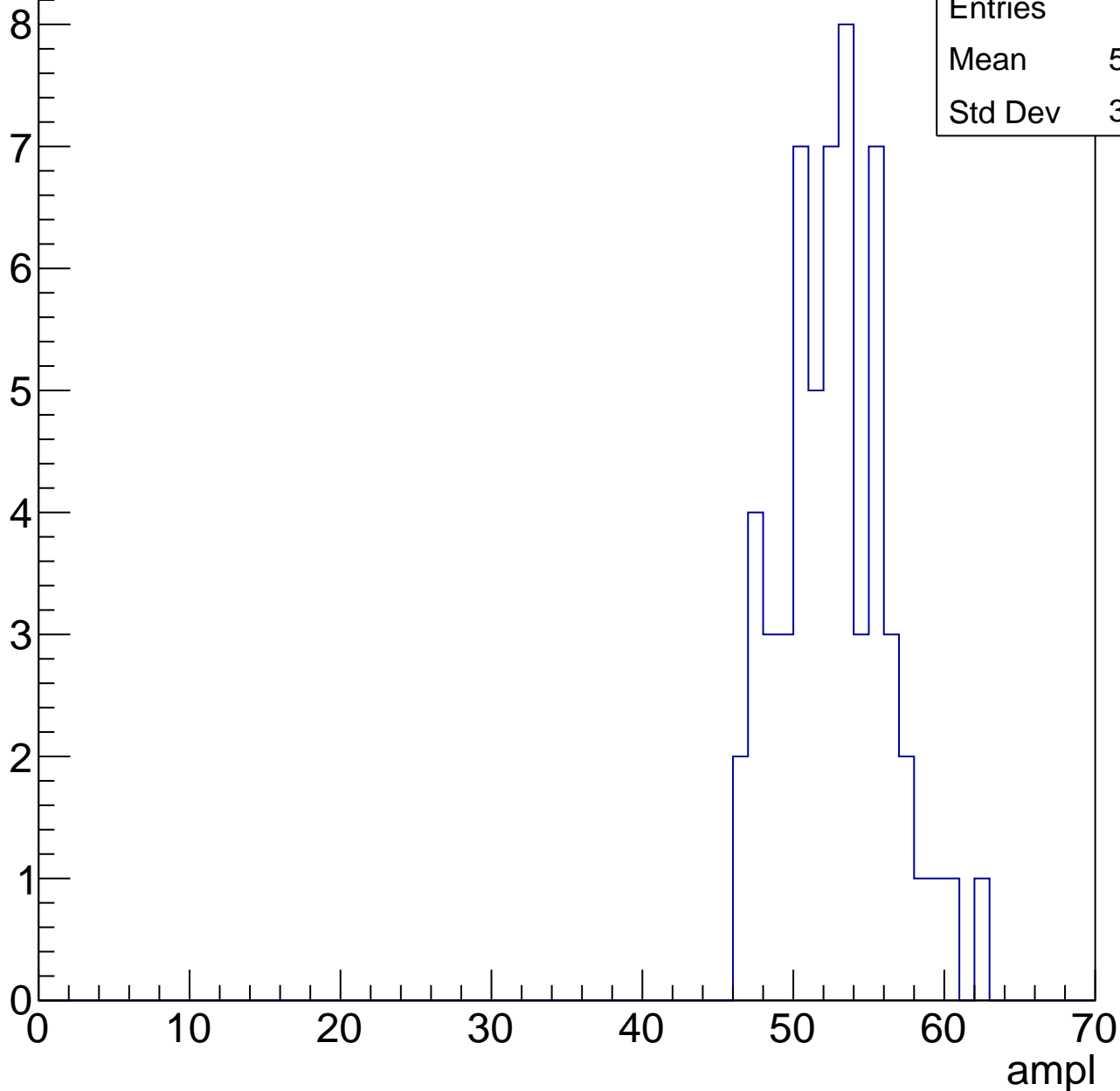


B0L001S, U18-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	52.28
Std Dev	3.488

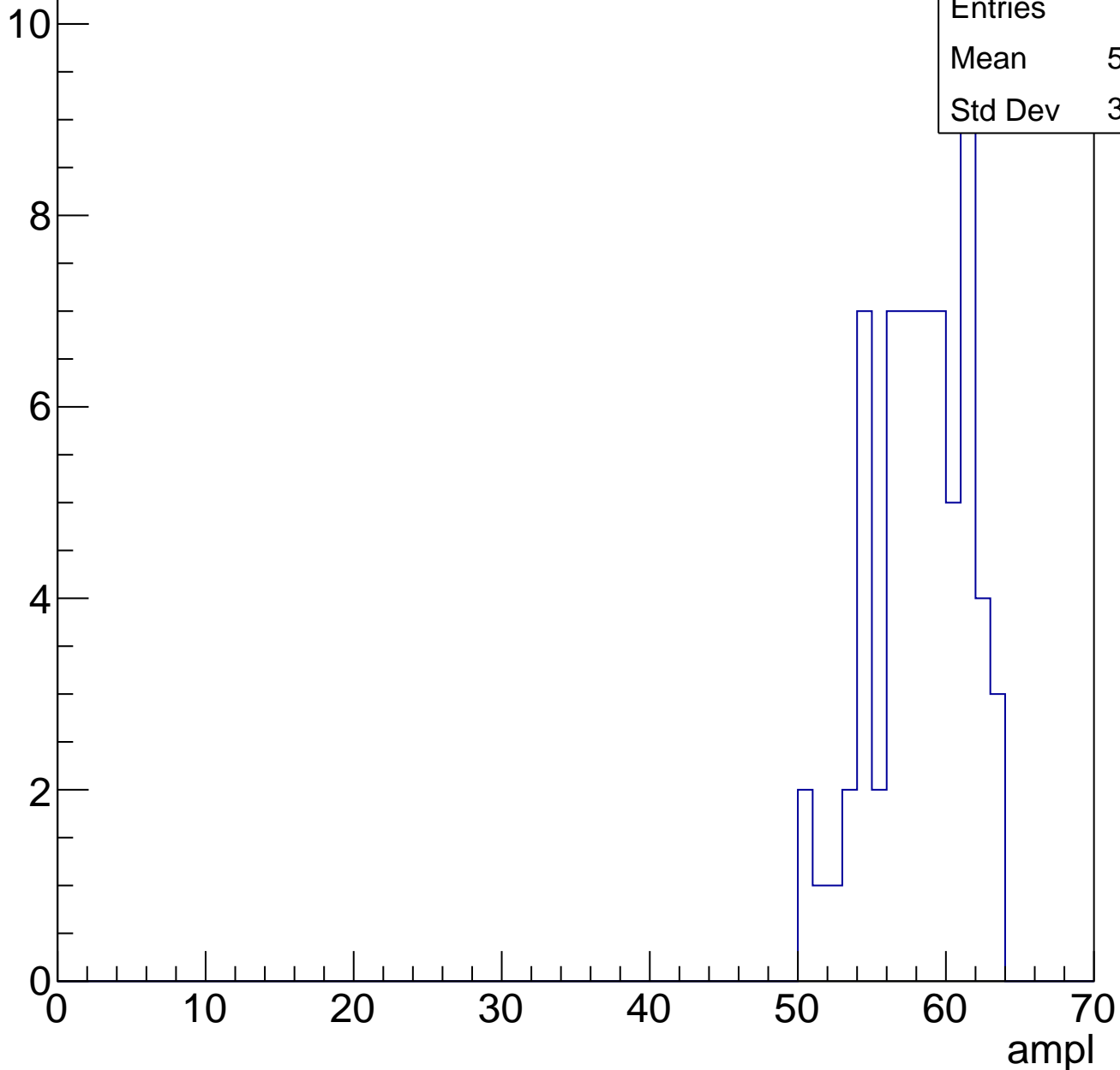


B0L001S, U18-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	57.75
Std Dev	3.235

Entry

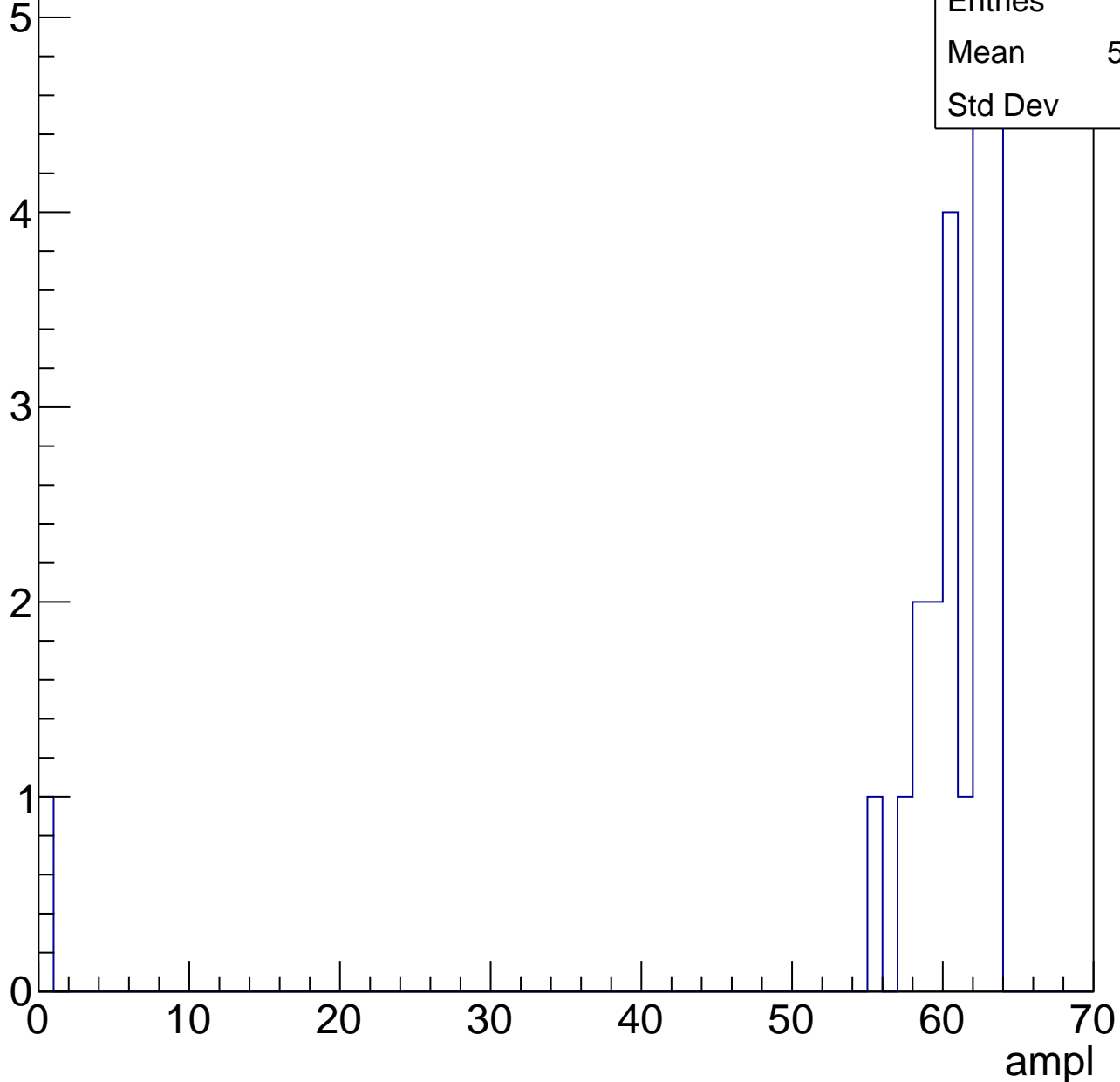


B0L001S, U18-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	57.82
Std Dev	12.8



B0L001S, U18-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch14, adc0

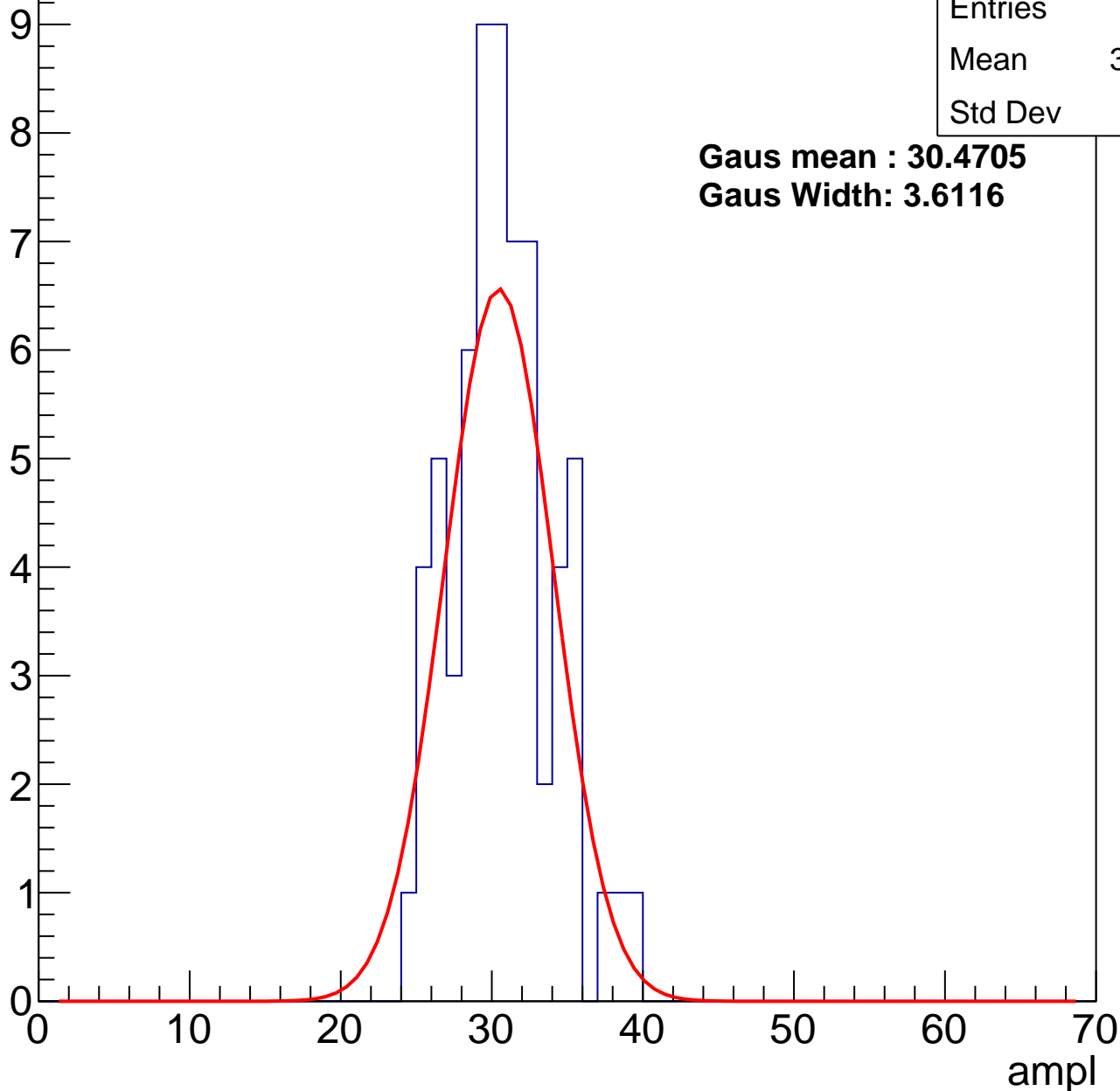
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.25
Std Dev	3.3

Gaus mean : 30.4705

Gaus Width: 3.6116



B0L001S, U18-ch14, adc1

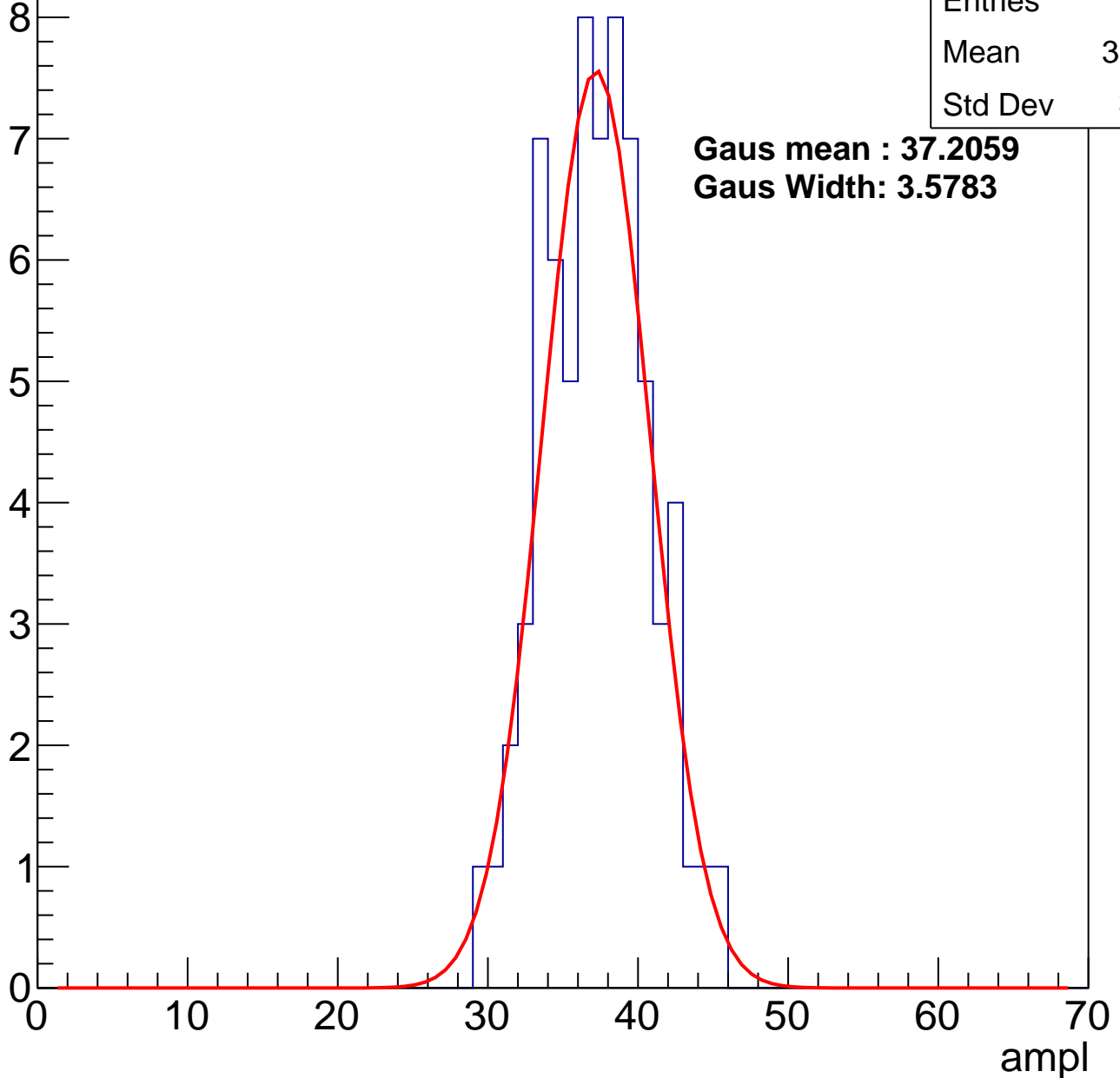
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.77
Std Dev	3.44

Gaus mean : 37.2059

Gaus Width: 3.5783



B0L001S, U18-ch14, adc2

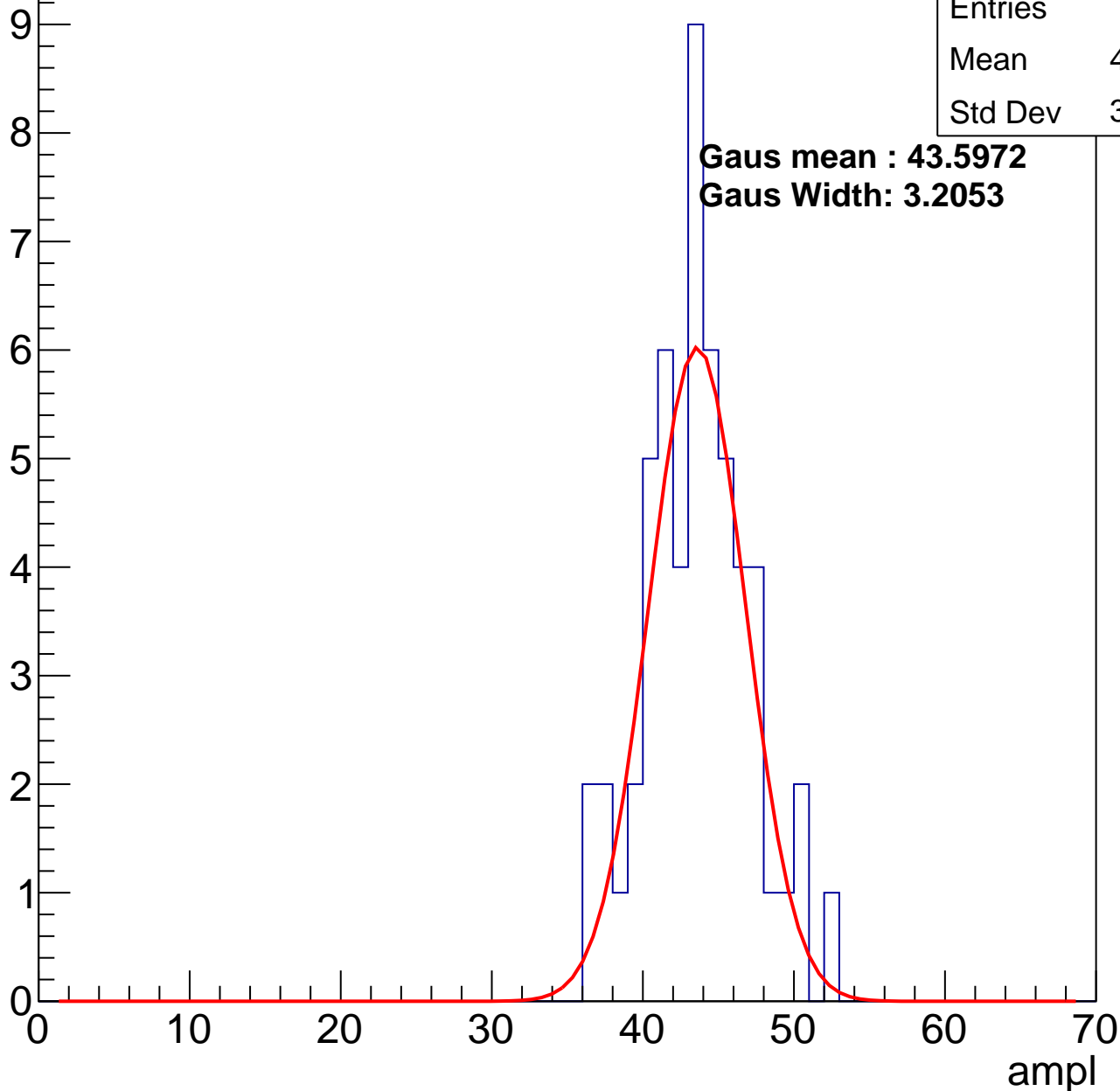
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.15
Std Dev	3.477

Gaus mean : 43.5972

Gaus Width: 3.2053

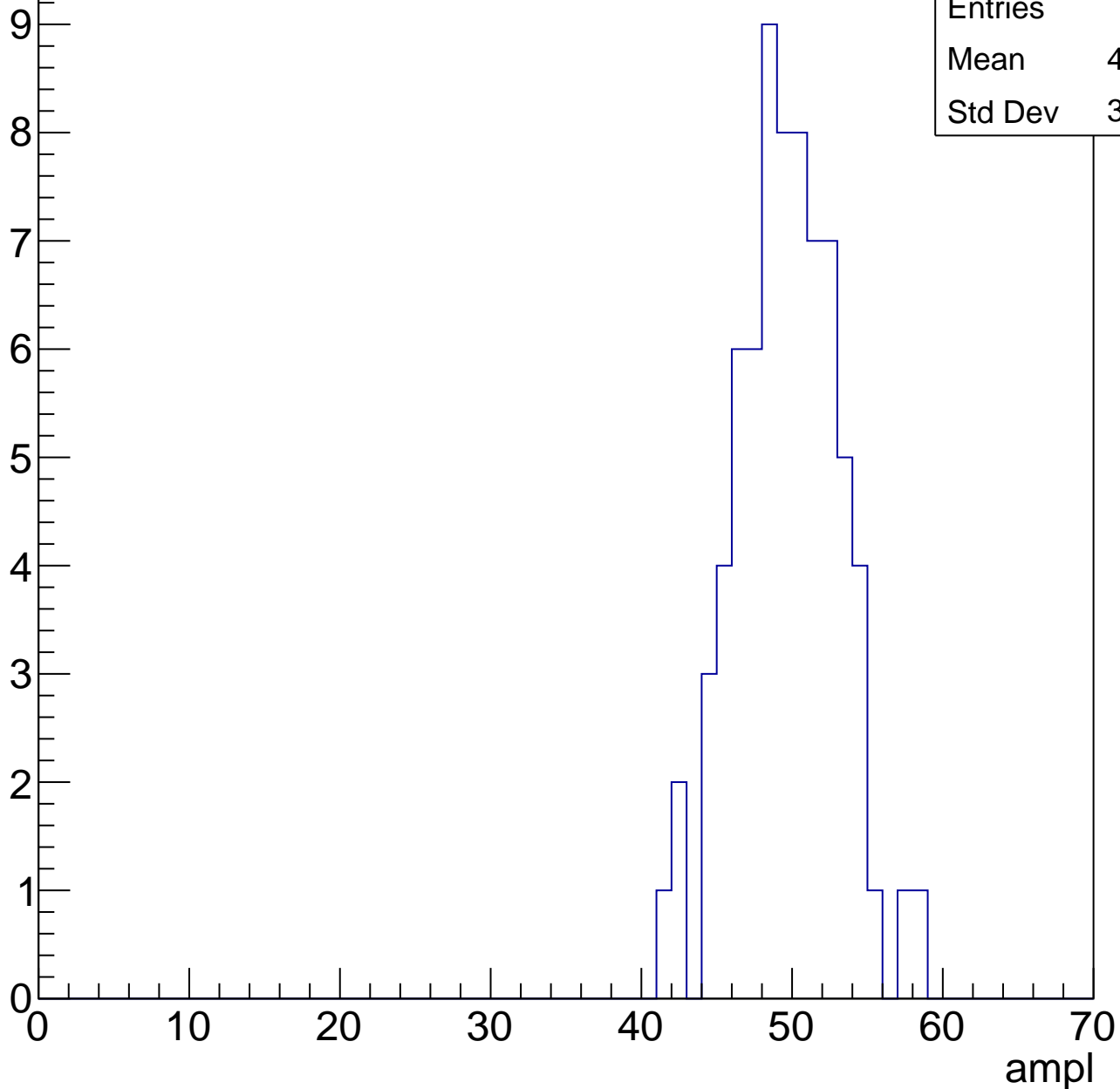


B0L001S, U18-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	49.19
Std Dev	3.403

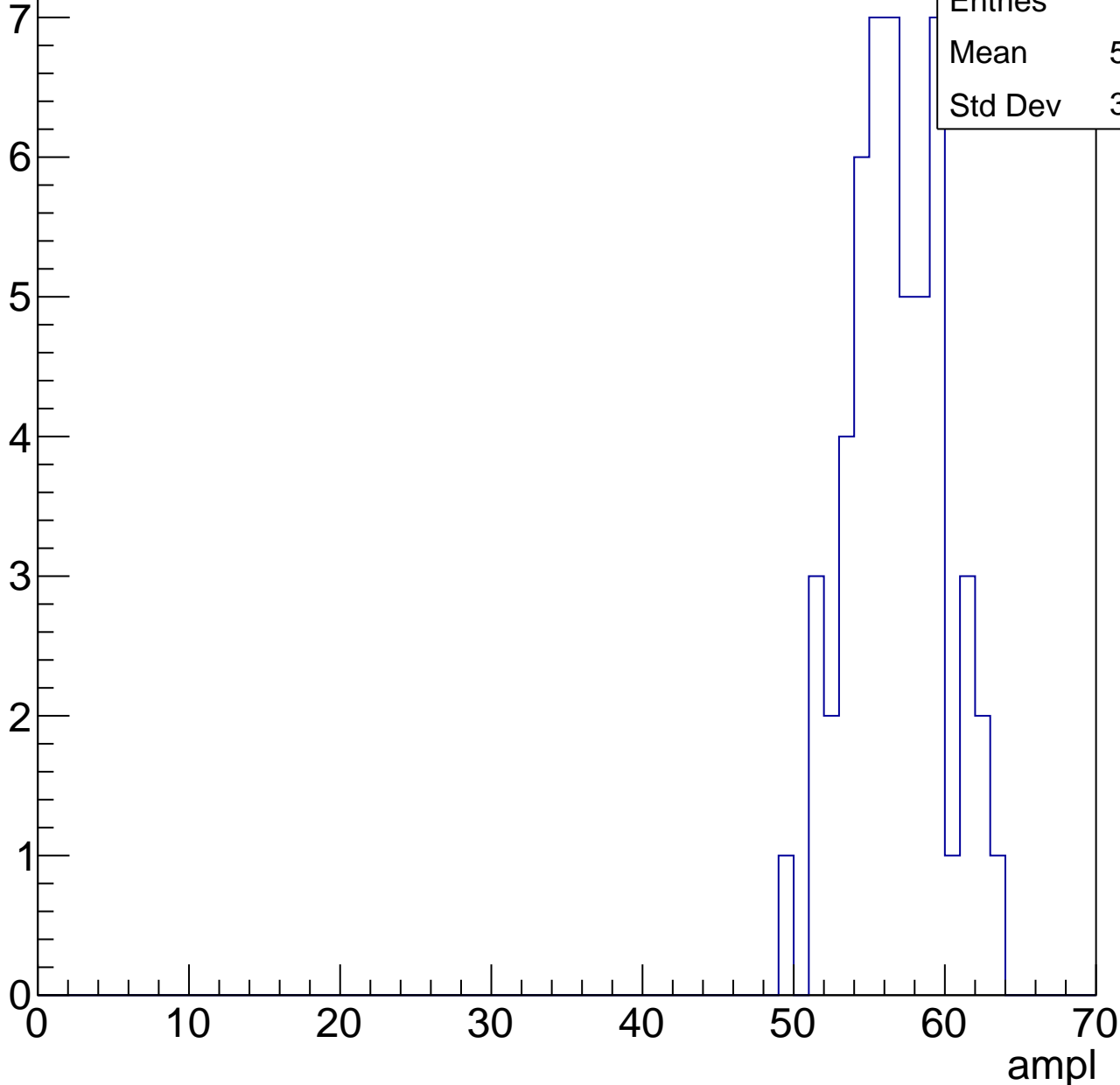


B0L001S, U18-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.24
Std Dev	3.103

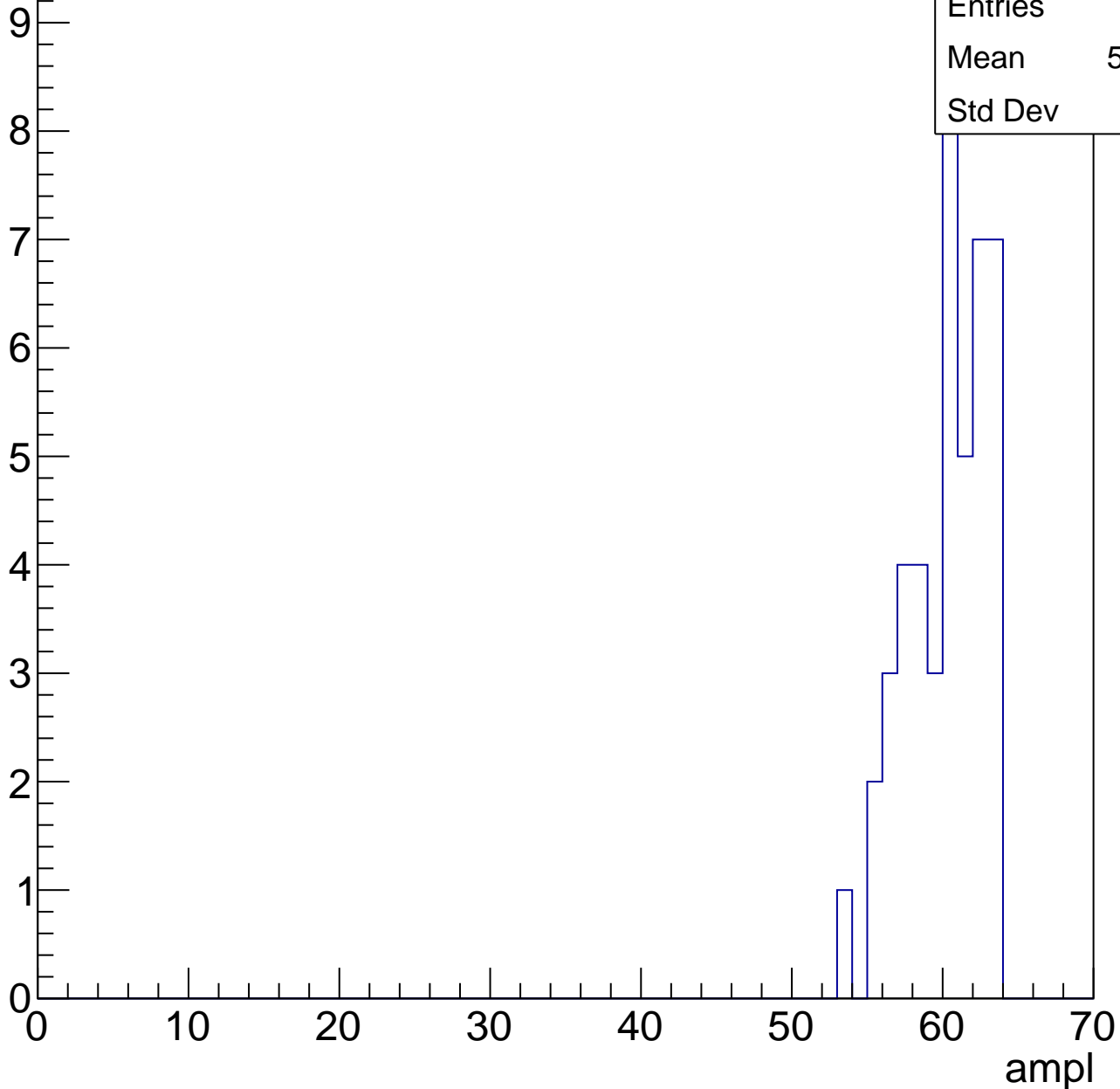


B0L001S, U18-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

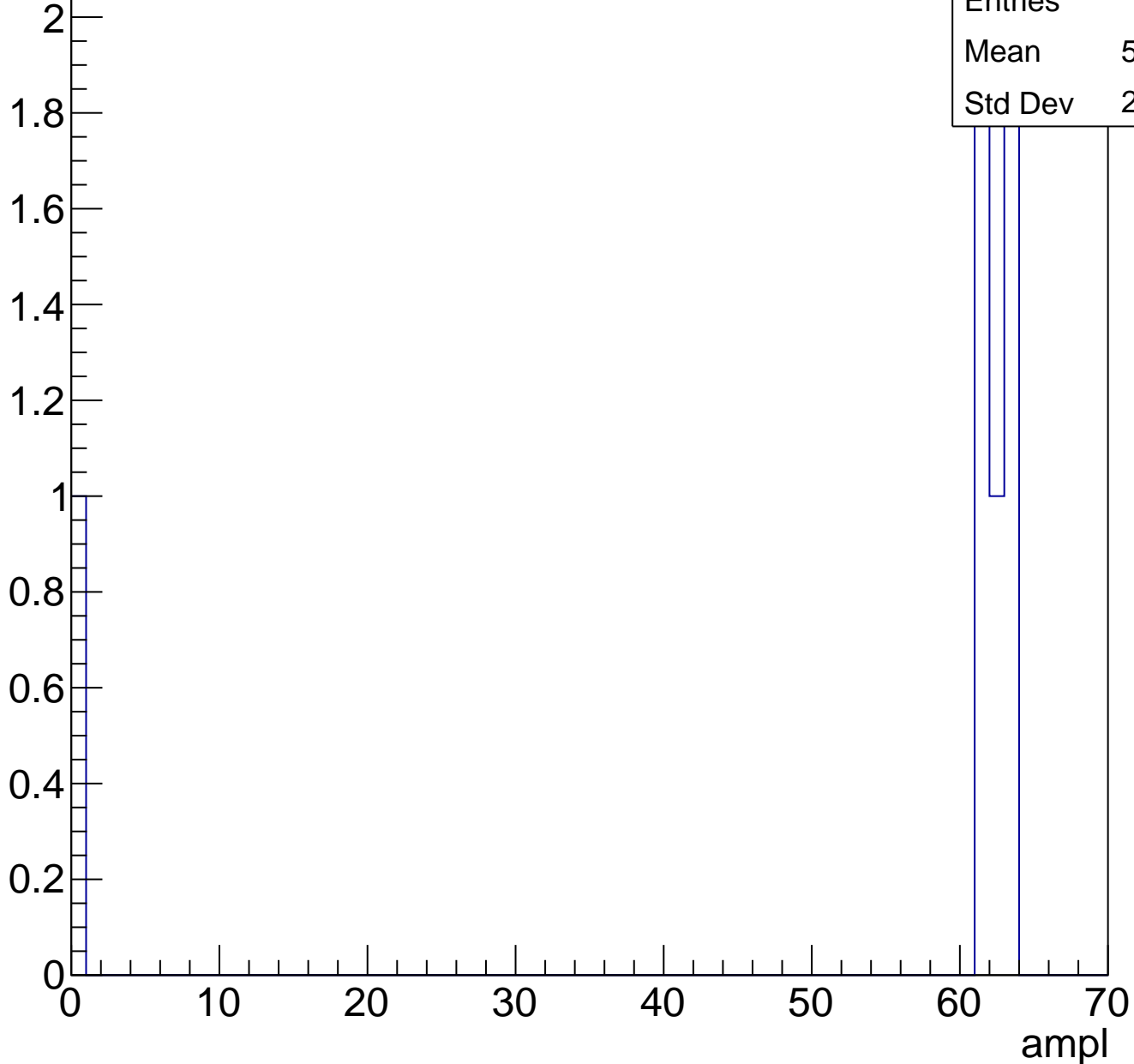
Entries	45
Mean	59.73
Std Dev	2.56



B0L001S, U18-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch15, adc0

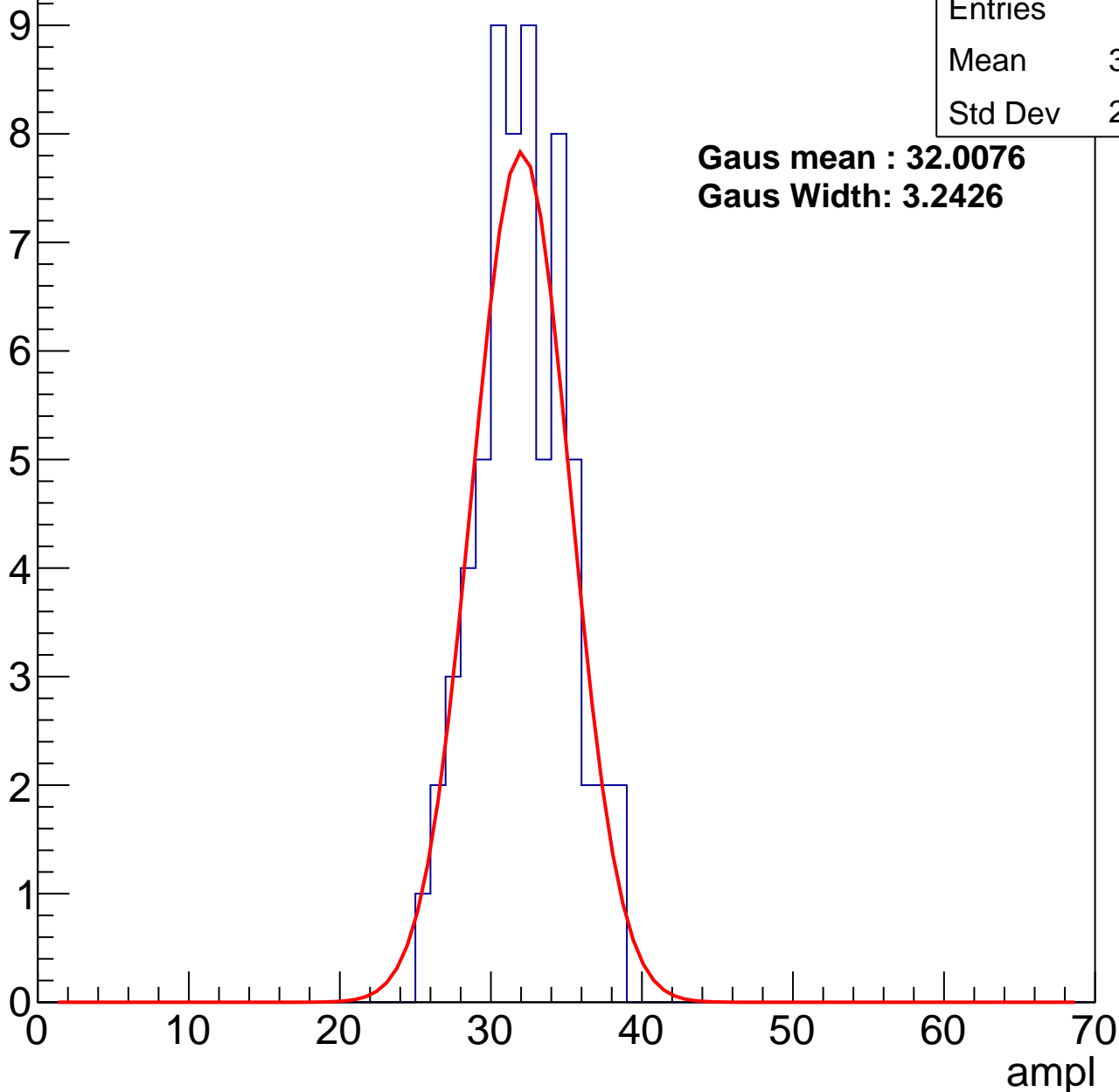
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.62
Std Dev	2.975

Gaus mean : 32.0076

Gaus Width: 3.2426



B0L001S, U18-ch15, adc1

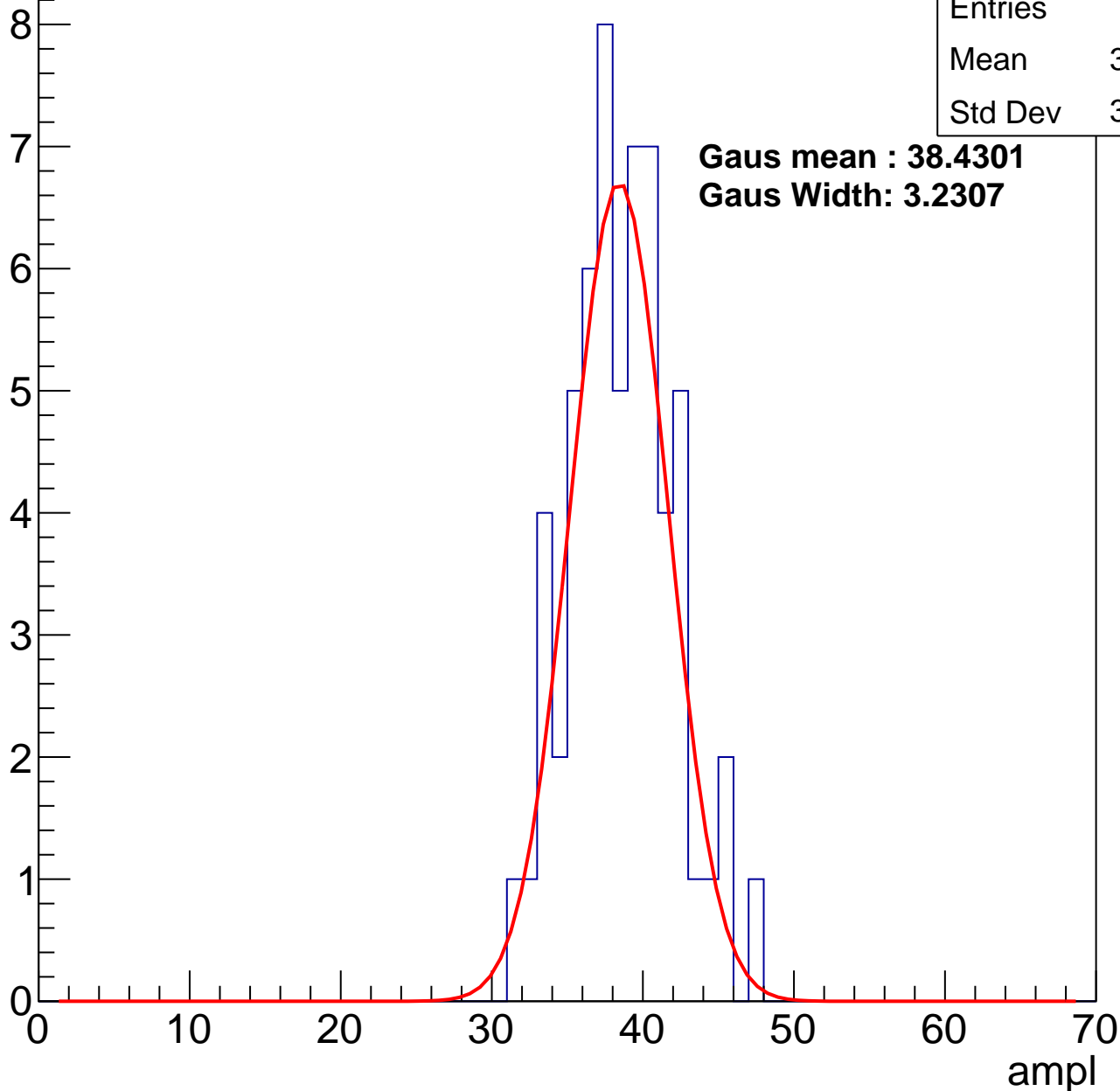
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.18
Std Dev	3.374

Gaus mean : 38.4301

Gaus Width: 3.2307



B0L001S, U18-ch15, adc2

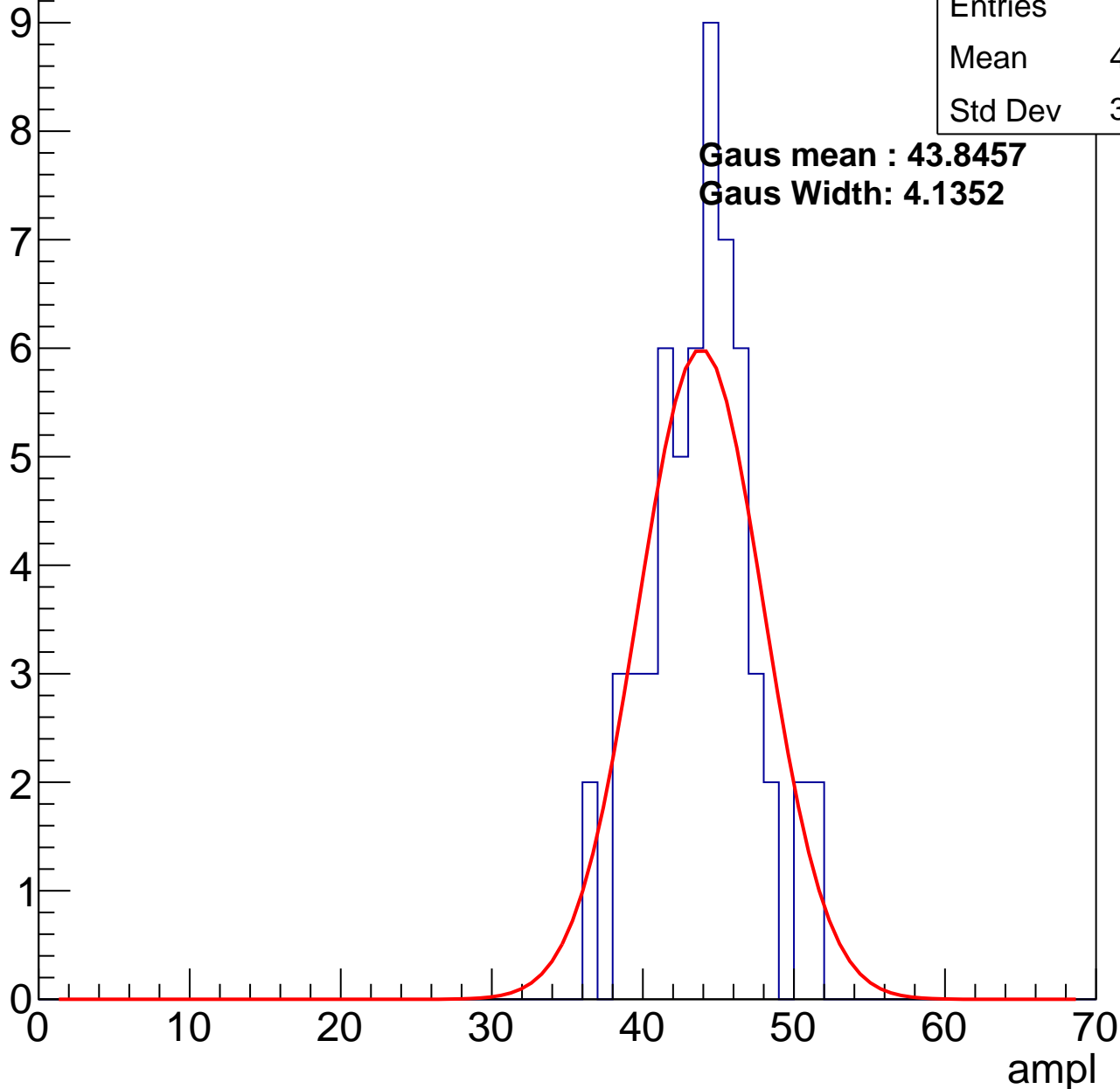
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.44
Std Dev	3.396

Gaus mean : 43.8457

Gaus Width: 4.1352



B0L001S, U18-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	50.83
Std Dev	3.632

Entry

10
8
6
4
2
0

ampl

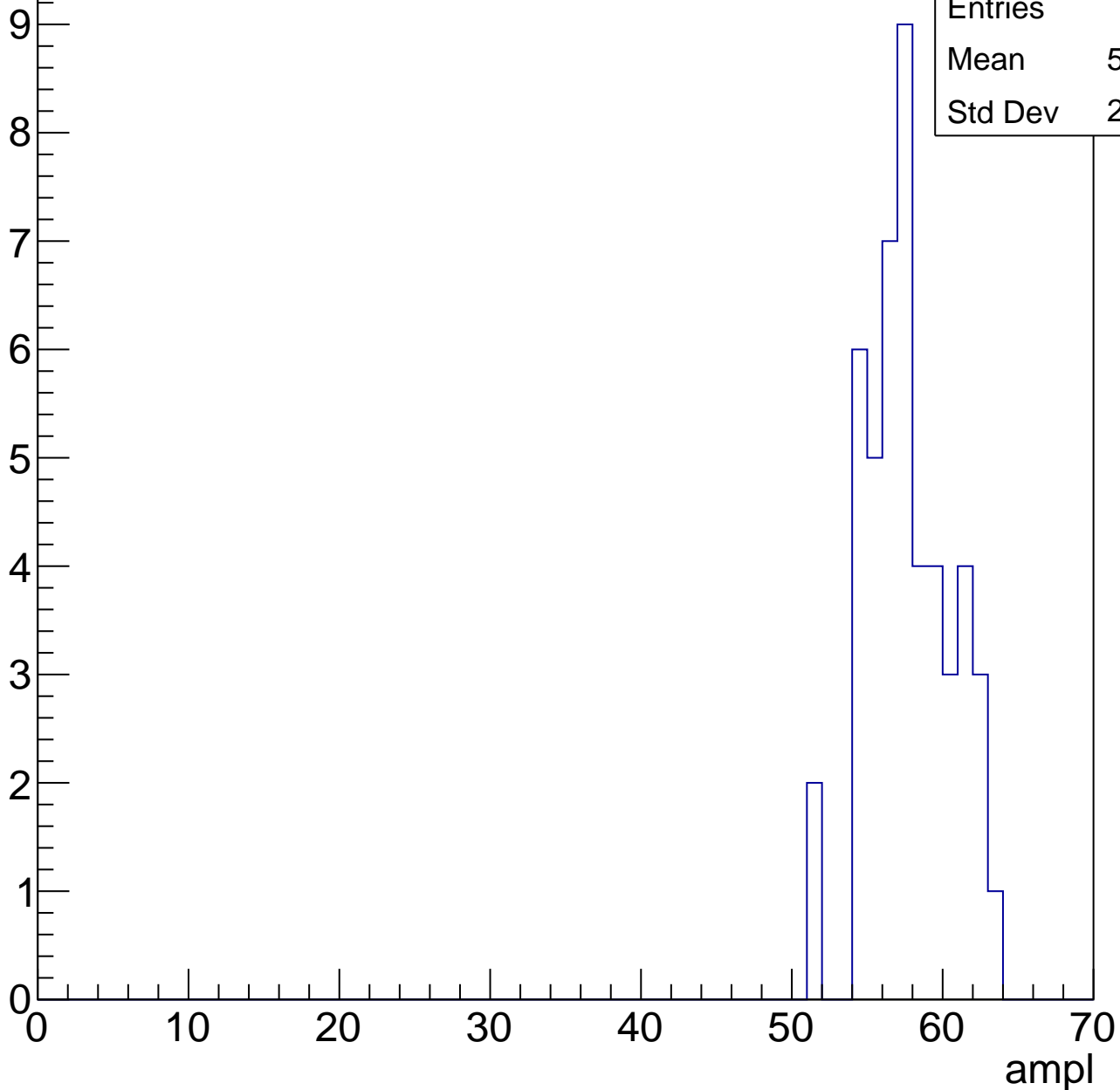
0 10 20 30 40 50 60 70

B0L001S, U18-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57.23
Std Dev	2.786

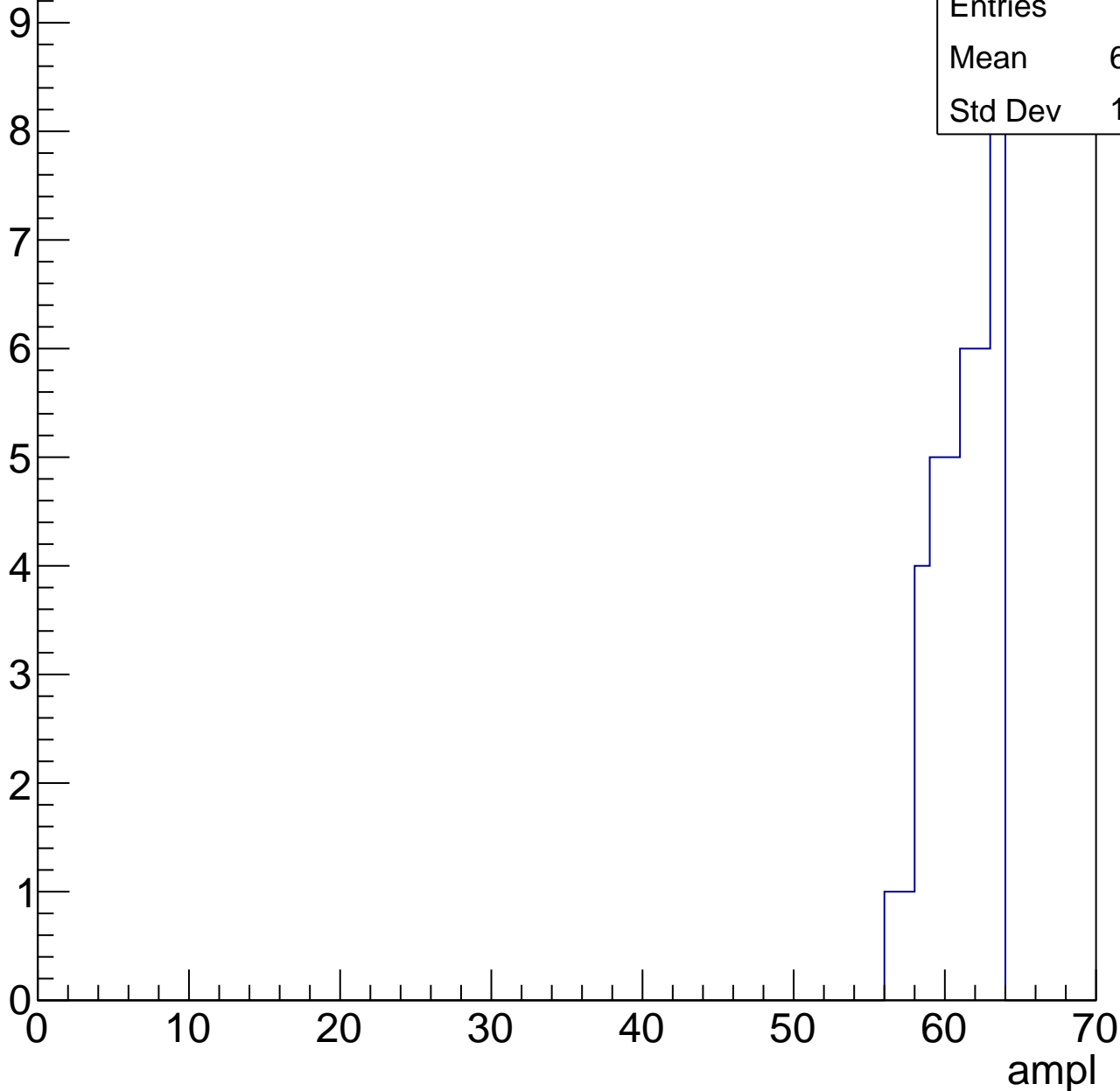


B0L001S, U18-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	60.68
Std Dev	1.946



B0L001S, U18-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch16, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	30.32
Std Dev	4.812

Gaus mean : 31.2283

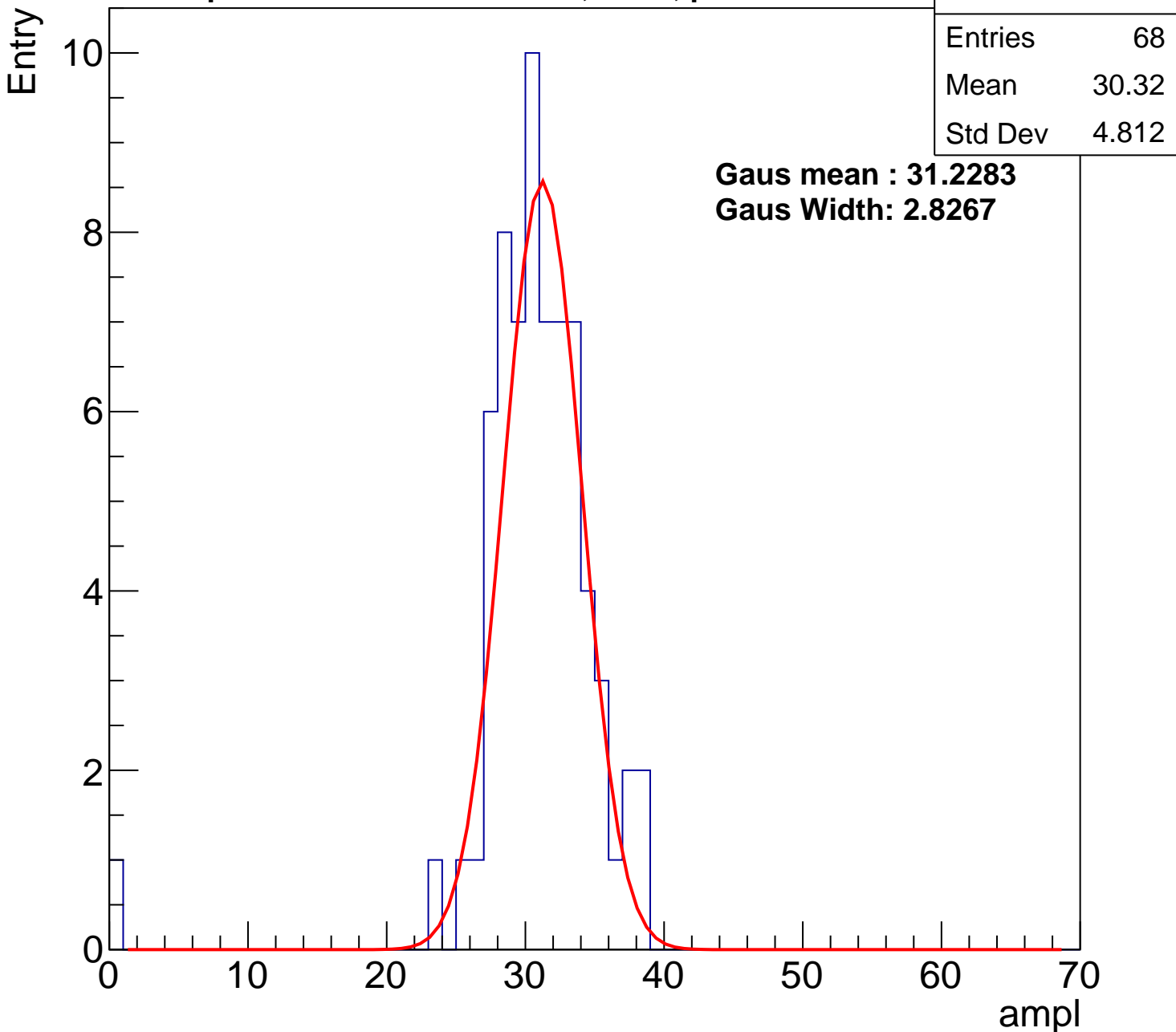
Gaus Width: 2.8267

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch16, adc1

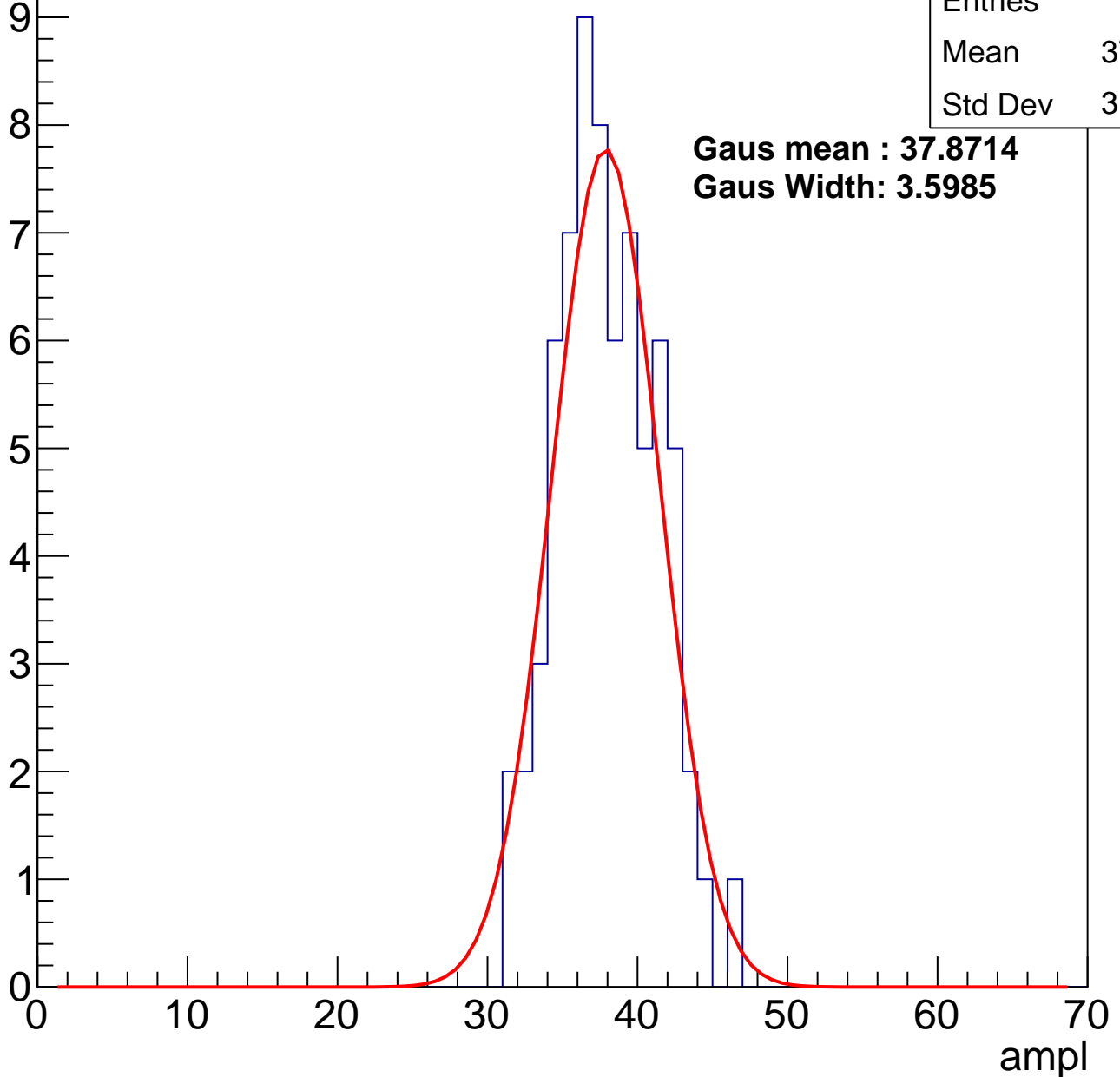
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.53
Std Dev	3.259

Gaus mean : 37.8714

Gaus Width: 3.5985



B0L001S, U18-ch16, adc2

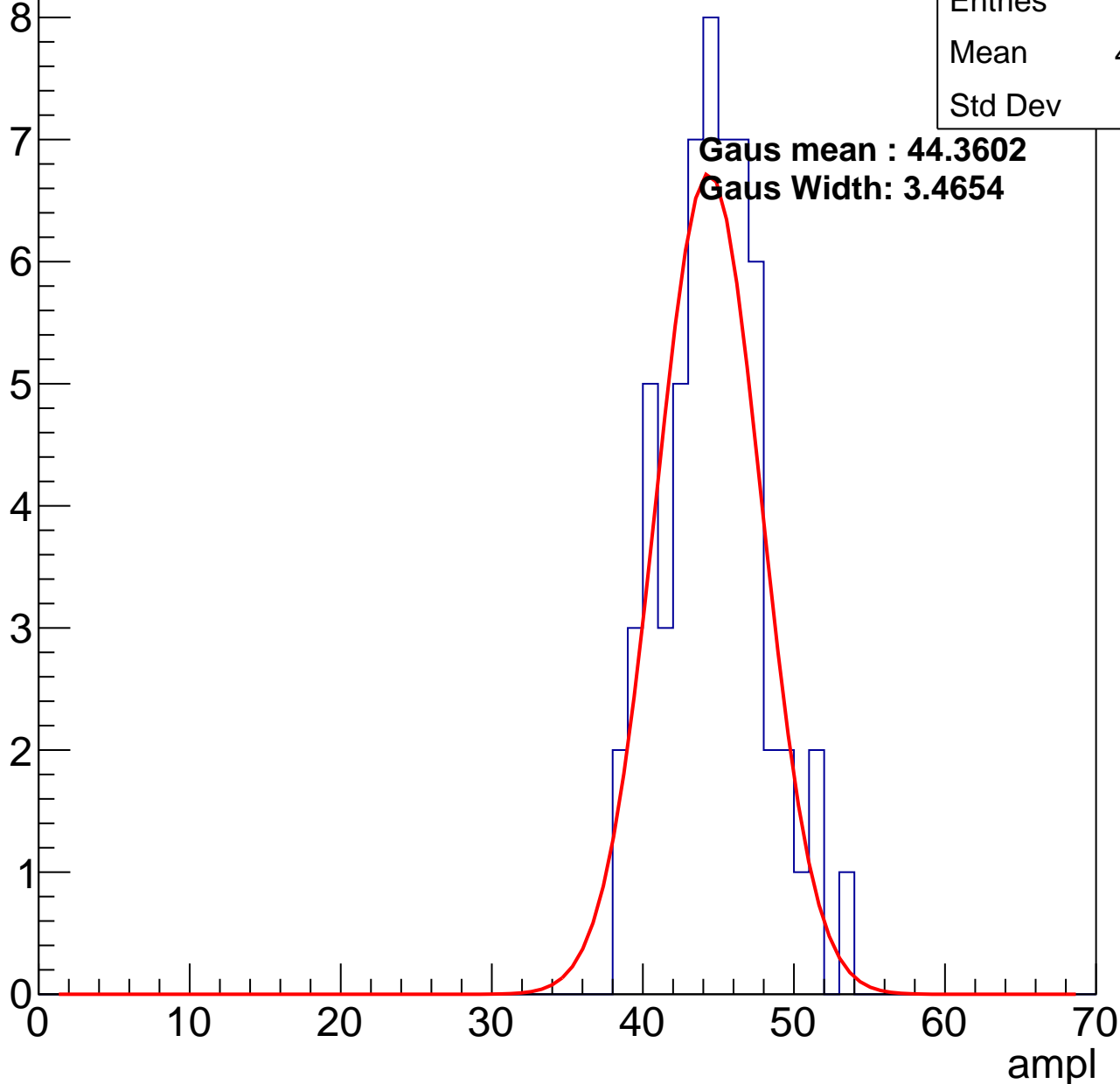
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.21
Std Dev	3.3

Gaus mean : 44.3602

Gaus Width: 3.4654

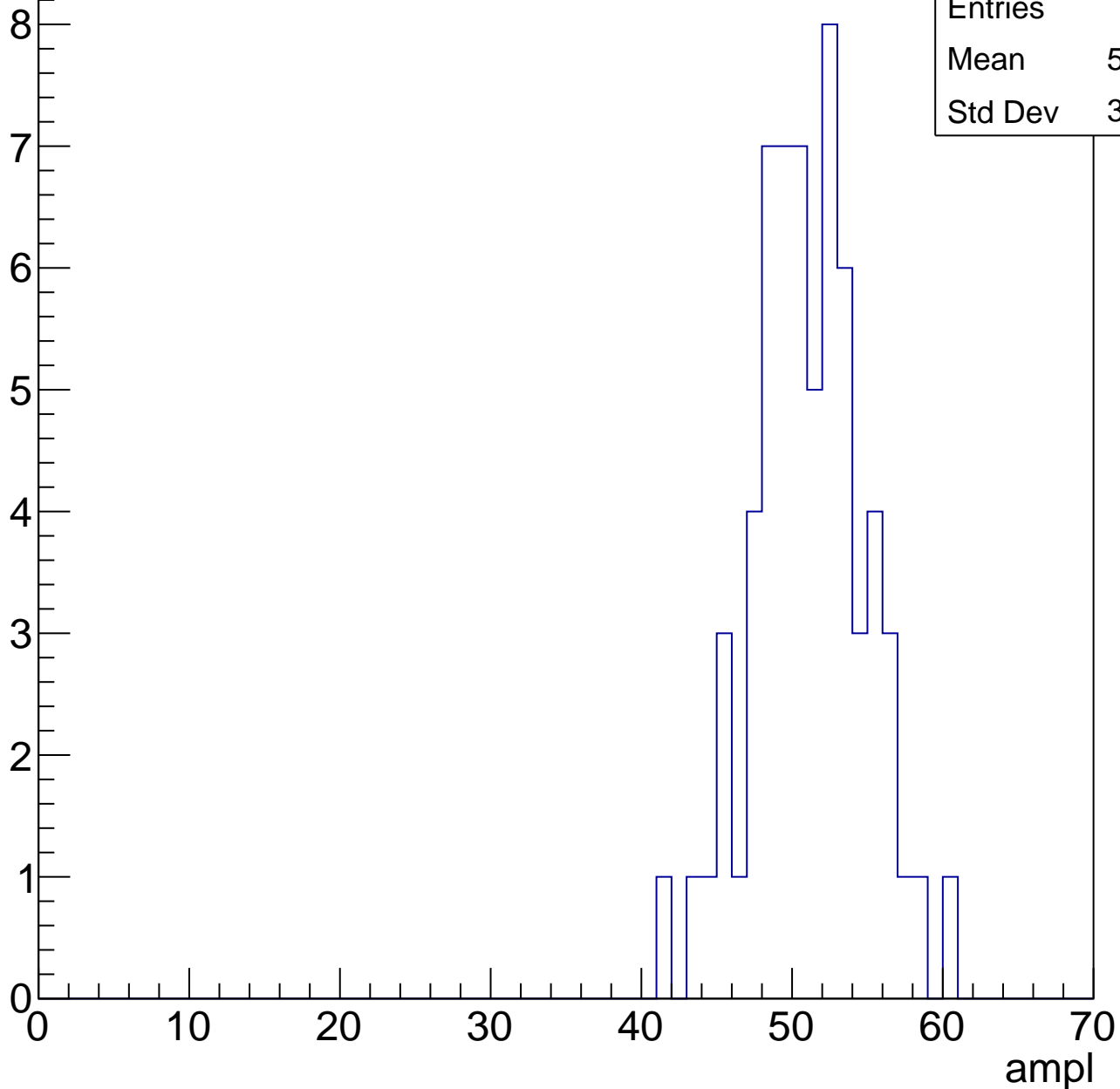


B0L001S, U18-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.62
Std Dev	3.672

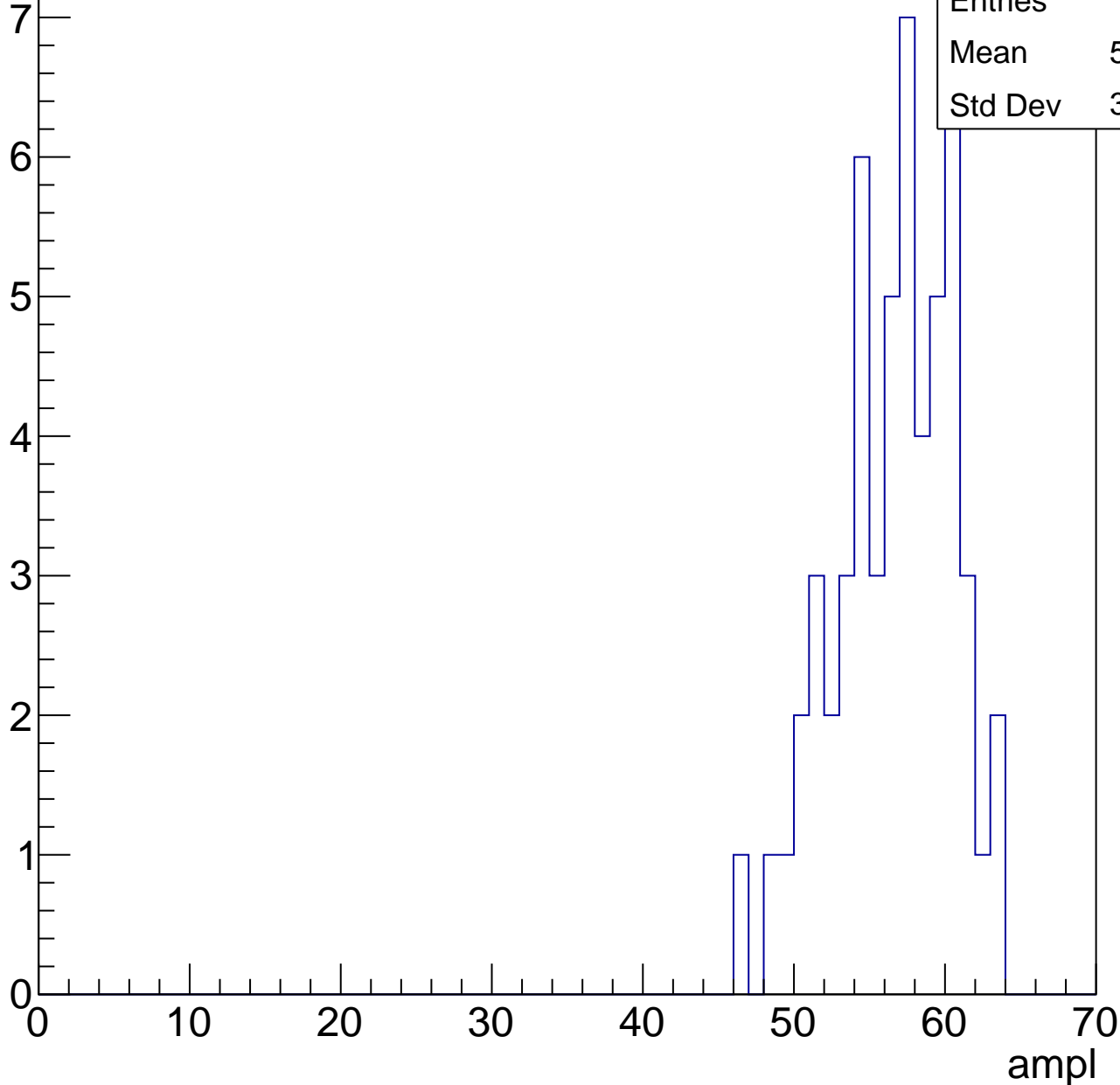


B0L001S, U18-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.16
Std Dev	3.858

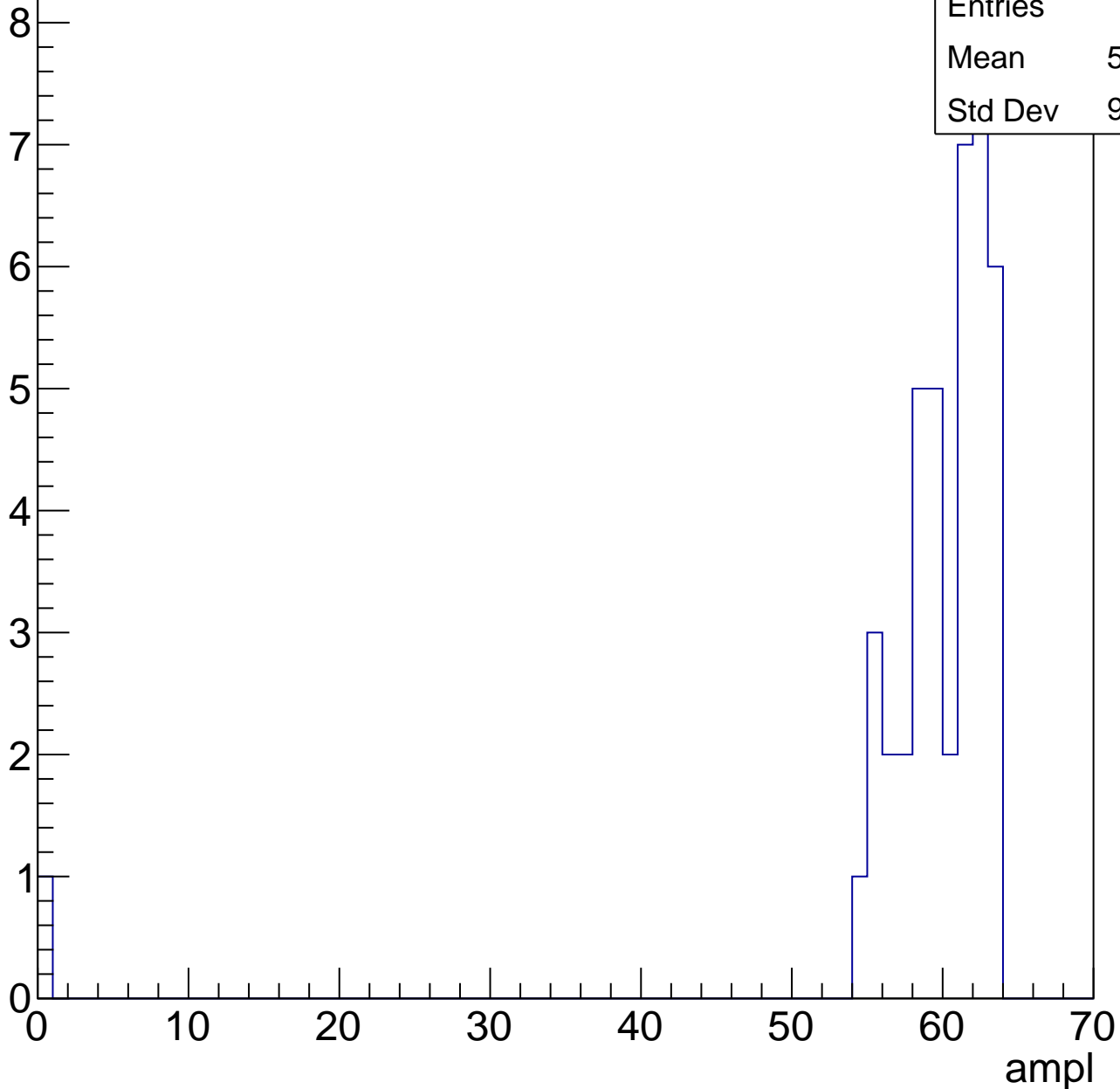


B0L001S, U18-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.36
Std Dev	9.469



B0L001S, U18-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

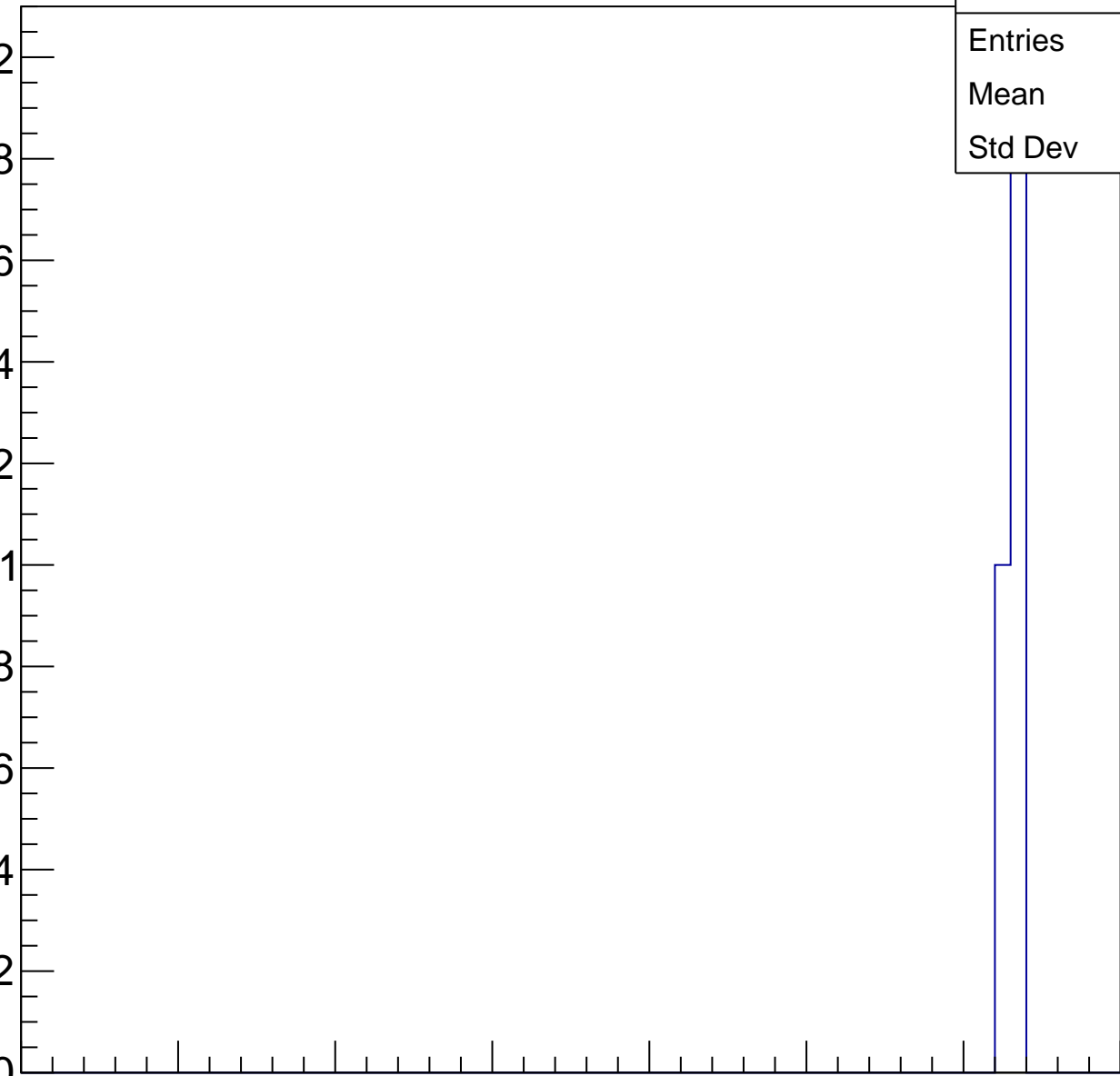
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch17, adc0

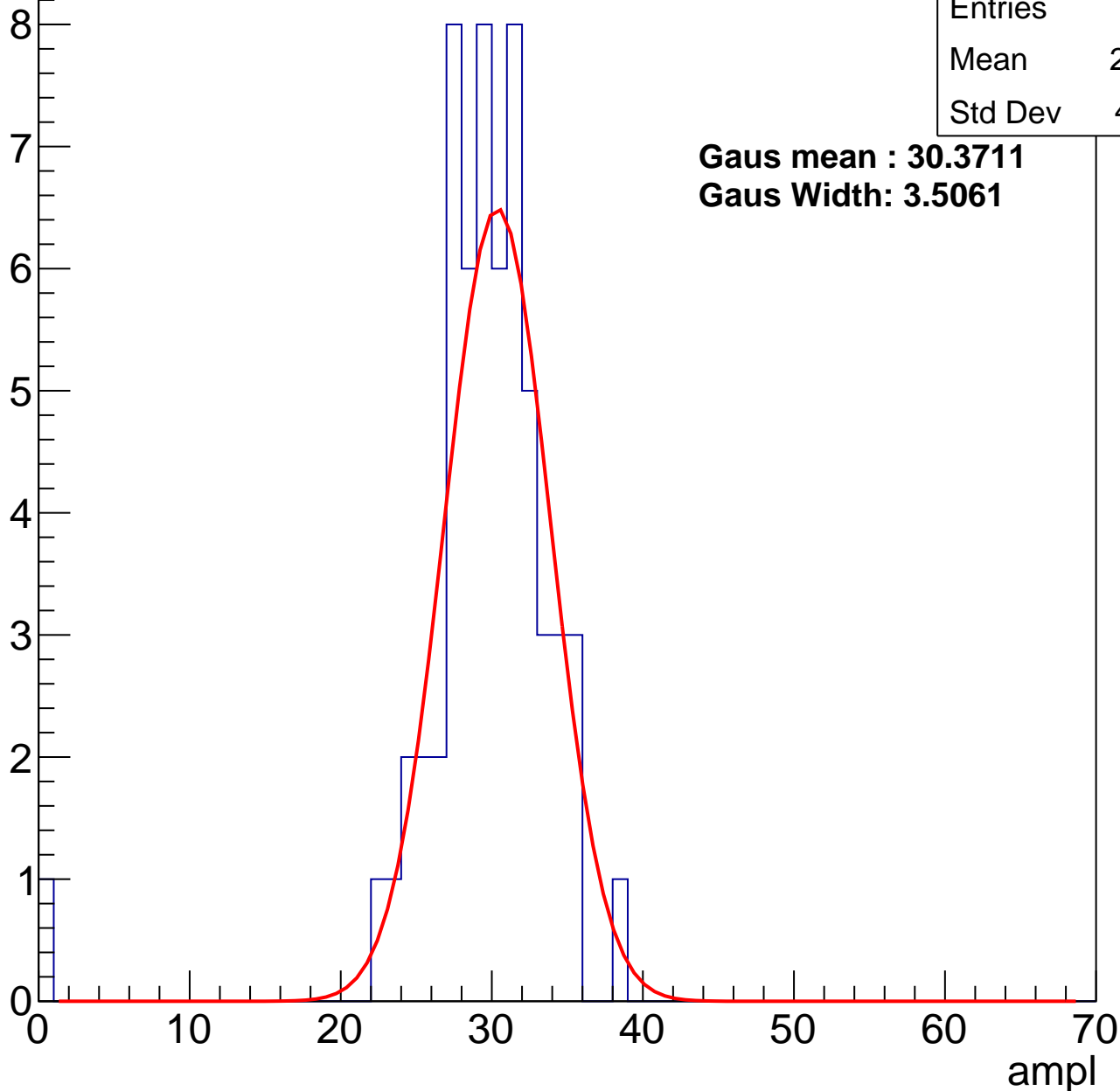
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.05
Std Dev	4.931

Gaus mean : 30.3711

Gaus Width: 3.5061



B0L001S, U18-ch17, adc1

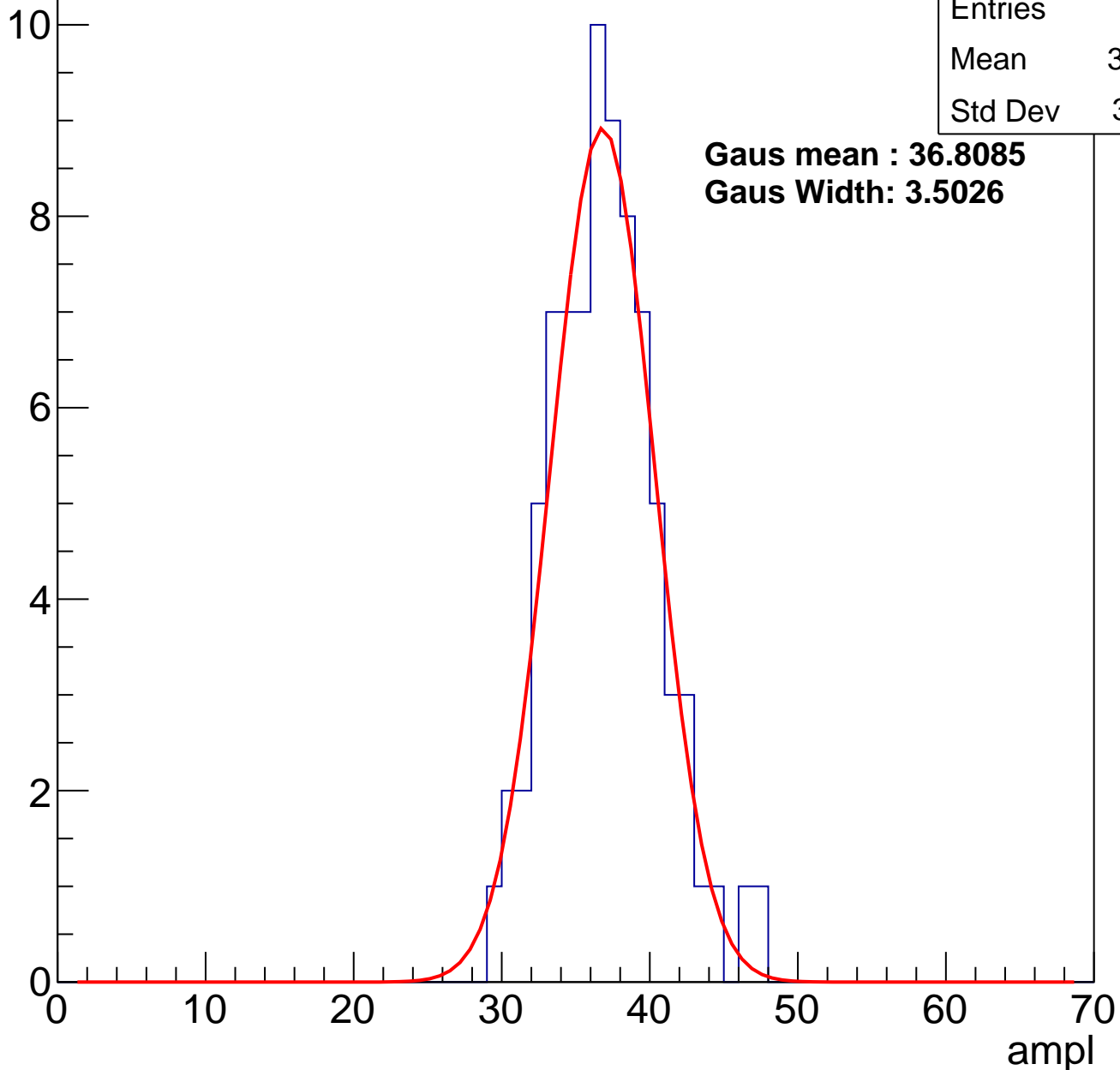
calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	36.55
Std Dev	3.581

Gaus mean : 36.8085

Gaus Width: 3.5026

Entry



B0L001S, U18-ch17, adc2

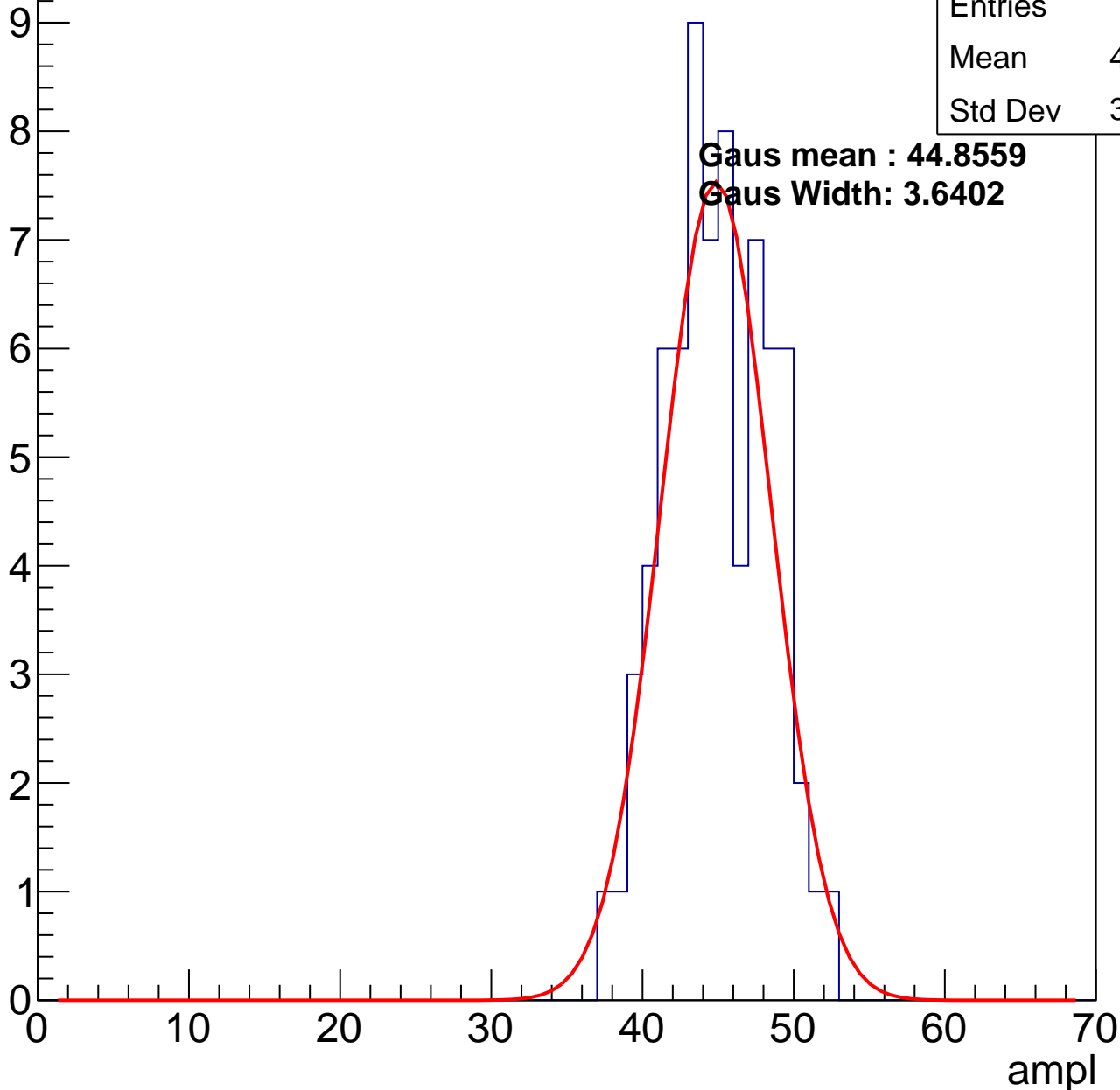
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	44.49
Std Dev	3.358

Gaus mean : 44.8559

Gaus Width: 3.6402

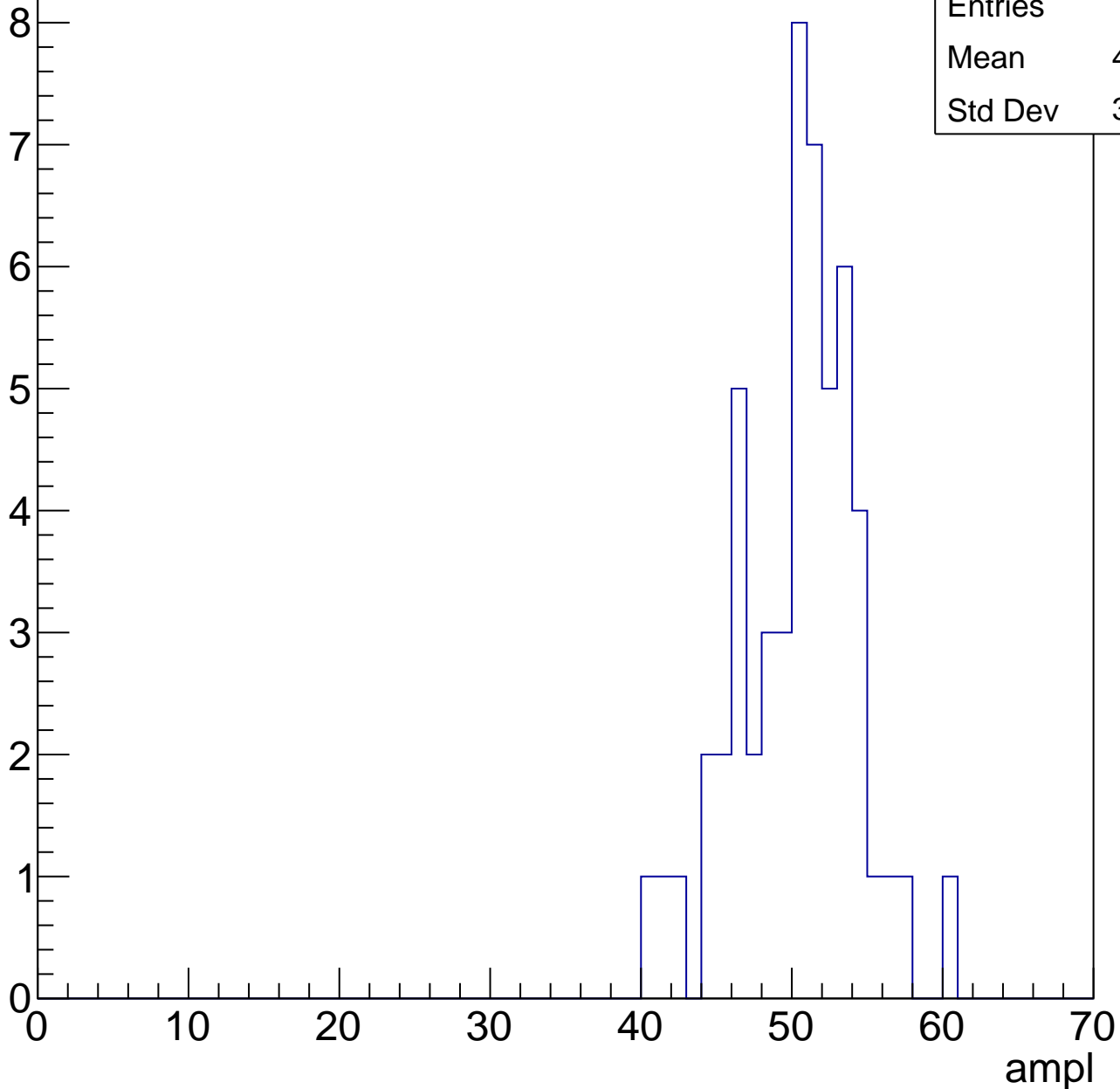


B0L001S, U18-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

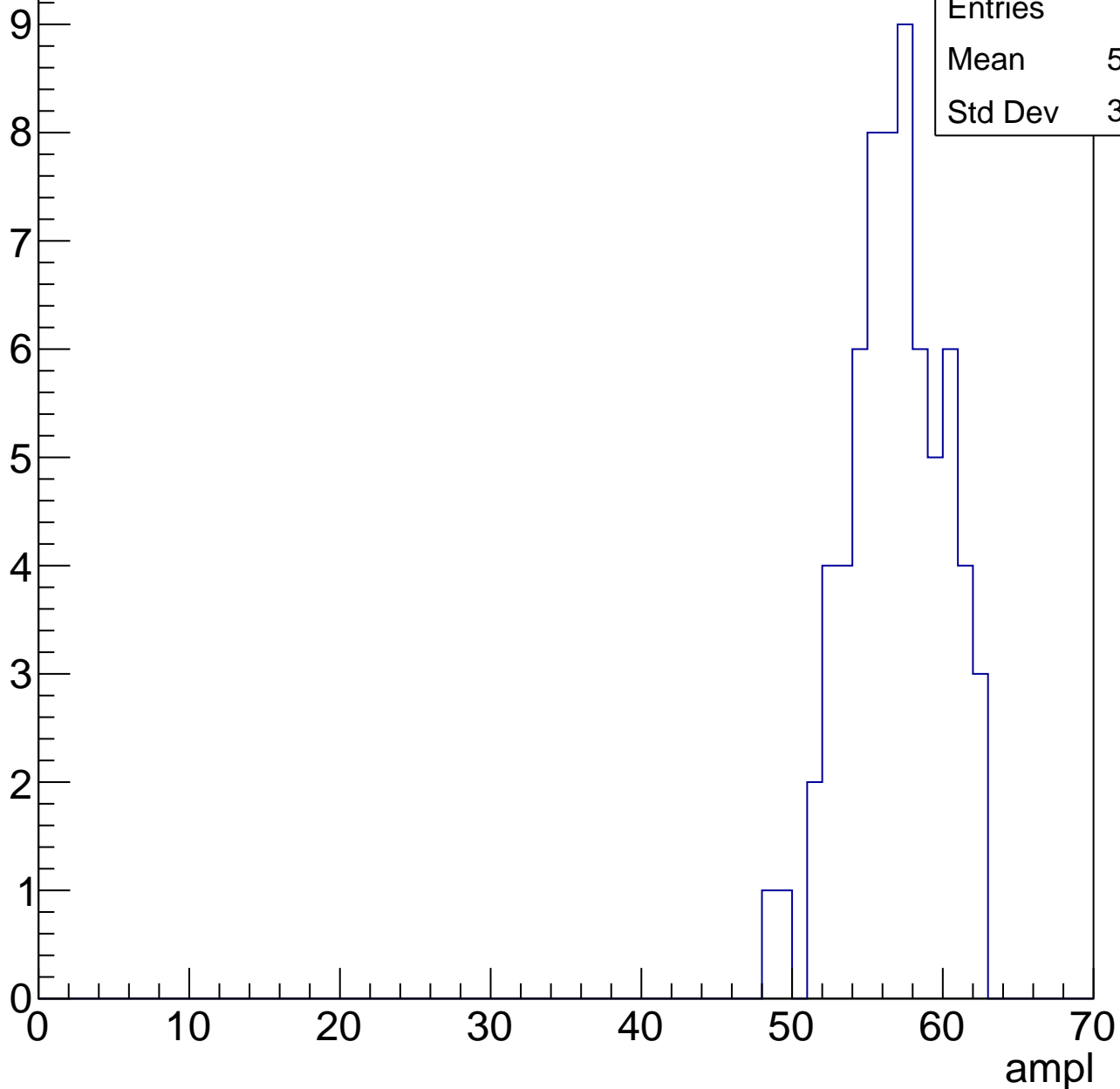
Entries	54
Mean	49.91
Std Dev	3.931



B0L001S, U18-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



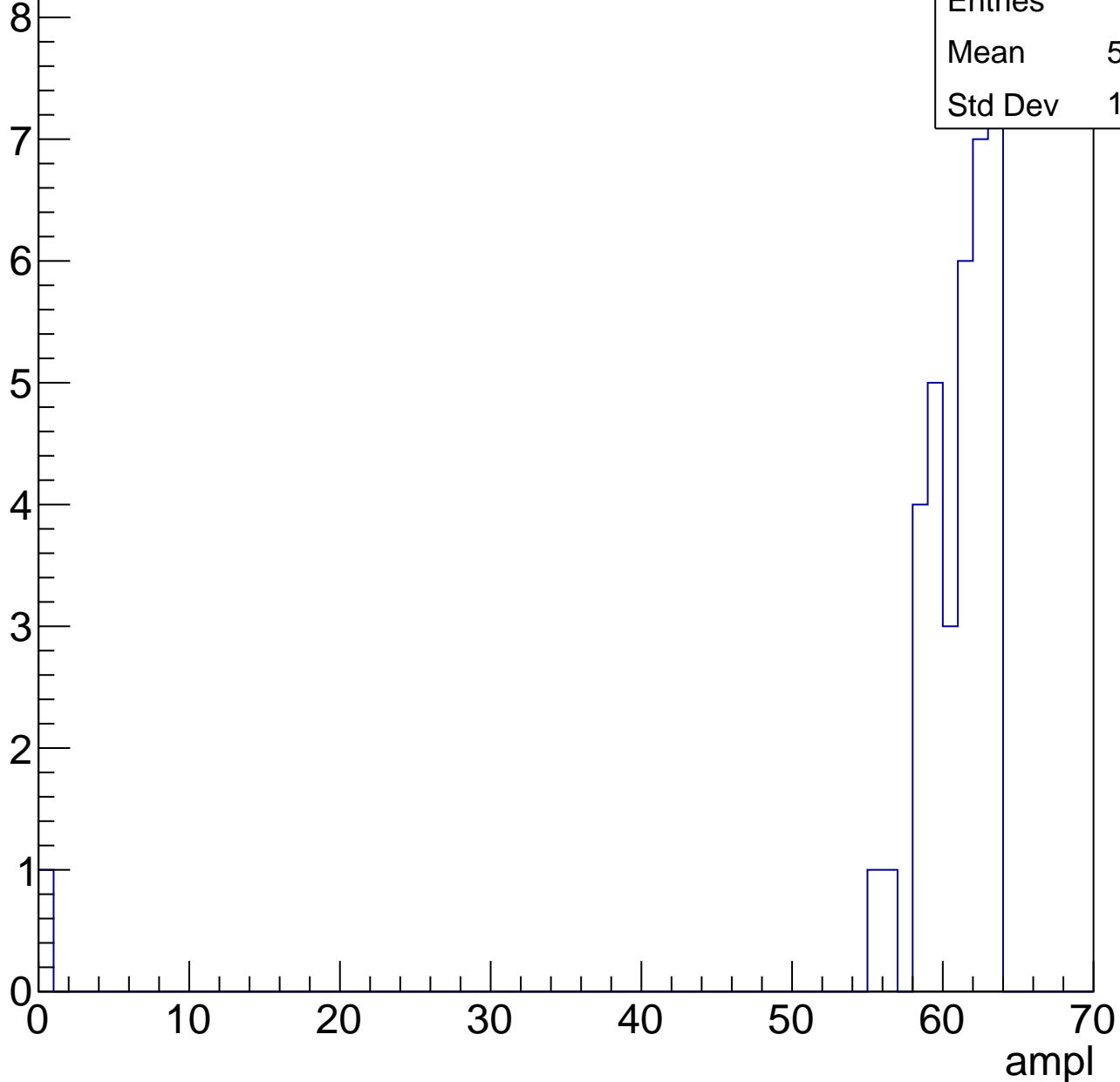
Entries	67
Mean	56.37
Std Dev	3.157

B0L001S, U18-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.94
Std Dev	10.18



B0L001S, U18-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch18, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	30.18
Std Dev	5.183

Gaus mean : 31.3104

Gaus Width: 3.8748

Entry

10
8
6
4
2
0

ampl

0

10

20

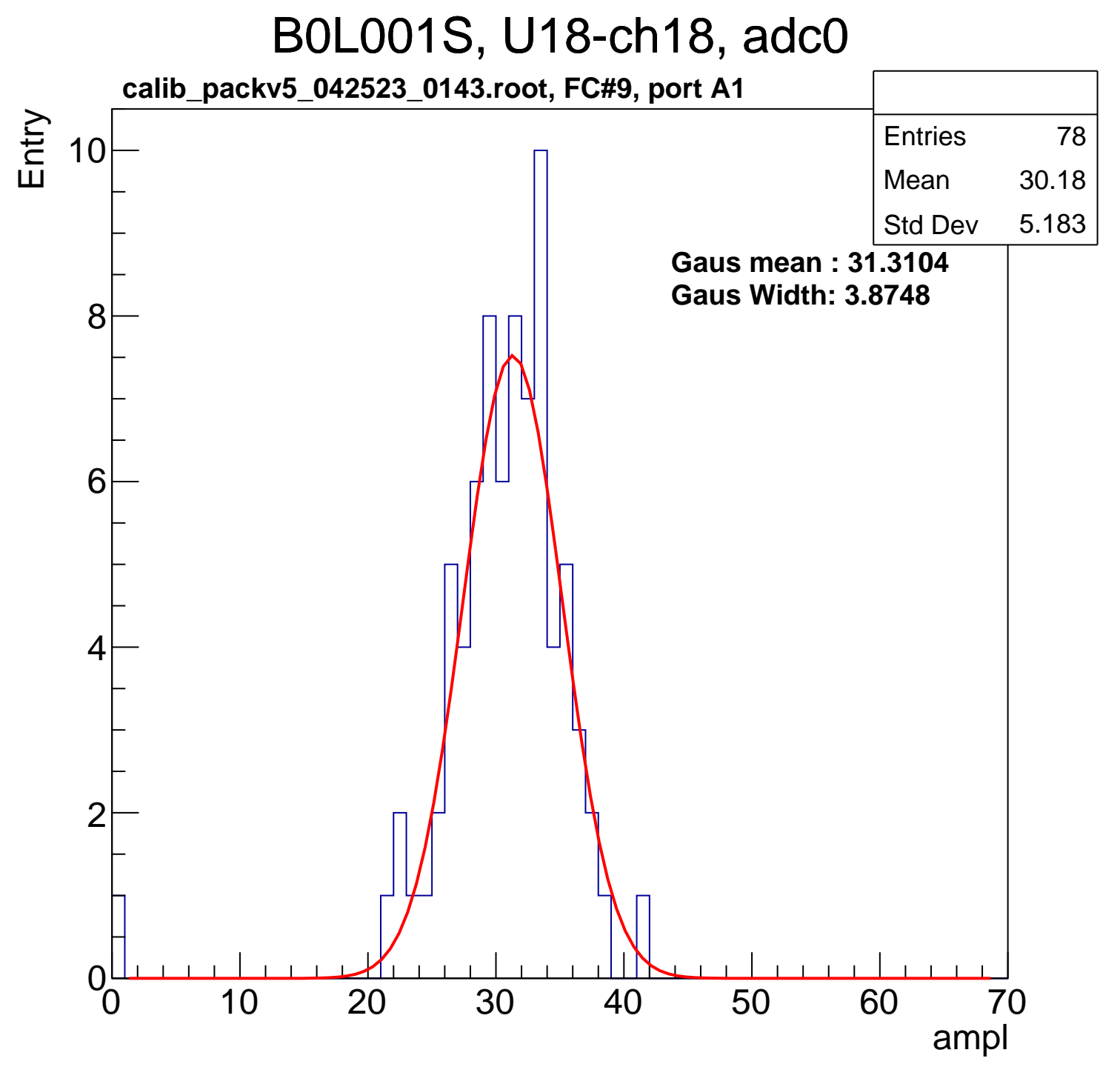
30

40

50

60

70



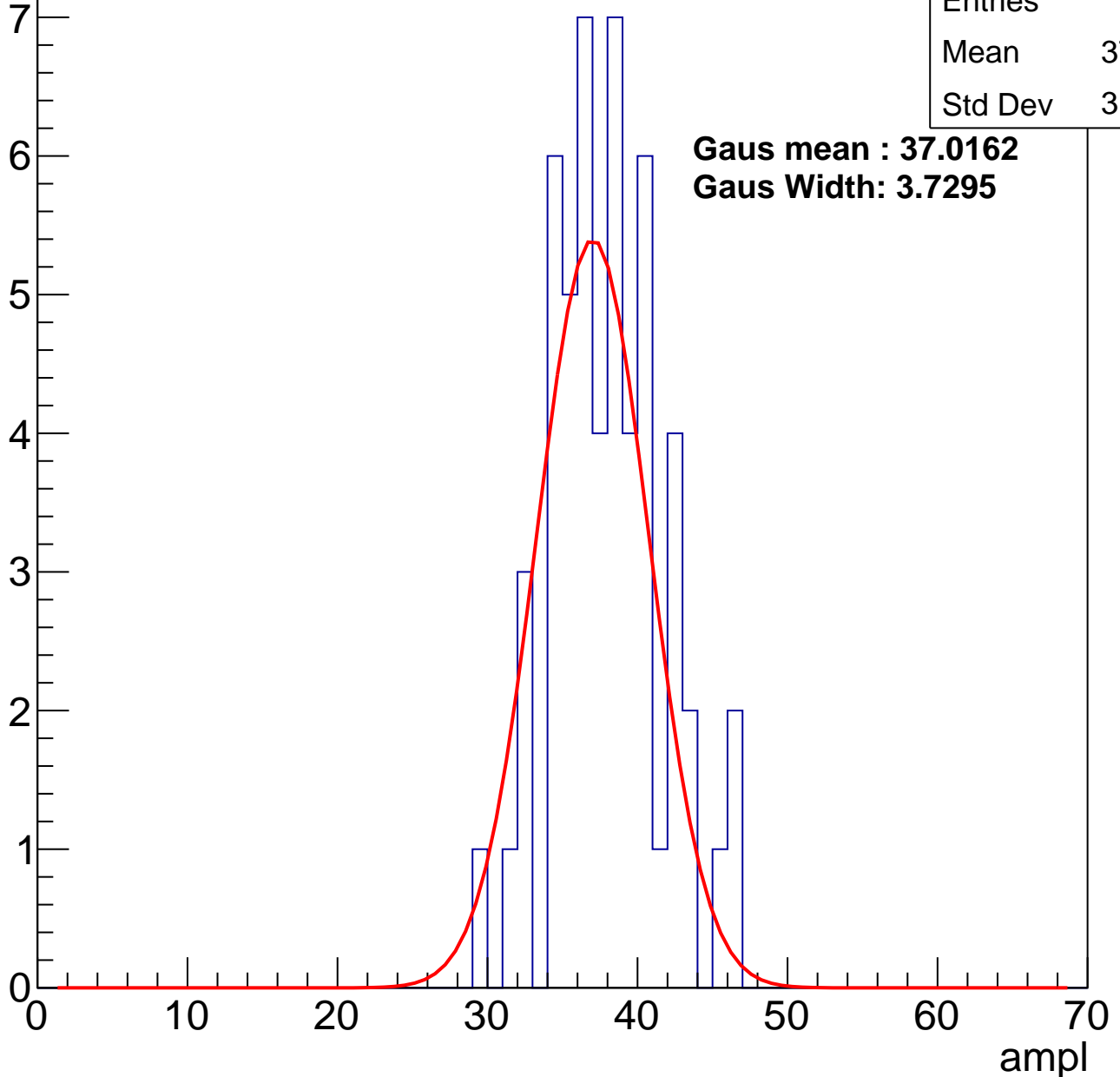
B0L001S, U18-ch18, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	37.57
Std Dev	3.675

Gaus mean : 37.0162
Gaus Width: 3.7295



B0L001S, U18-ch18, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	43.81
Std Dev	3.735

Gaus mean : 44.1802

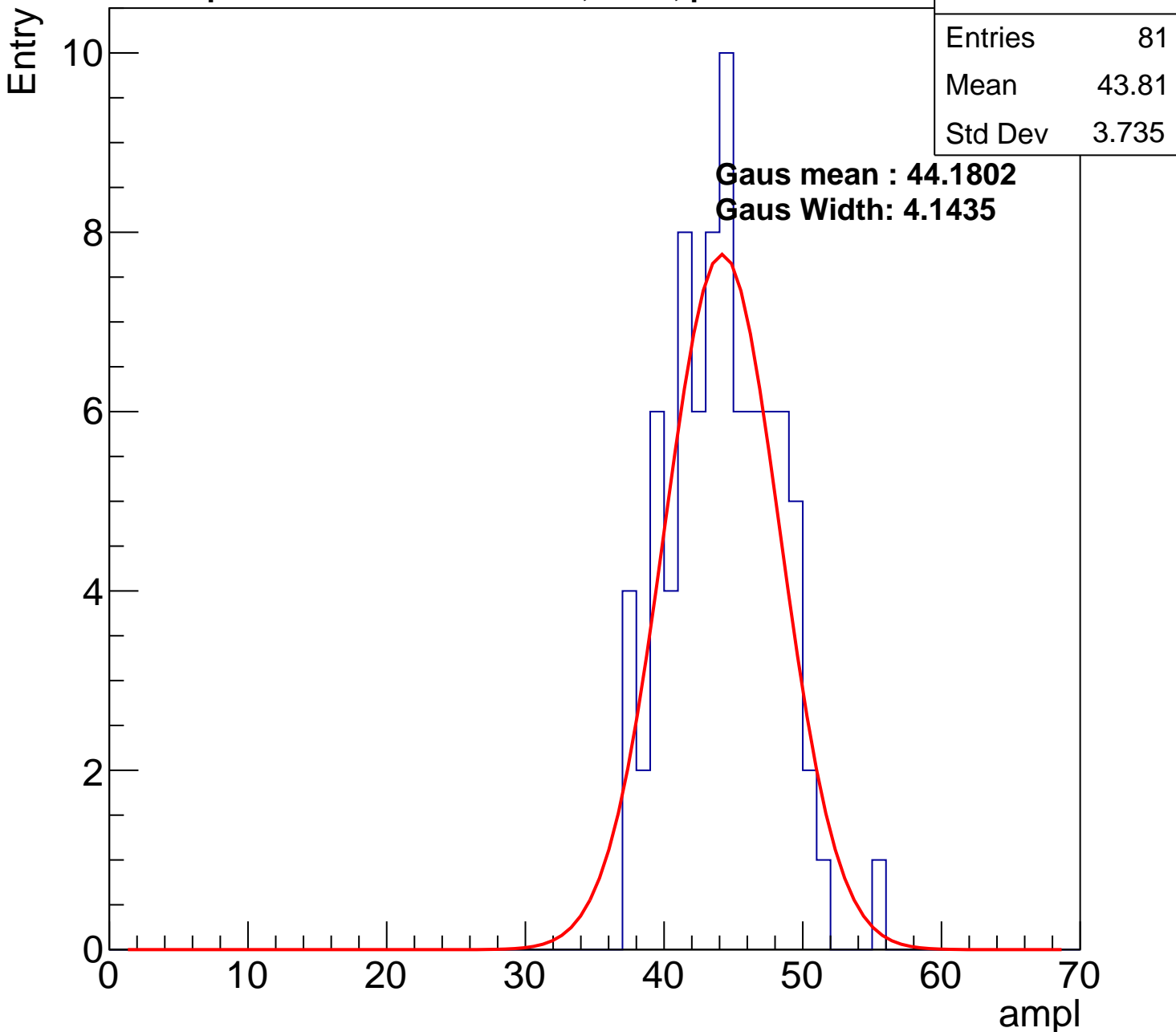
Gaus Width: 4.1435

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

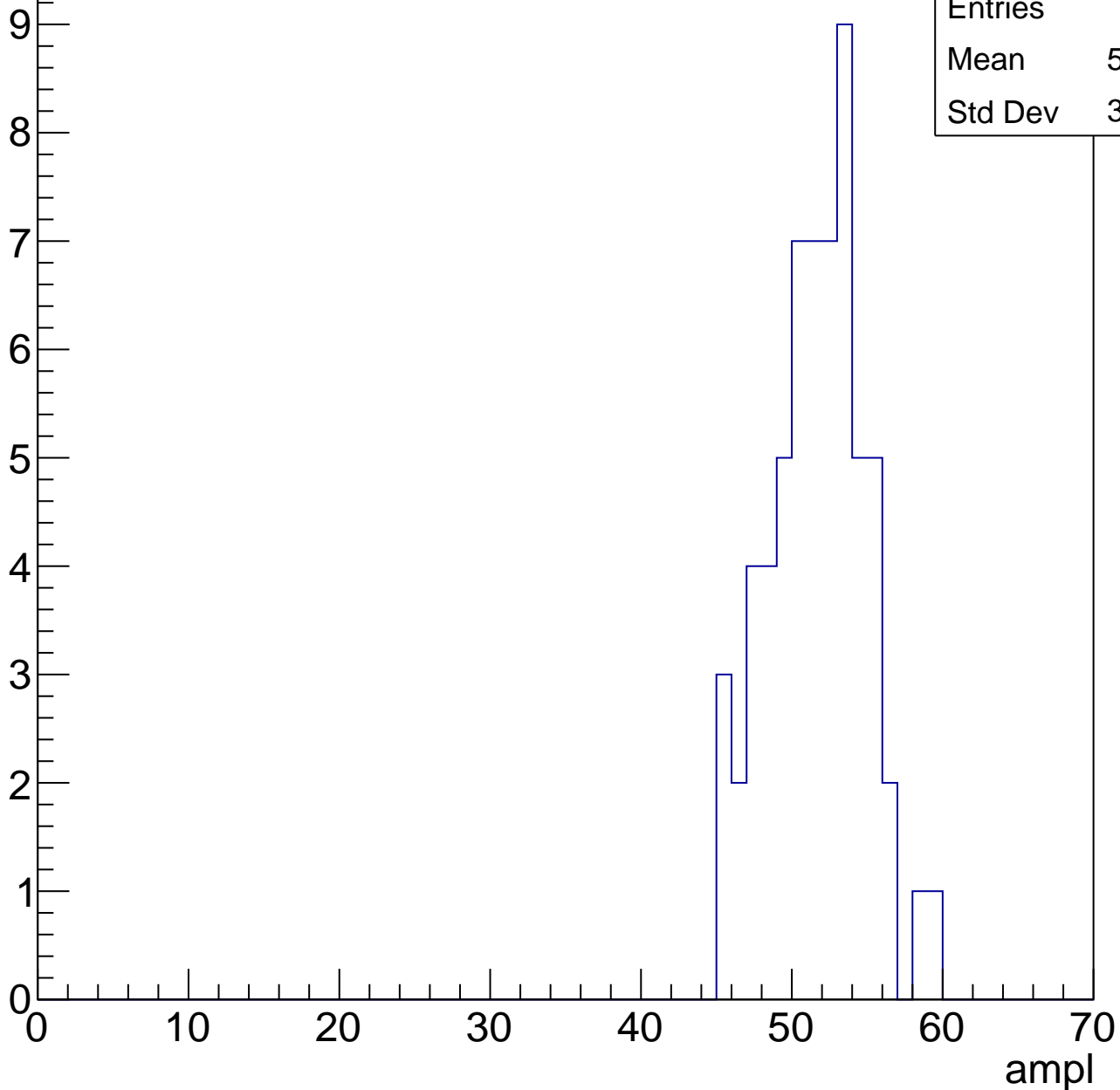


B0L001S, U18-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.19
Std Dev	3.146



B0L001S, U18-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	57.07
Std Dev	3.051

Entry

10

8

6

4

2

0

0

10

20

30

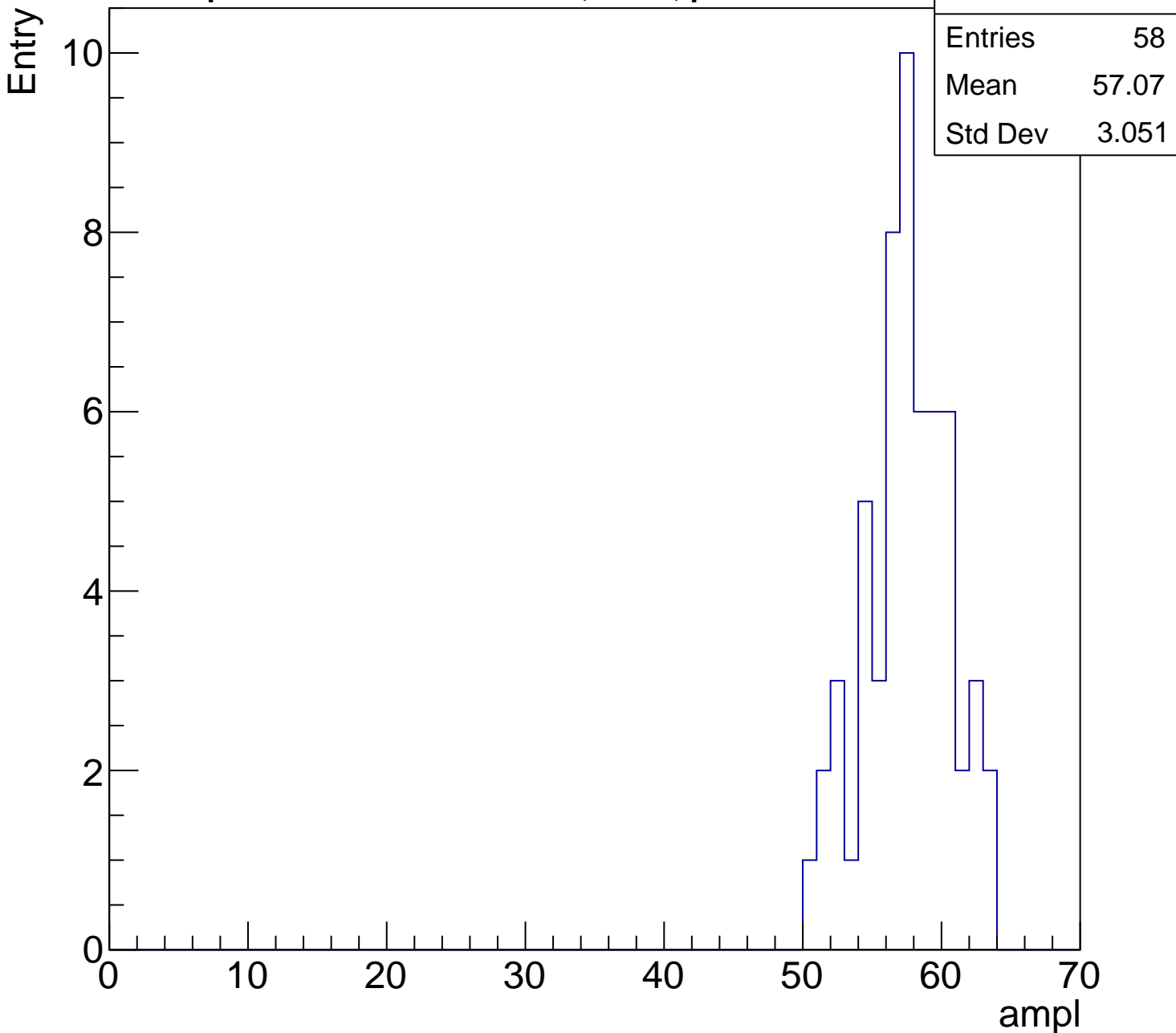
40

50

60

70

ampl

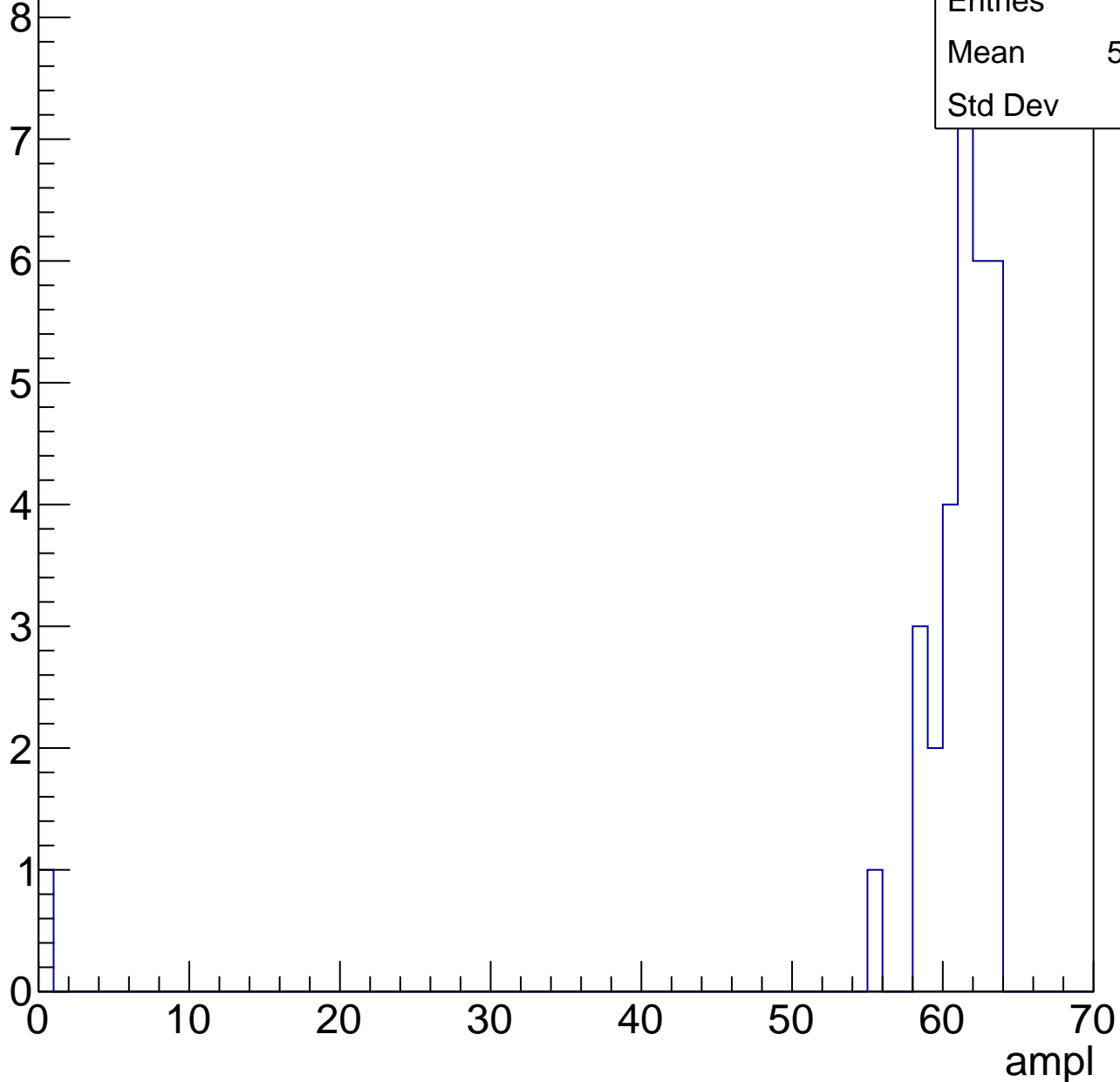


B0L001S, U18-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

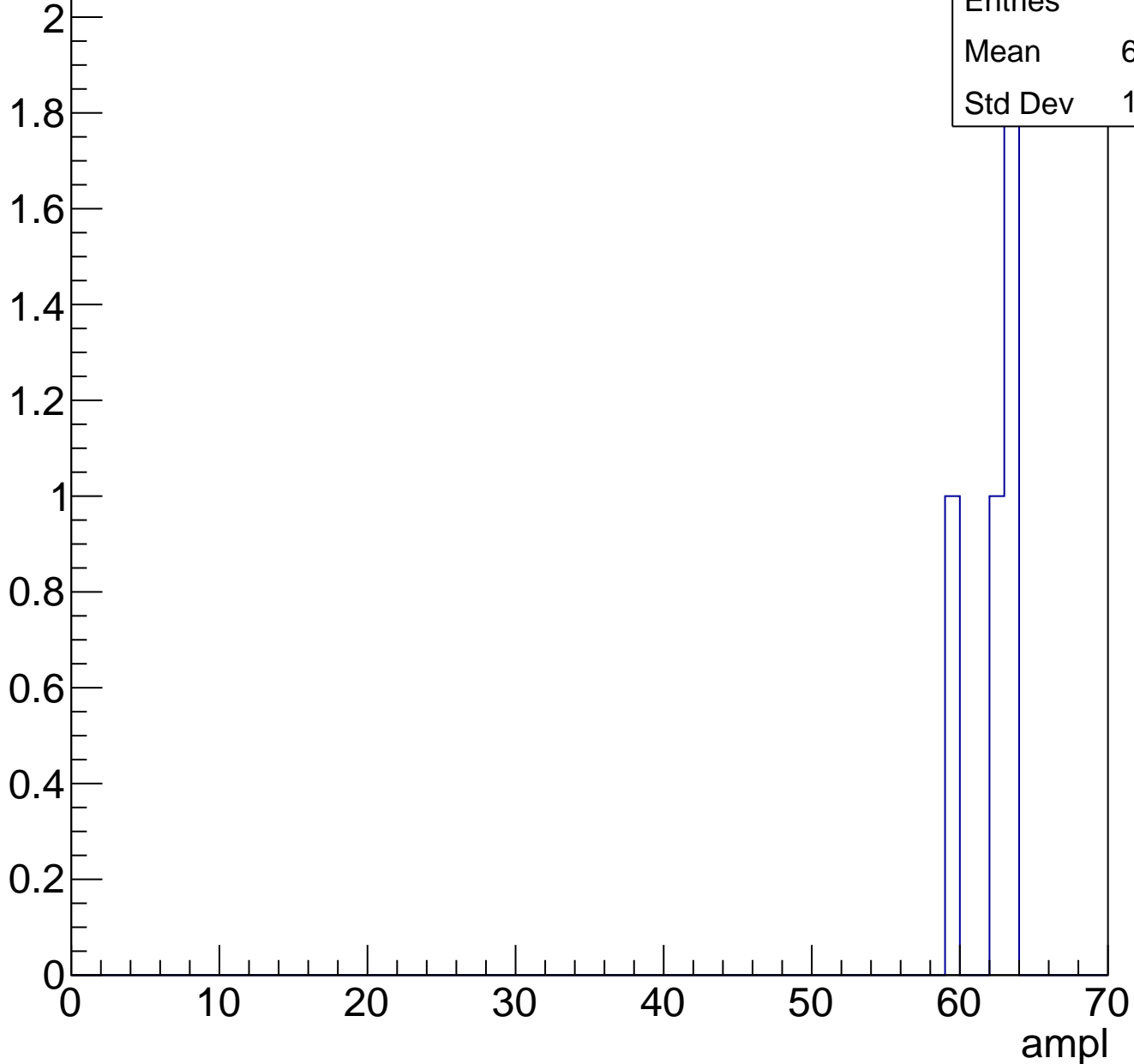
Entries	31
Mean	58.87
Std Dev	10.9



B0L001S, U18-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch19, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	29.37
Std Dev	3.51

Gaus mean : 30.6013

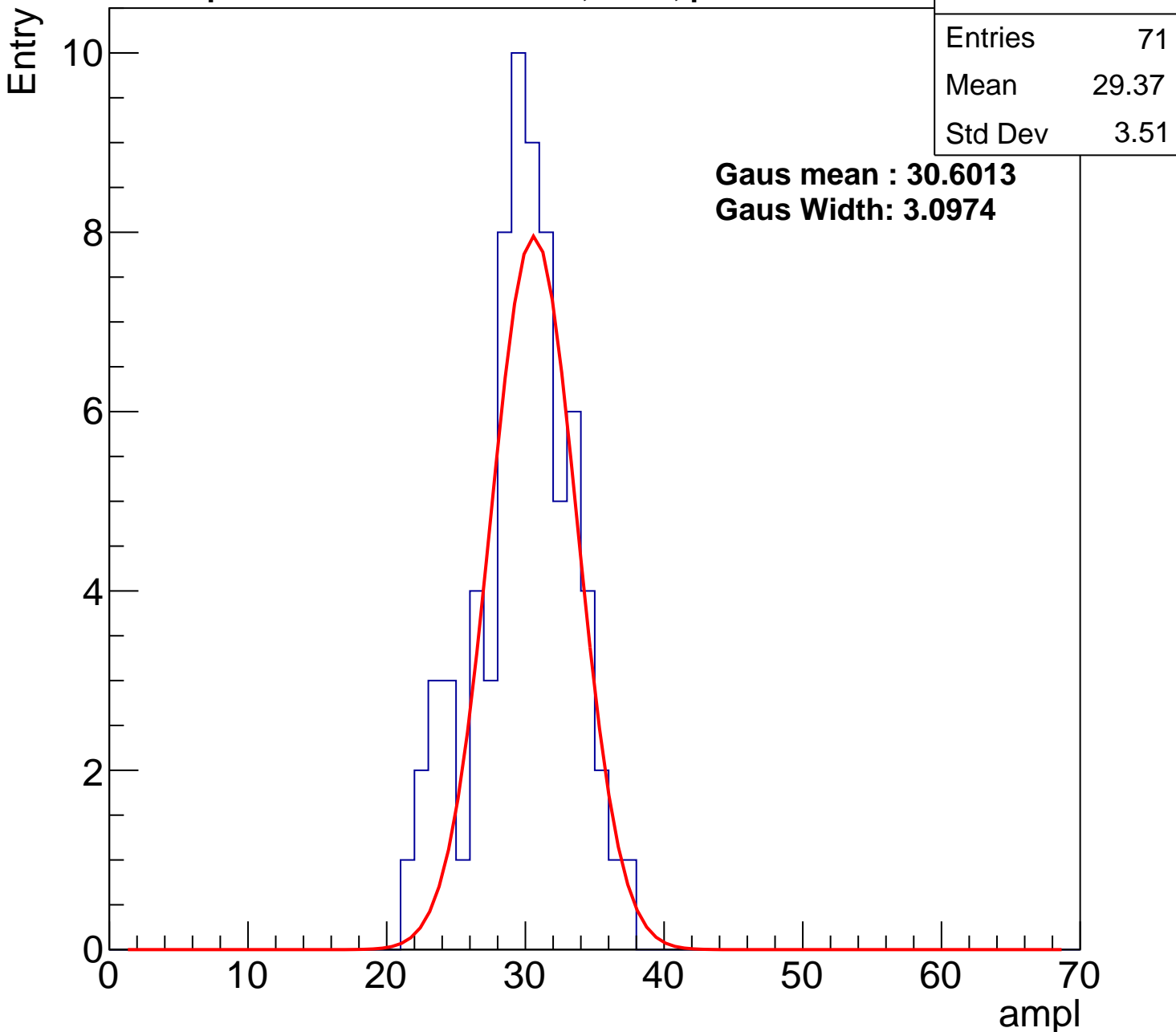
Gaus Width: 3.0974

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch19, adc1

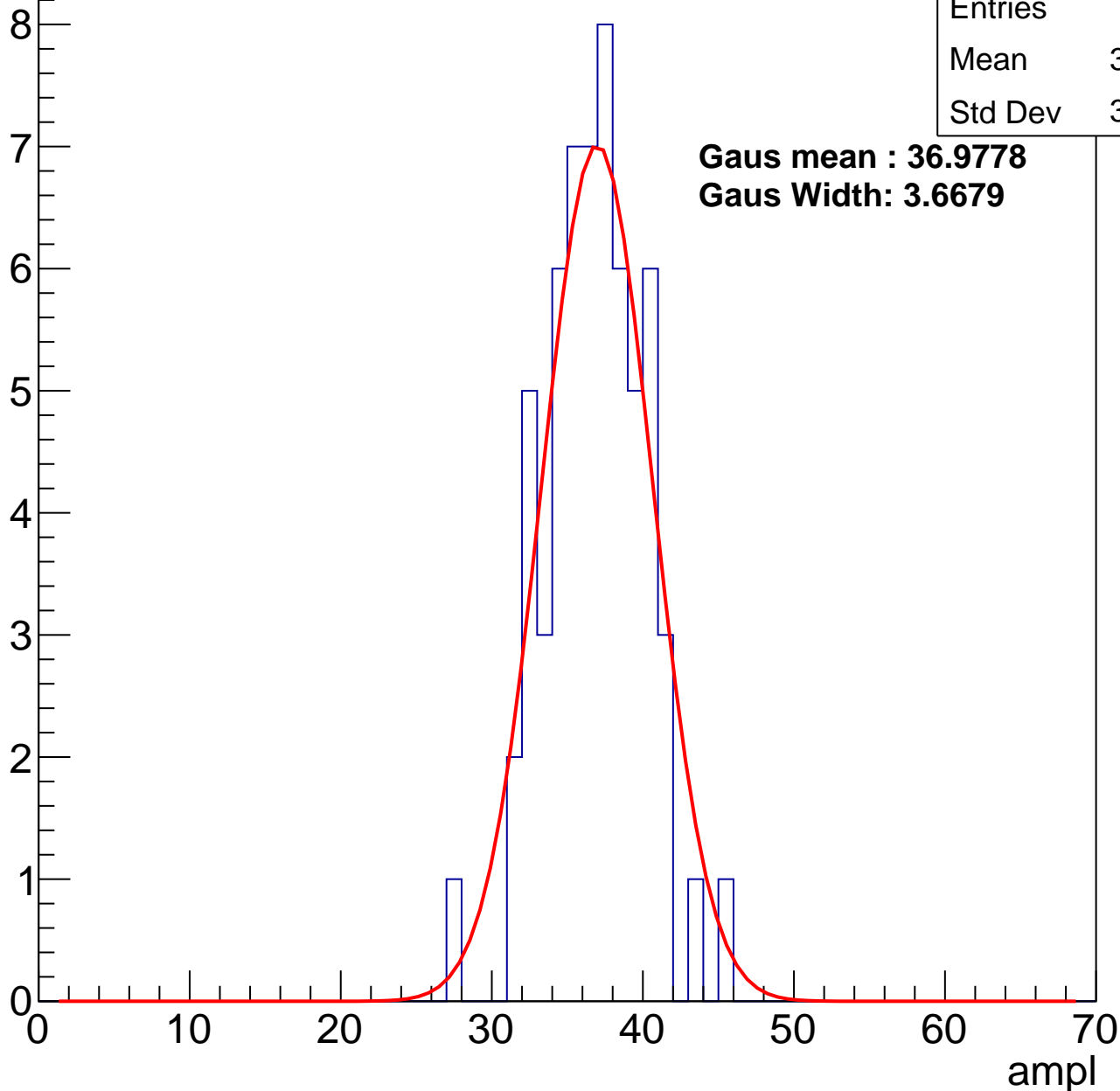
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.38
Std Dev	3.235

Gaus mean : 36.9778

Gaus Width: 3.6679



B0L001S, U18-ch19, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	43.3
Std Dev	3.773

Gaus mean : 43.7560

Gaus Width: 3.9438

Entry

10

8

6

4

2

0

0

10

20

30

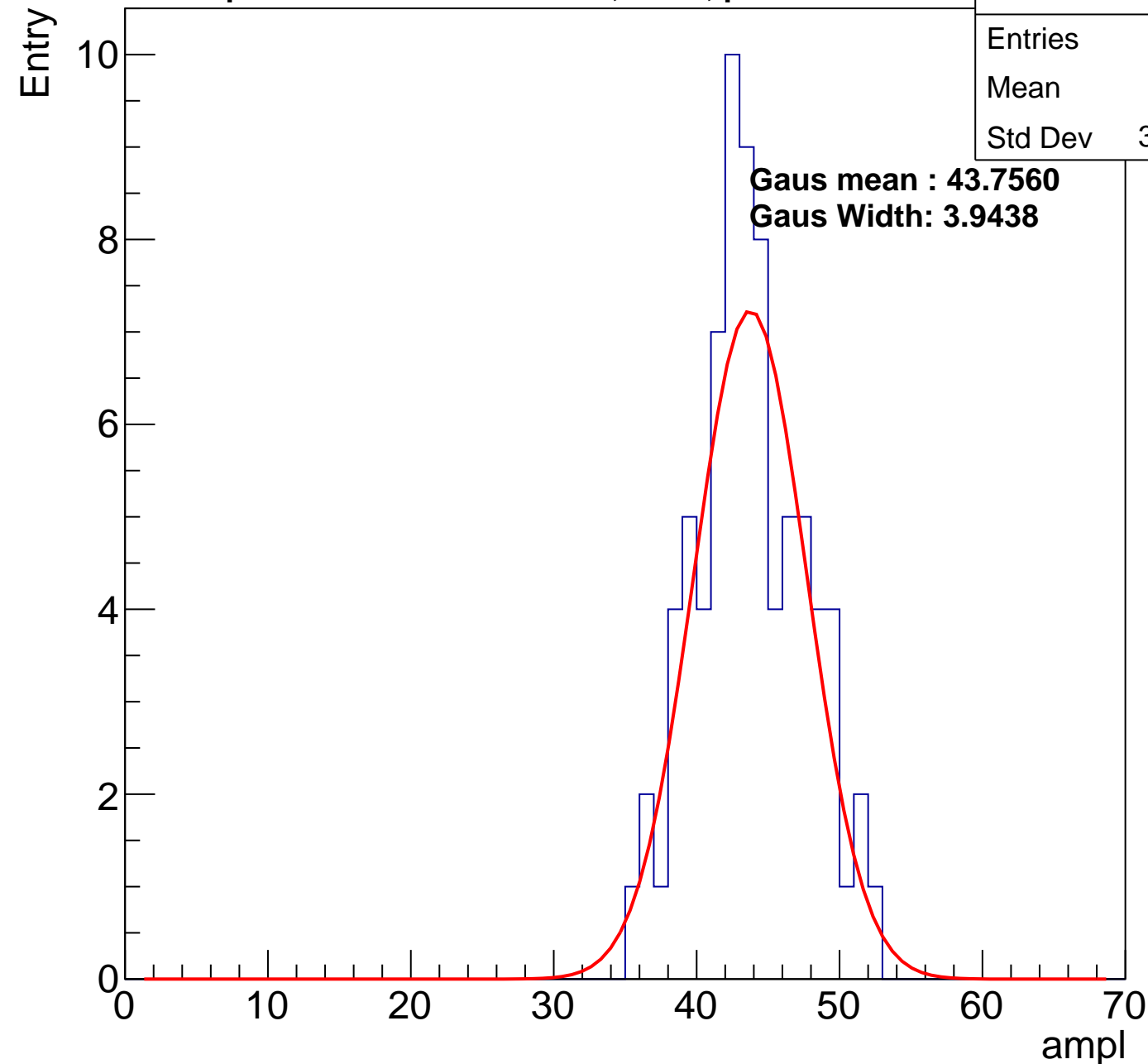
40

50

60

70

ampl

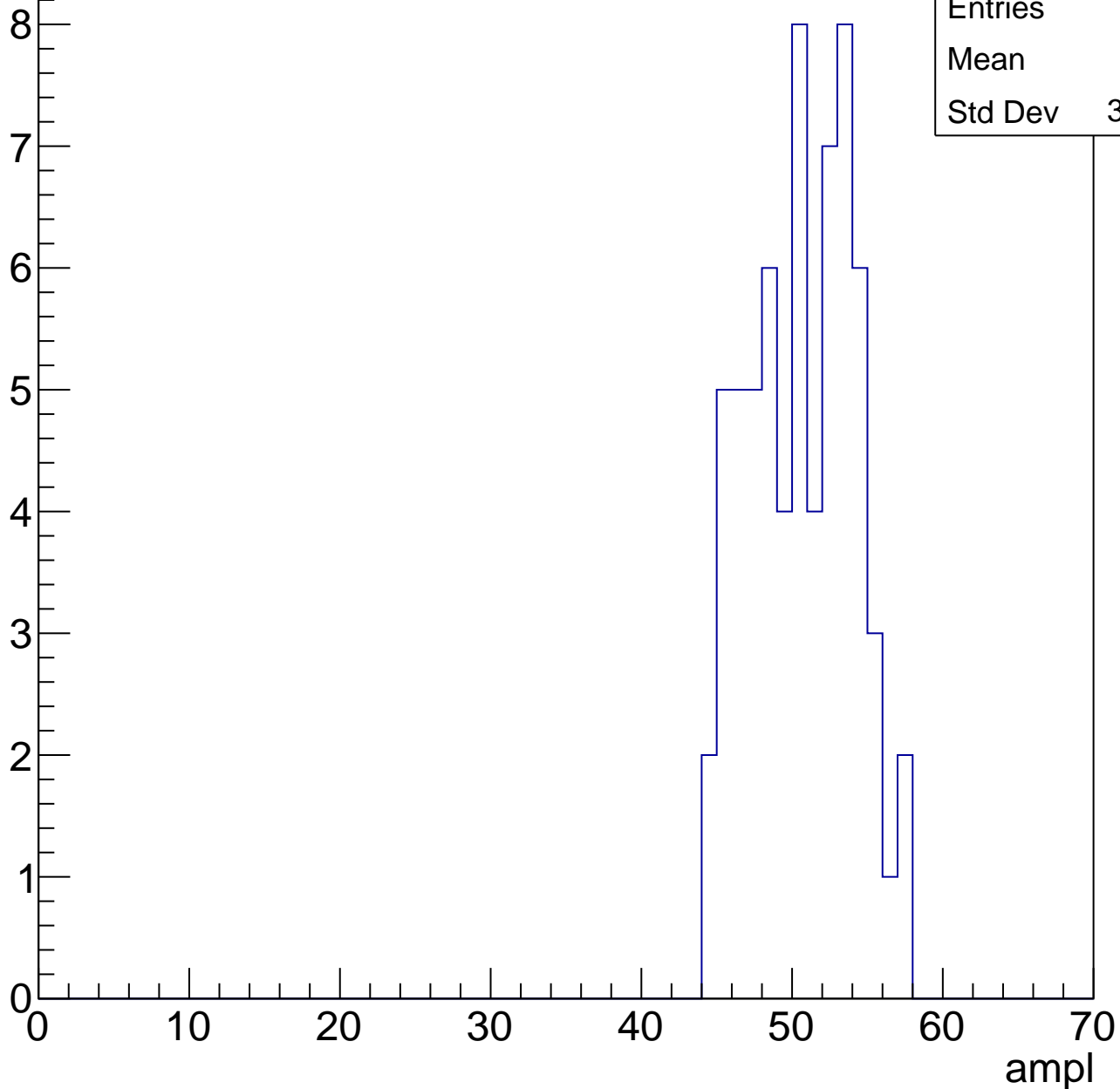


B0L001S, U18-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.2
Std Dev	3.385

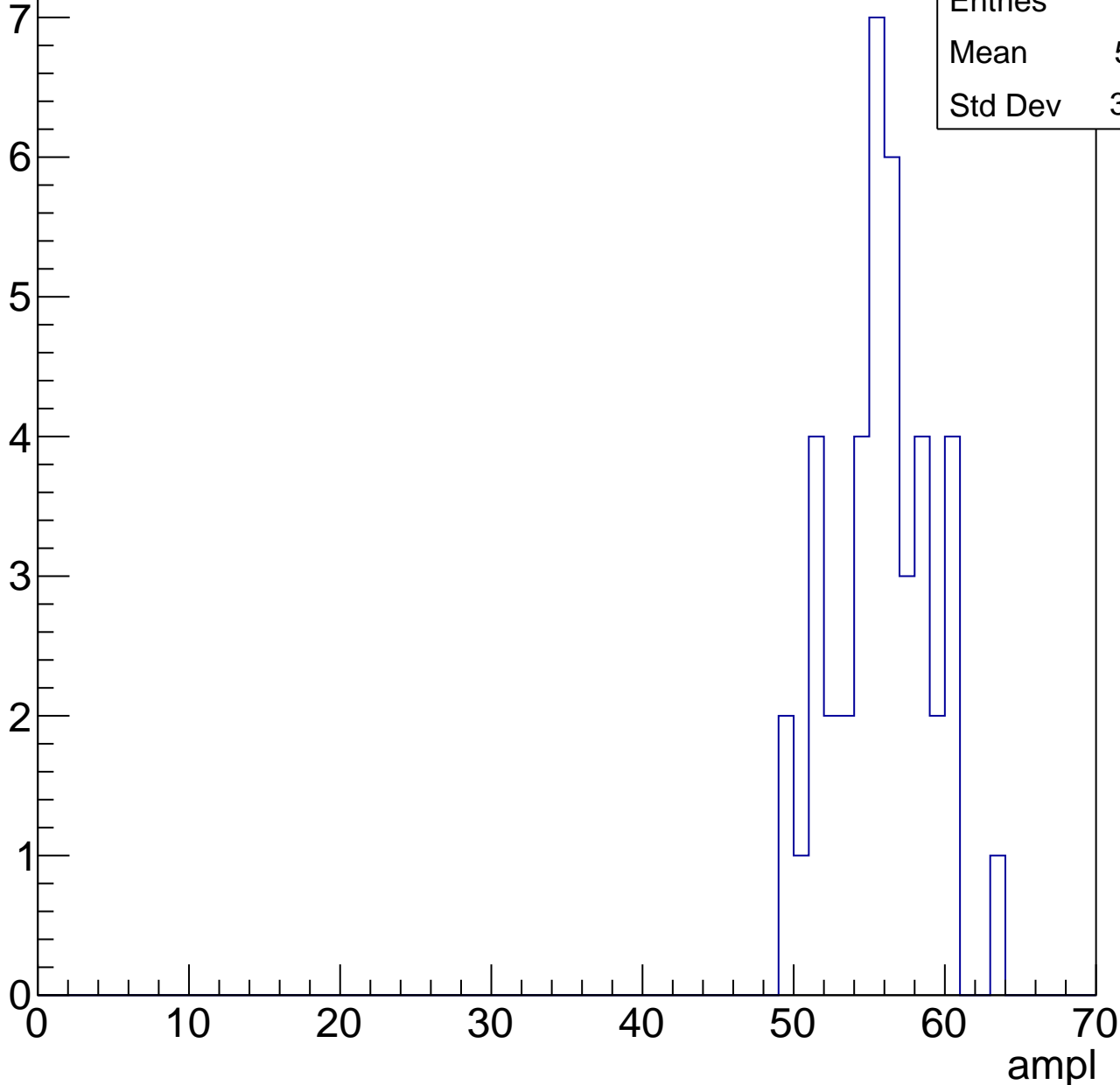


B0L001S, U18-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	55.31
Std Dev	3.226

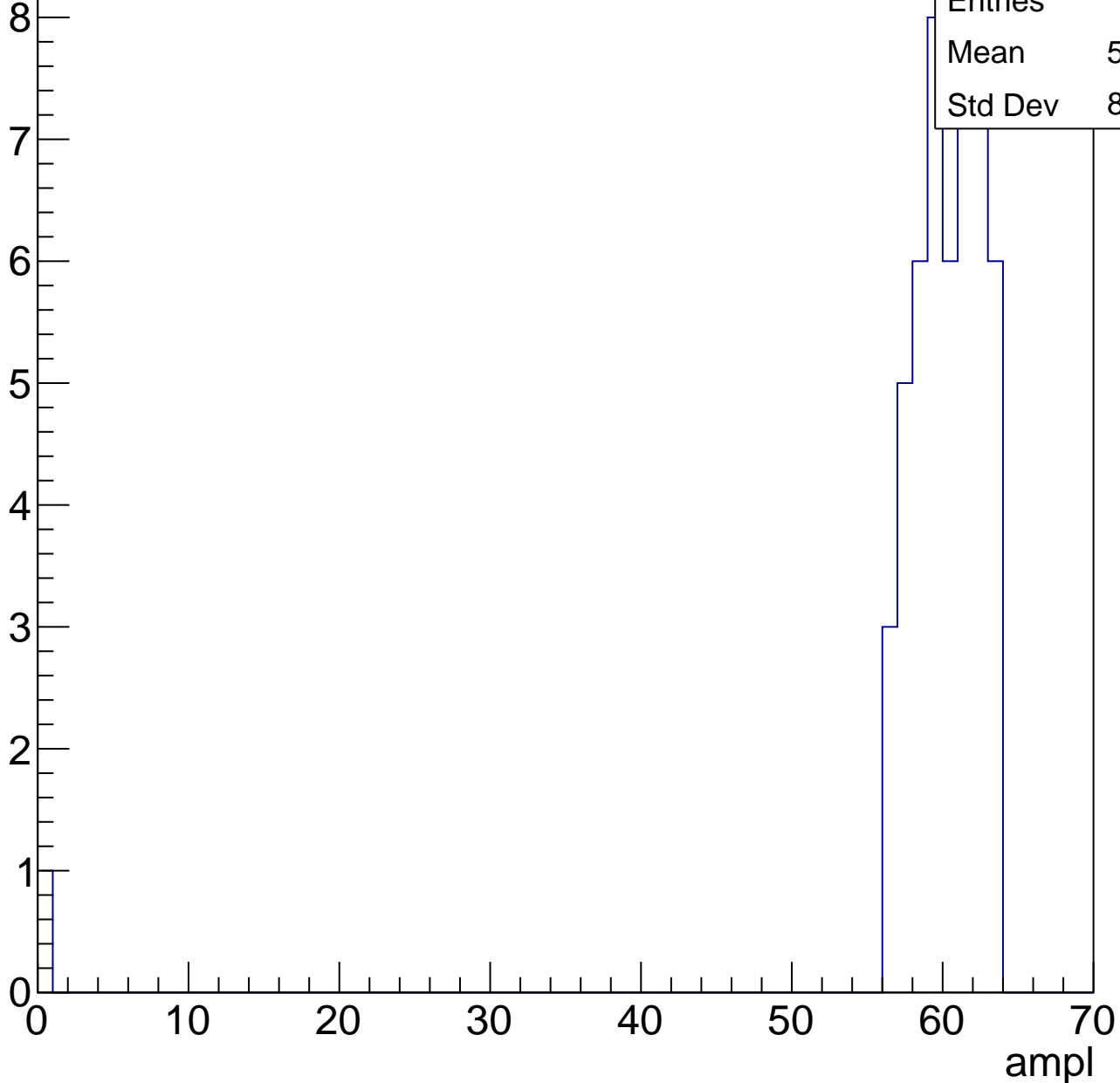


B0L001S, U18-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

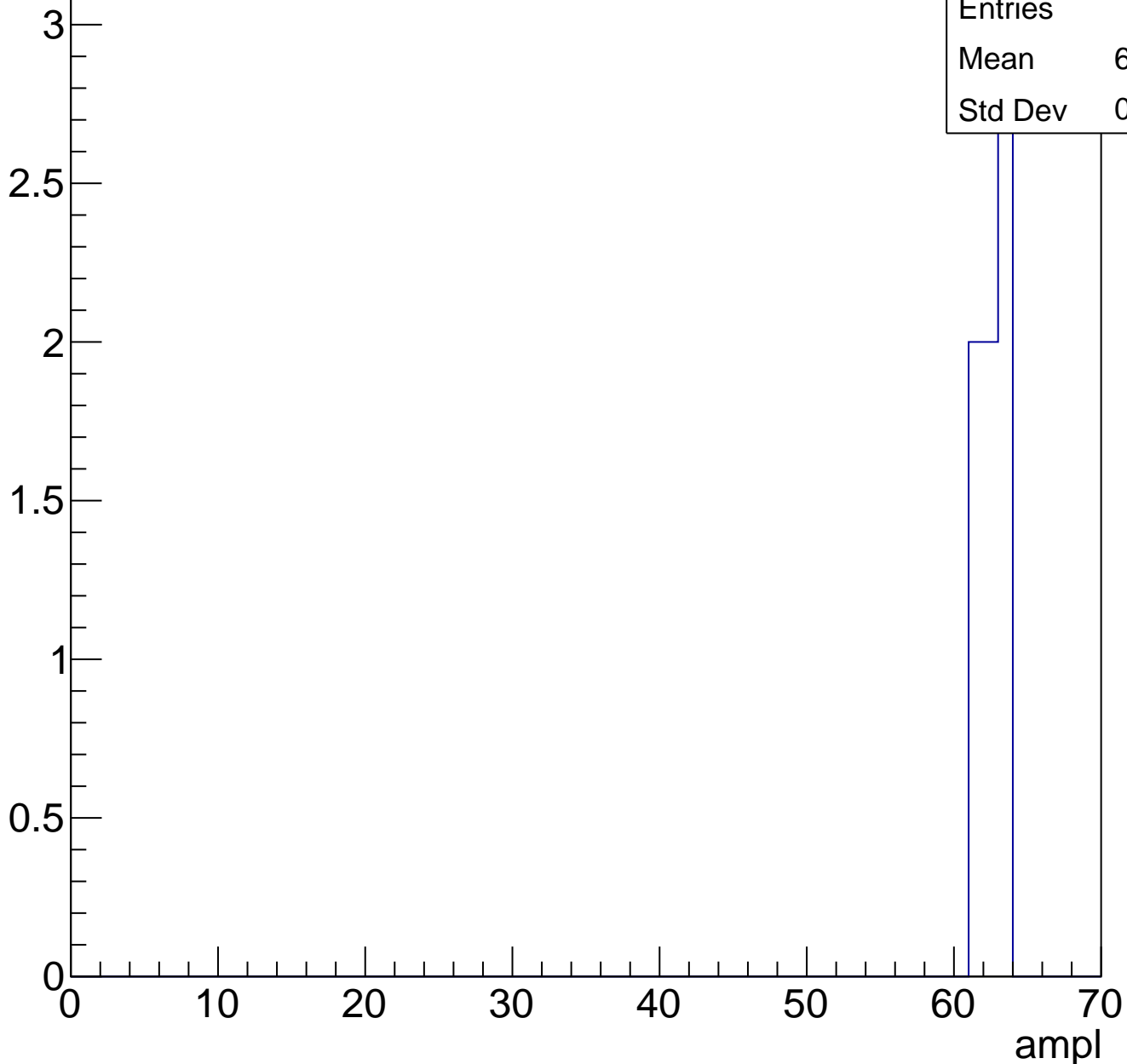
Entries	51
Mean	58.73
Std Dev	8.559



B0L001S, U18-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch20, adc0

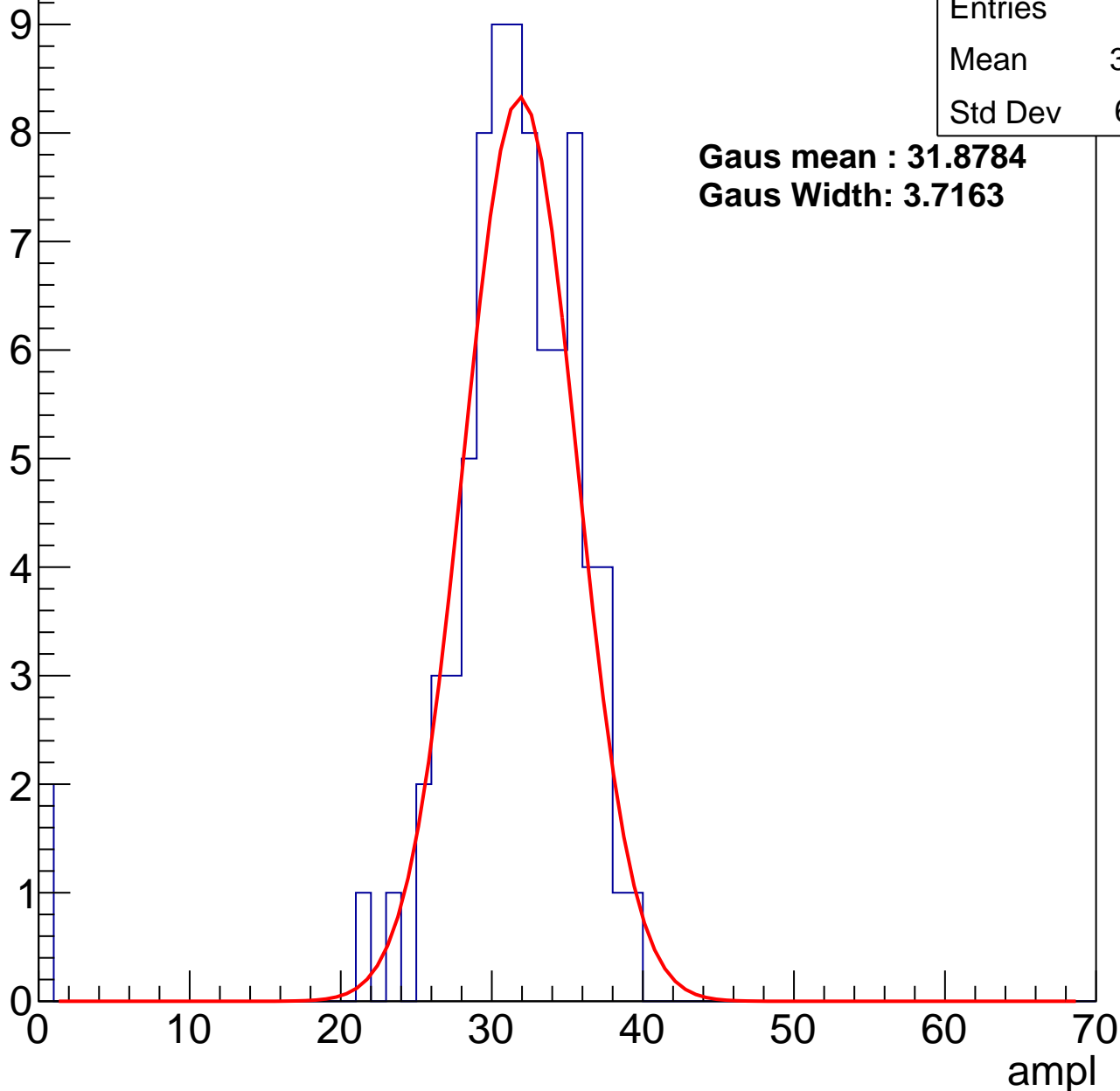
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.63
Std Dev	6.011

Gaus mean : 31.8784

Gaus Width: 3.7163



B0L001S, U18-ch20, adc1

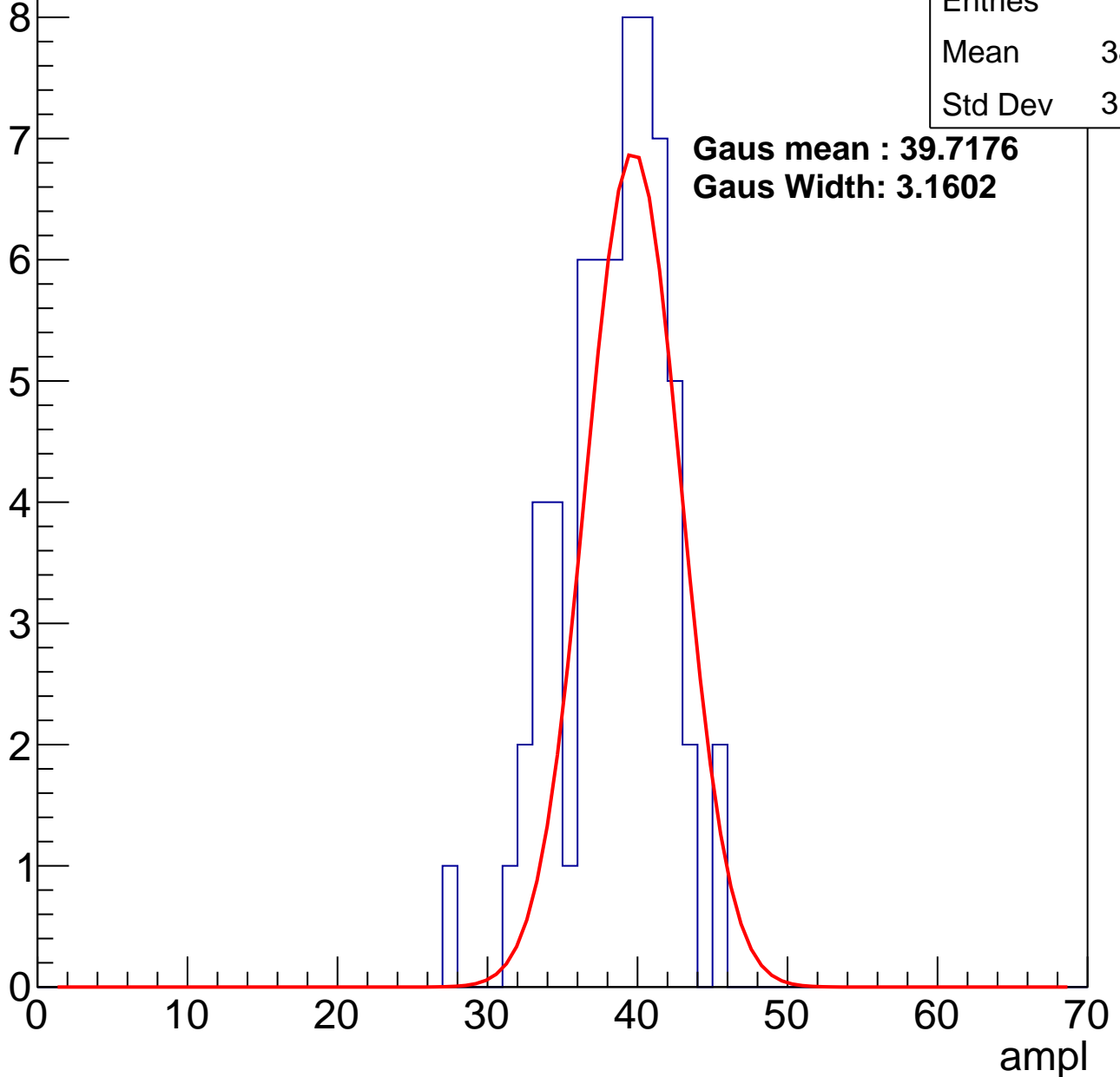
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.03
Std Dev	3.509

Gaus mean : 39.7176

Gaus Width: 3.1602



B0L001S, U18-ch20, adc2

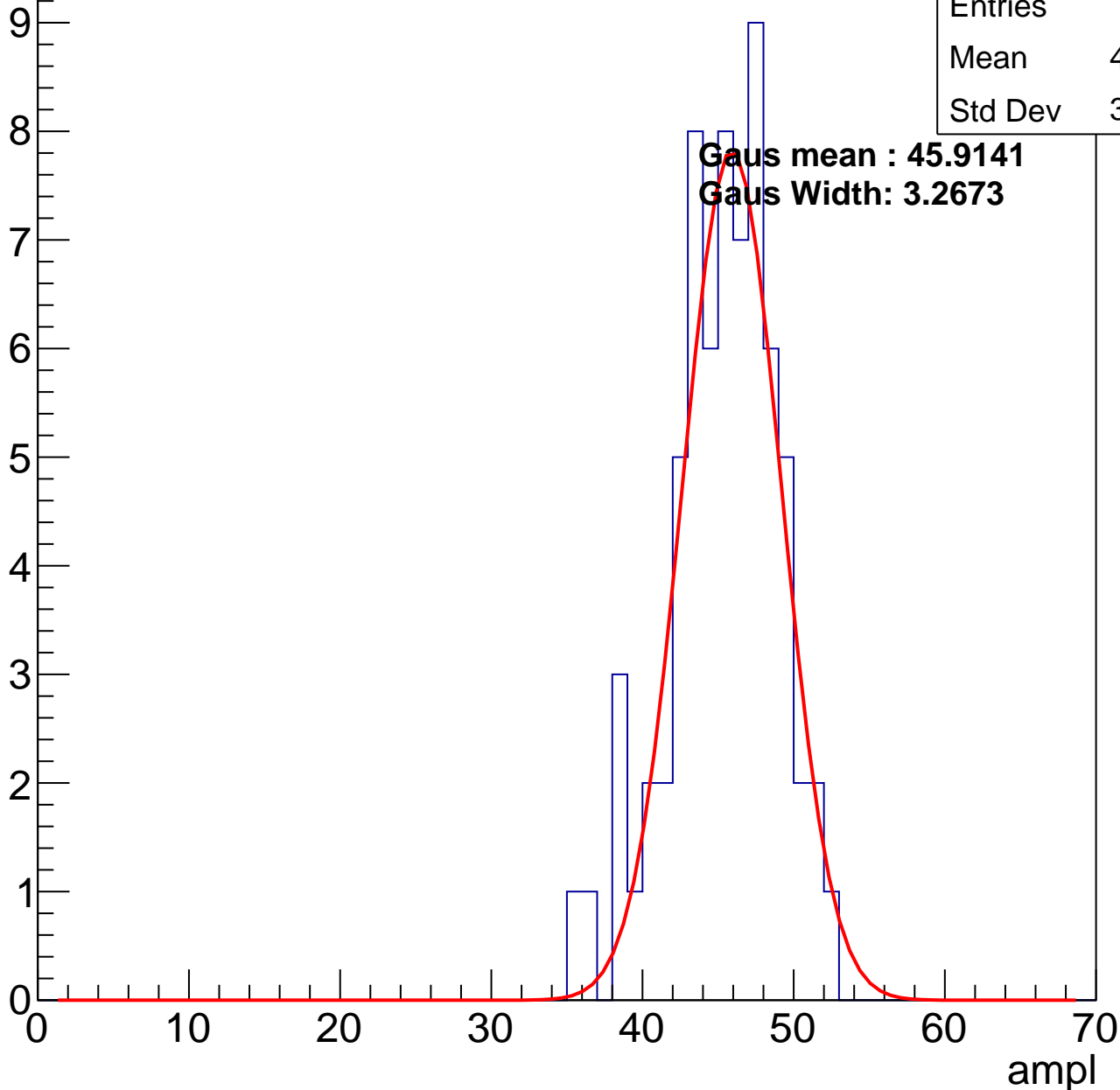
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.87
Std Dev	3.563

Gaus mean : 45.9141

Gaus Width: 3.2673

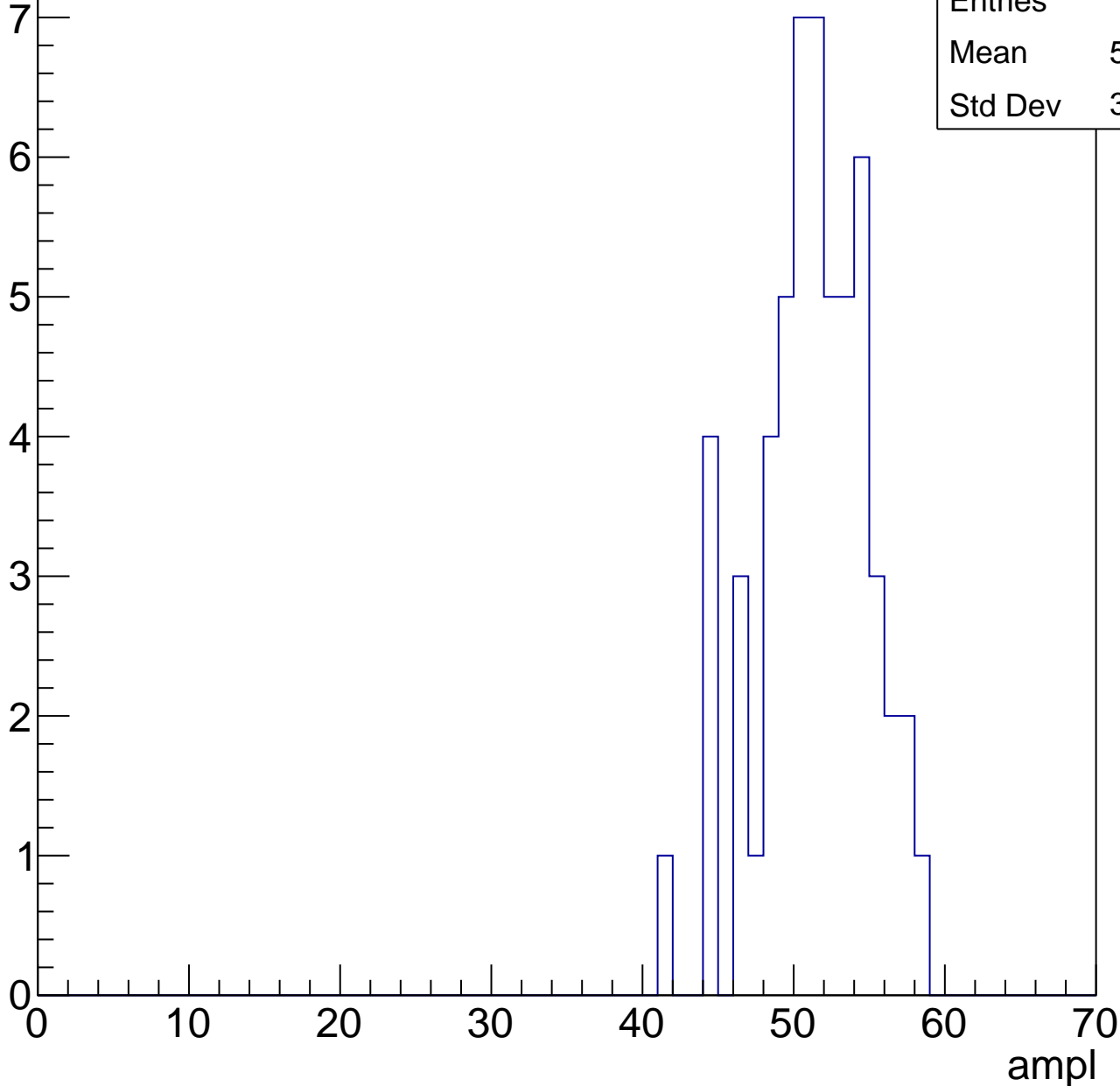


B0L001S, U18-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	50.79
Std Dev	3.648

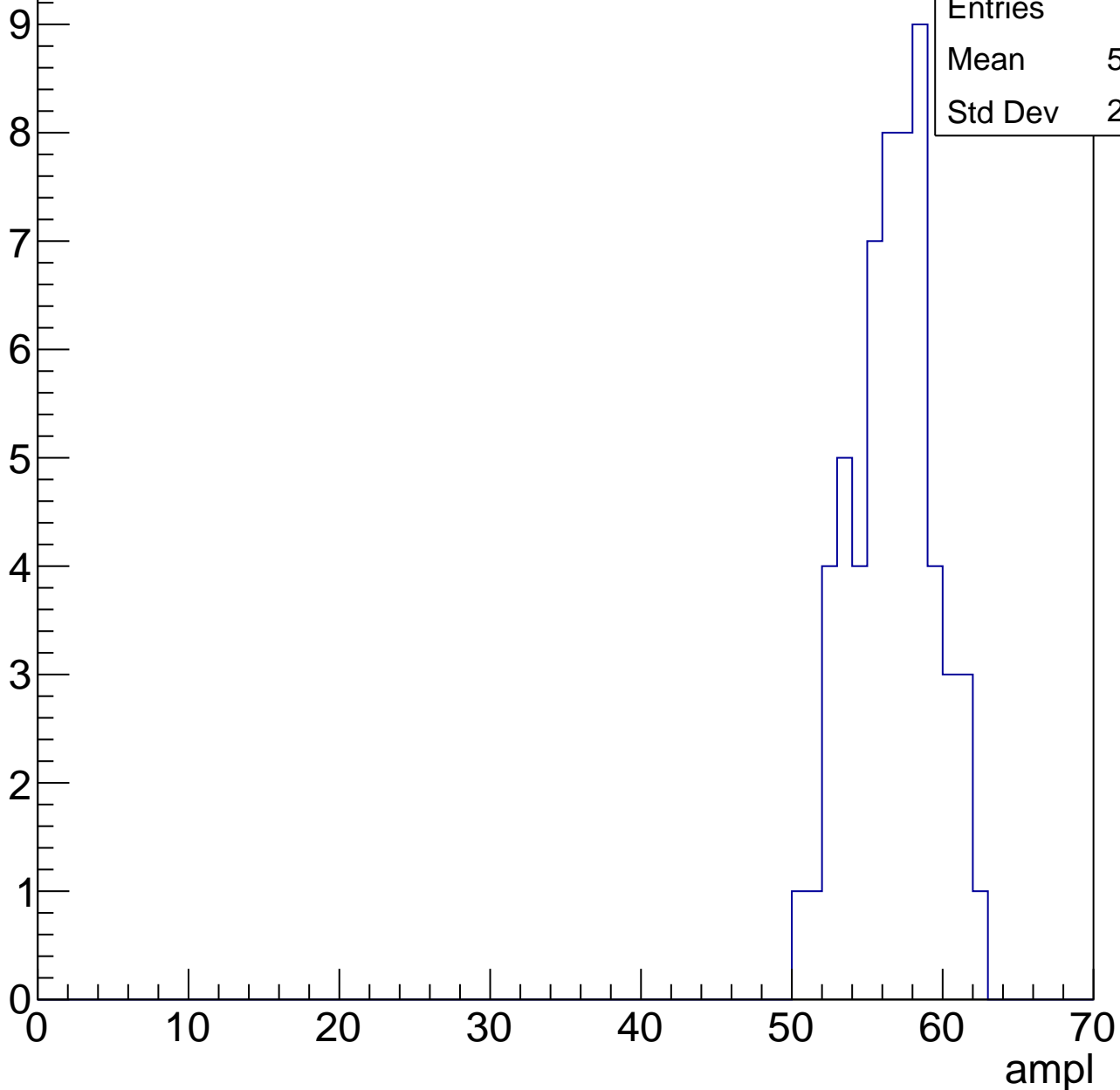


B0L001S, U18-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.24
Std Dev	2.718

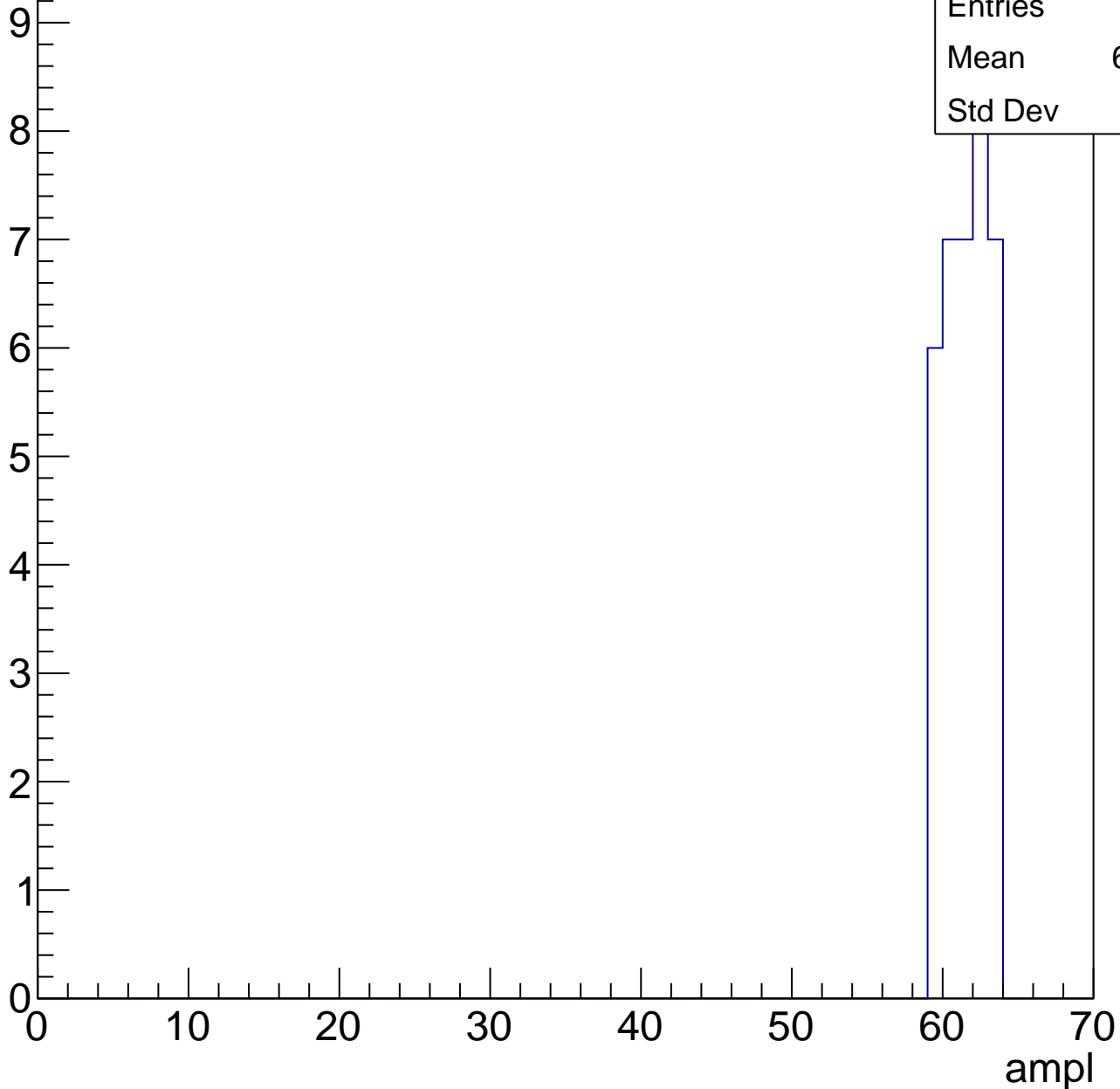


B0L001S, U18-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	61.11
Std Dev	1.37



B0L001S, U18-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch21, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	30.14
Std Dev	3.427

Gaus mean : 30.5539

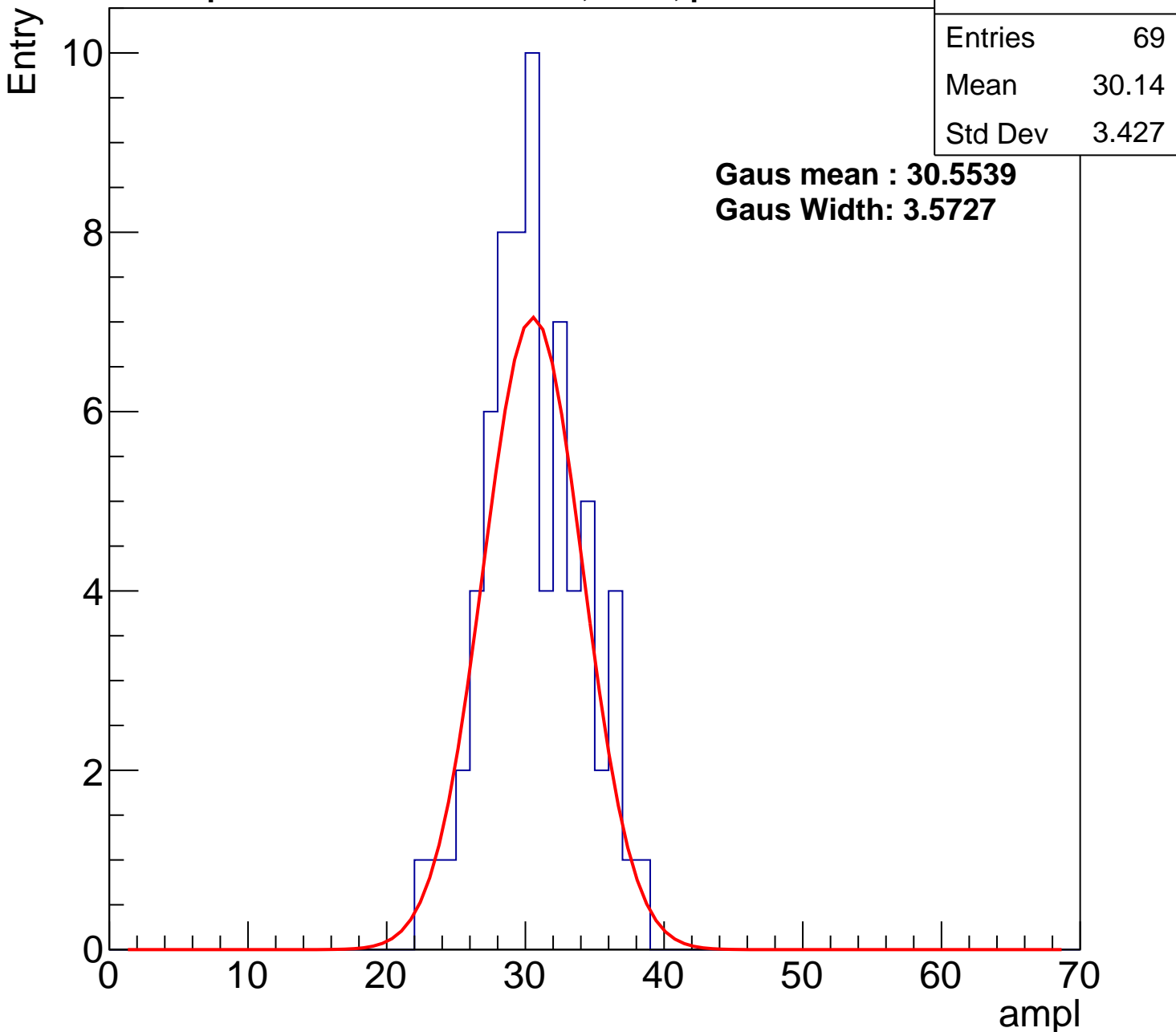
Gaus Width: 3.5727

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U18-ch21, adc1

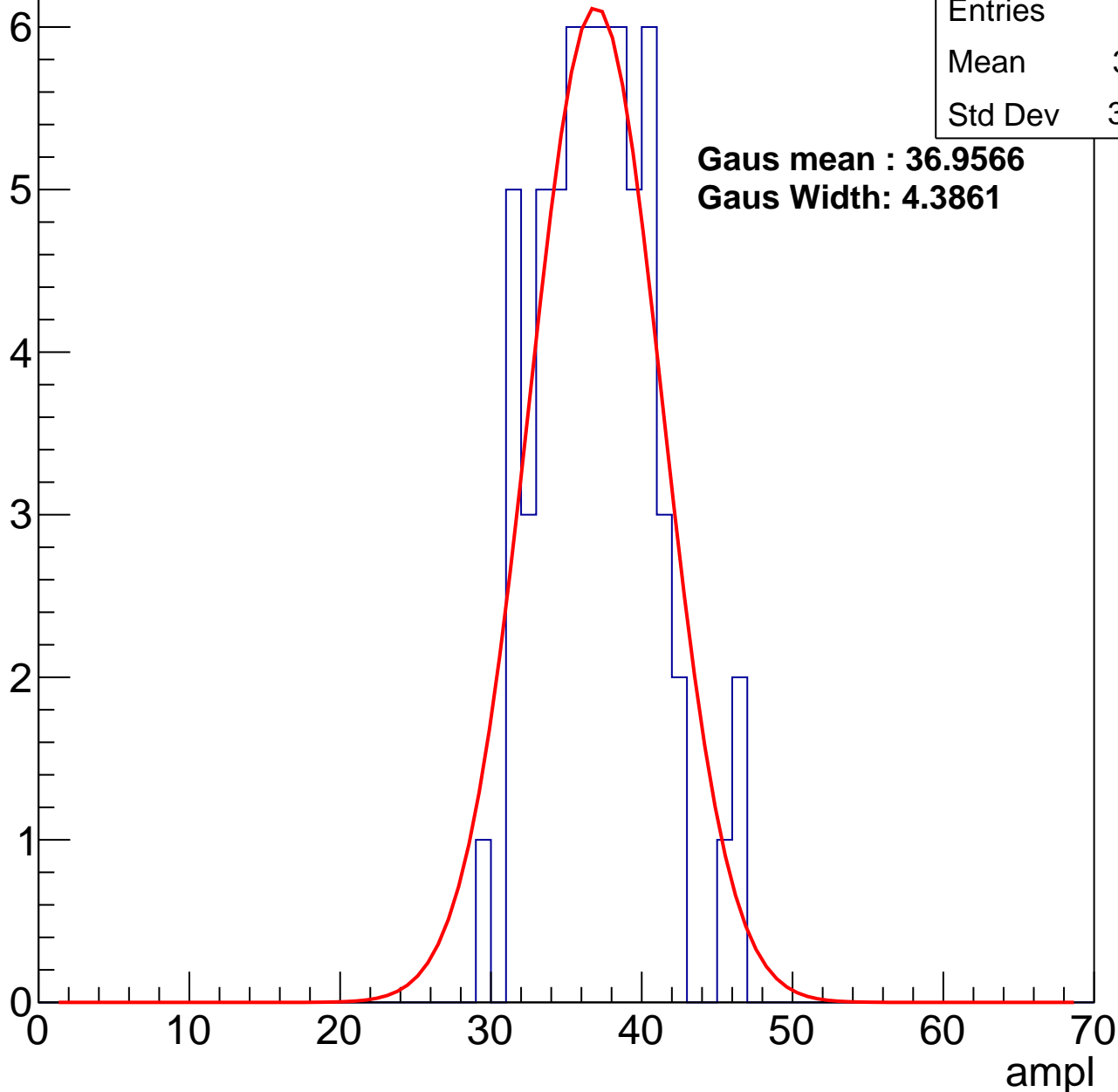
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	36.61
Std Dev	3.743

Gaus mean : 36.9566

Gaus Width: 4.3861



B0L001S, U18-ch21, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	43.07
Std Dev	3.603

Gaus mean : 43.3091

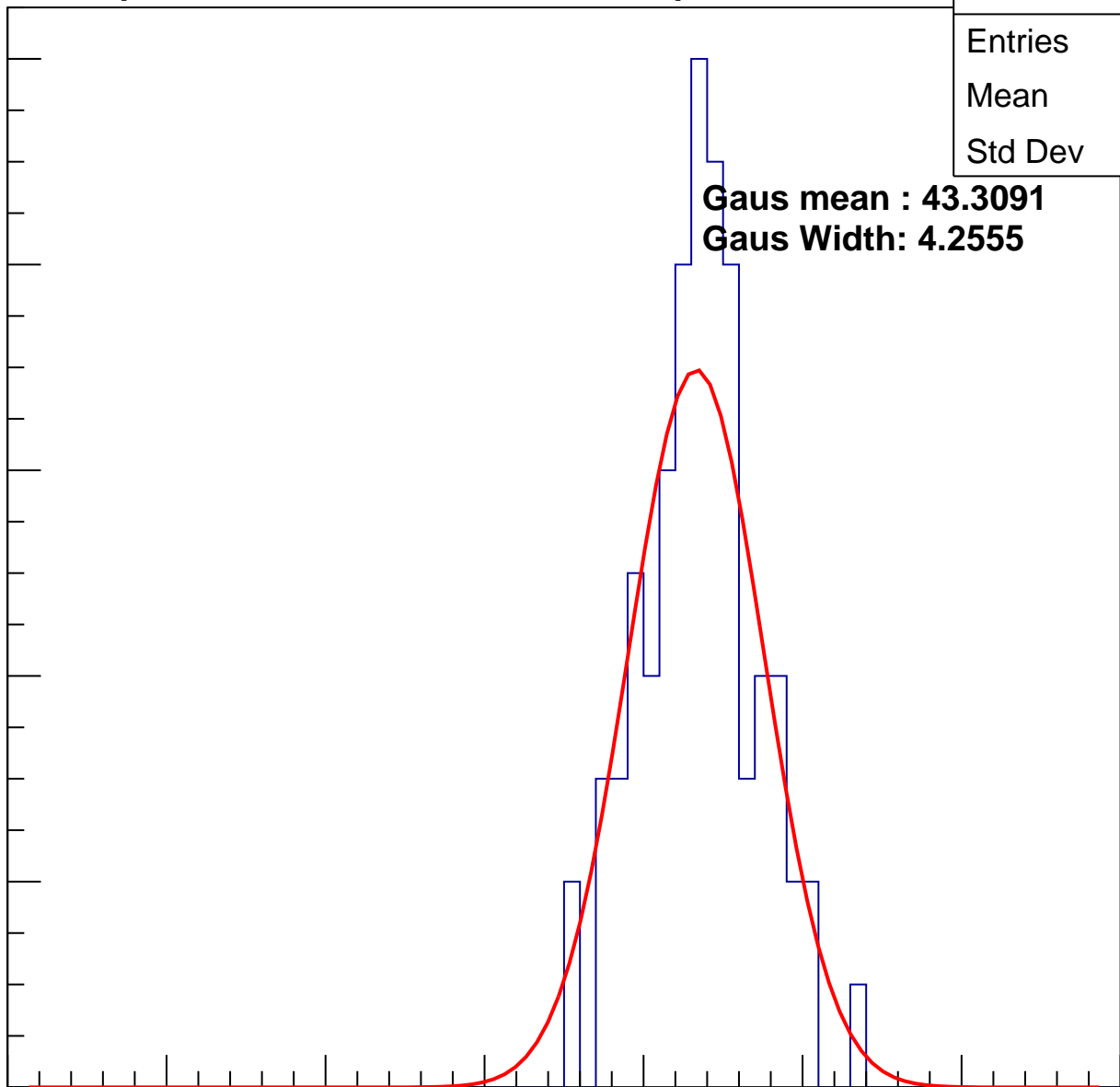
Gaus Width: 4.2555

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

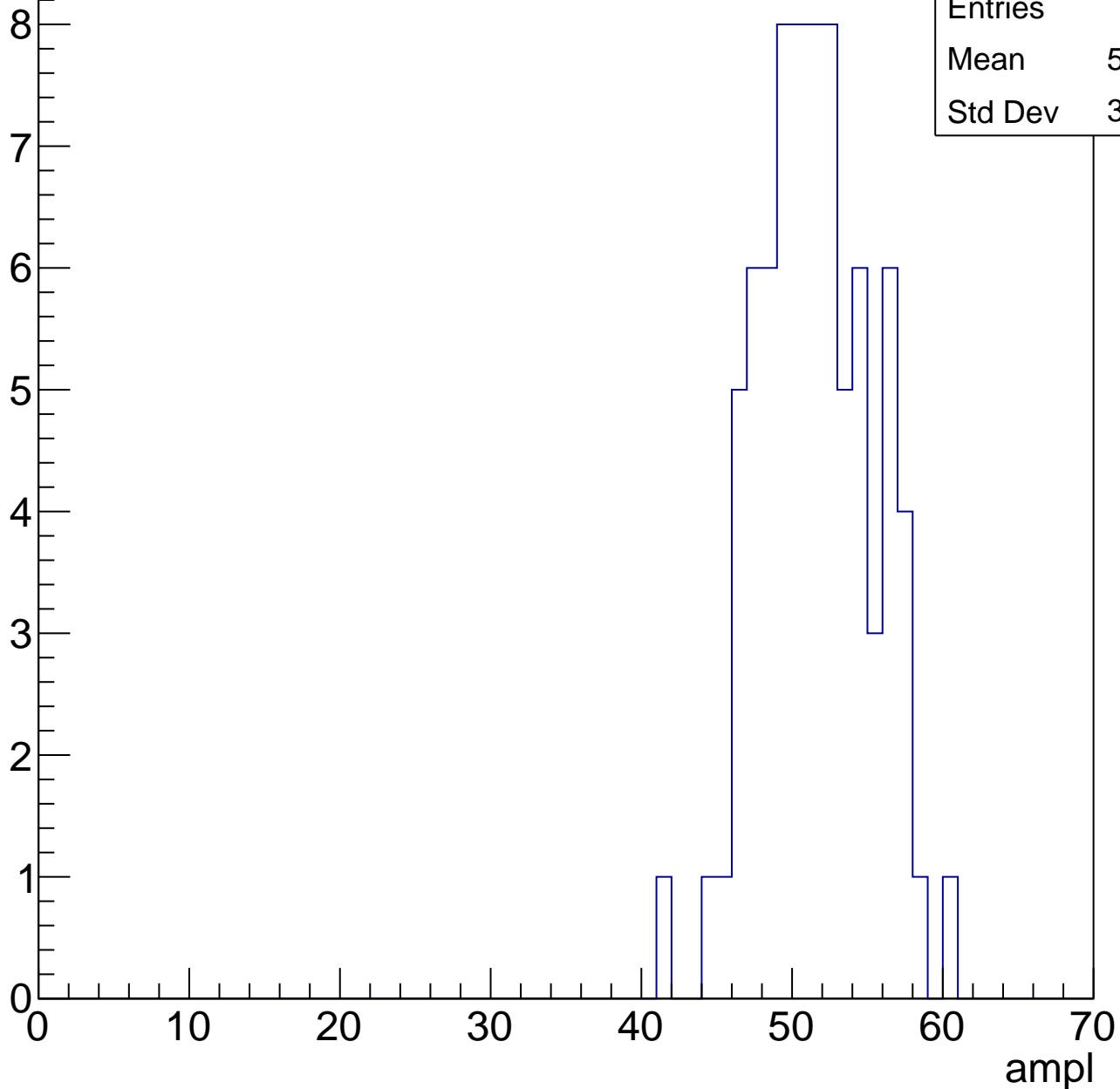


B0L001S, U18-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	51.05
Std Dev	3.676

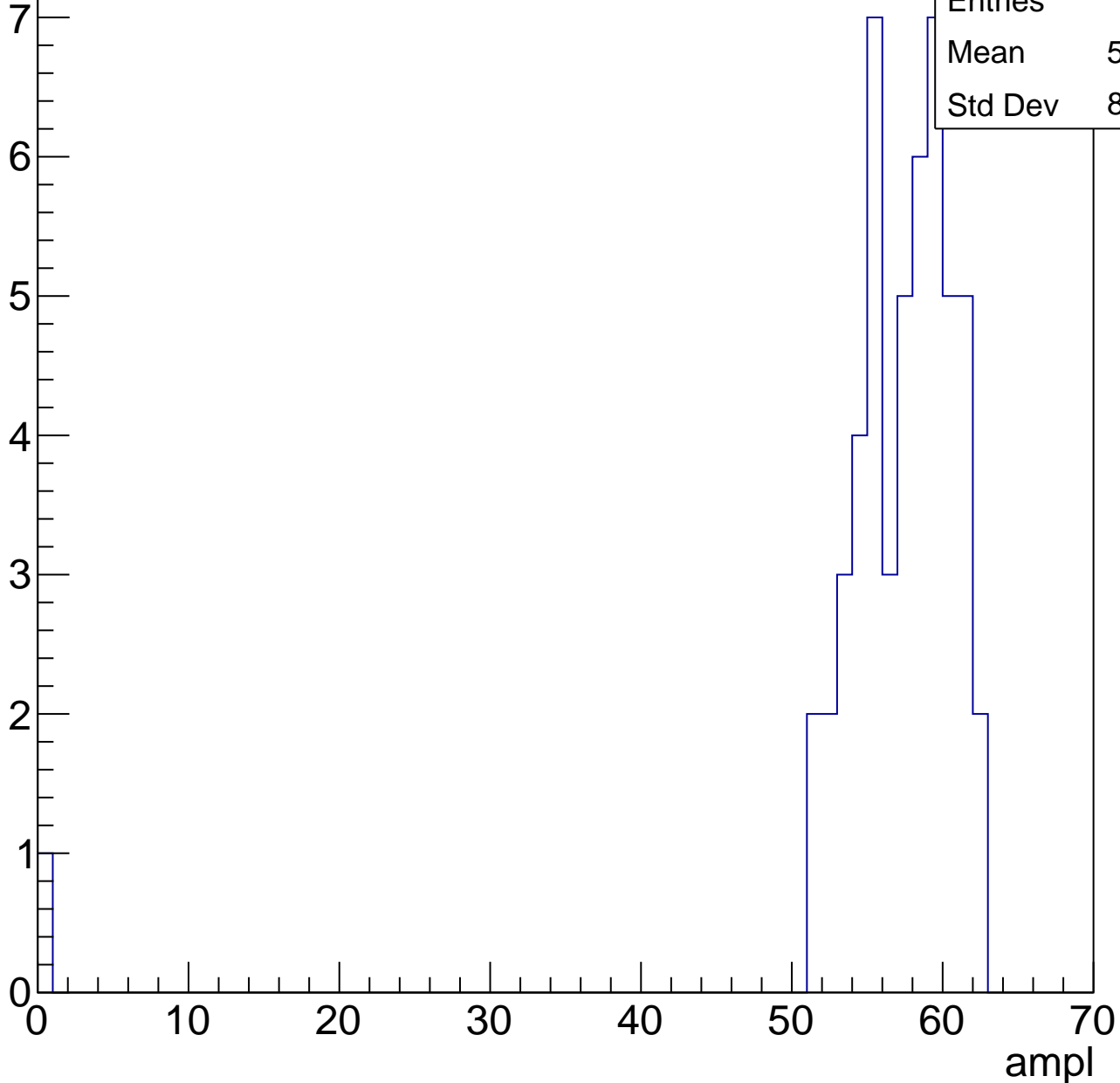


B0L001S, U18-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	55.94
Std Dev	8.363

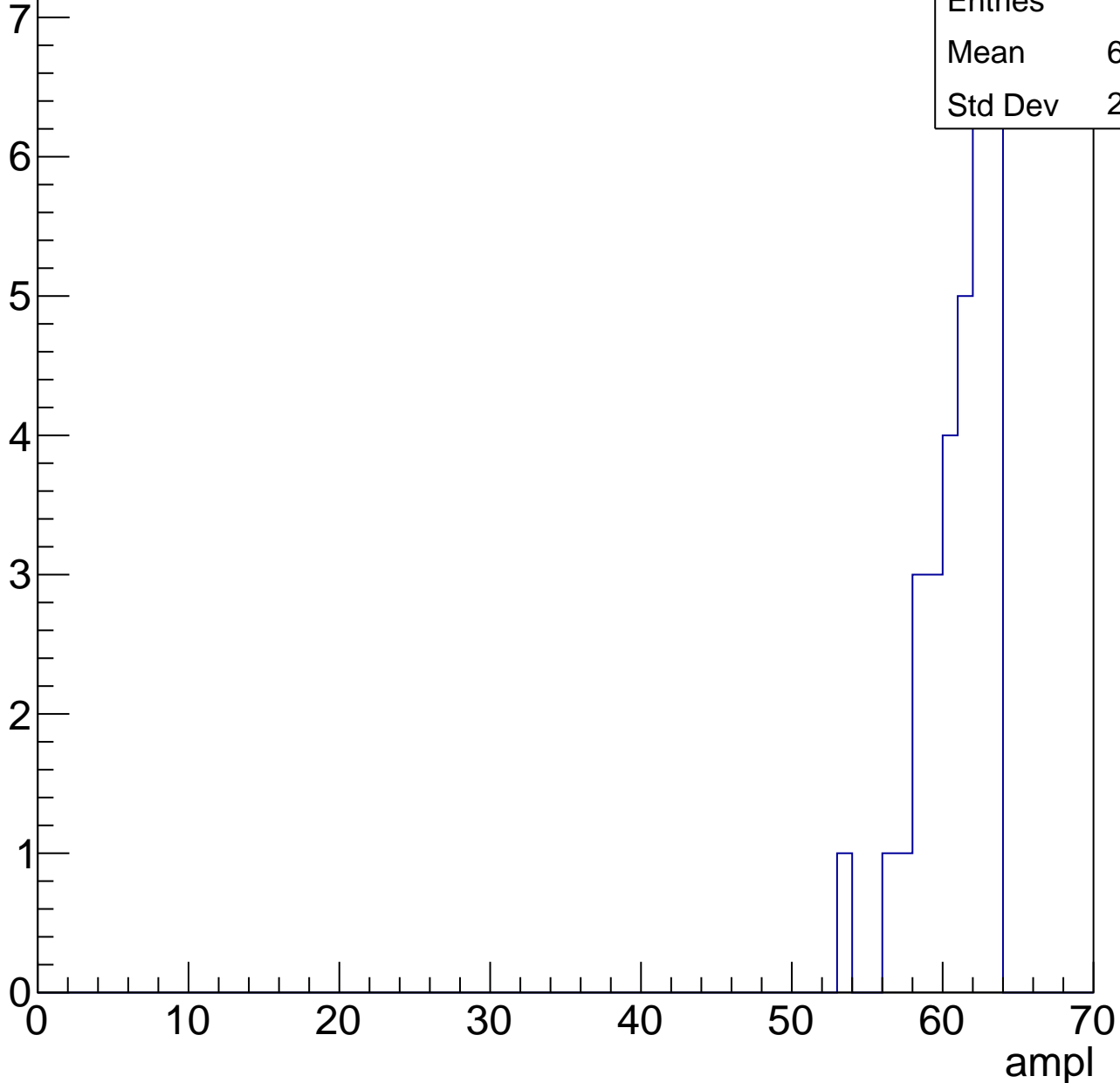


B0L001S, U18-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.53
Std Dev	2.345



B0L001S, U18-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

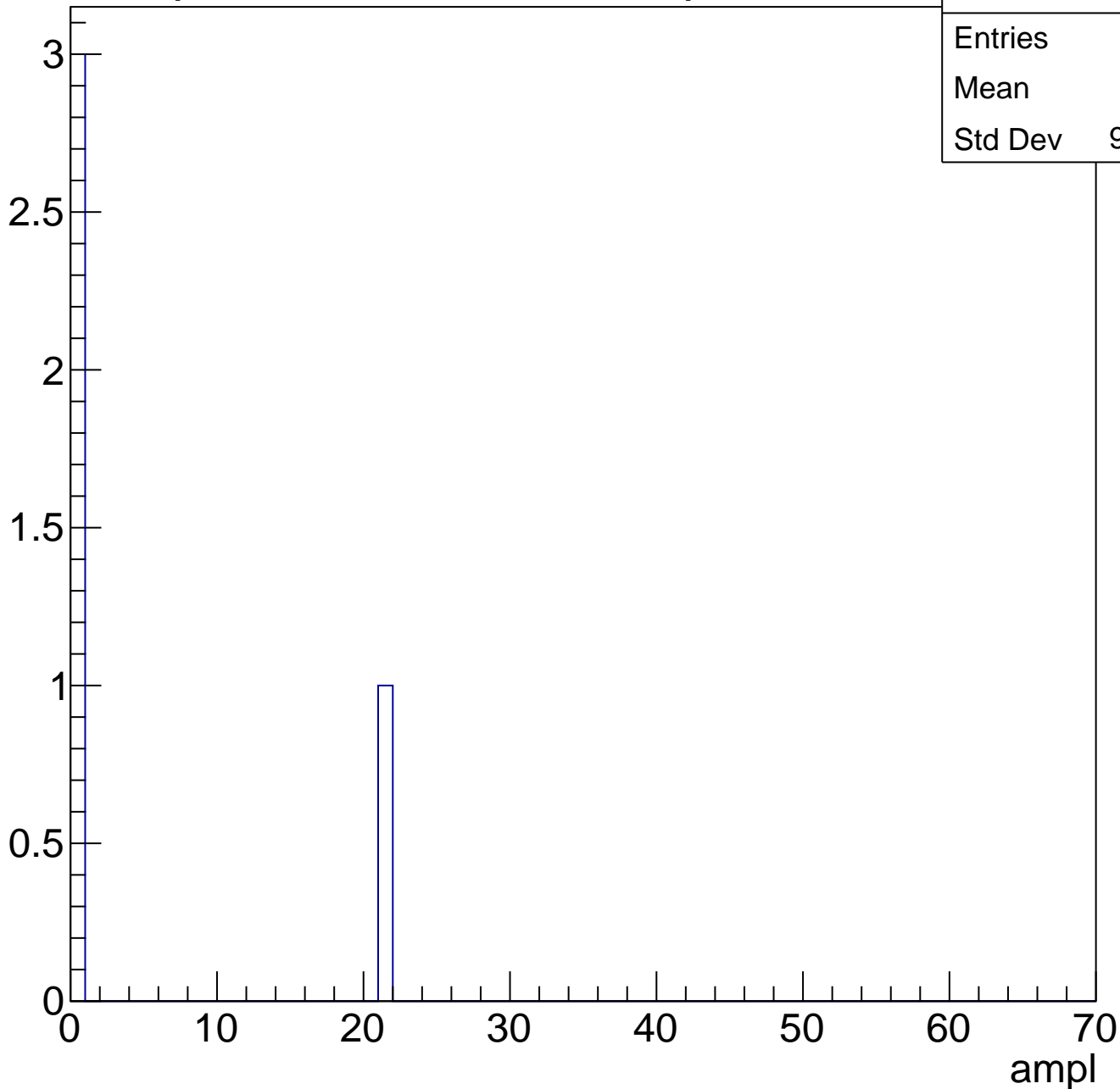
Entry



B0L001S, U18-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch22, adc0

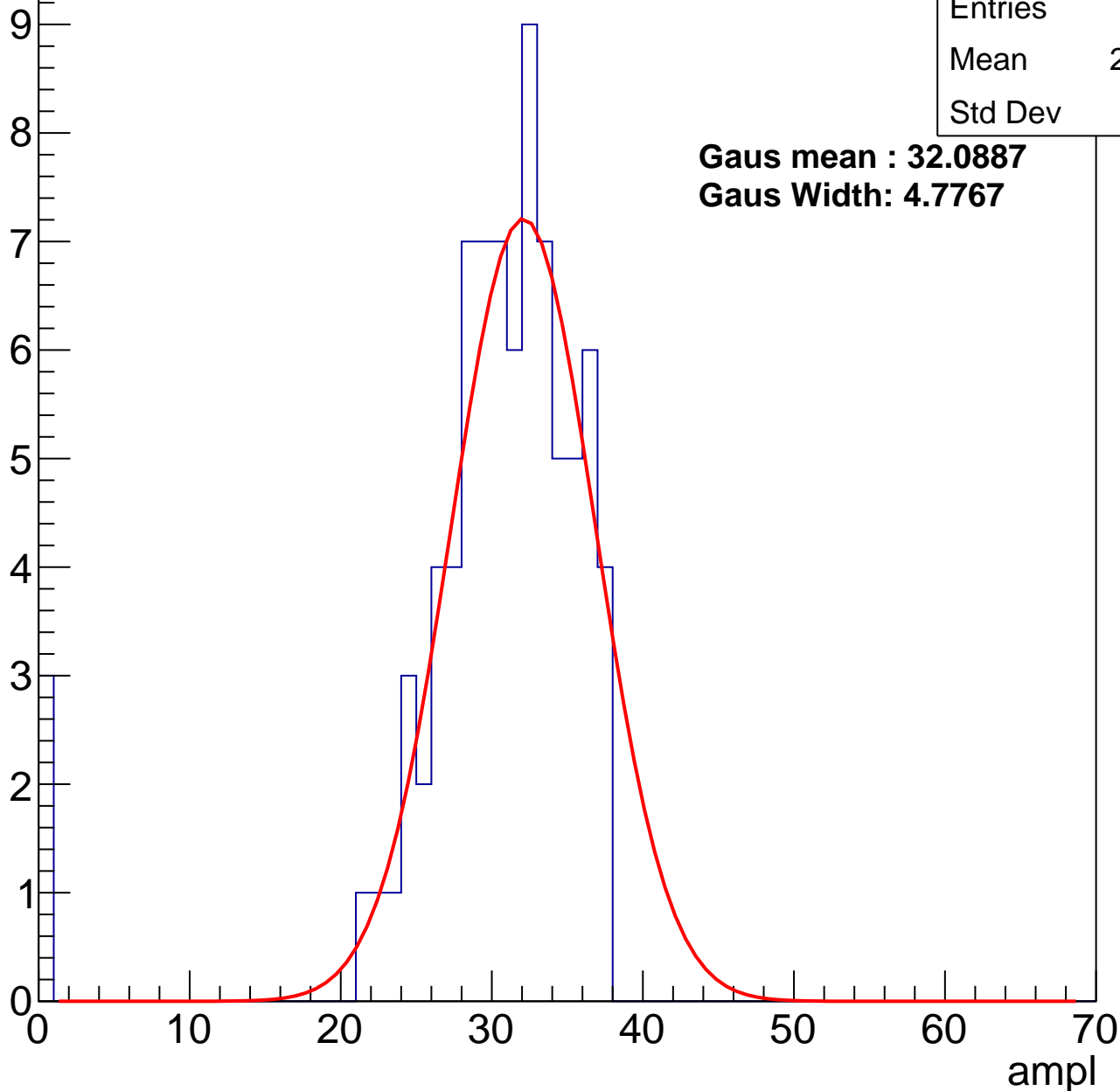
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	29.55
Std Dev	6.89

Gaus mean : 32.0887

Gaus Width: 4.7767



B0L001S, U18-ch22, adc1

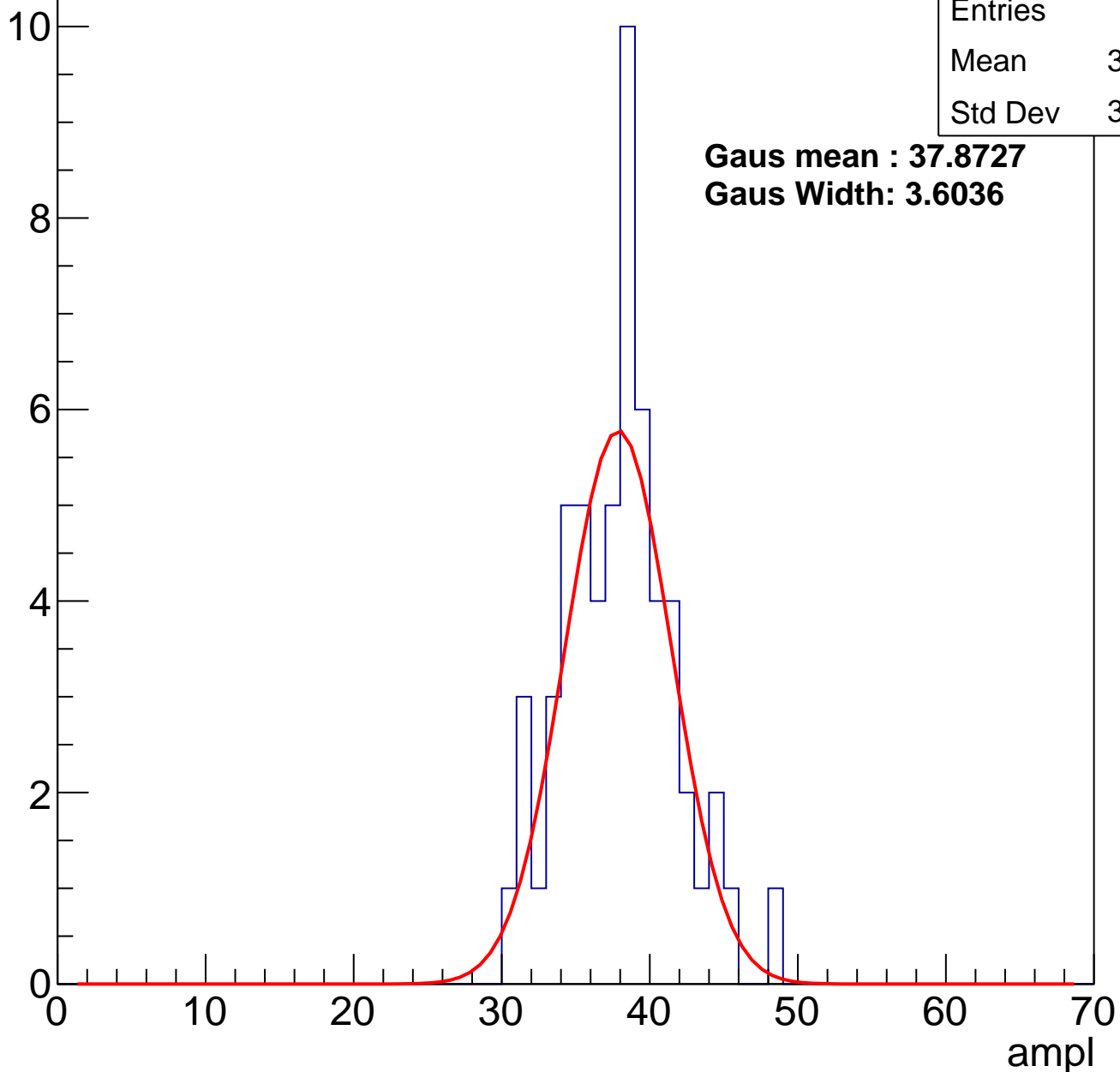
calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	37.48
Std Dev	3.687

Gaus mean : 37.8727

Gaus Width: 3.6036

Entry



B0L001S, U18-ch22, adc2

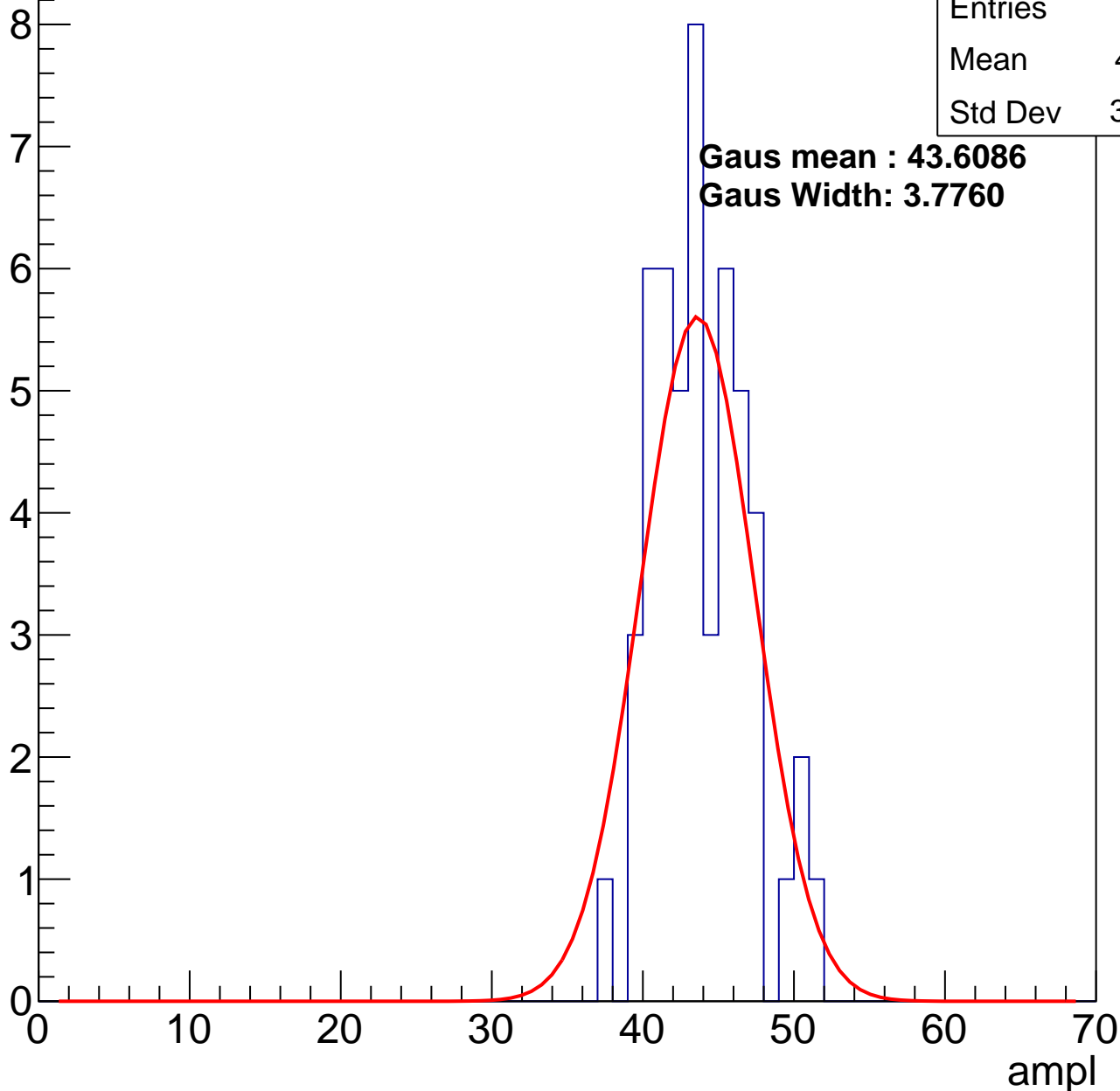
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	43.41
Std Dev	3.107

Gaus mean : 43.6086

Gaus Width: 3.7760

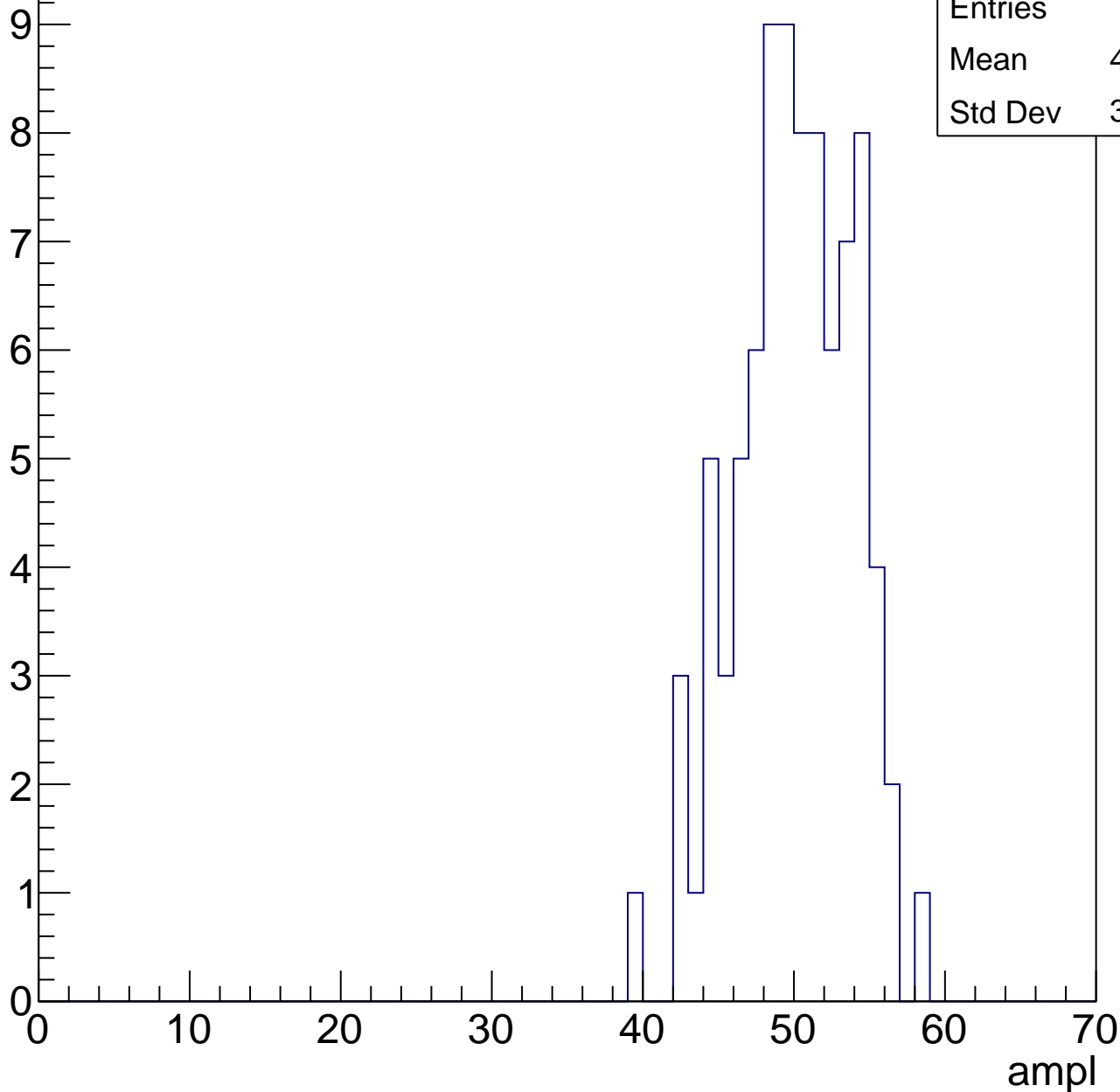


B0L001S, U18-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	49.55
Std Dev	3.793

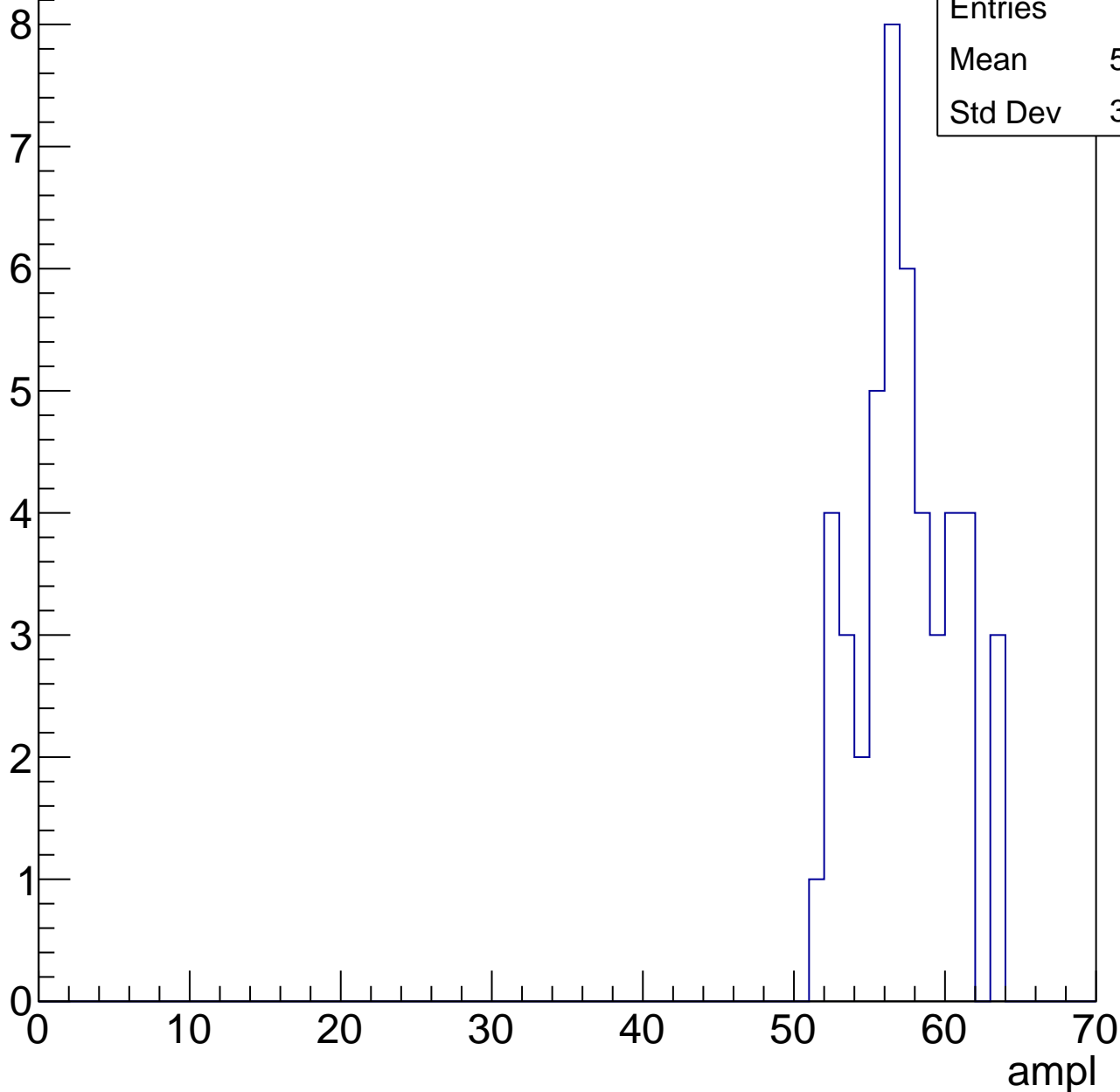


B0L001S, U18-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	56.87
Std Dev	3.105

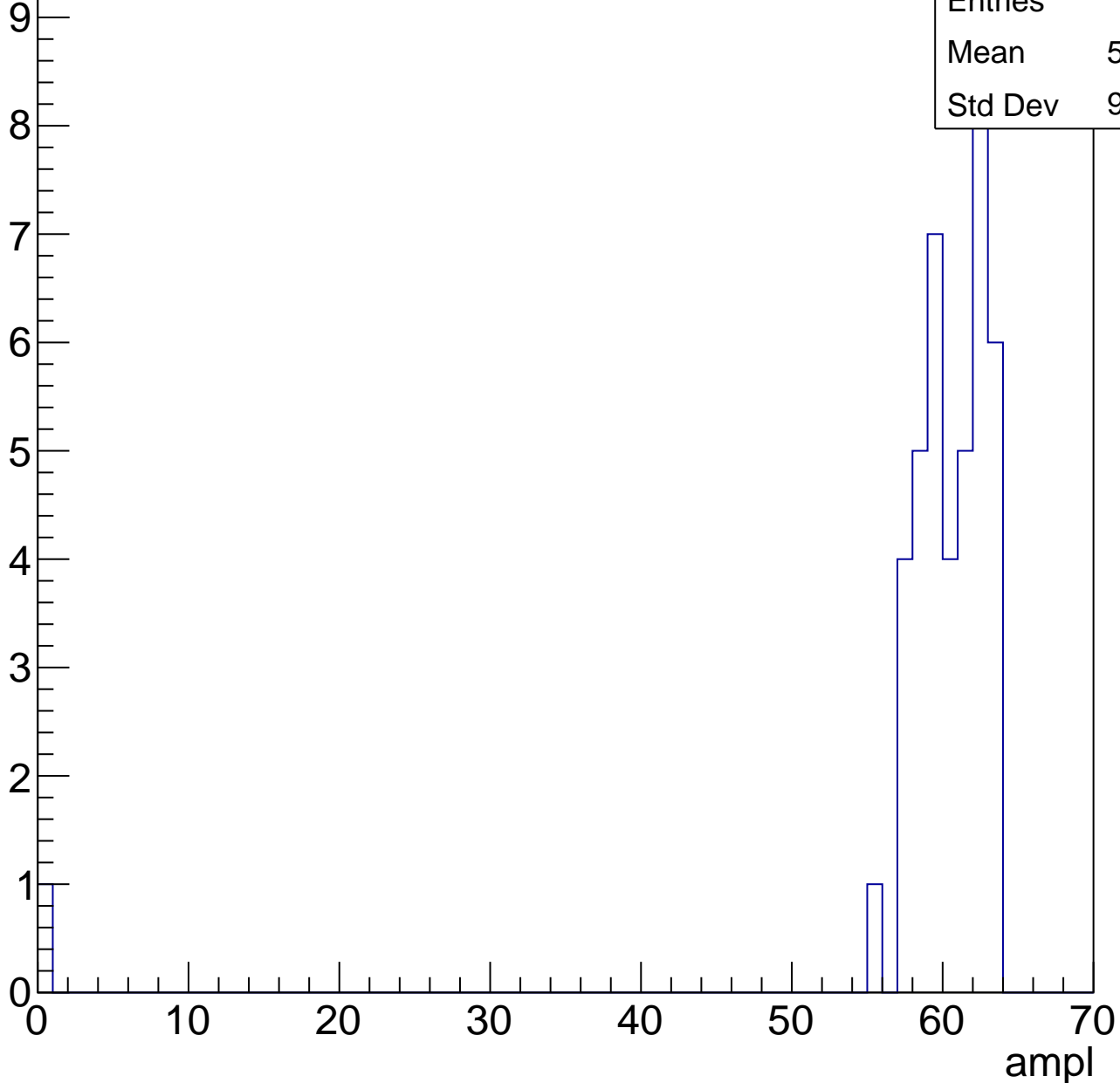


B0L001S, U18-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

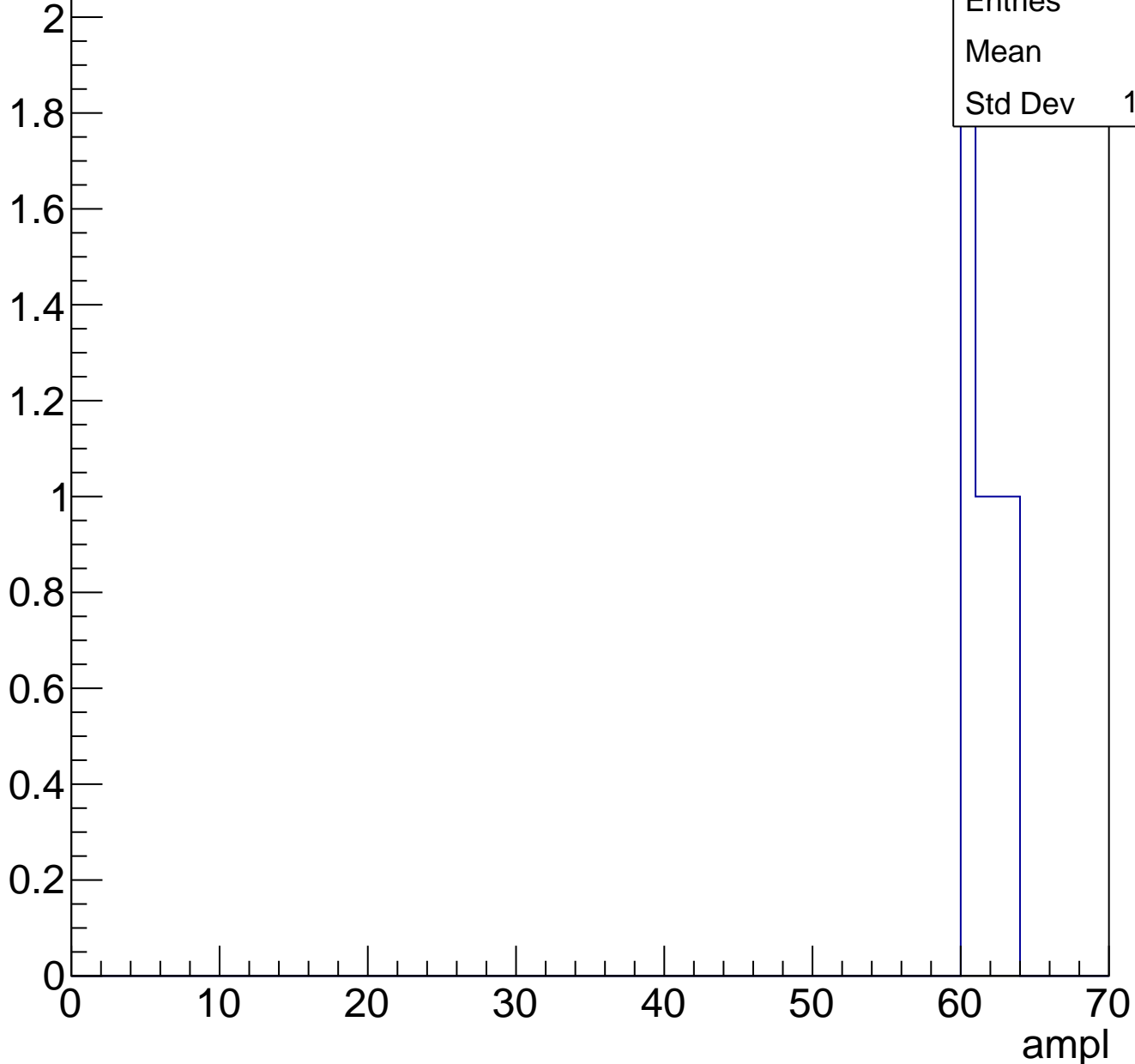
Entries	42
Mean	58.74
Std Dev	9.406



B0L001S, U18-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	61.2
Std Dev	1.166

B0L001S, U18-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch23, adc0

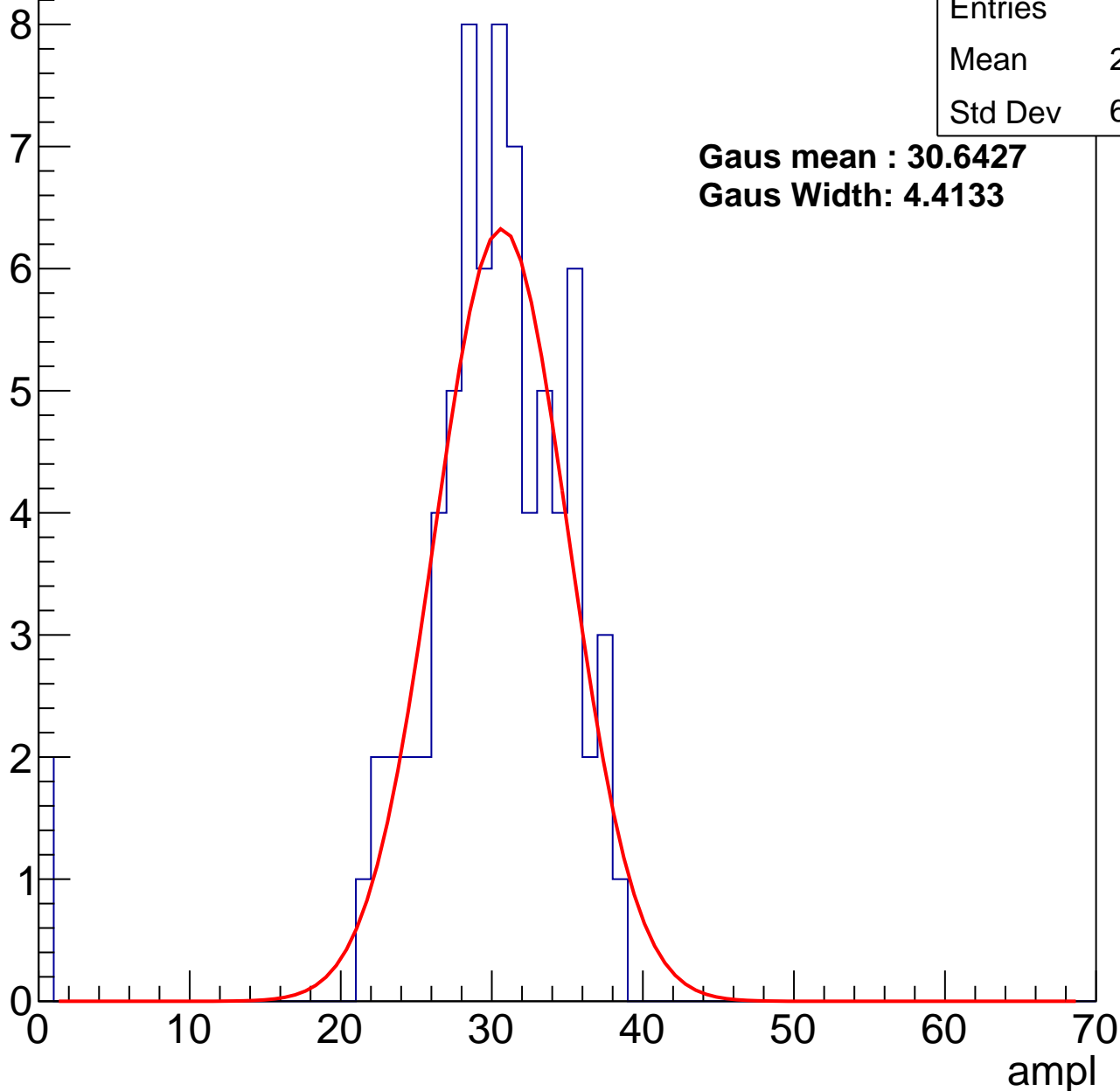
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.23
Std Dev	6.257

Gaus mean : 30.6427

Gaus Width: 4.4133



B0L001S, U18-ch23, adc1

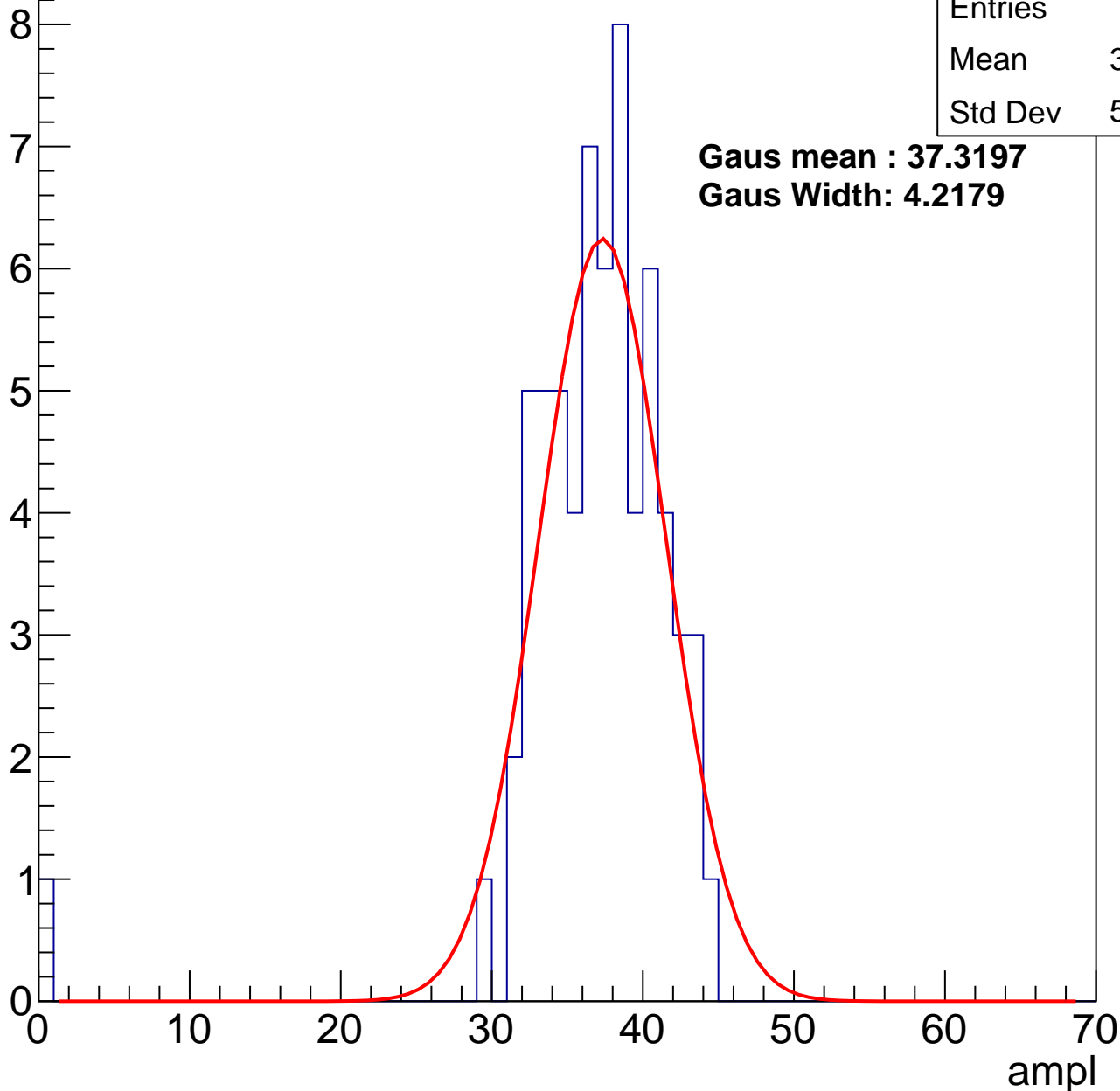
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.35
Std Dev	5.718

Gaus mean : 37.3197

Gaus Width: 4.2179



B0L001S, U18-ch23, adc2

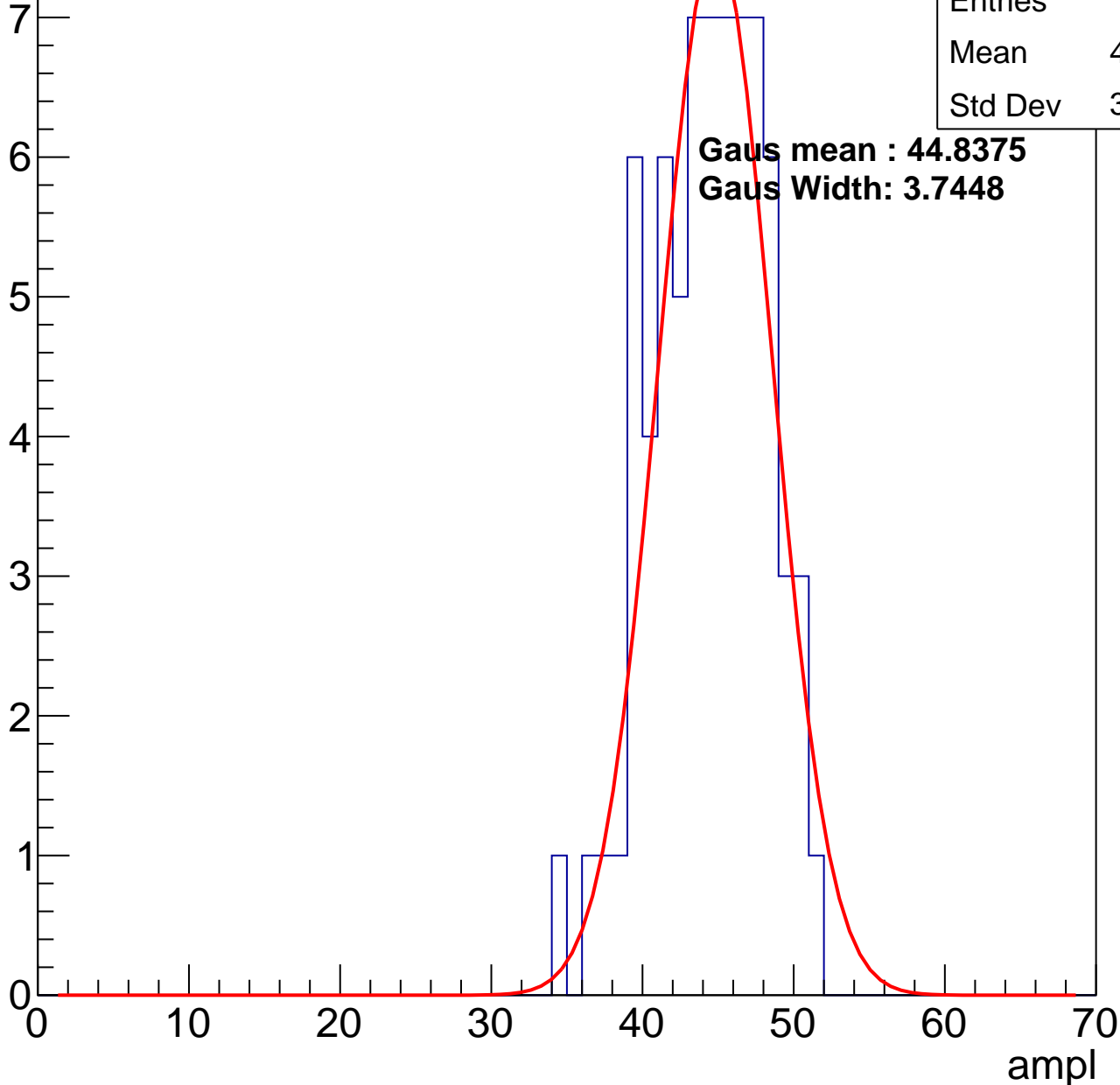
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.92
Std Dev	3.644

Gaus mean : 44.8375

Gaus Width: 3.7448

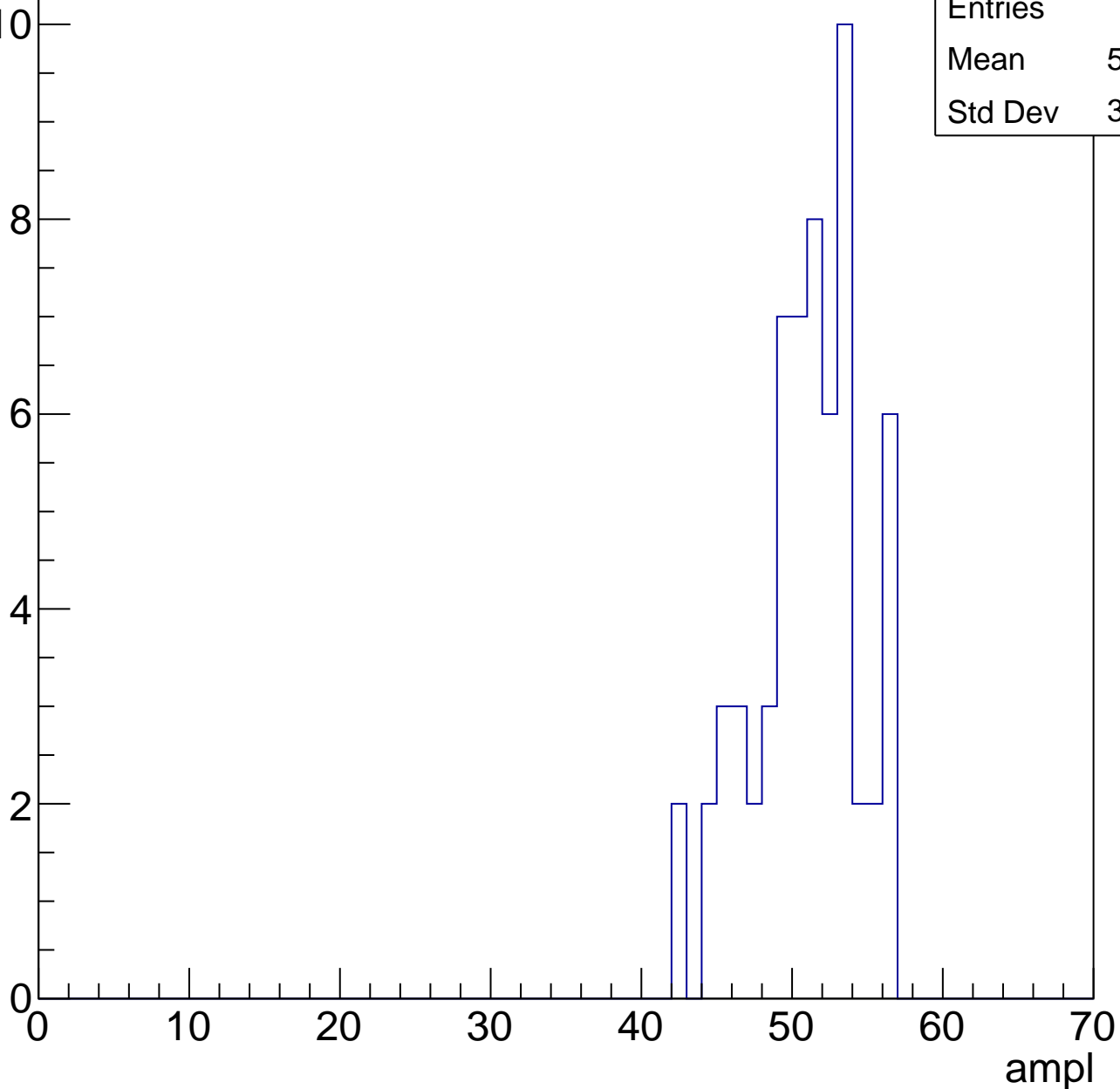


B0L001S, U18-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.48
Std Dev	3.486

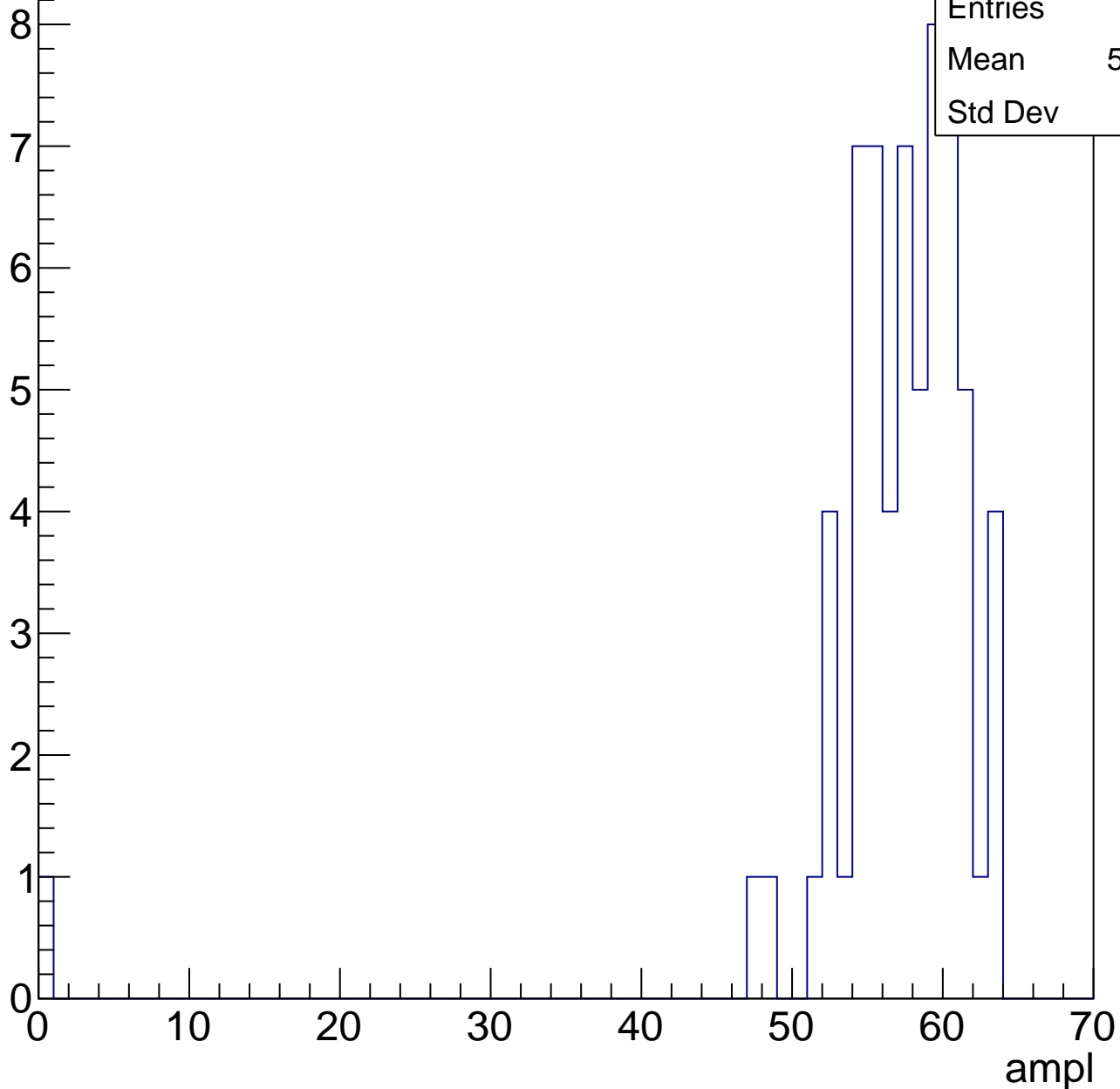


B0L001S, U18-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.22
Std Dev	7.84

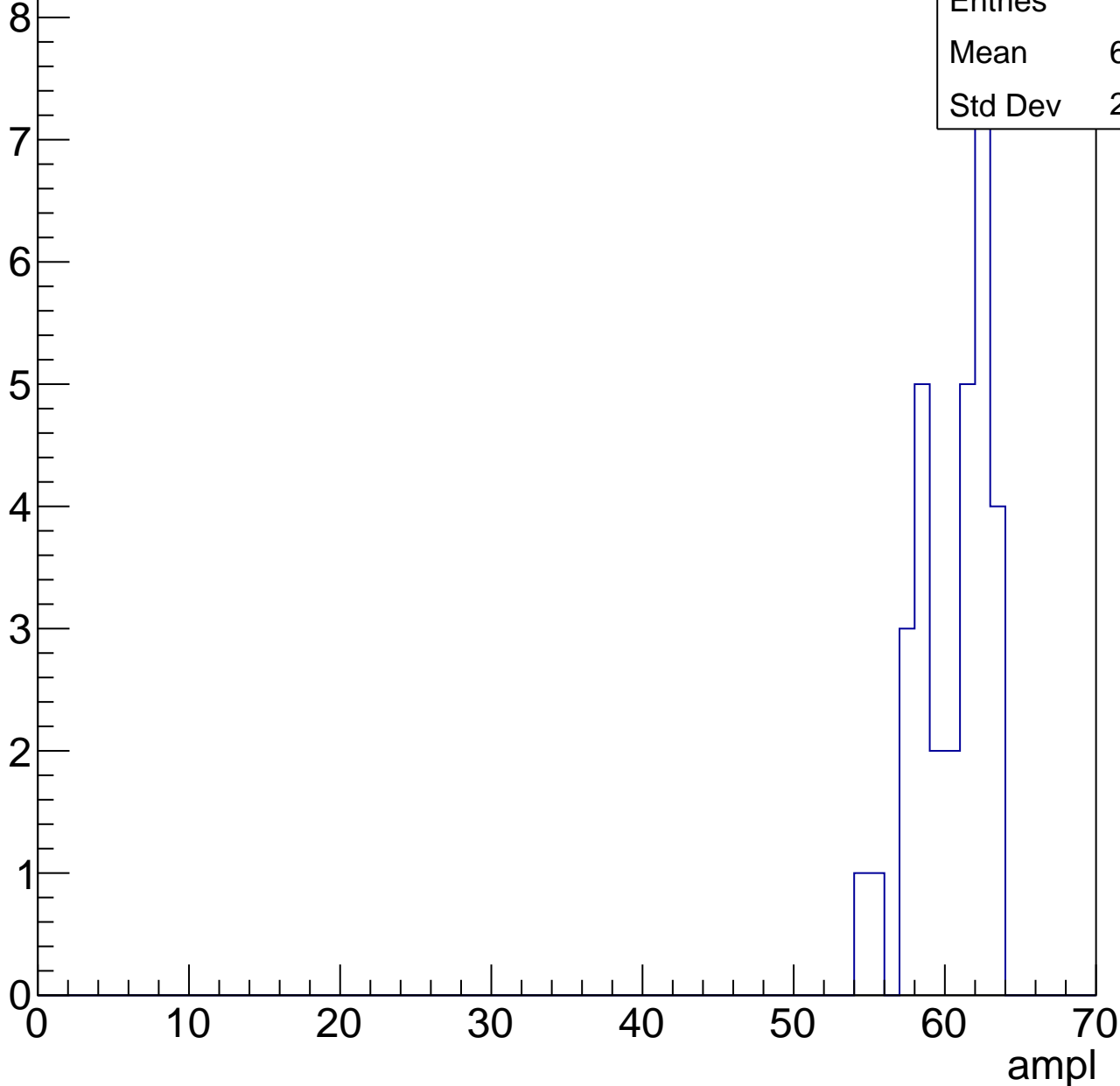


B0L001S, U18-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.03
Std Dev	2.429



B0L001S, U18-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch24, adc0

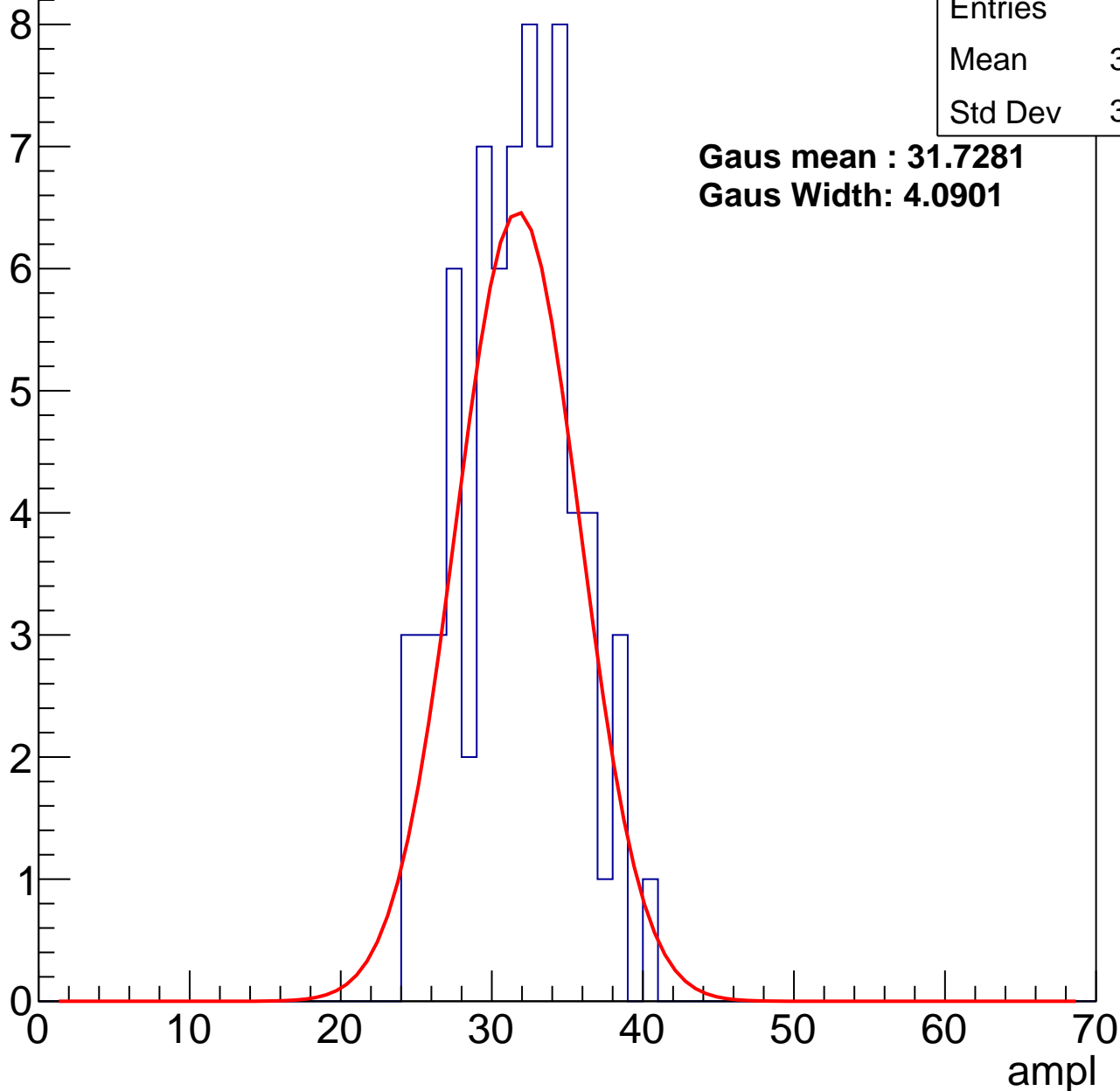
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	31.19
Std Dev	3.722

Gaus mean : 31.7281

Gaus Width: 4.0901



B0L001S, U18-ch24, adc1

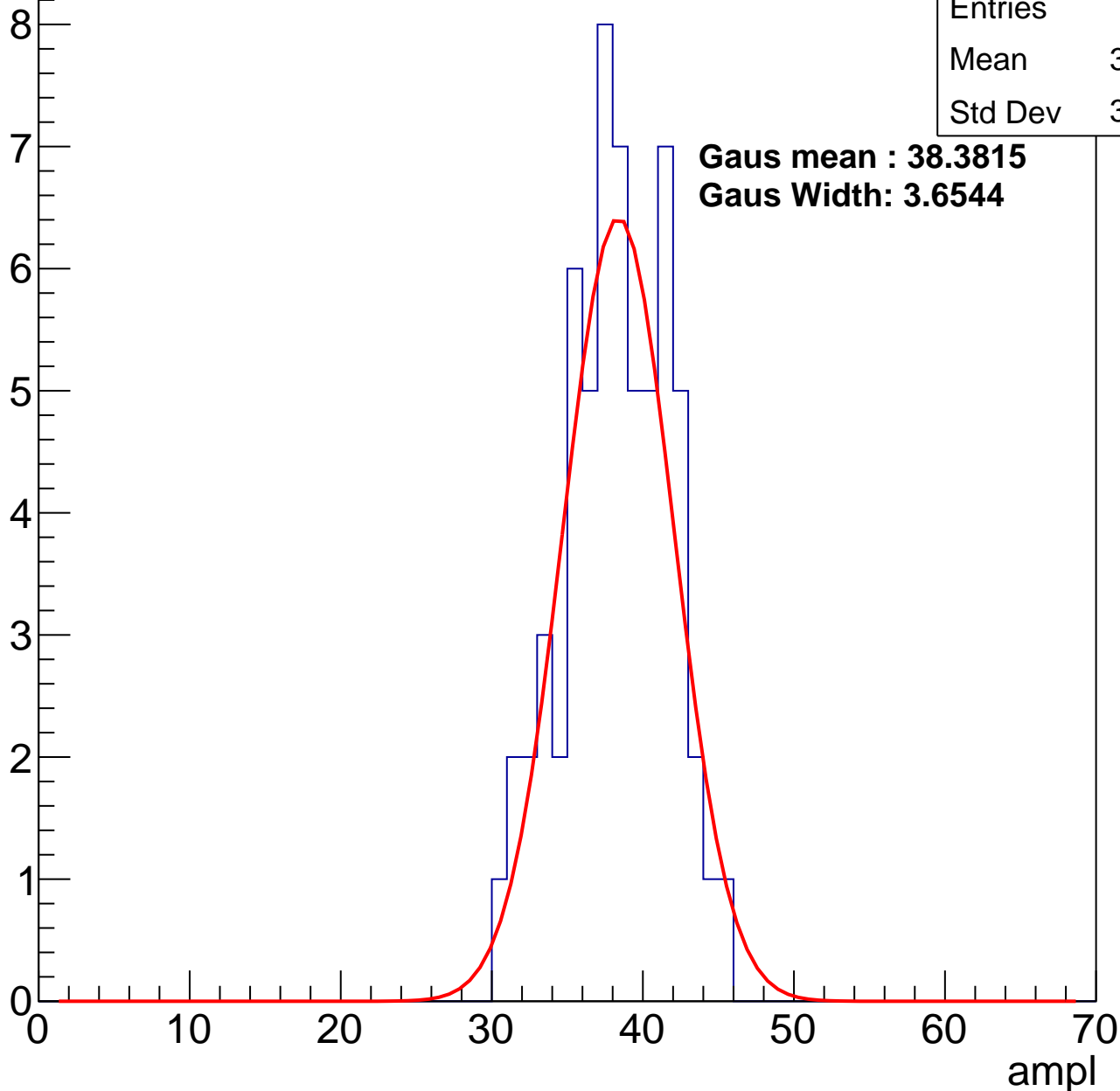
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.77
Std Dev	3.414

Gaus mean : 38.3815

Gaus Width: 3.6544



B0L001S, U18-ch24, adc2

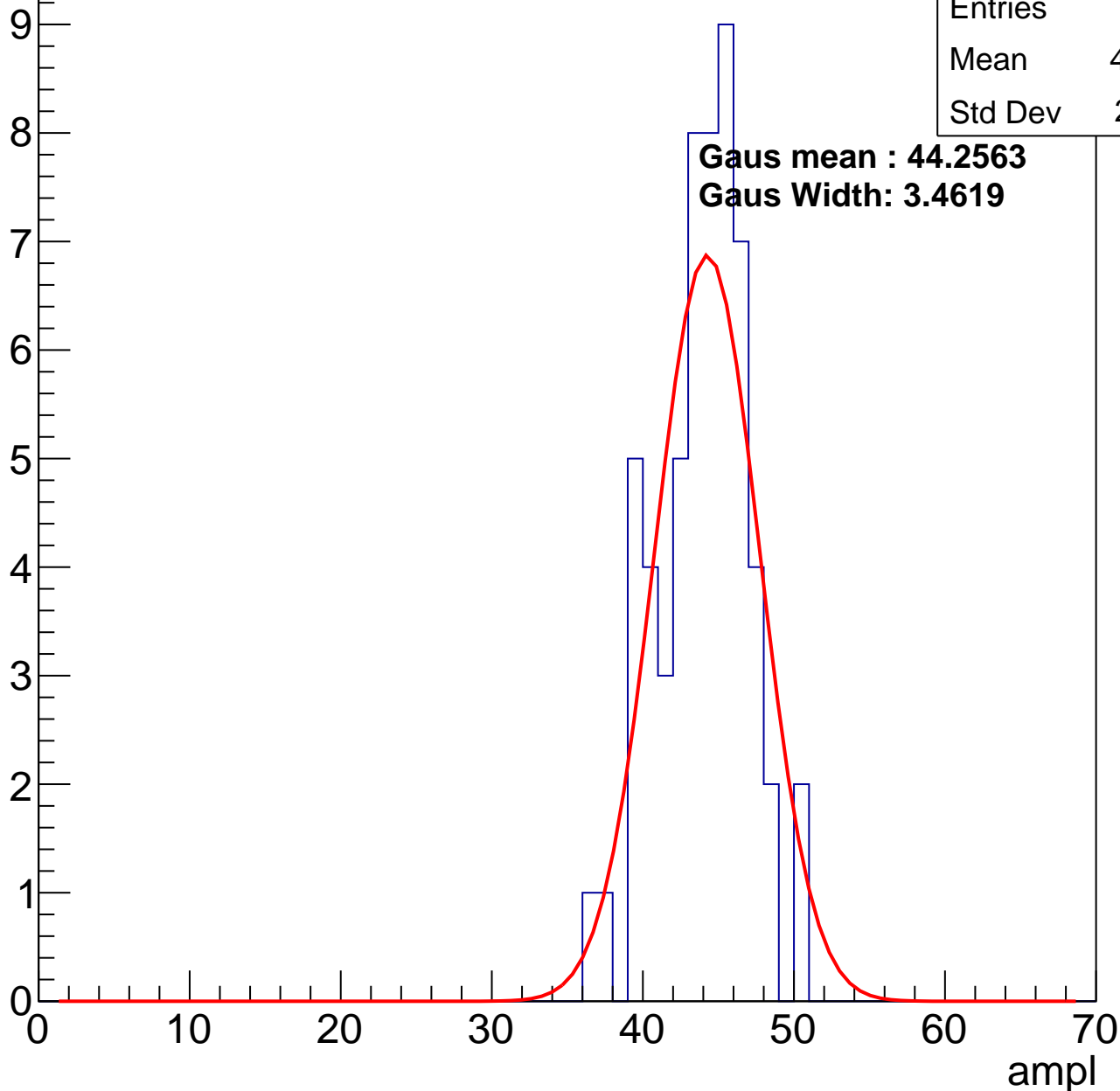
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.53
Std Dev	2.971

Gaus mean : 44.2563

Gaus Width: 3.4619

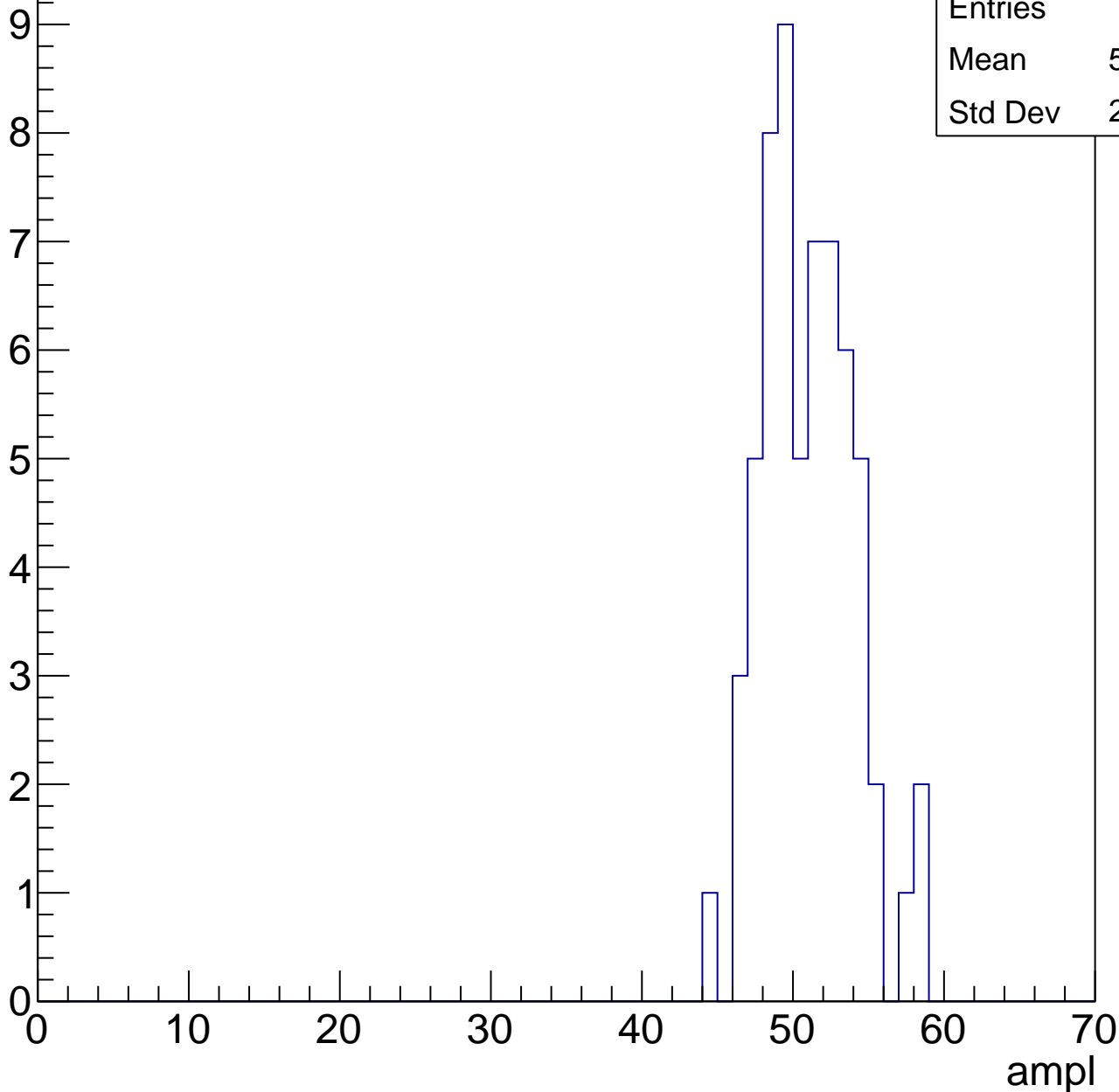


B0L001S, U18-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.56
Std Dev	2.995

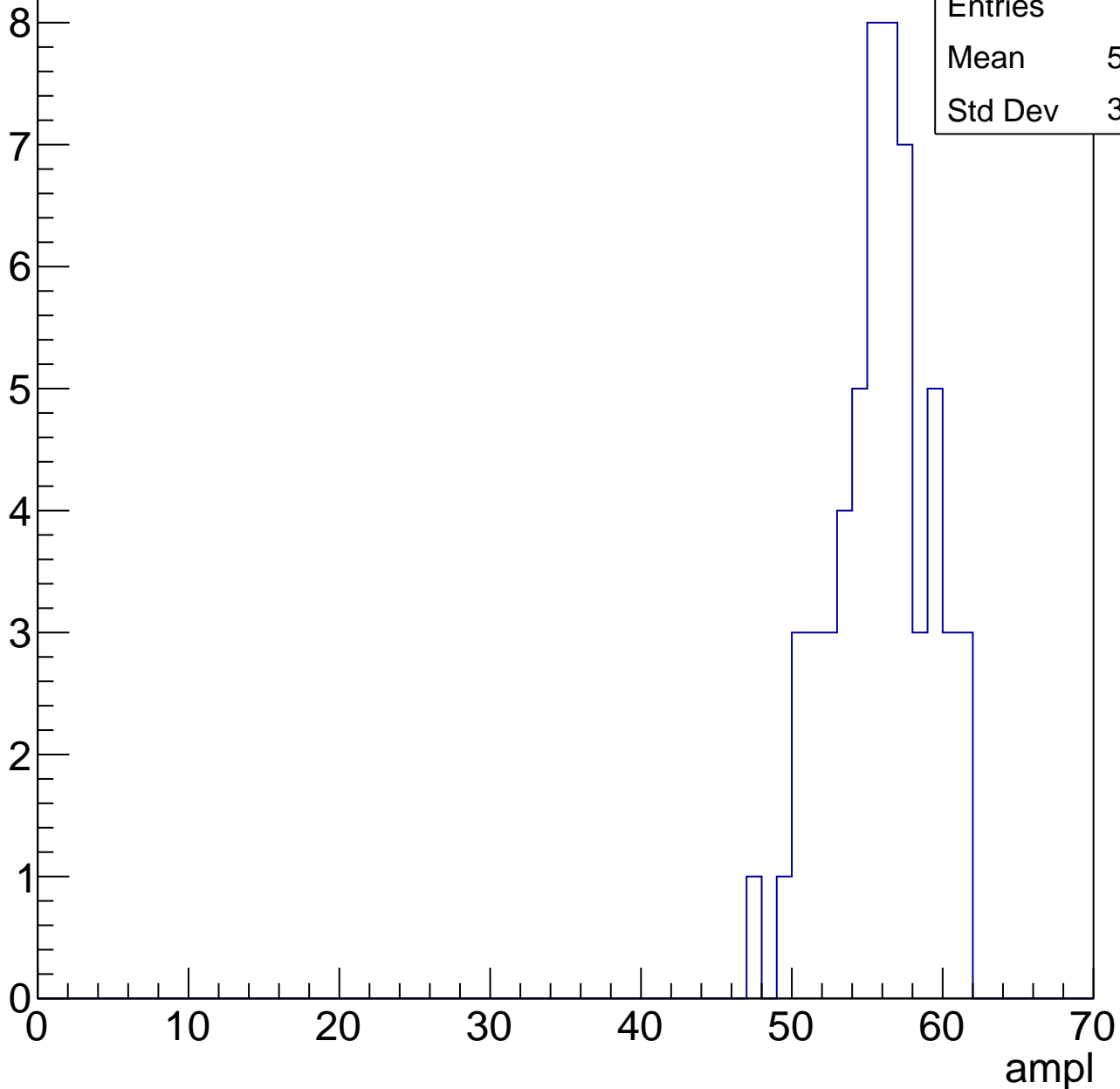


B0L001S, U18-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.37
Std Dev	3.215

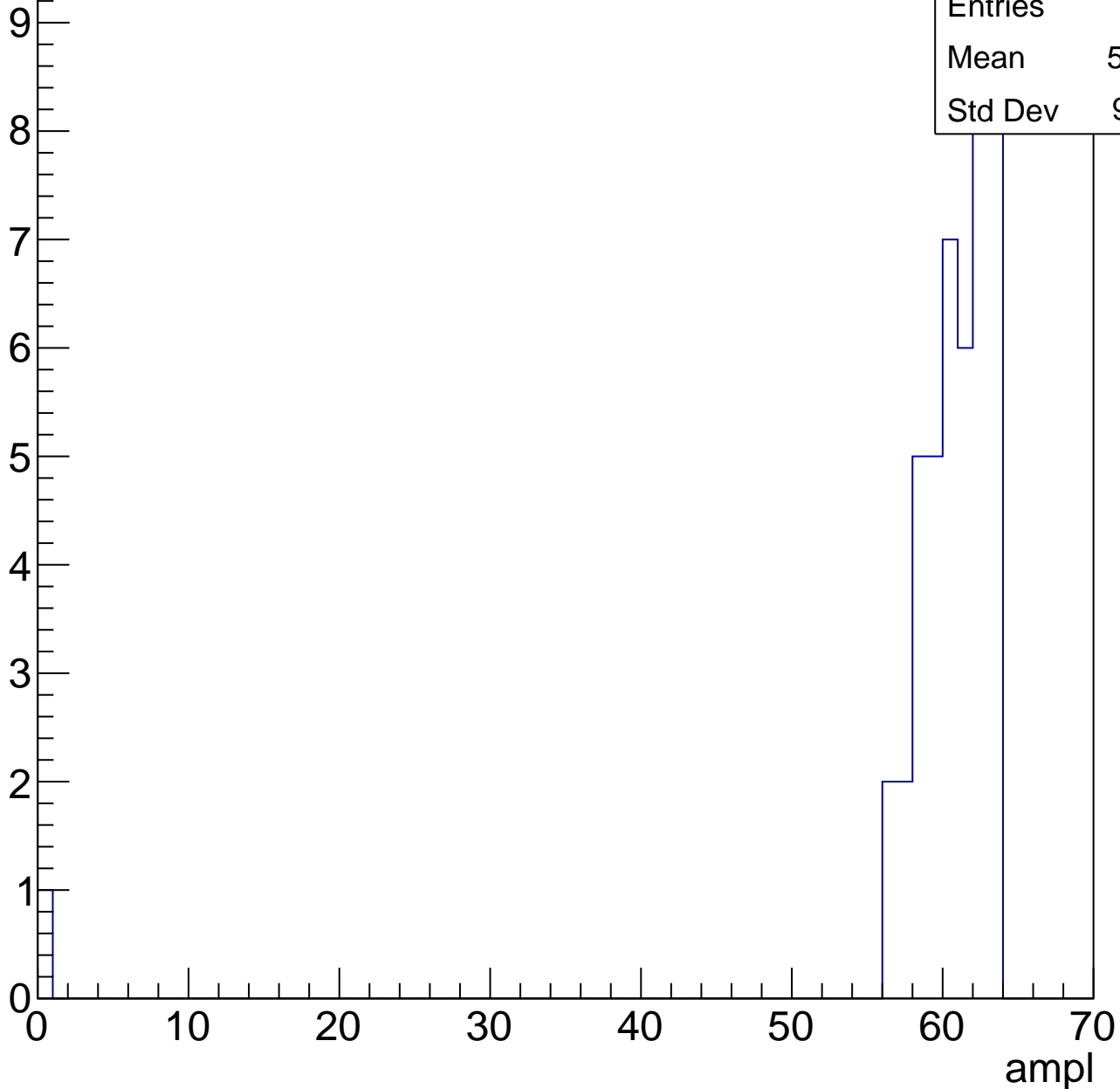


B0L001S, U18-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	59.09
Std Dev	9.131



B0L001S, U18-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	10
Std Dev	10

B0L001S, U18-ch25, adc0

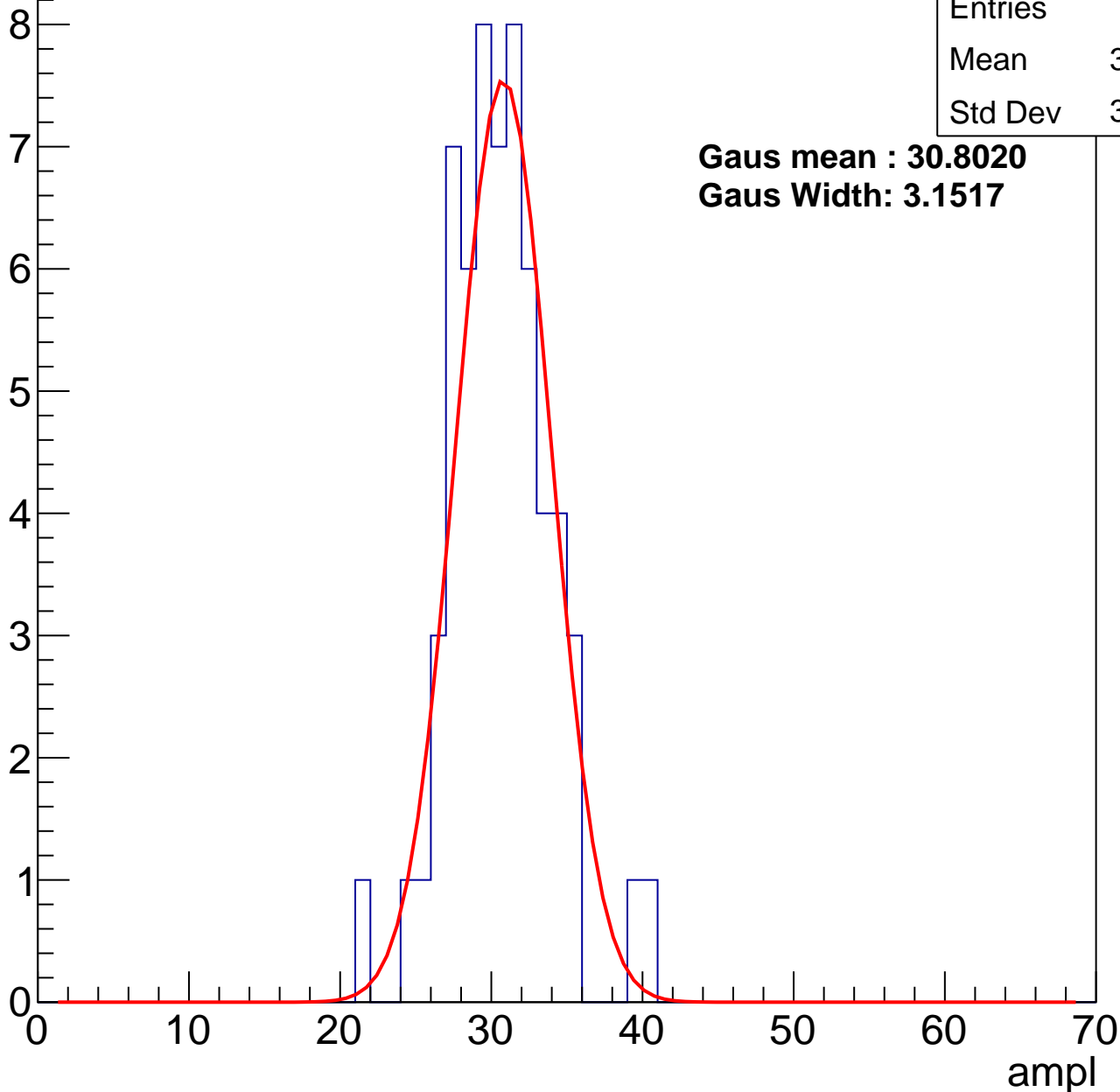
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.15
Std Dev	3.318

Gaus mean : 30.8020

Gaus Width: 3.1517



B0L001S, U18-ch25, adc1

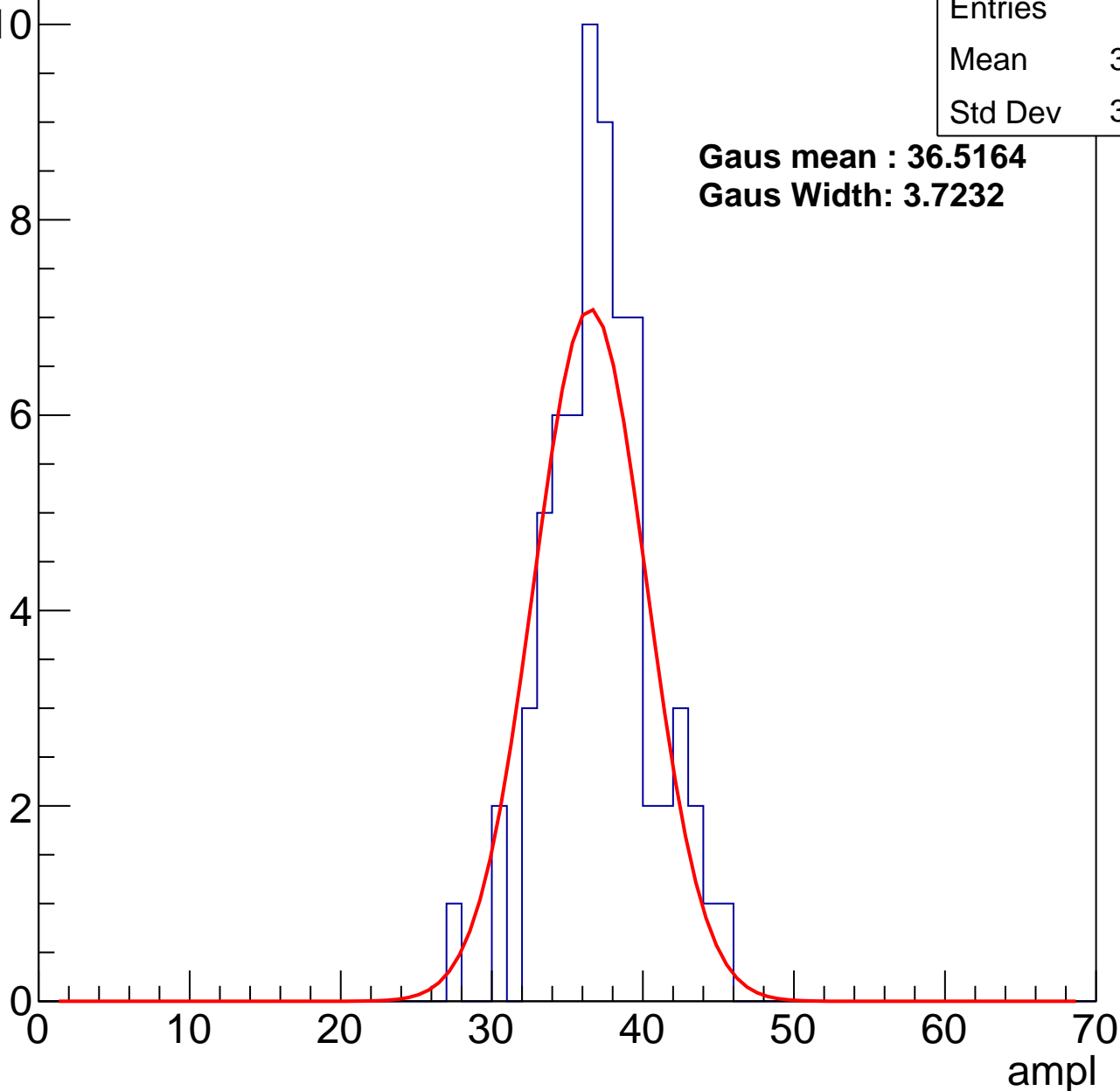
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.67
Std Dev	3.409

Gaus mean : 36.5164

Gaus Width: 3.7232



B0L001S, U18-ch25, adc2

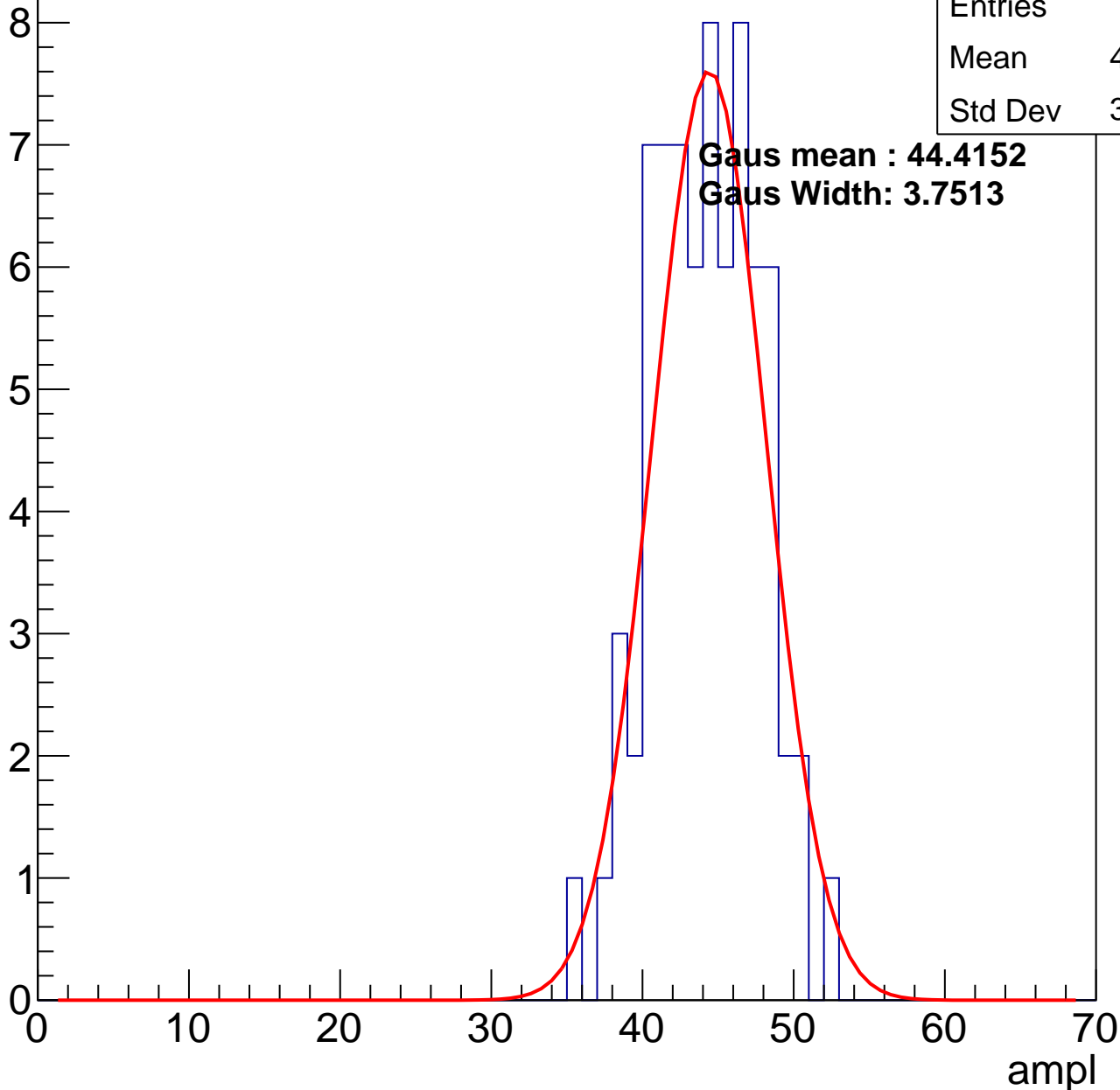
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.74
Std Dev	3.444

Gaus mean : 44.4152

Gaus Width: 3.7513

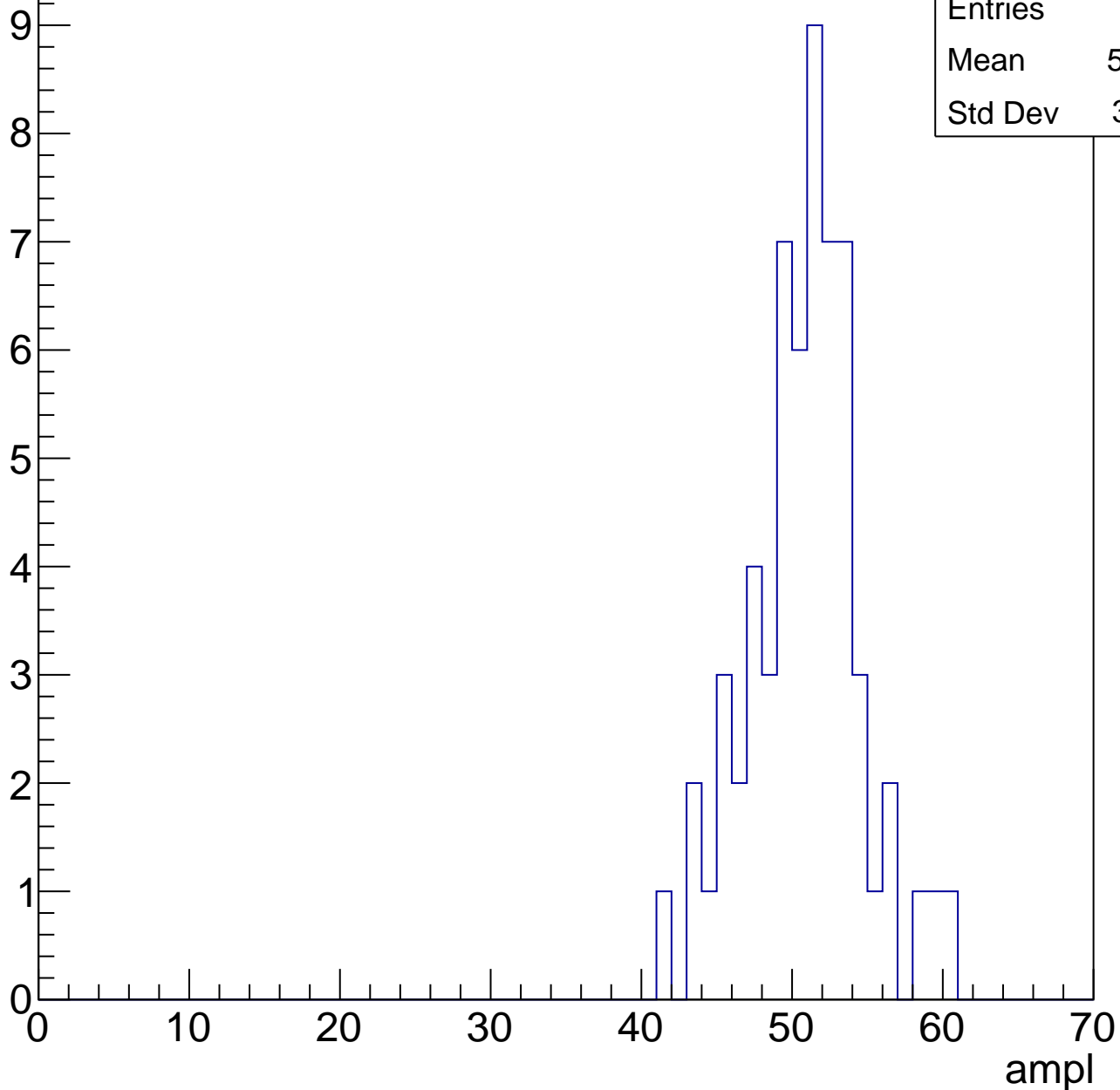


B0L001S, U18-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

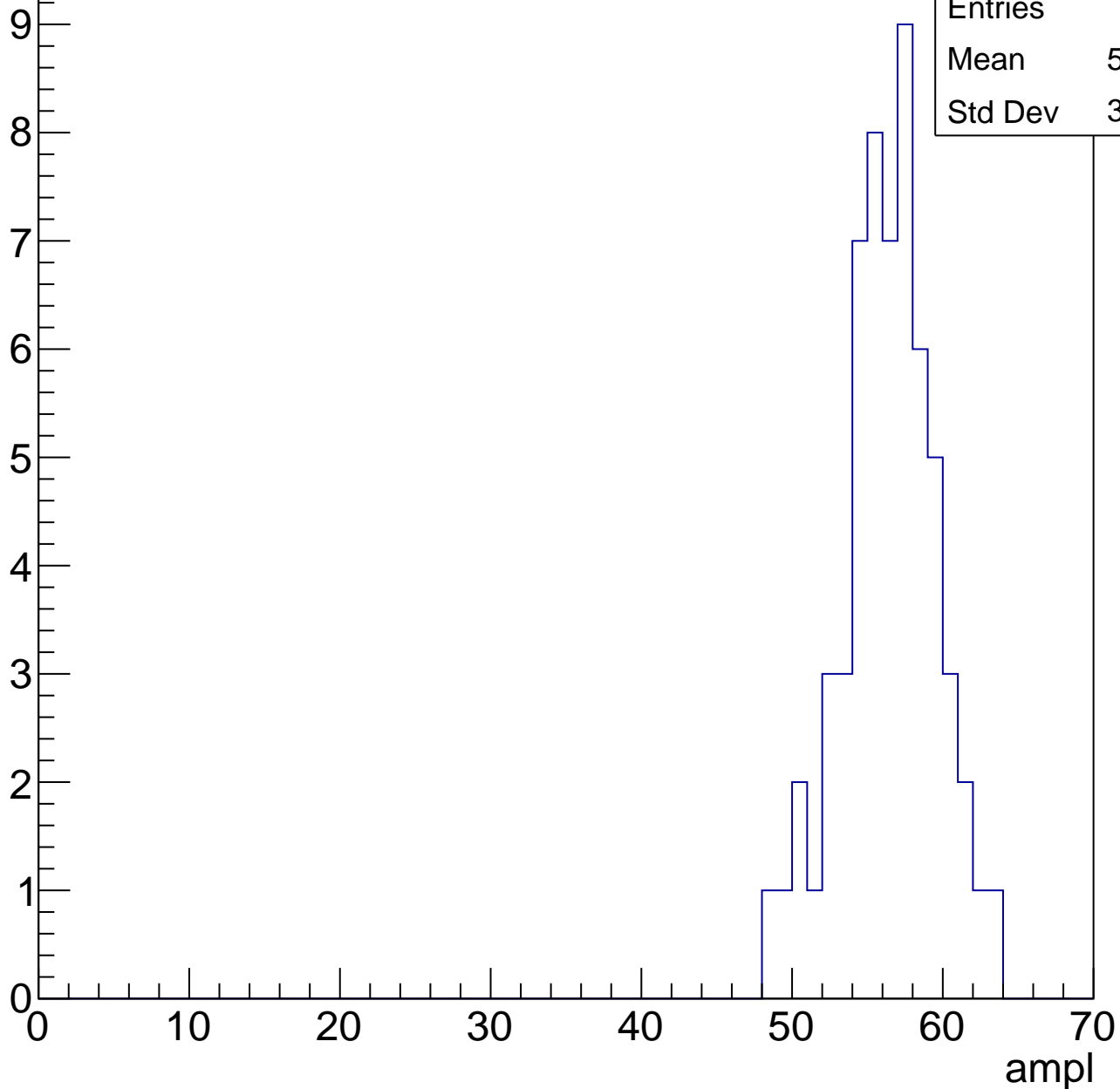
Entries	61
Mean	50.38
Std Dev	3.751



B0L001S, U18-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

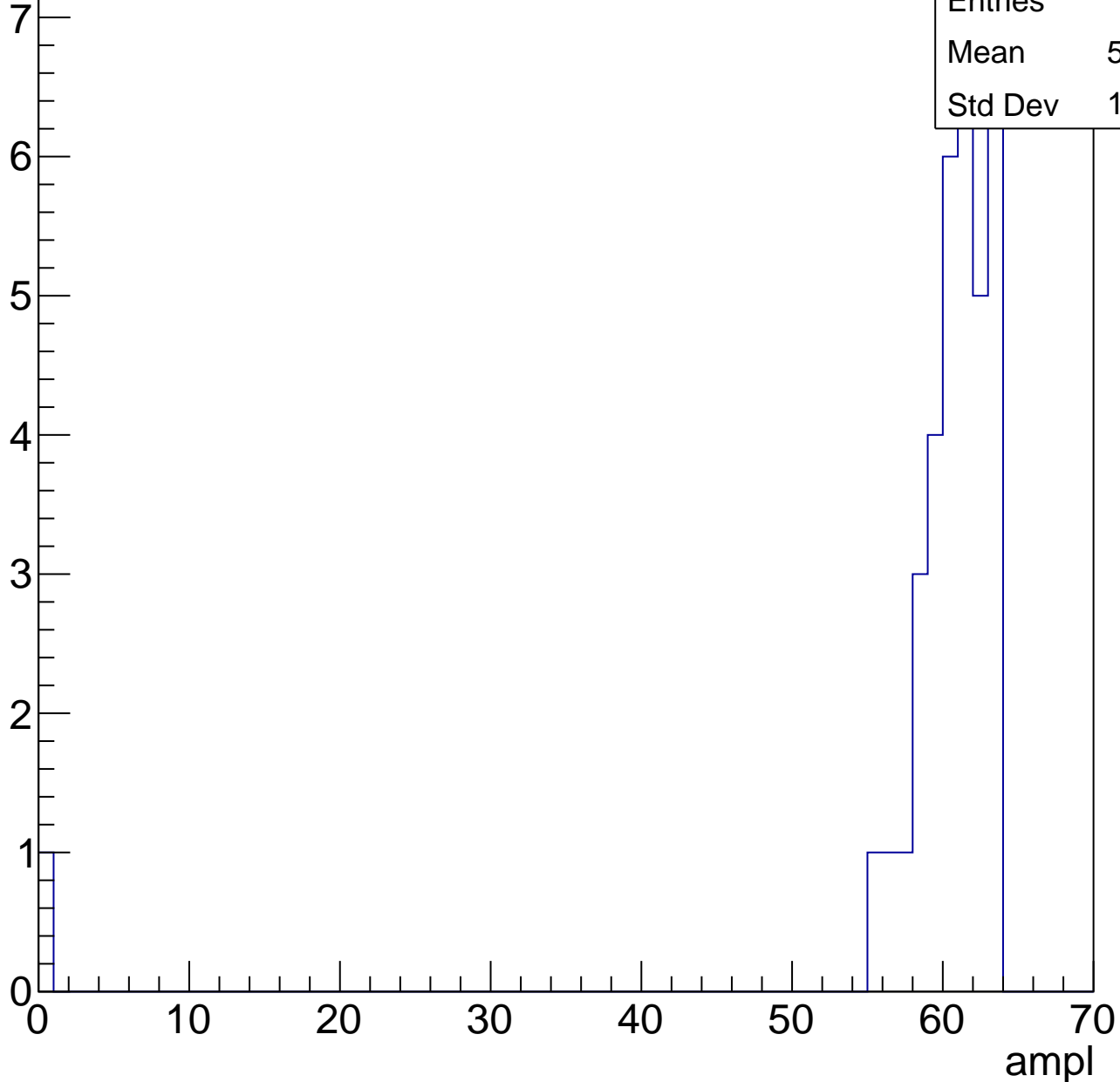
Entry



B0L001S, U18-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

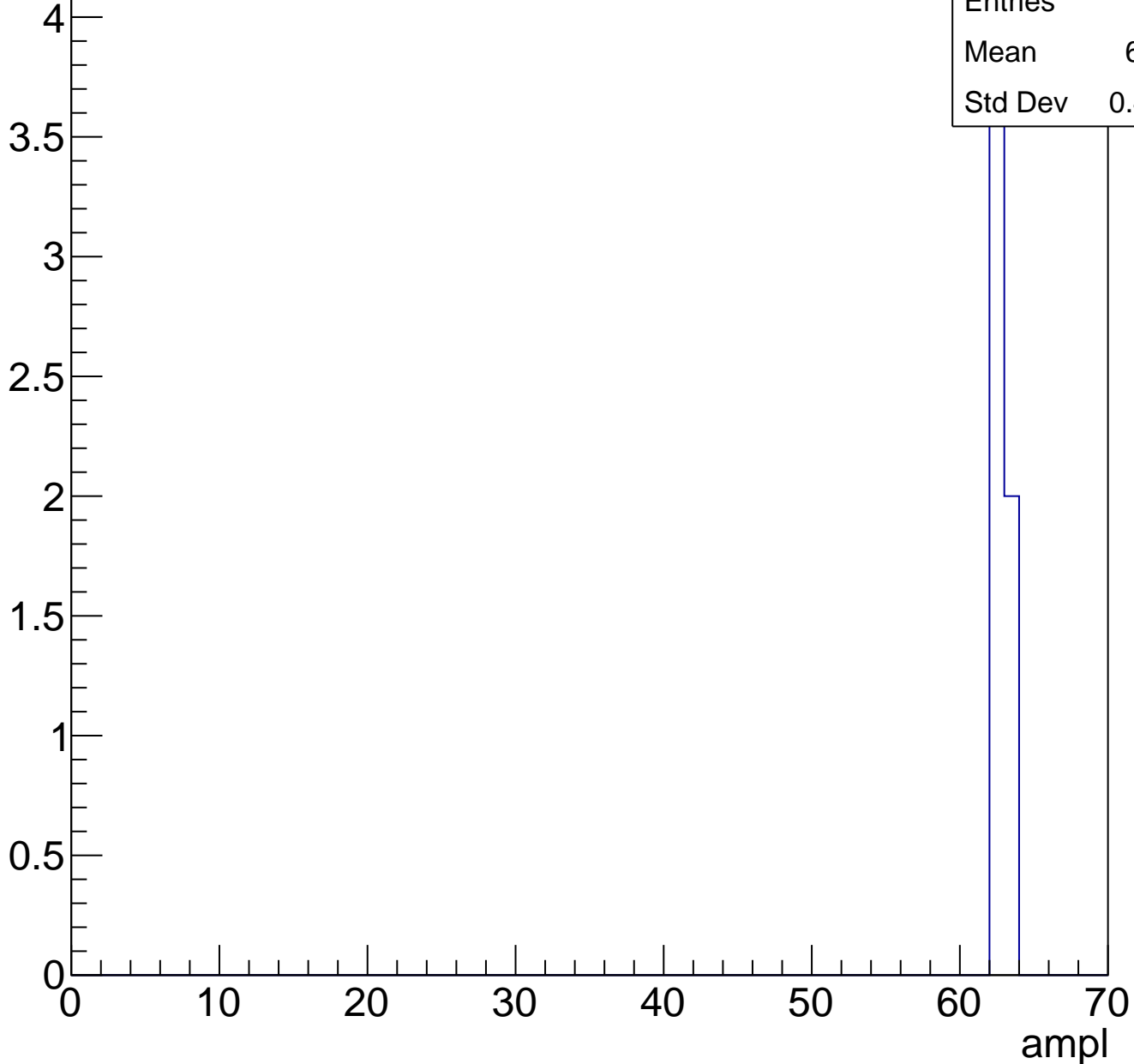
Entry



B0L001S, U18-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch26, adc0

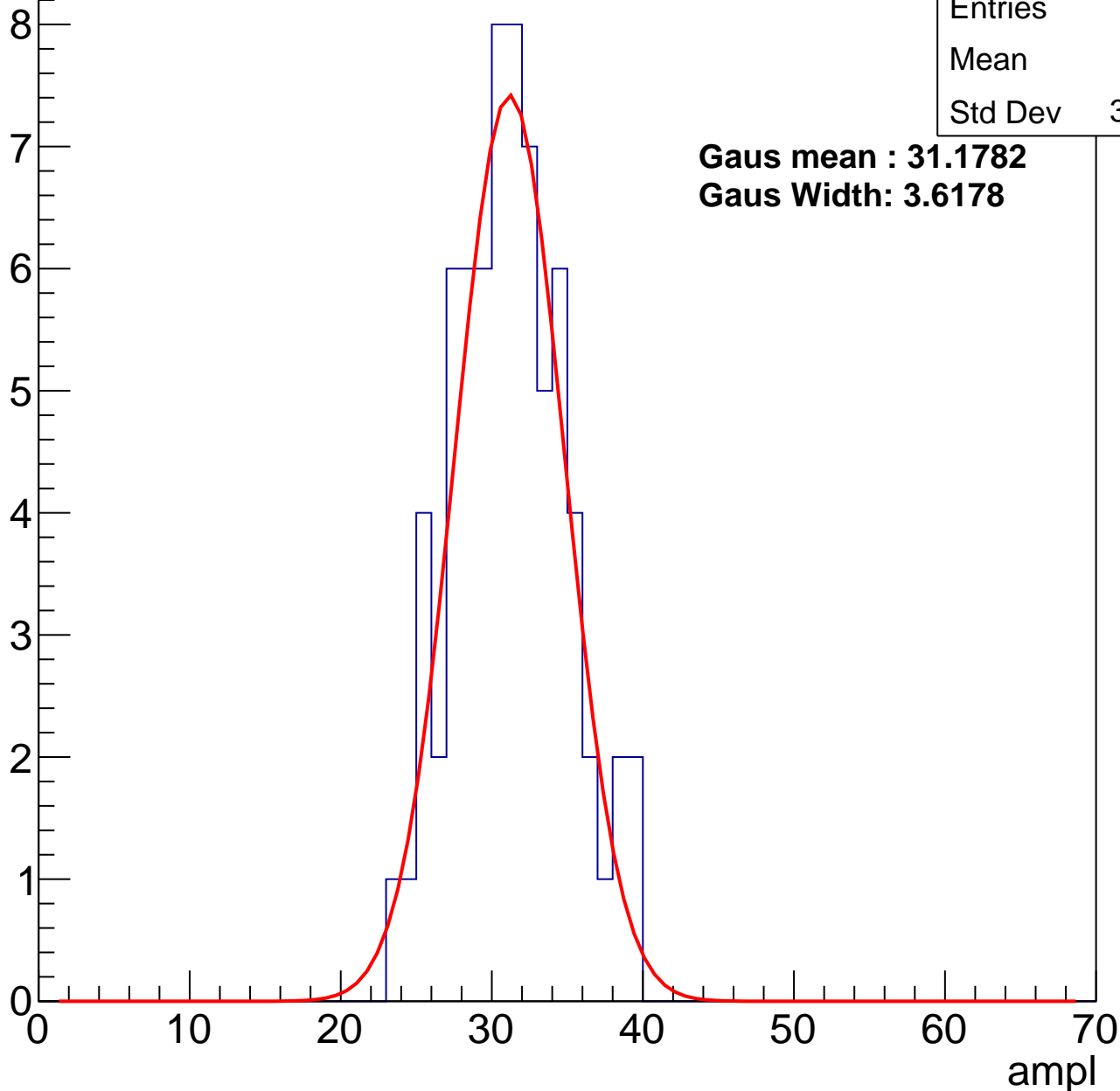
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	30.8
Std Dev	3.645

Gaus mean : 31.1782

Gaus Width: 3.6178



B0L001S, U18-ch26, adc1

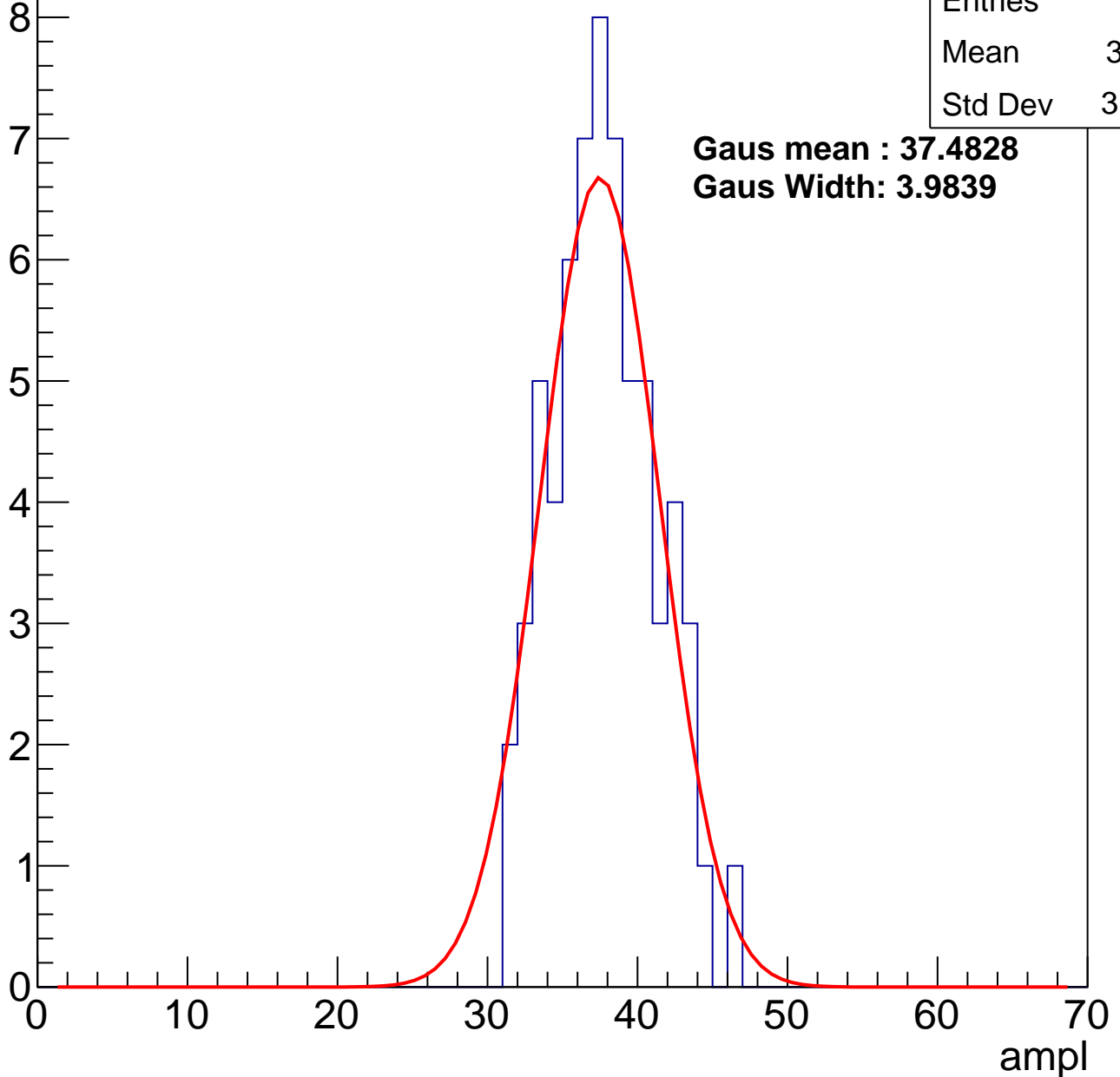
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.31
Std Dev	3.414

Gaus mean : 37.4828

Gaus Width: 3.9839



B0L001S, U18-ch26, adc2

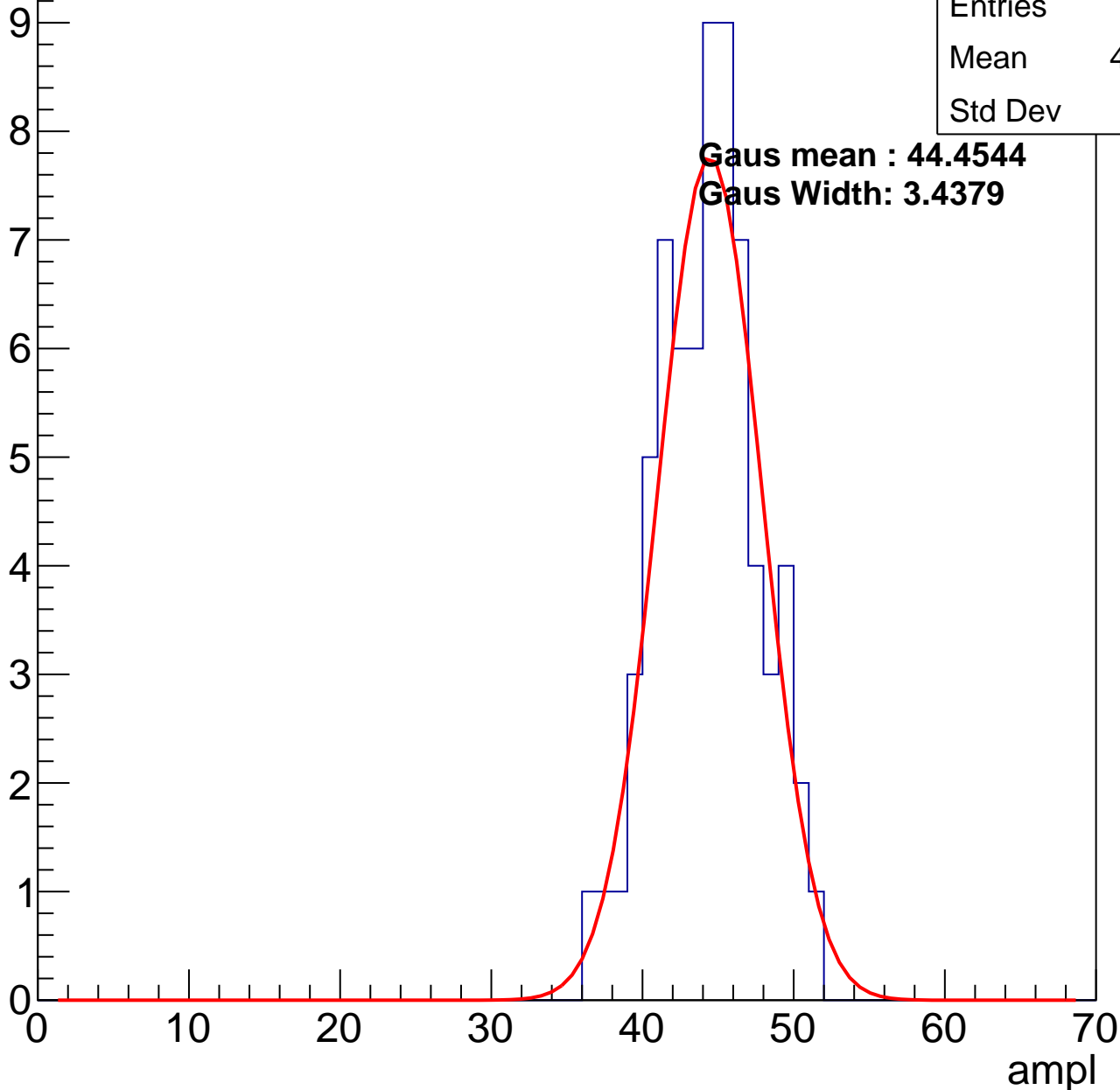
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.87
Std Dev	3.27

Gaus mean : 44.4544

Gaus Width: 3.4379

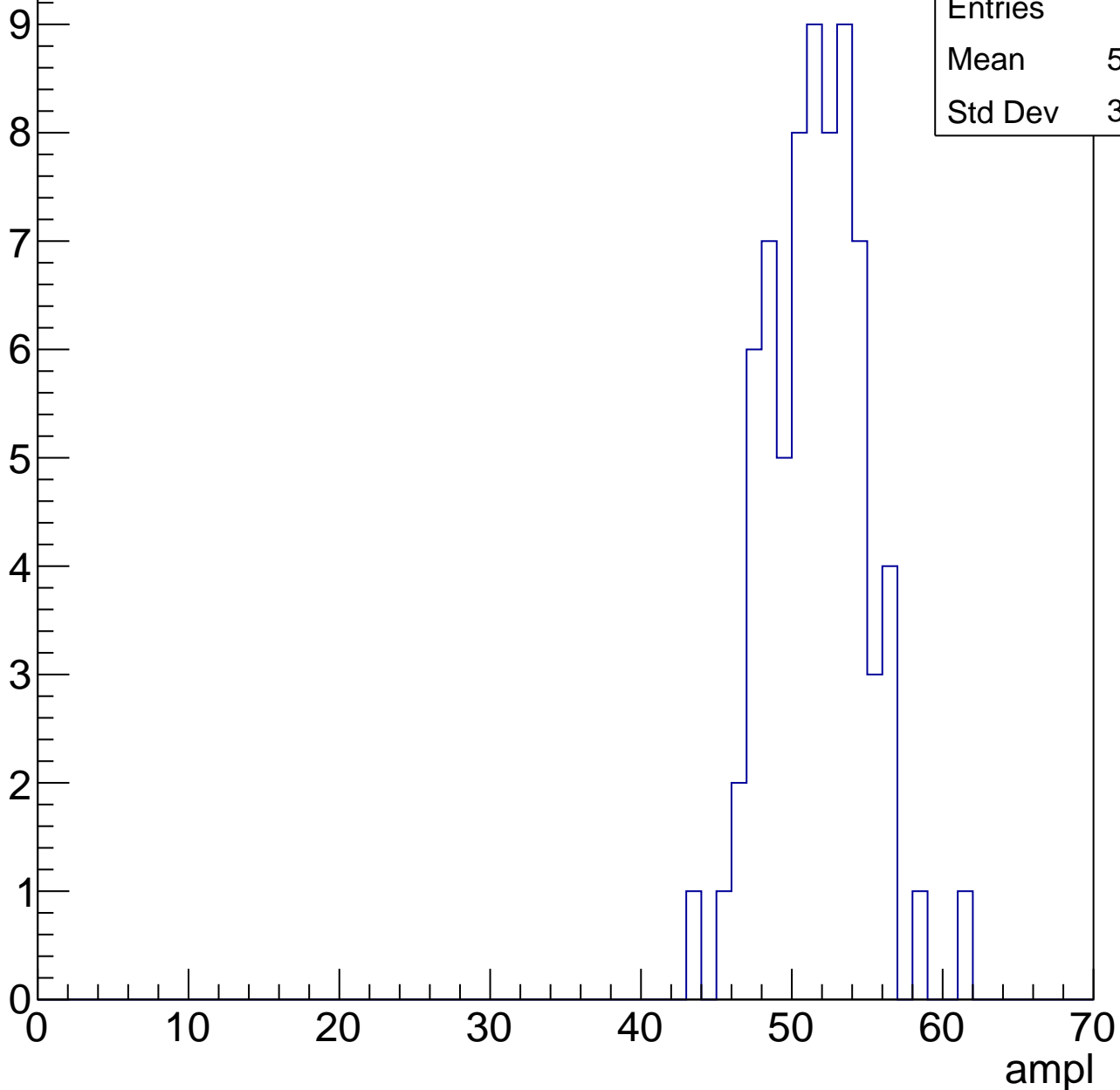


B0L001S, U18-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.12
Std Dev	3.206

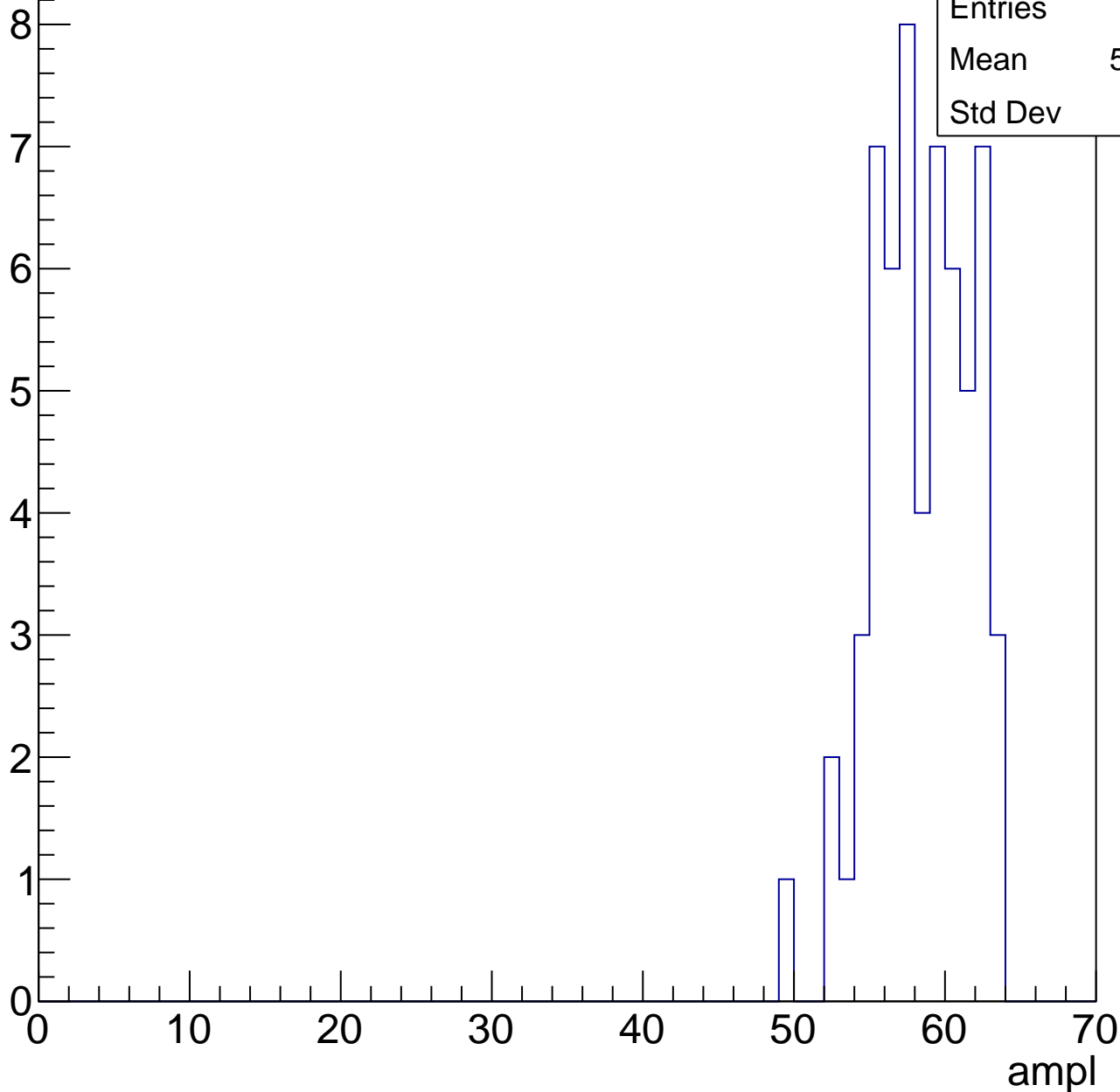


B0L001S, U18-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.97
Std Dev	3.12

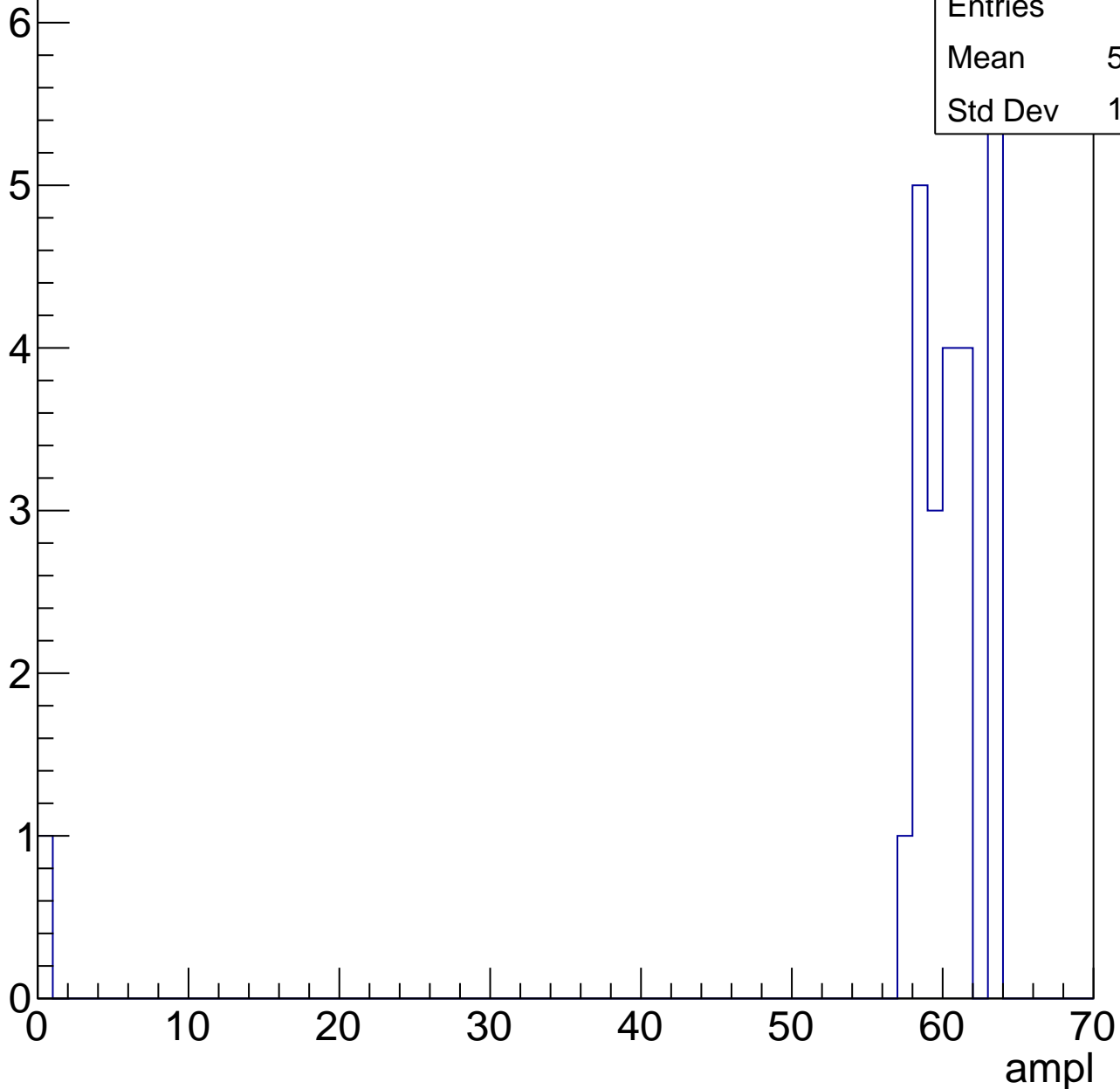


B0L001S, U18-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

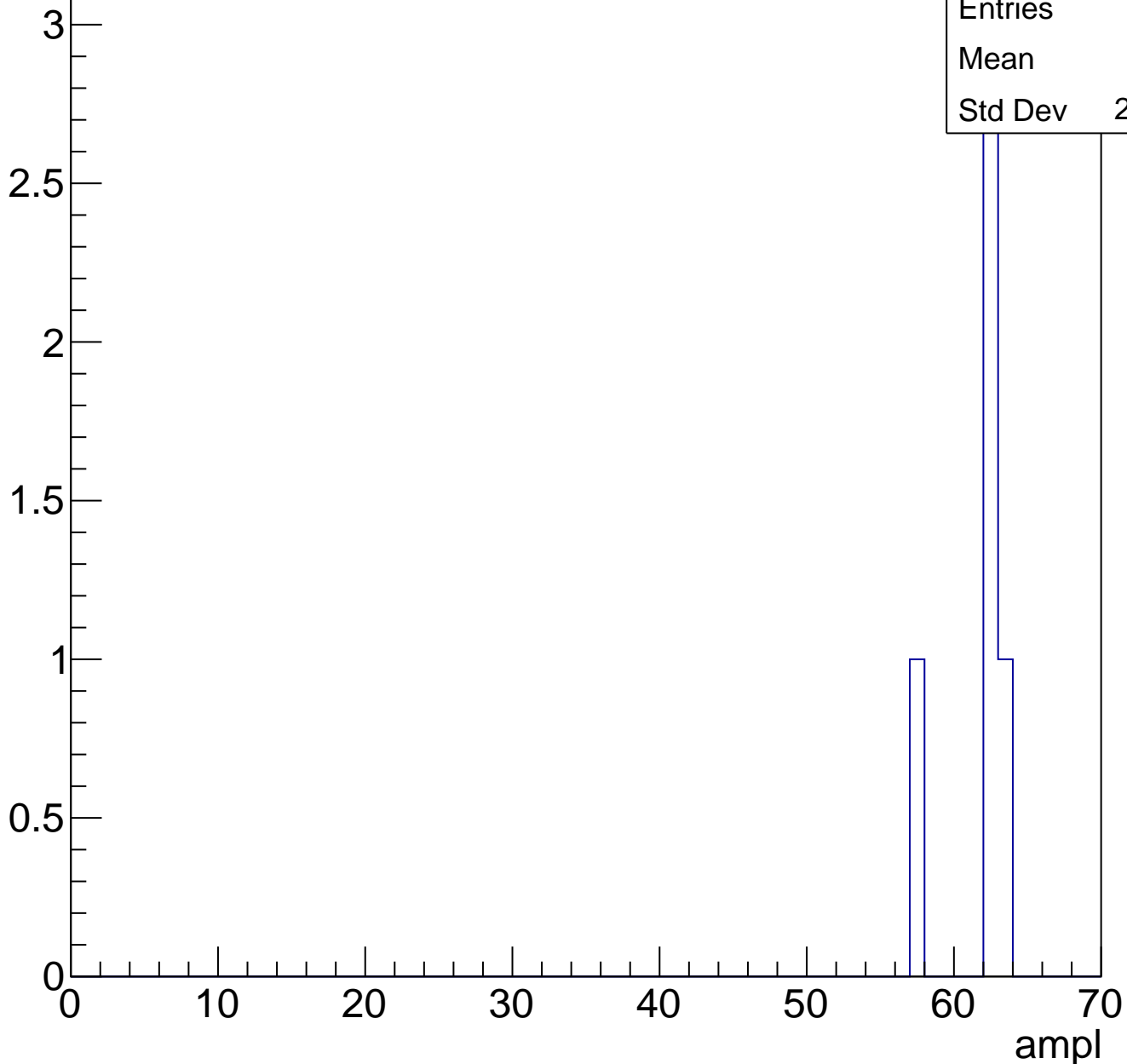
Entries	24
Mean	57.75
Std Dev	12.19



B0L001S, U18-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	61.2
Std Dev	2.135

B0L001S, U18-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch27, adc0

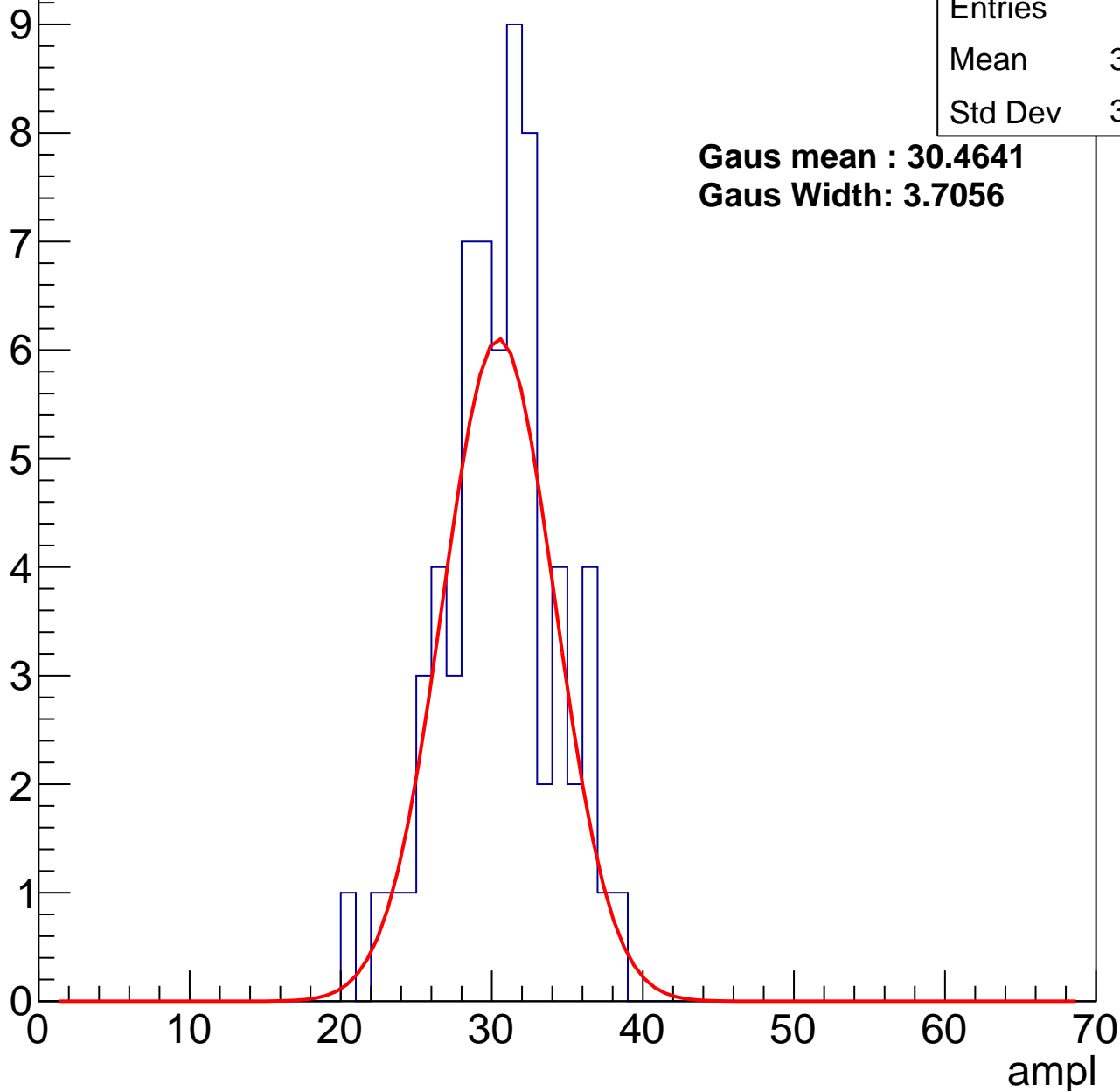
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.06
Std Dev	3.679

Gaus mean : 30.4641

Gaus Width: 3.7056



B0L001S, U18-ch27, adc1

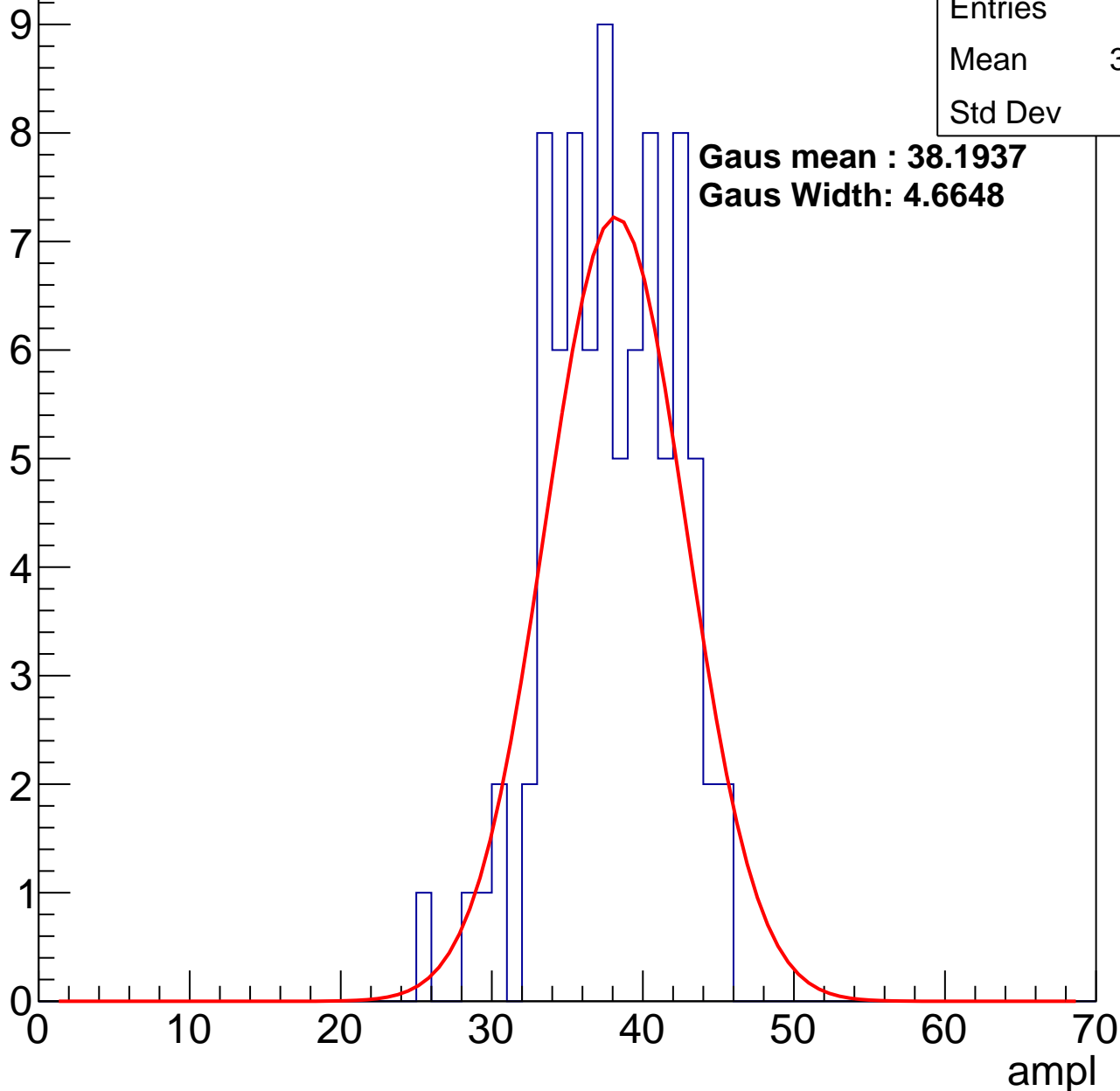
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	37.42
Std Dev	4.1

Gaus mean : 38.1937

Gaus Width: 4.6648



B0L001S, U18-ch27, adc2

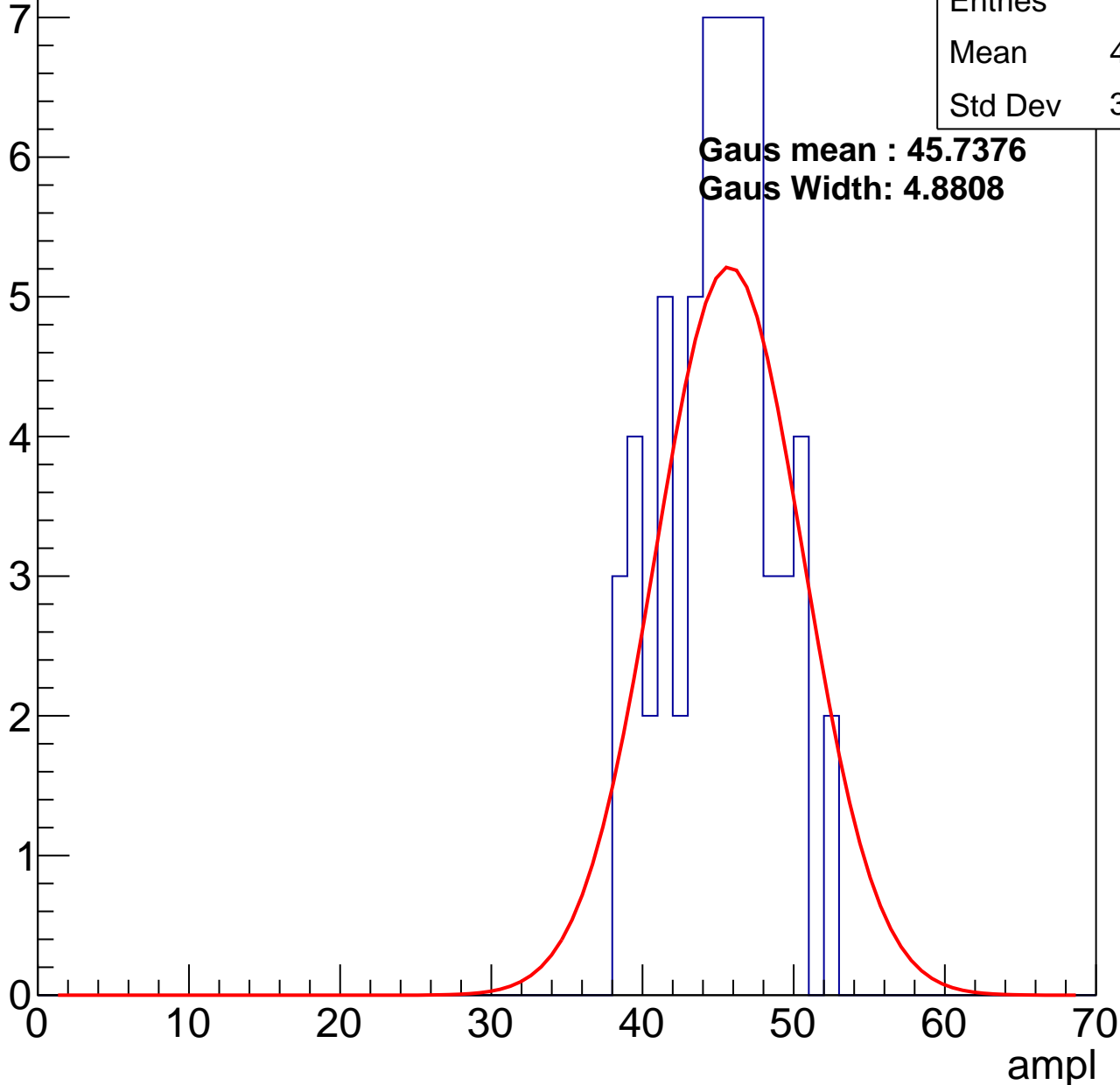
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.64
Std Dev	3.544

Gaus mean : 45.7376

Gaus Width: 4.8808

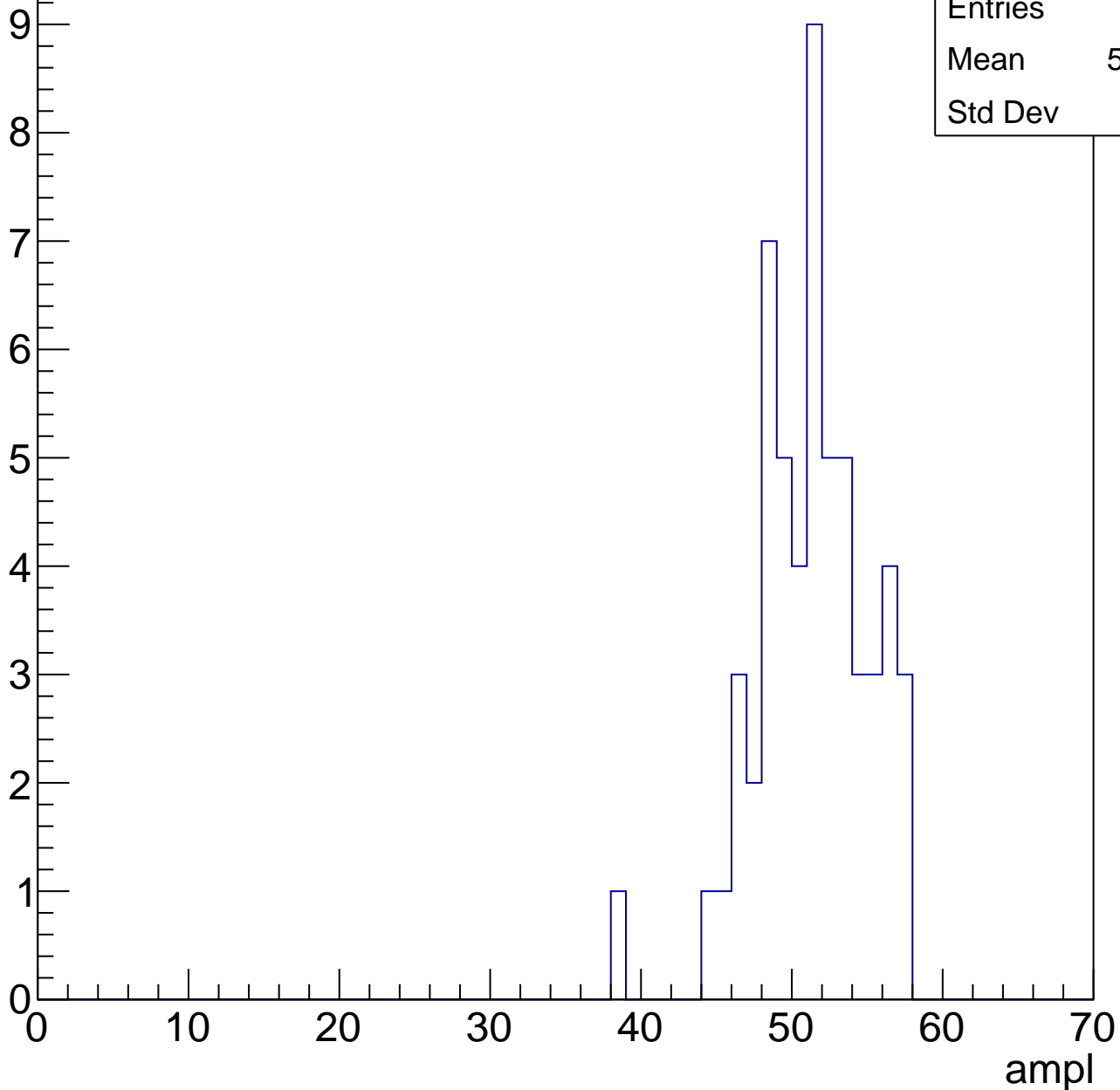


B0L001S, U18-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	50.82
Std Dev	3.67

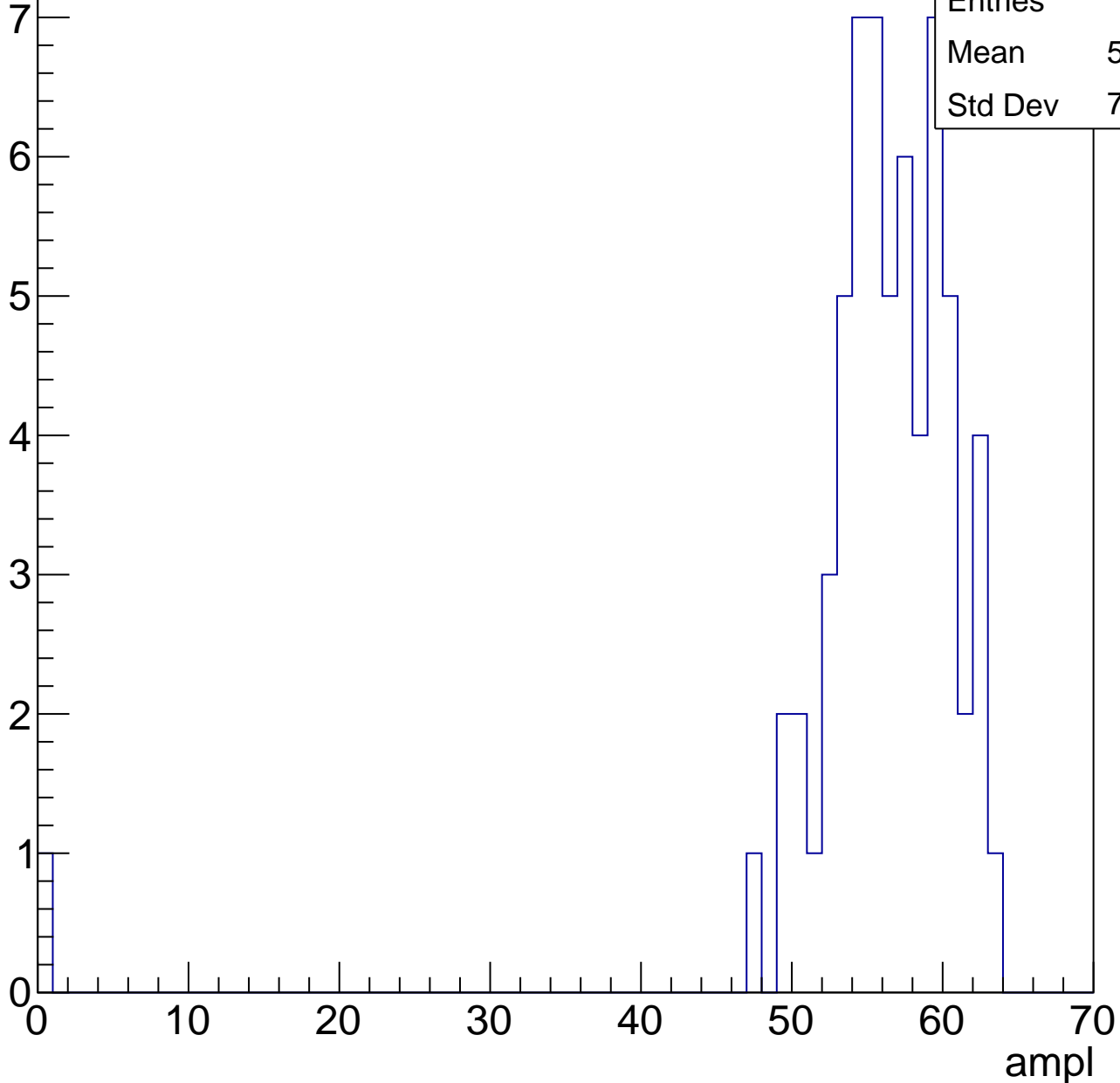


B0L001S, U18-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

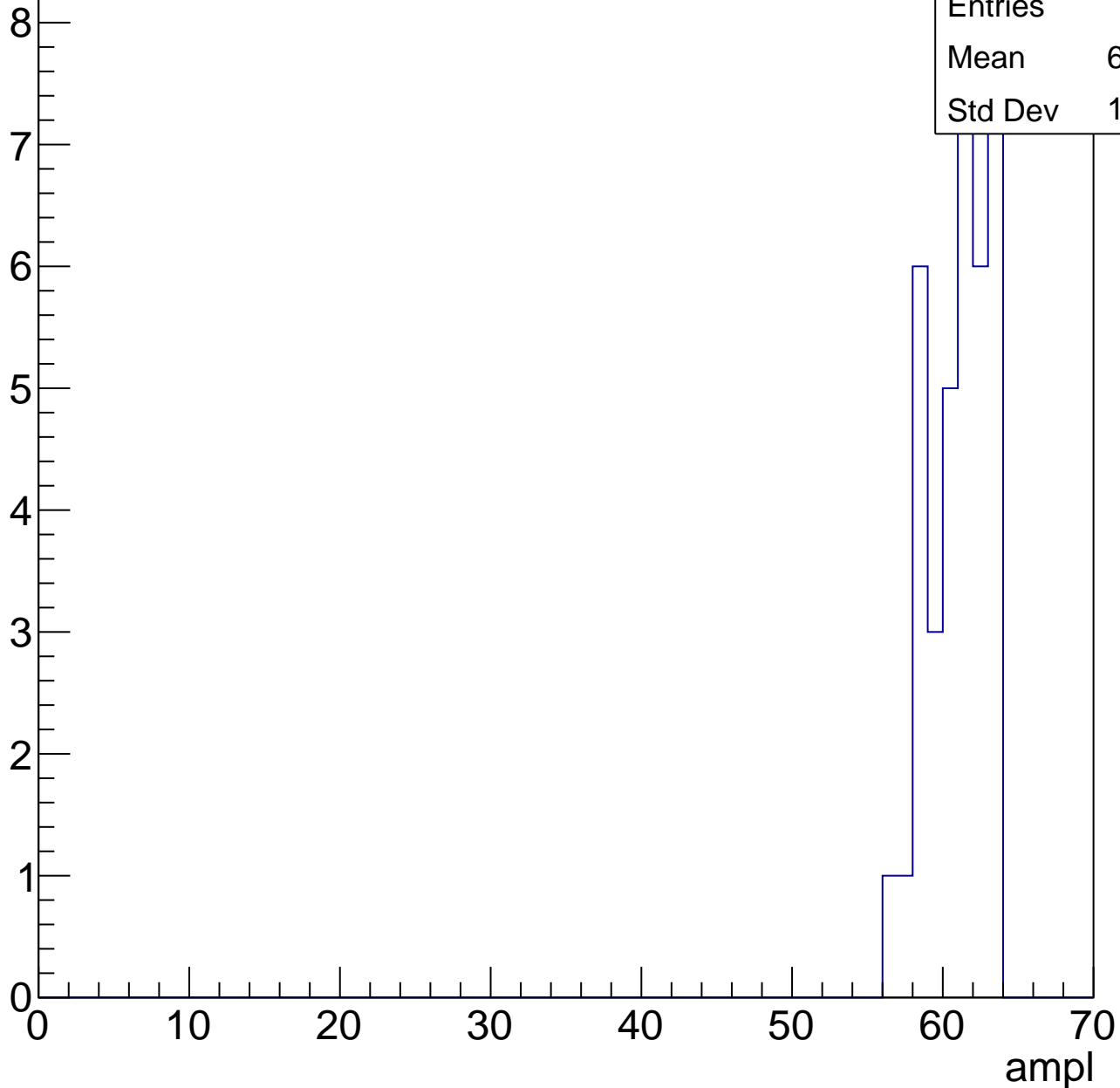
Entries	63
Mean	55.24
Std Dev	7.879



B0L001S, U18-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch28, adc0

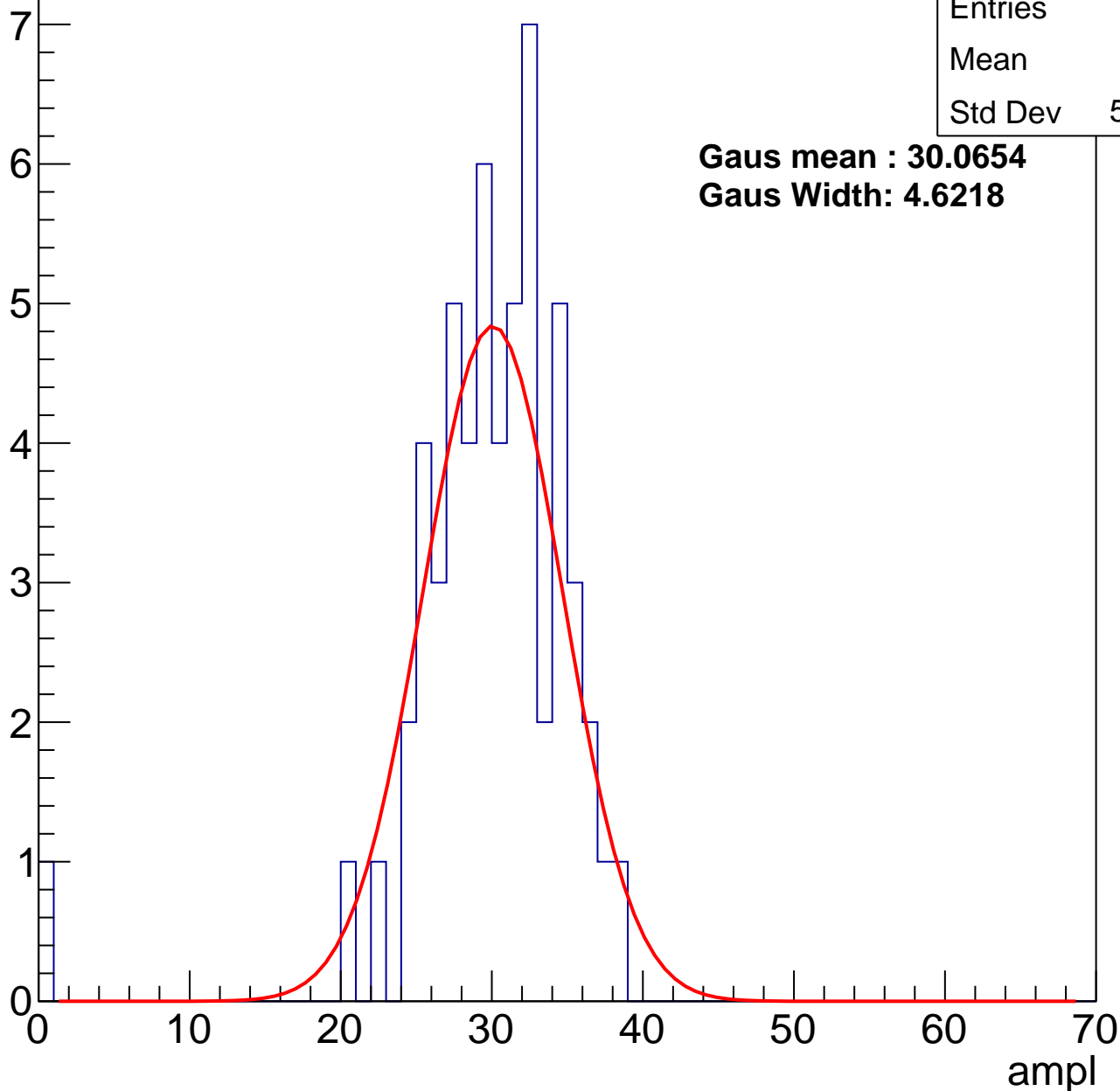
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.4
Std Dev	5.496

Gaus mean : 30.0654

Gaus Width: 4.6218



B0L001S, U18-ch28, adc1

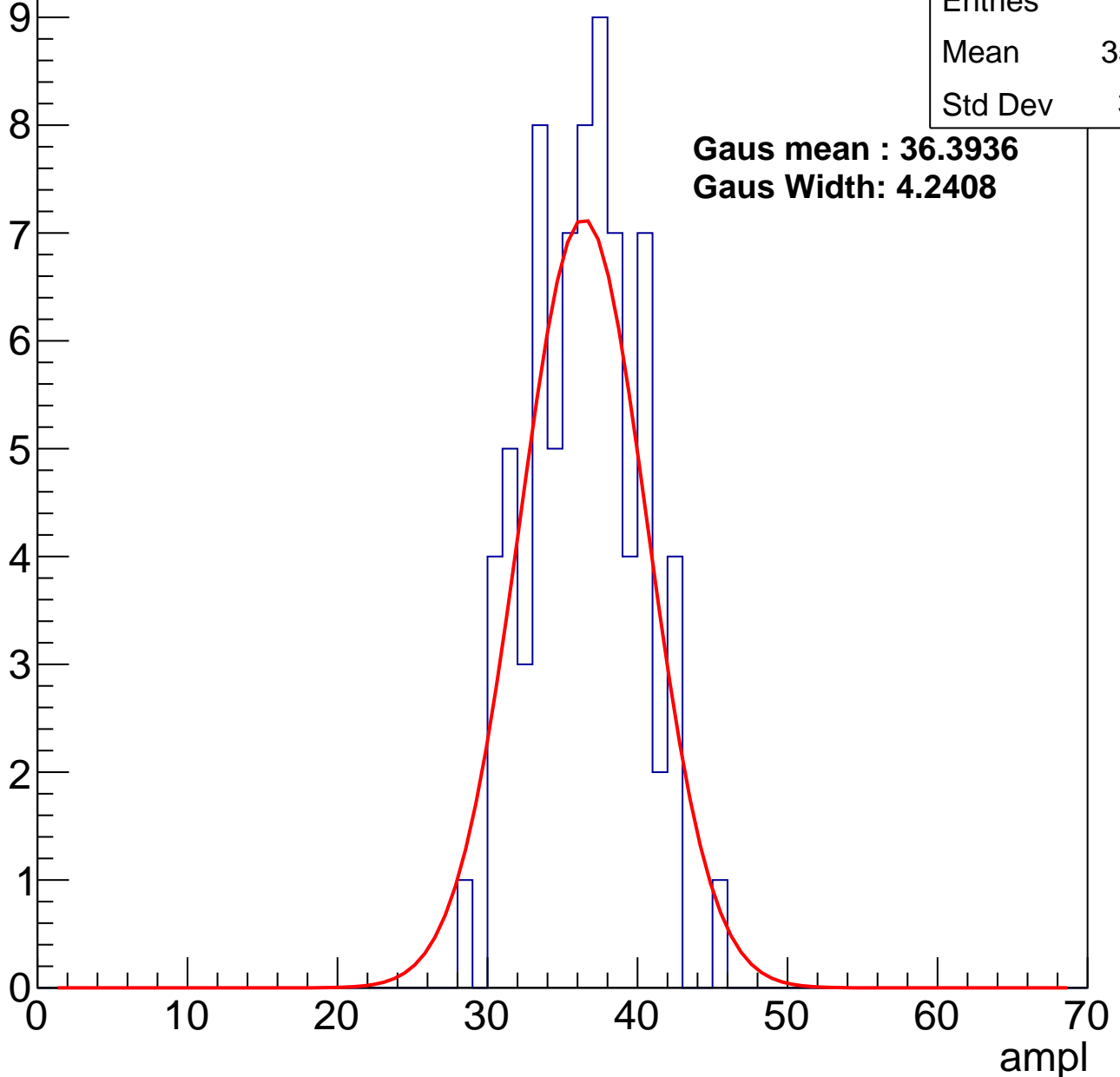
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	35.95
Std Dev	3.54

Gaus mean : 36.3936

Gaus Width: 4.2408



B0L001S, U18-ch28, adc2

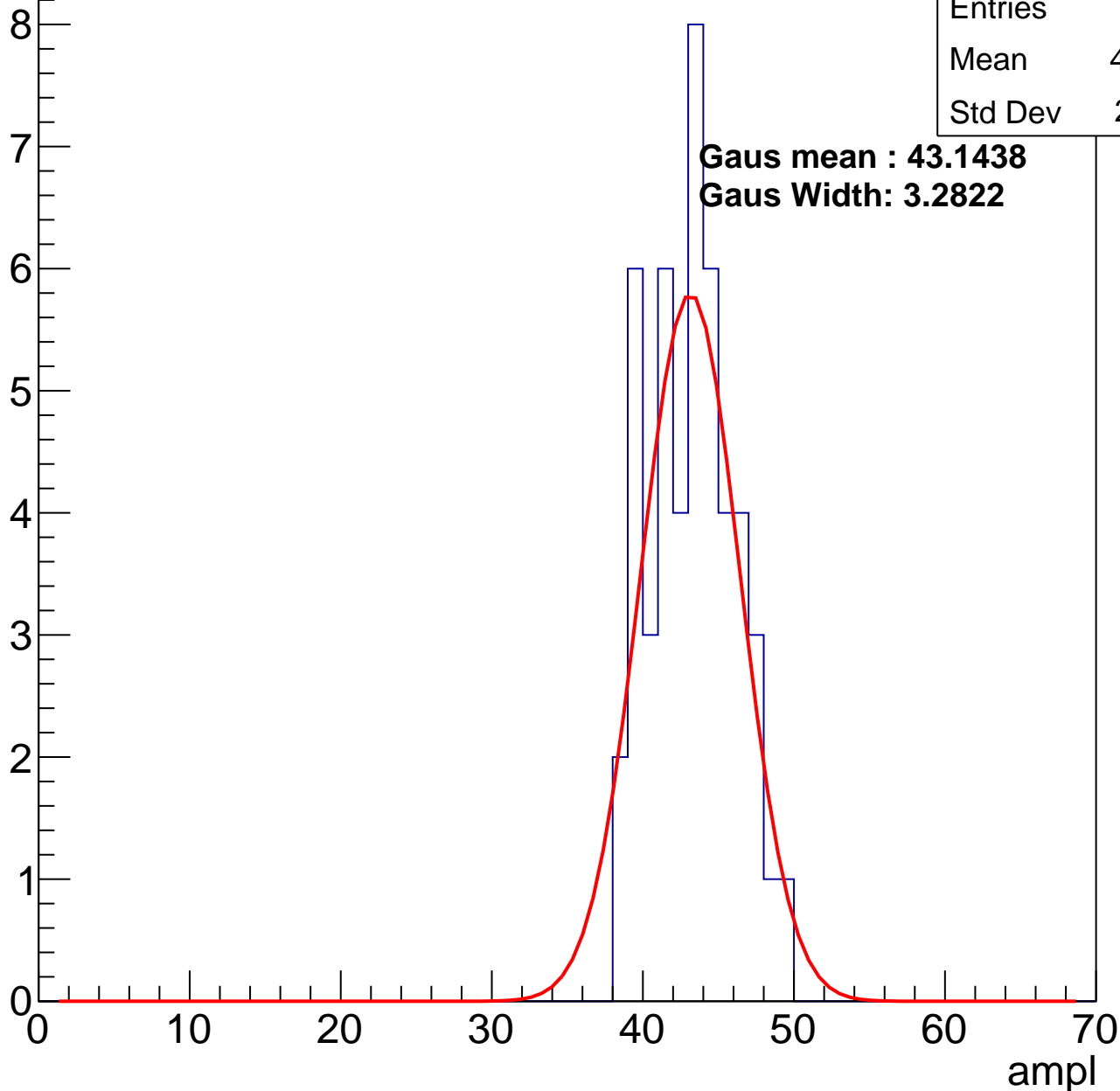
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	42.79
Std Dev	2.761

Gaus mean : 43.1438

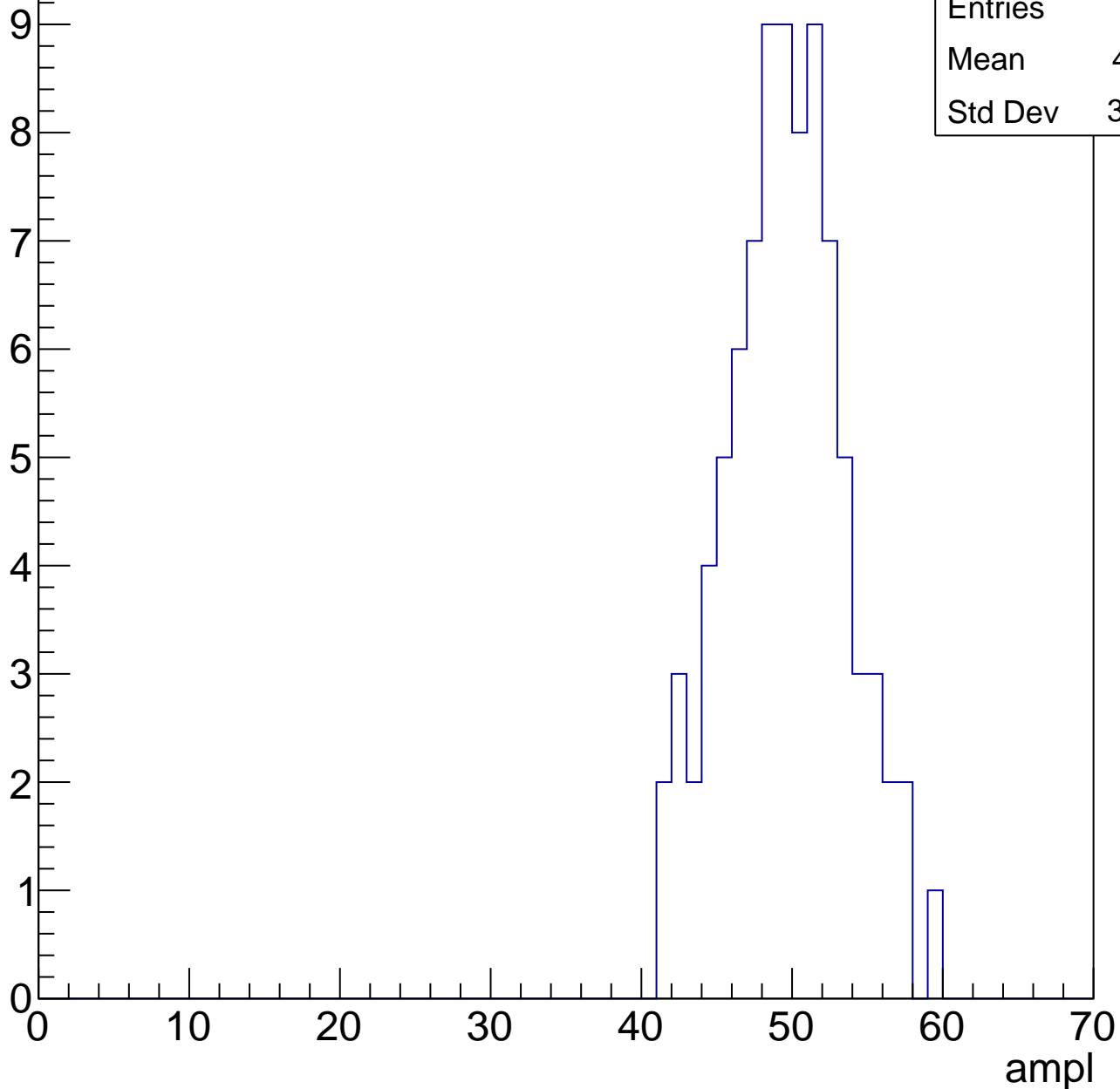
Gaus Width: 3.2822



B0L001S, U18-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

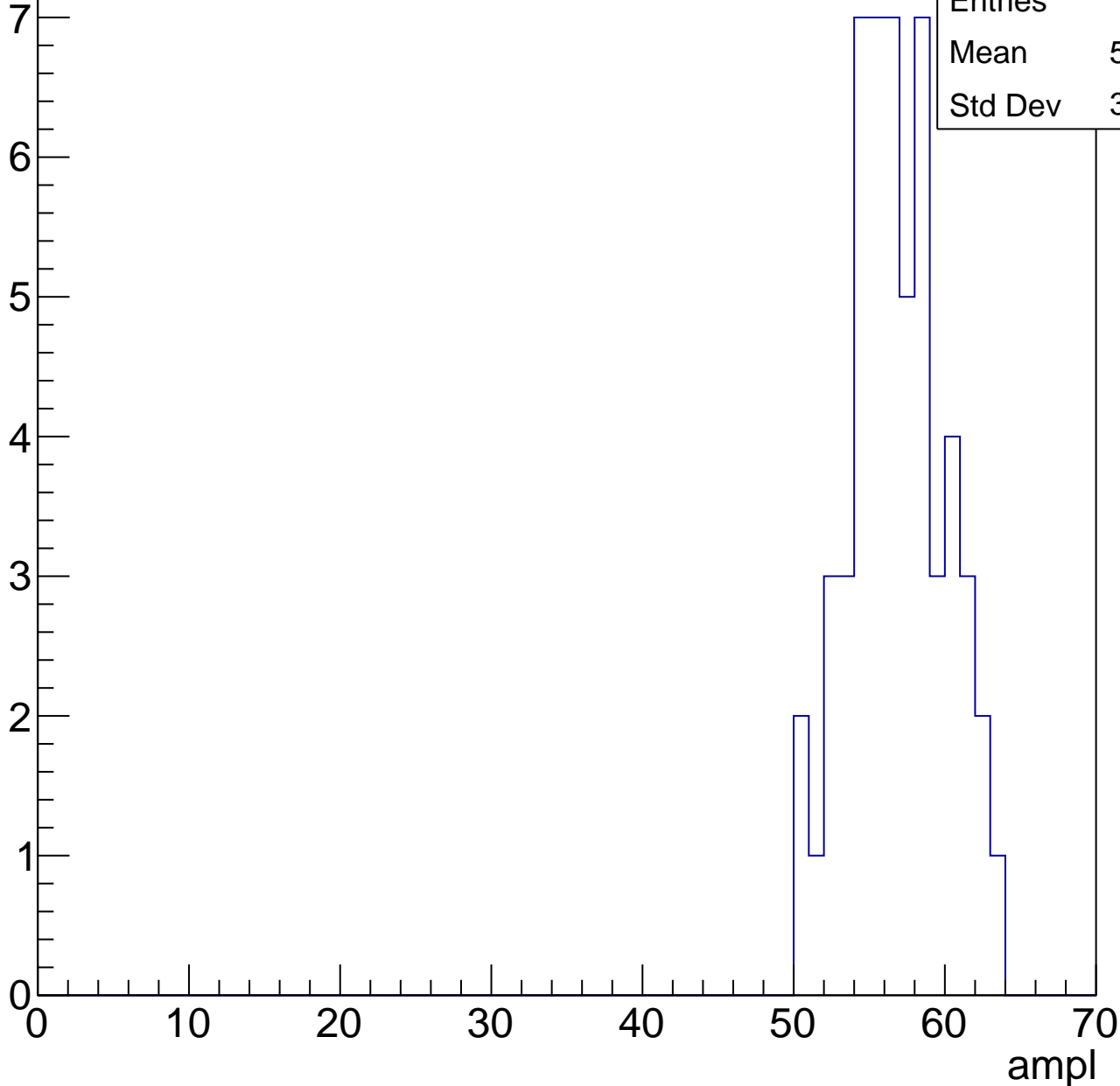


B0L001S, U18-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

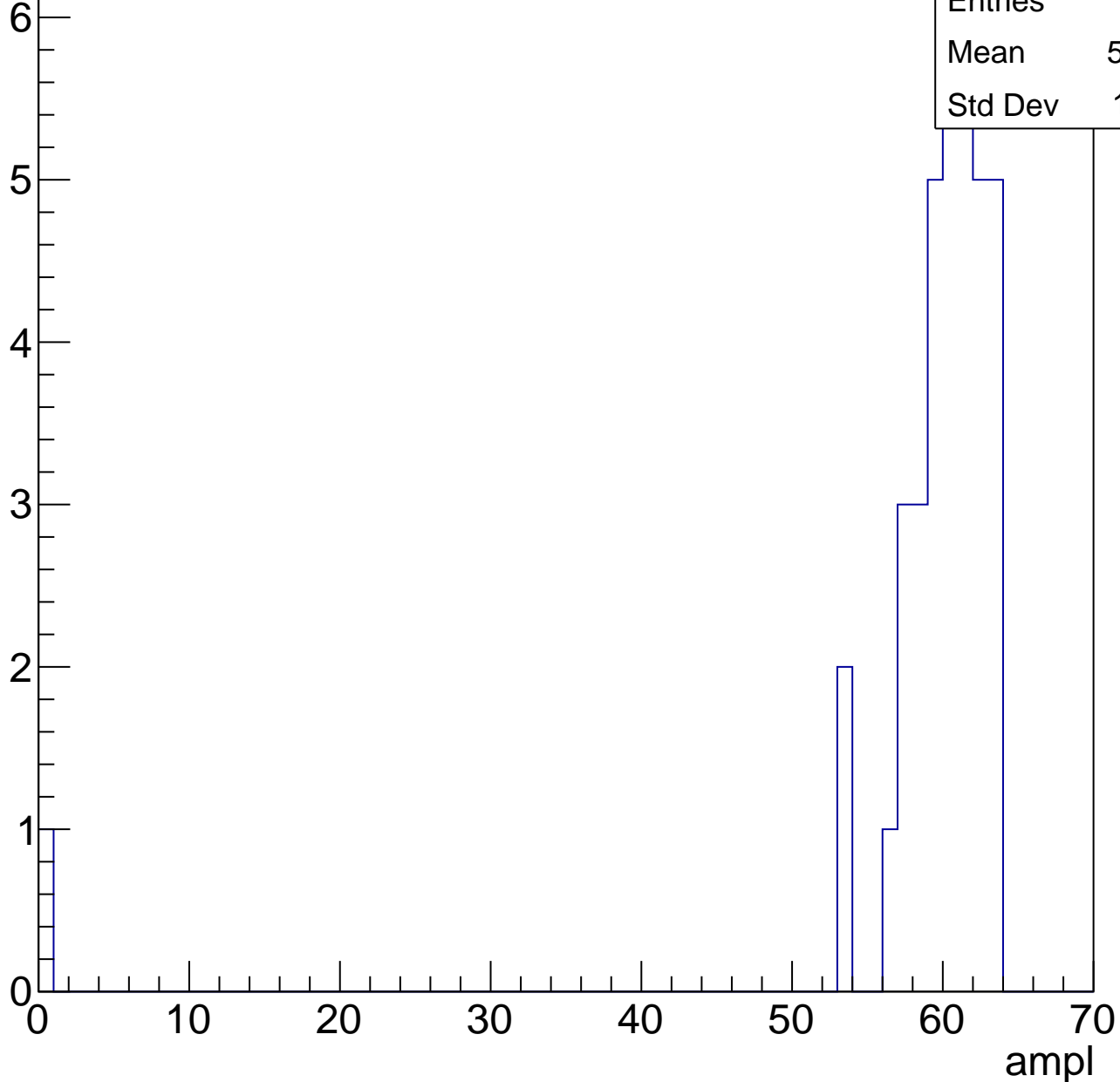
Entries	55
Mean	56.35
Std Dev	3.076



B0L001S, U18-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

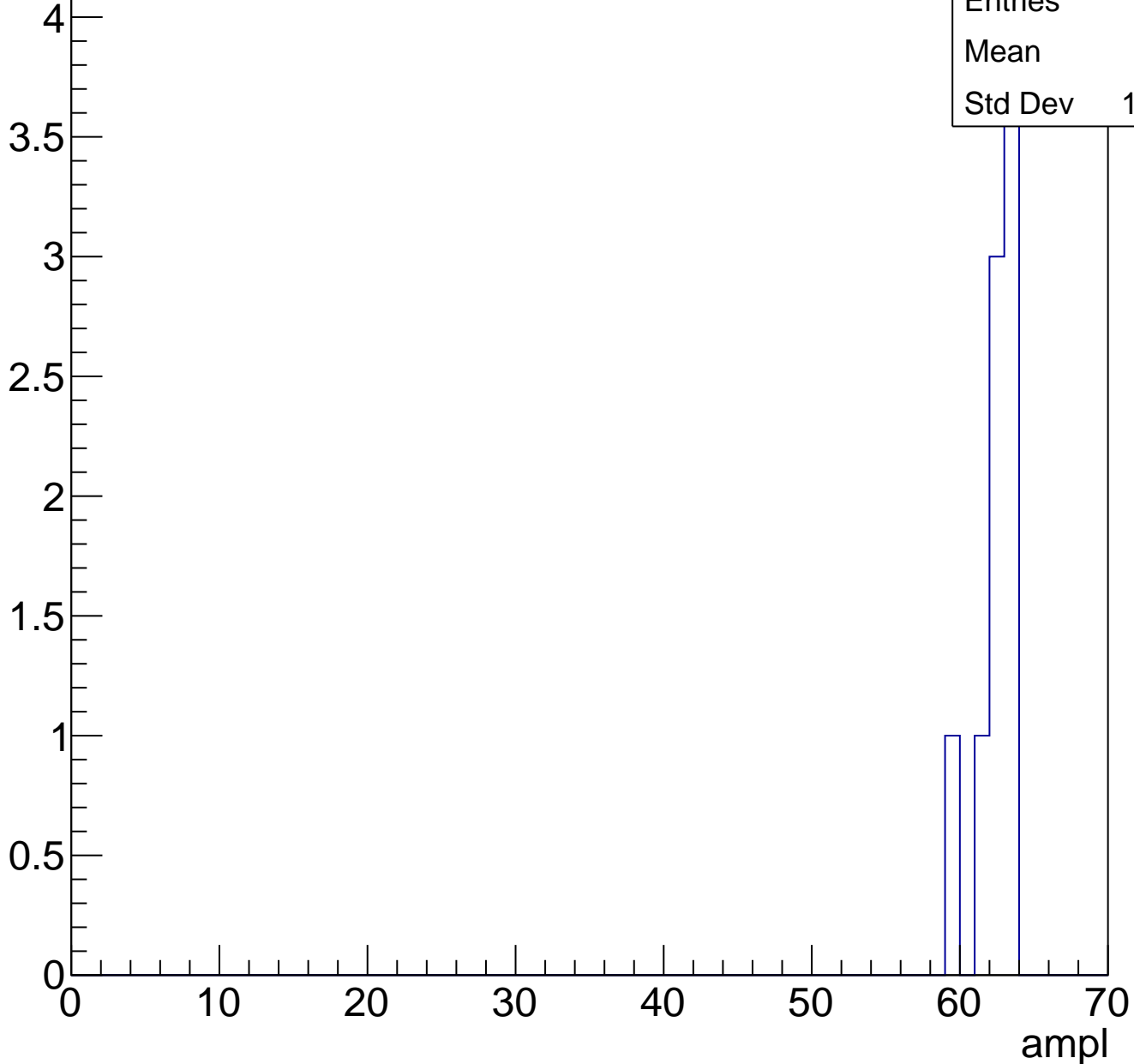
Entry



B0L001S, U18-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	9
Mean	62
Std Dev	1.247

B0L001S, U18-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch29, adc0

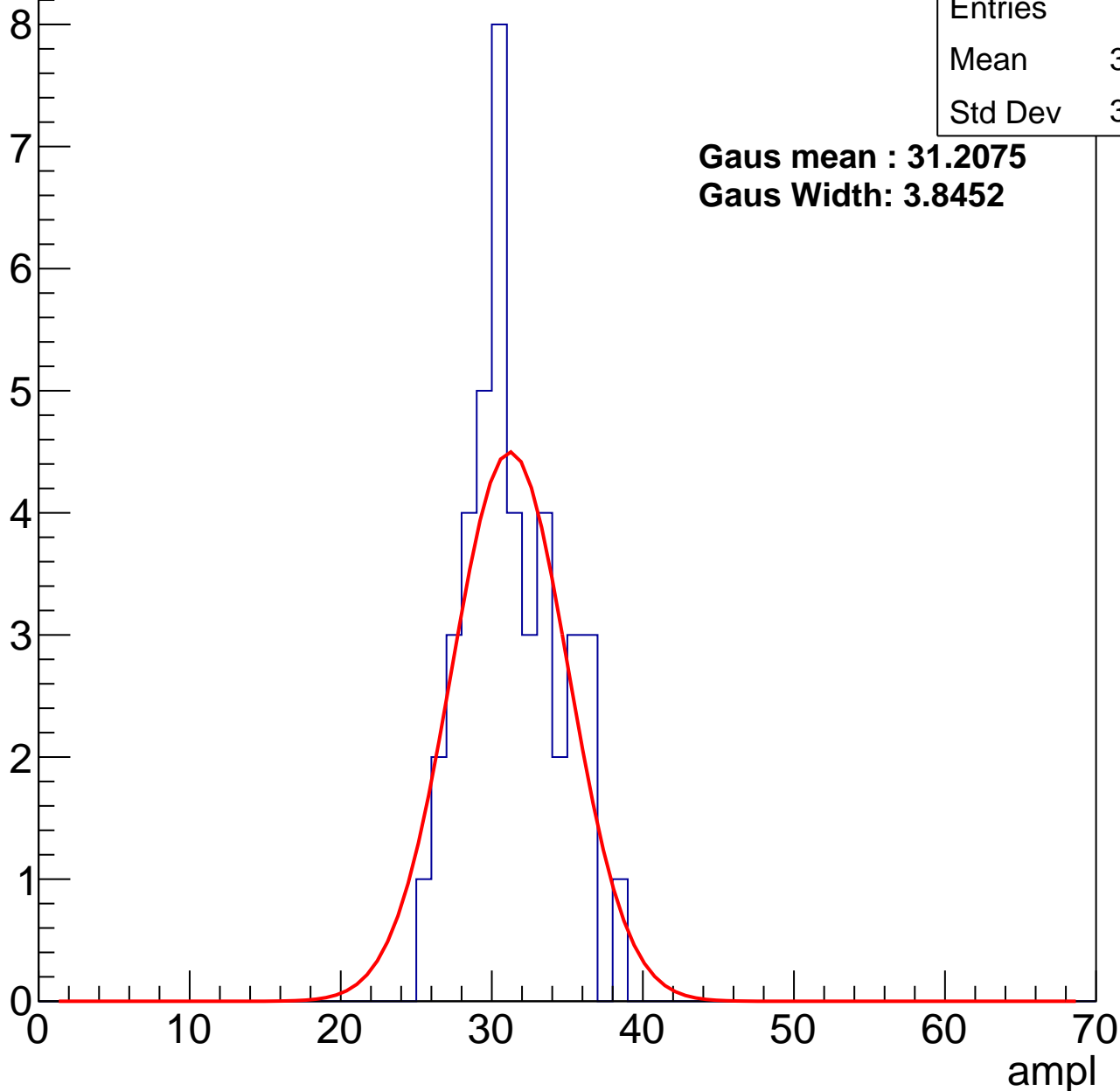
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	30.84
Std Dev	3.072

Gaus mean : 31.2075

Gaus Width: 3.8452



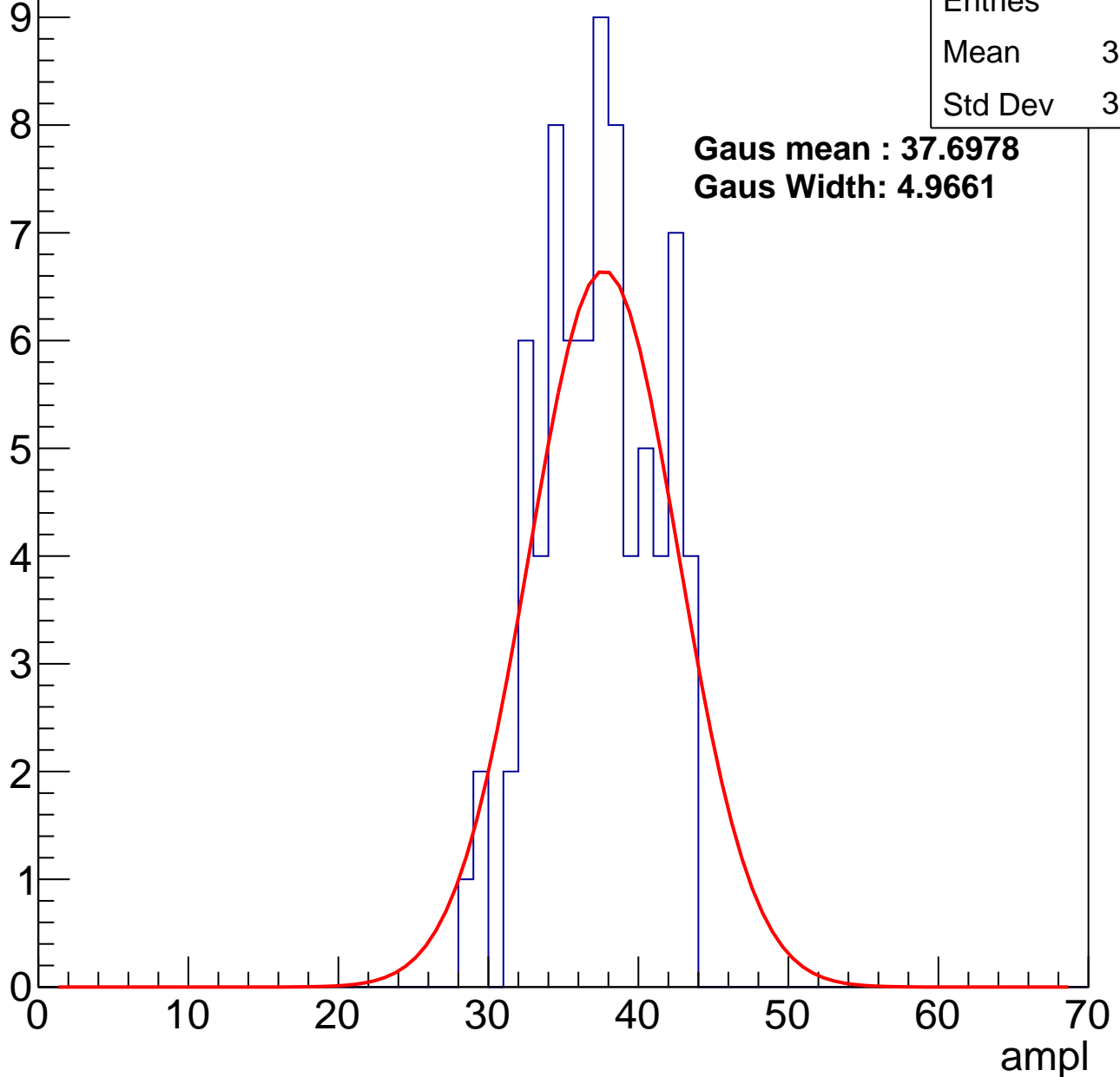
B0L001S, U18-ch29, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	36.75
Std Dev	3.707

Gaus mean : 37.6978
Gaus Width: 4.9661



B0L001S, U18-ch29, adc2

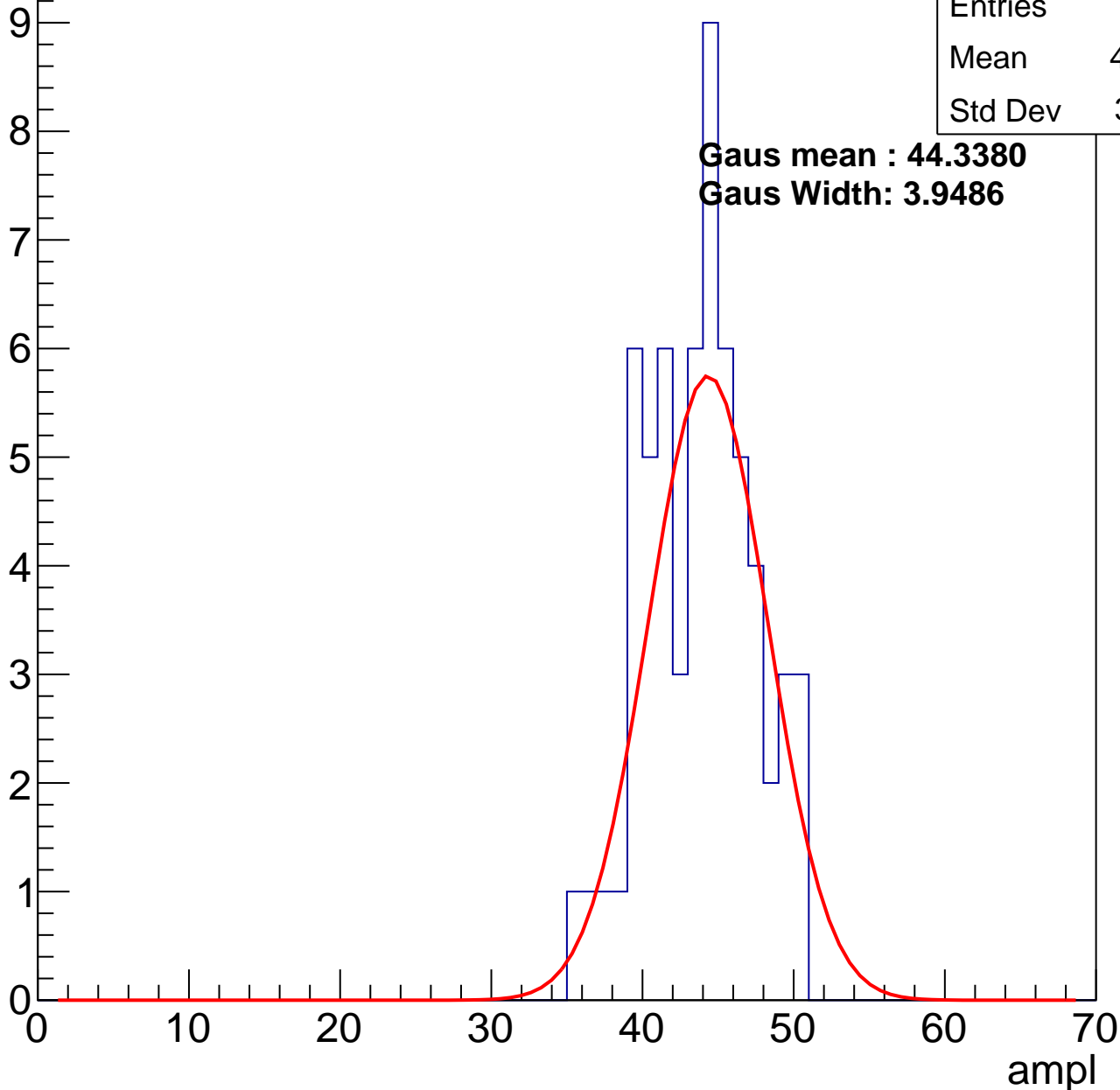
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.34
Std Dev	3.551

Gaus mean : 44.3380

Gaus Width: 3.9486

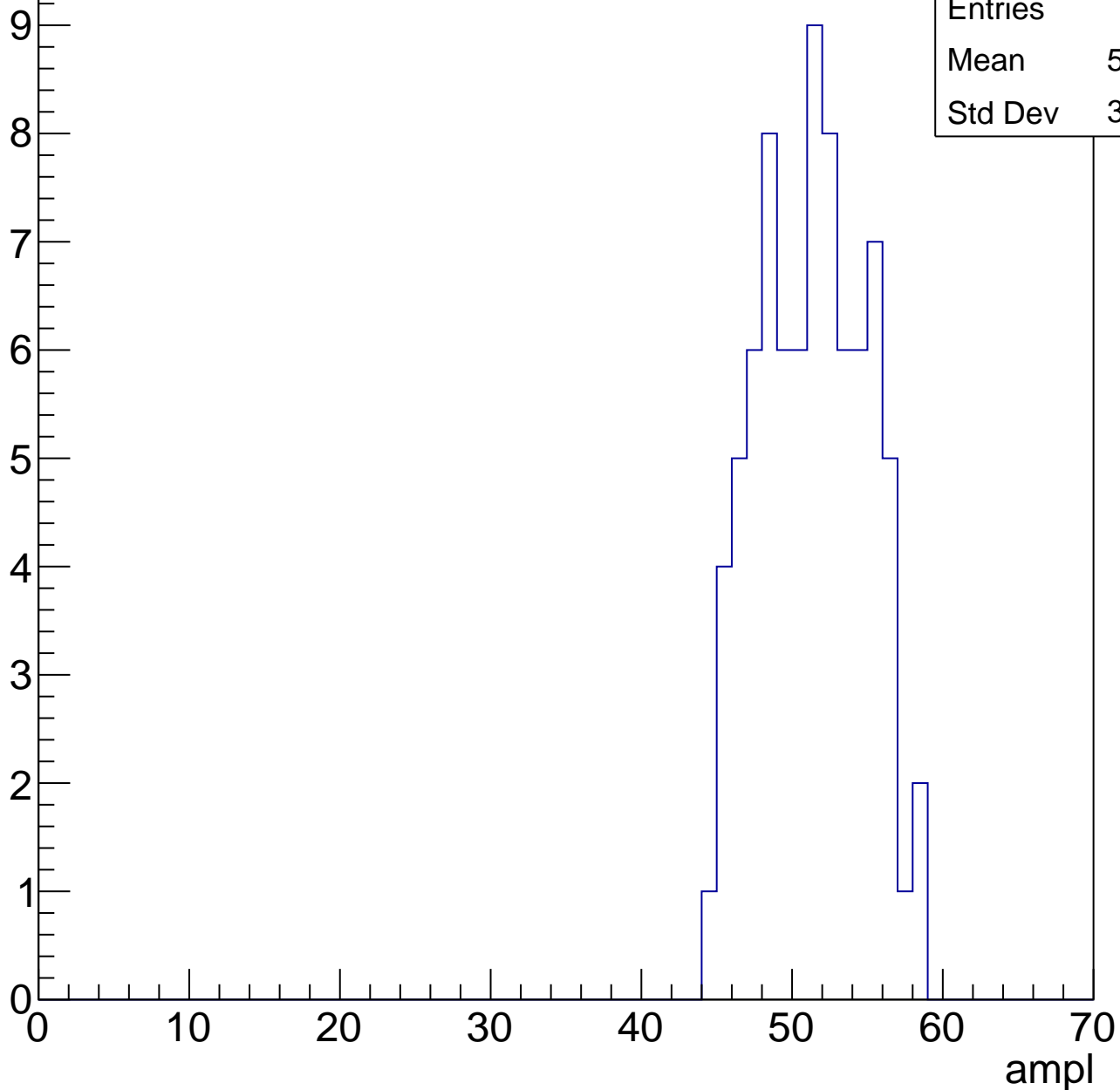


B0L001S, U18-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

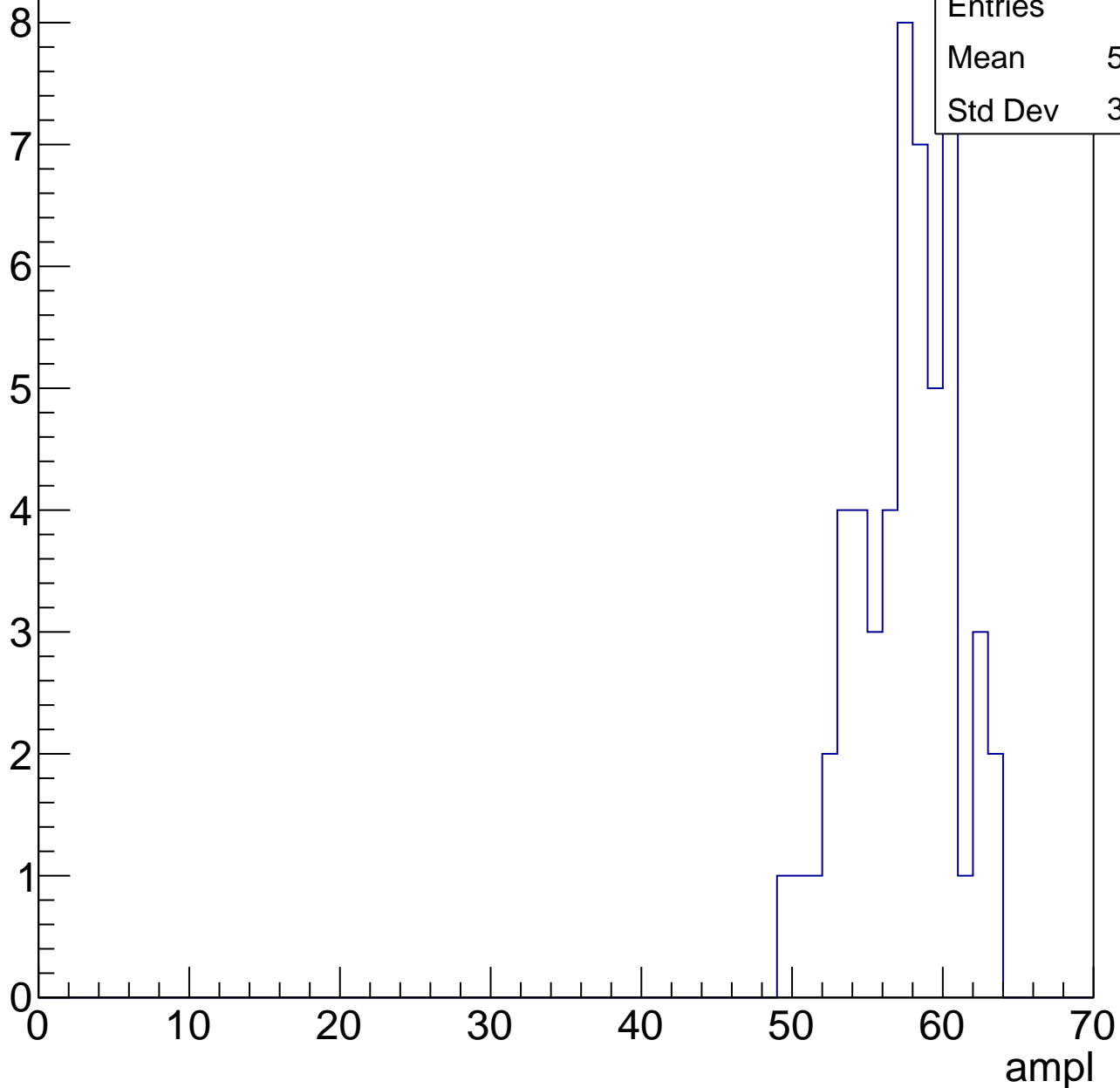
Entries	80
Mean	50.86
Std Dev	3.485



B0L001S, U18-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



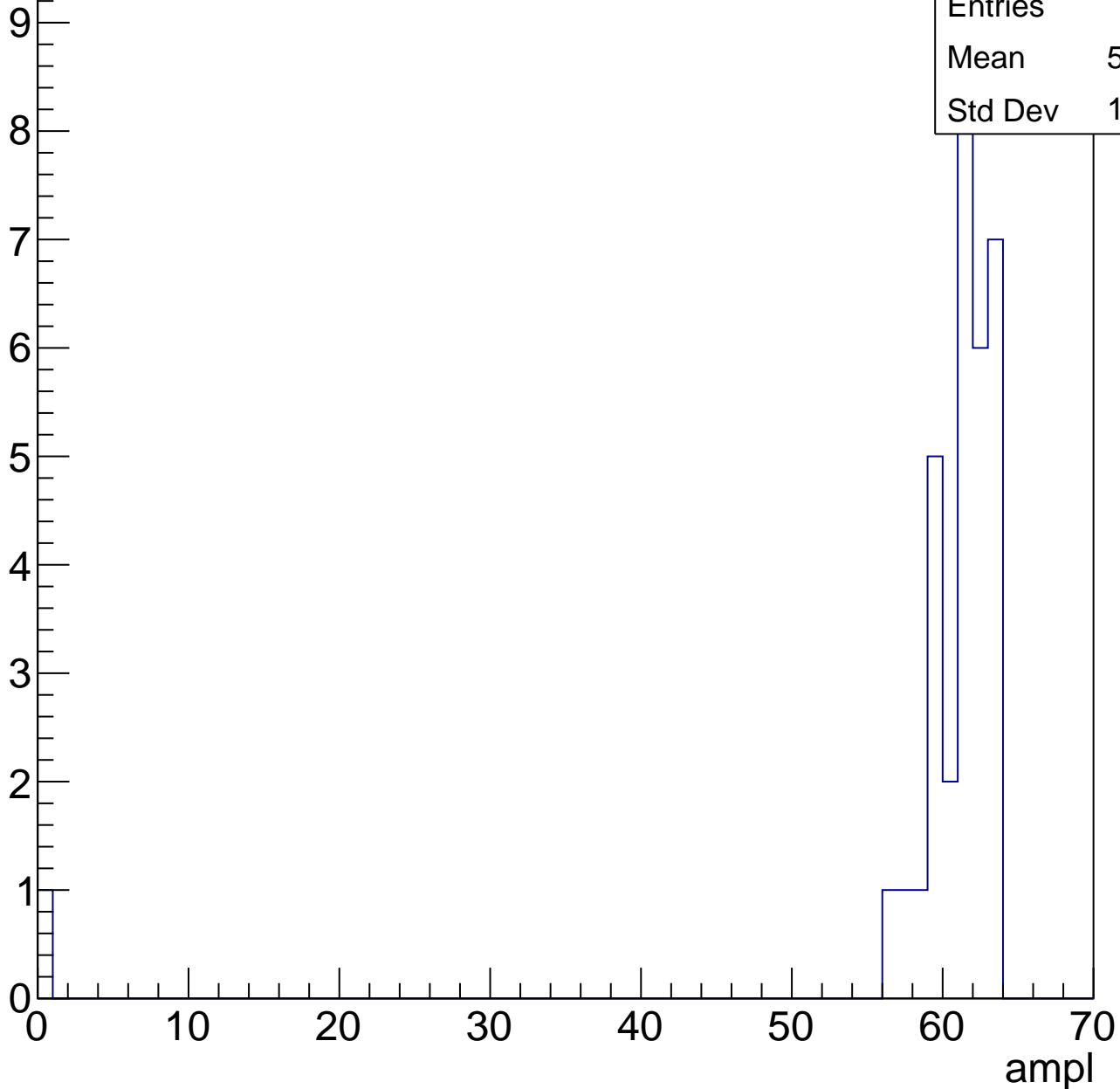
Entries	54
Mean	57.06
Std Dev	3.268

B0L001S, U18-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.03
Std Dev	10.59



B0L001S, U18-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch30, adc0

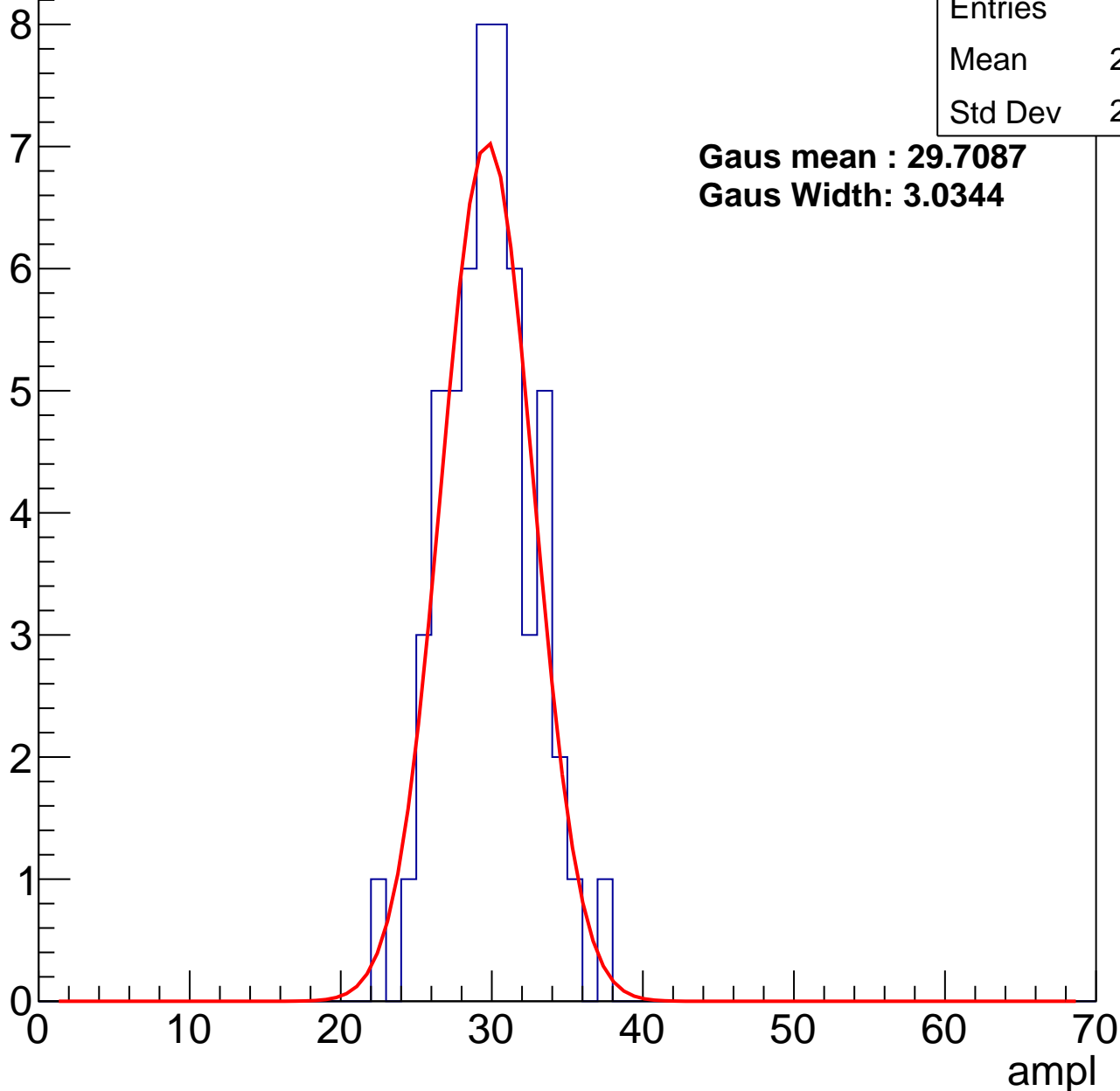
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.33
Std Dev	2.942

Gaus mean : 29.7087

Gaus Width: 3.0344



B0L001S, U18-ch30, adc1

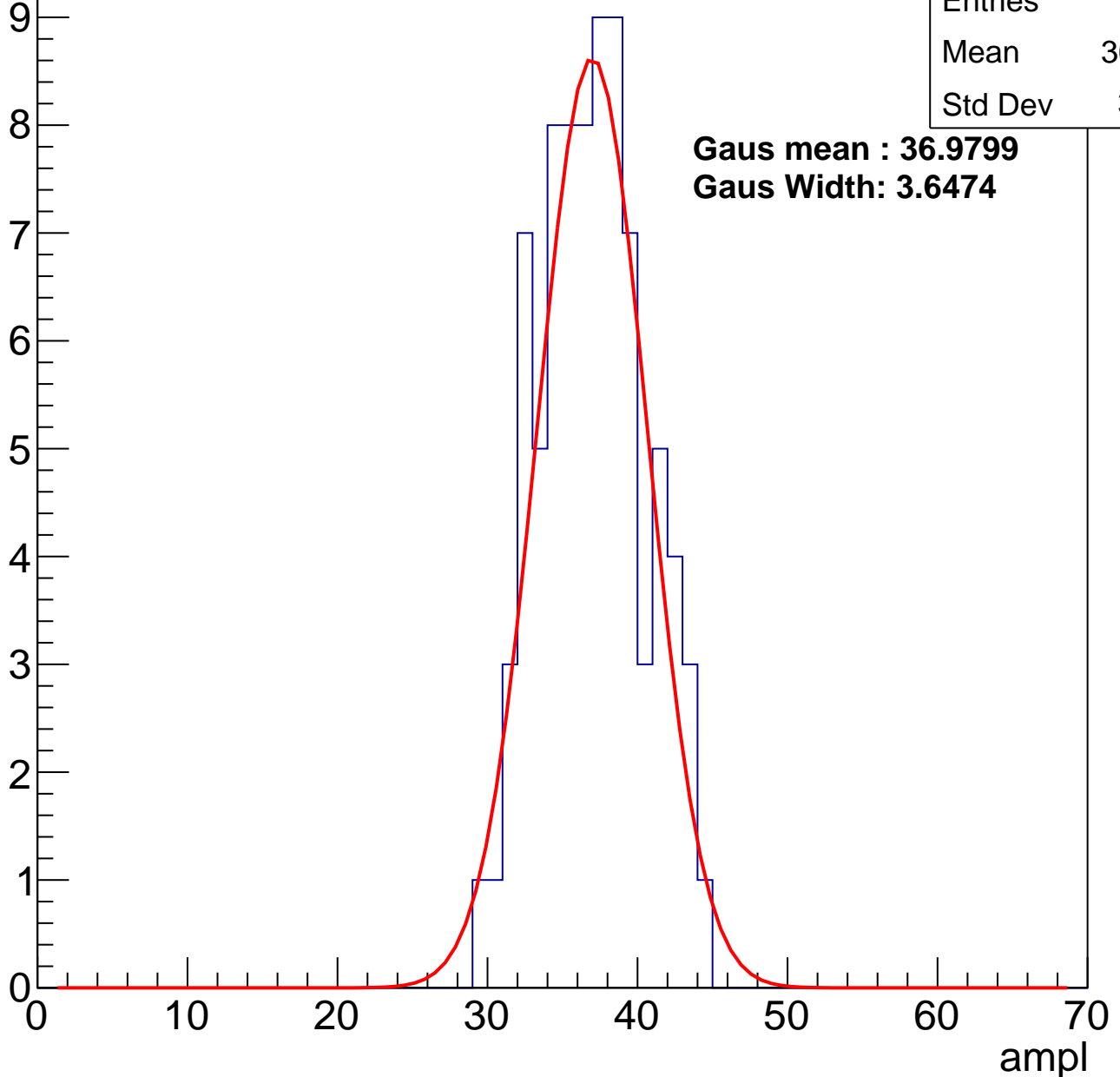
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	36.52
Std Dev	3.44

Gaus mean : 36.9799

Gaus Width: 3.6474



B0L001S, U18-ch30, adc2

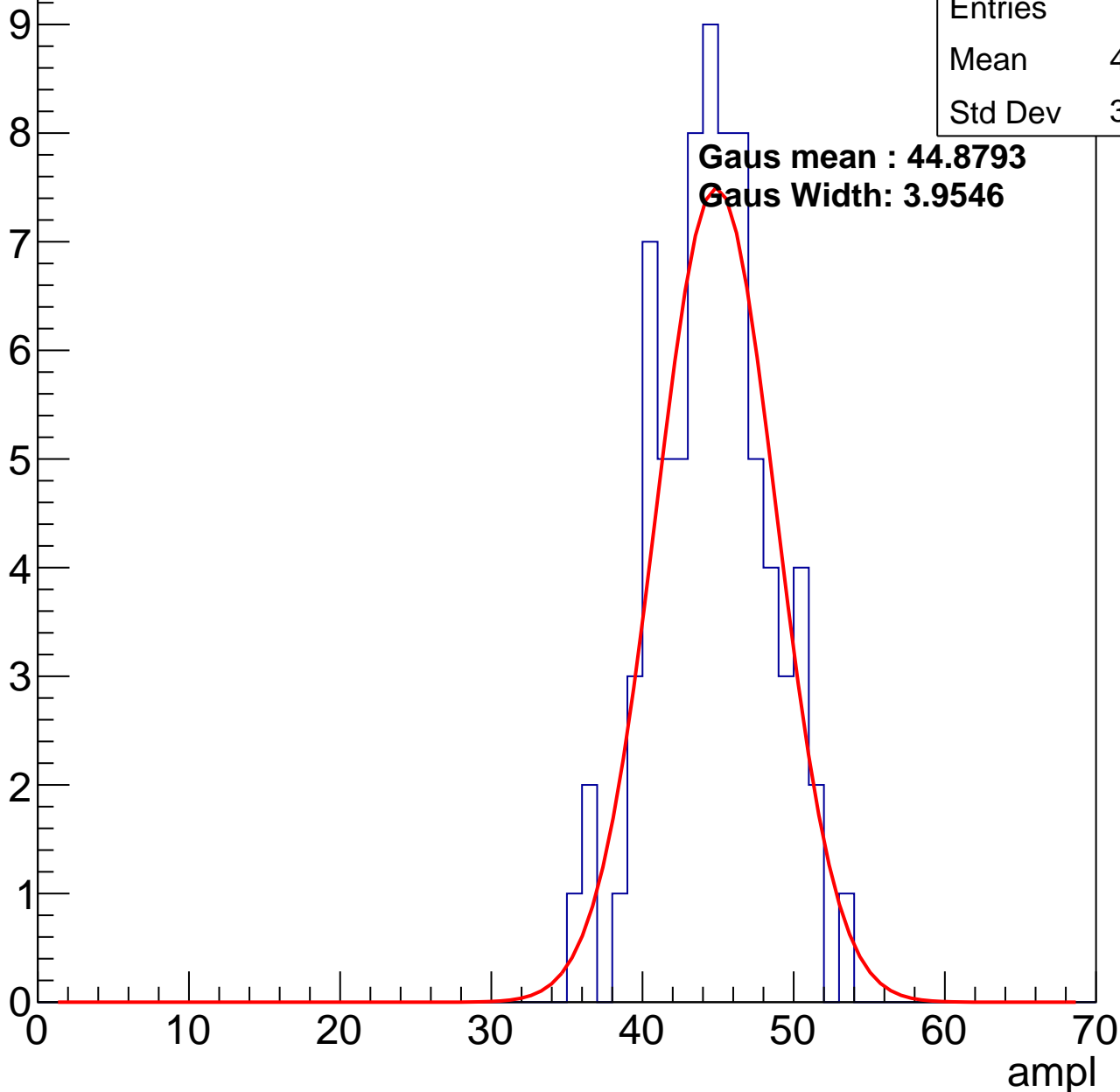
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.13
Std Dev	3.718

Gaus mean : 44.8793

Gaus Width: 3.9546



B0L001S, U18-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

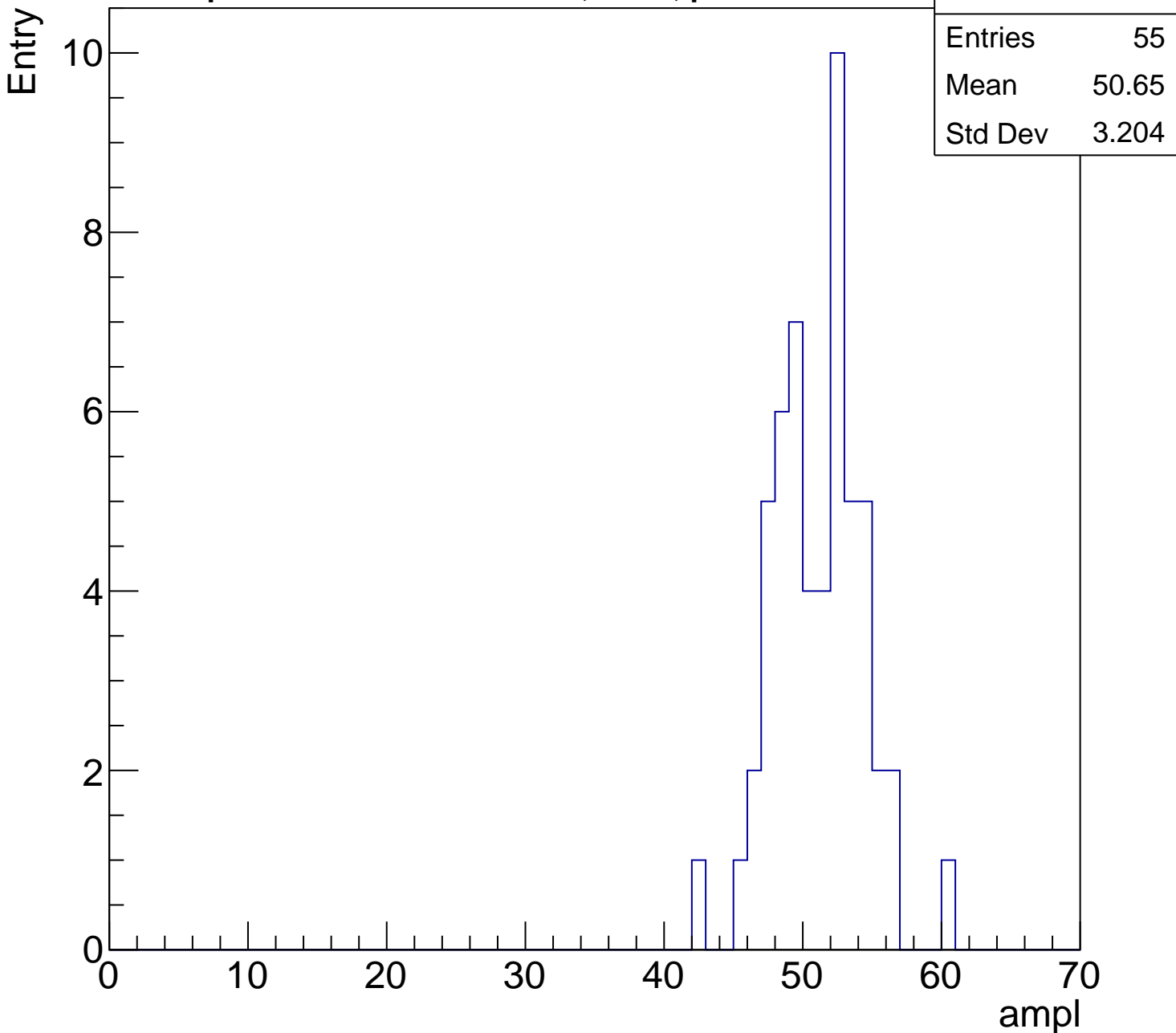
Entries	55
Mean	50.65
Std Dev	3.204

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

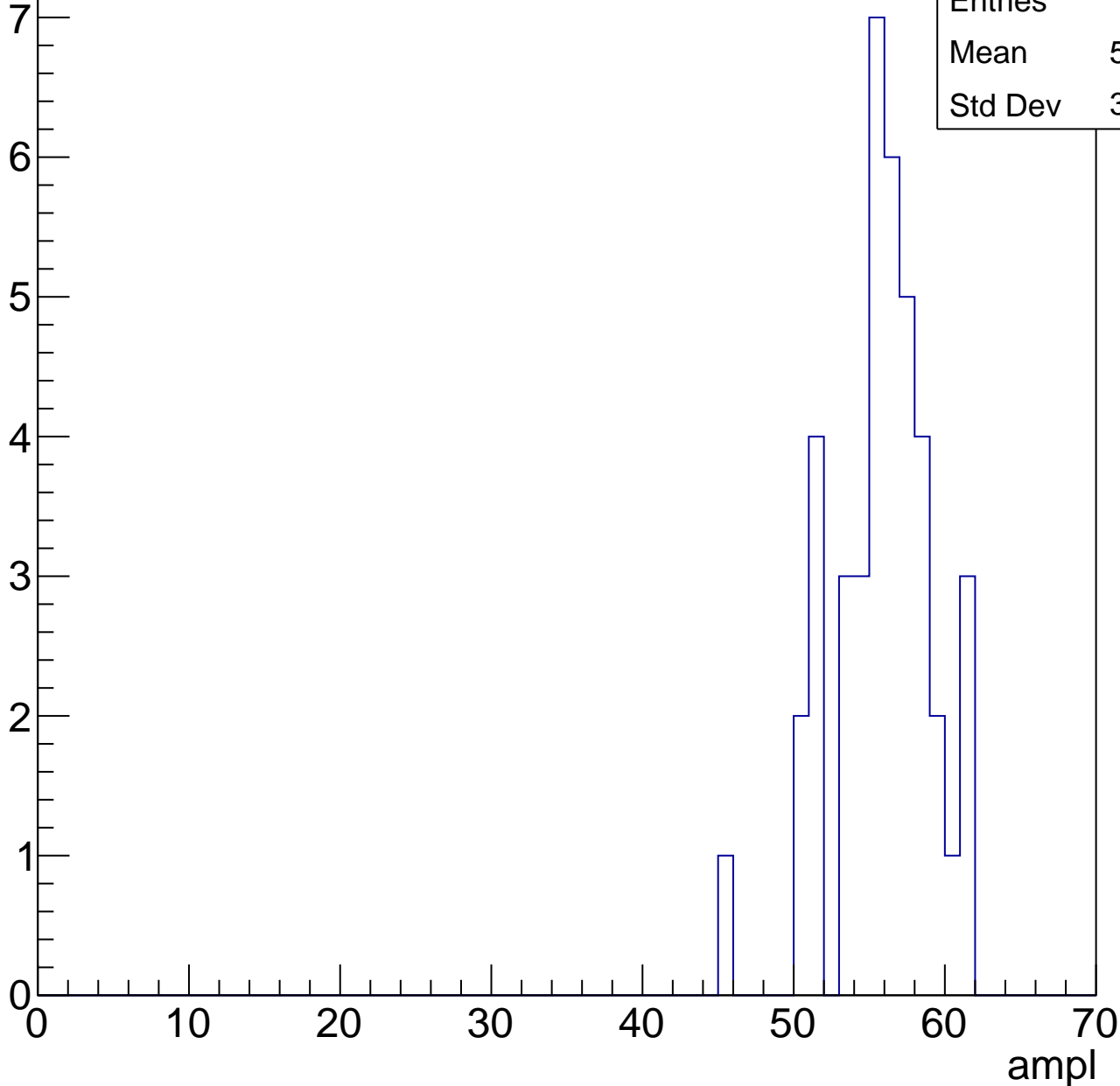


B0L001S, U18-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

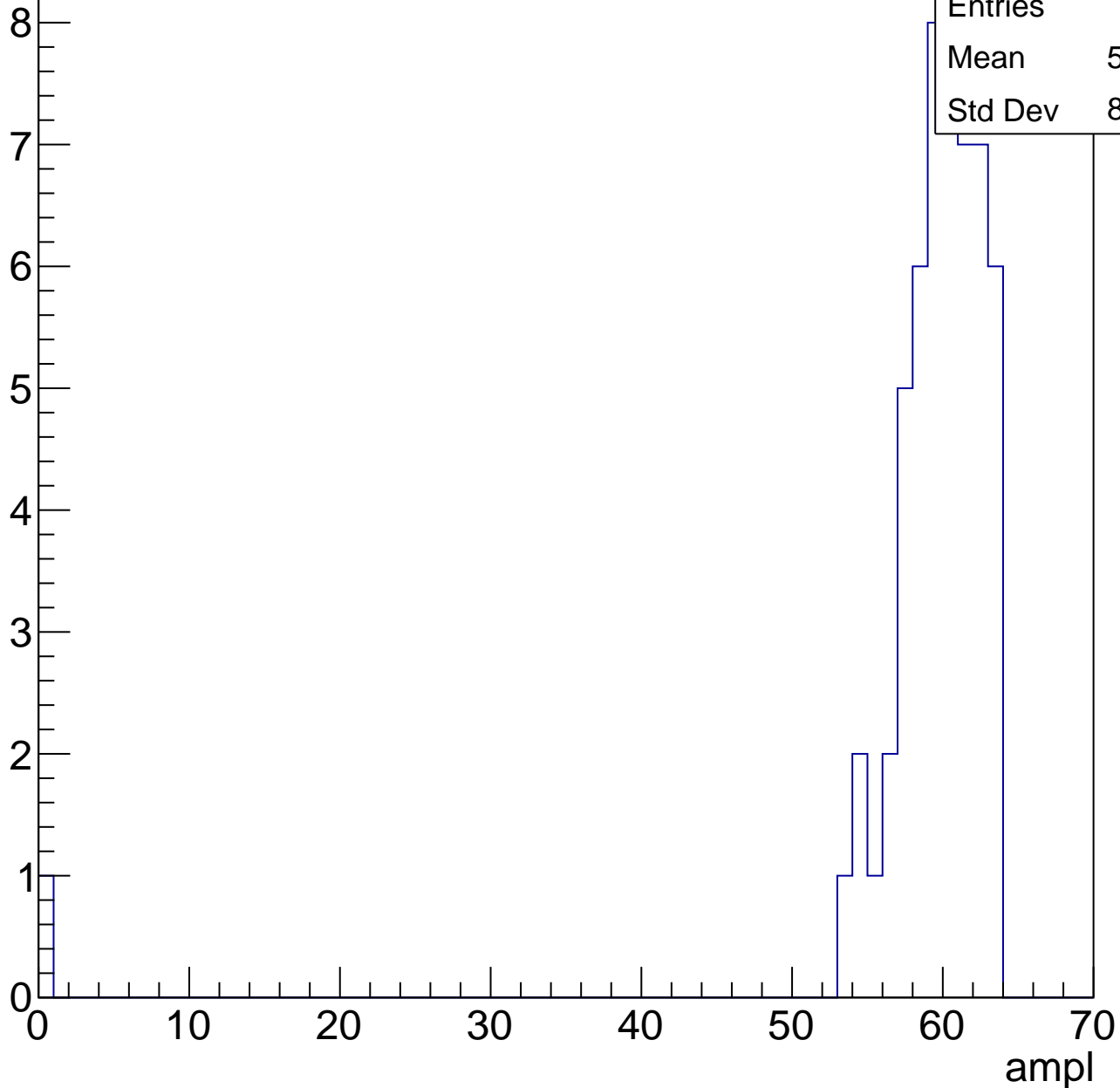
Entries	41
Mean	55.34
Std Dev	3.317



B0L001S, U18-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch31, adc0

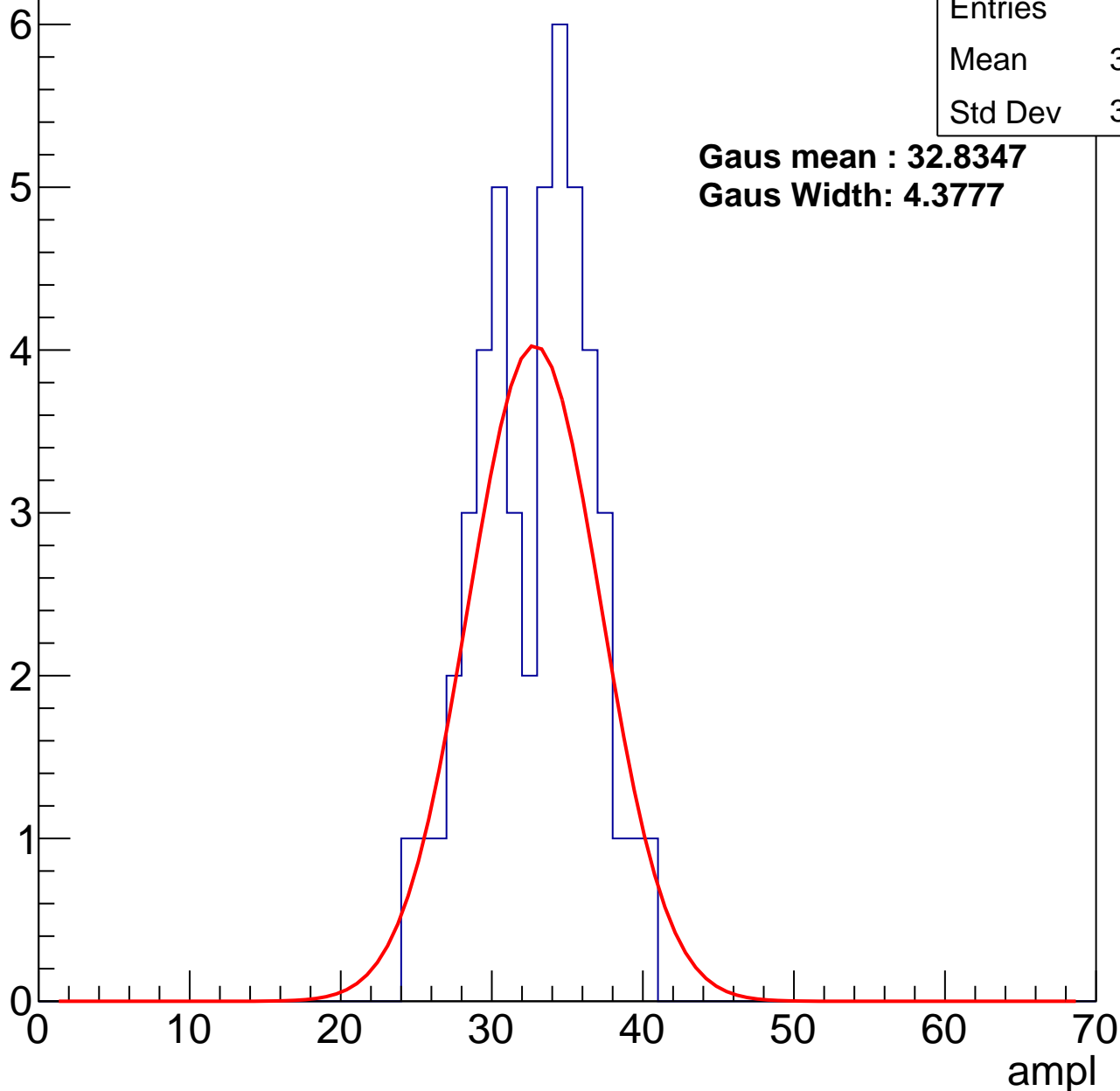
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	32.33
Std Dev	3.716

Gaus mean : 32.8347

Gaus Width: 4.3777



B0L001S, U18-ch31, adc1

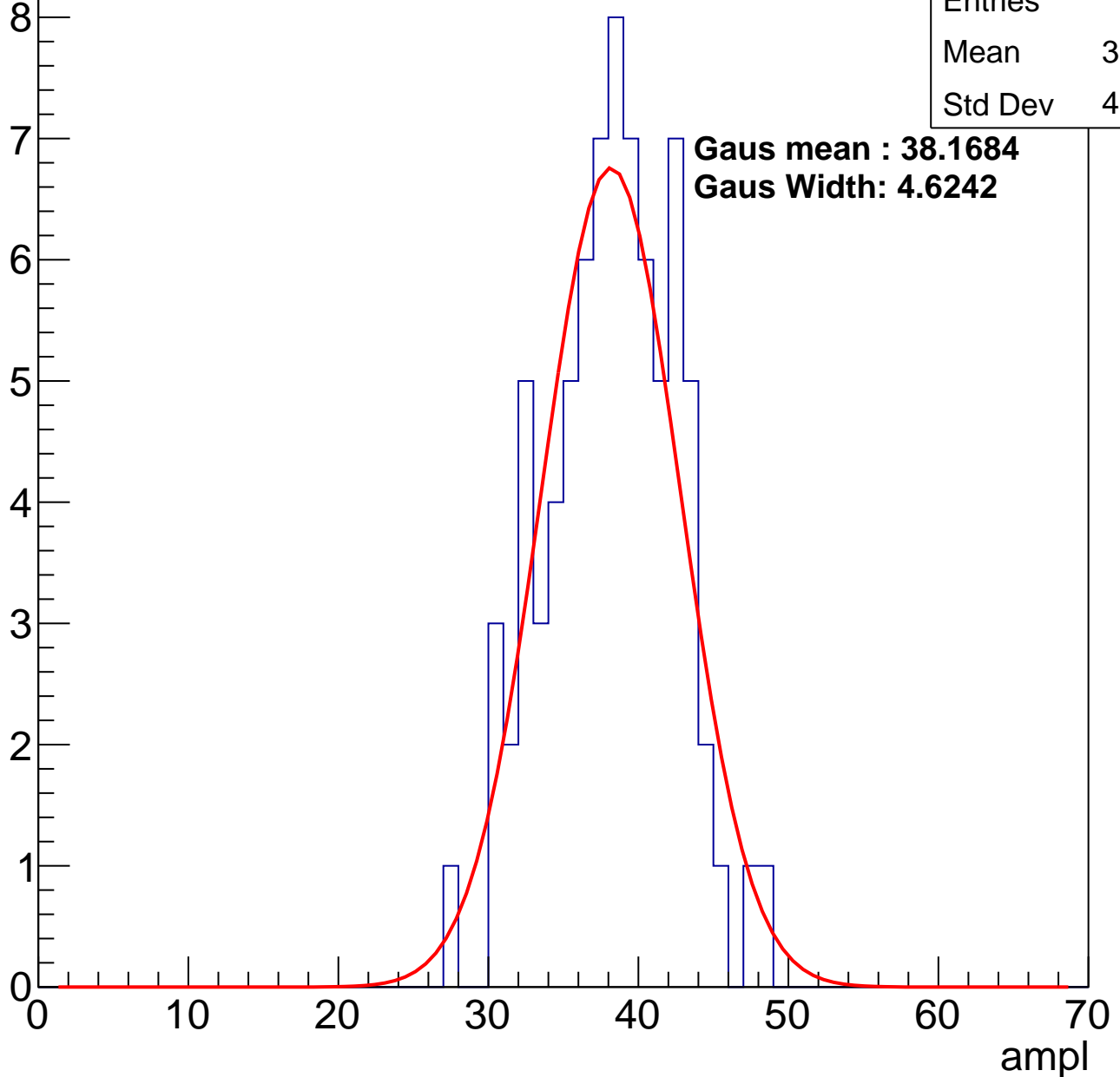
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	37.76
Std Dev	4.234

Gaus mean : 38.1684

Gaus Width: 4.6242



B0L001S, U18-ch31, adc2

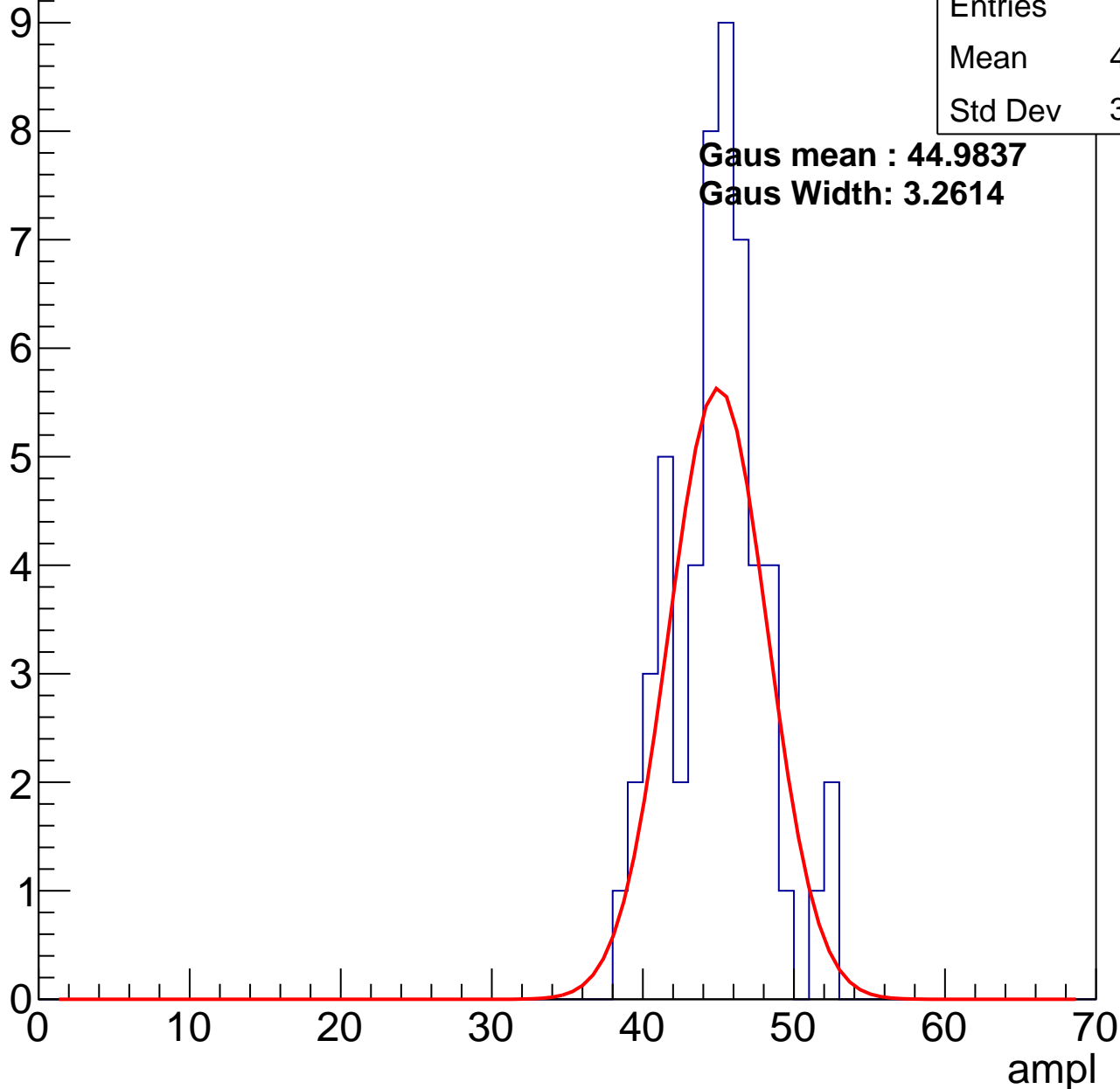
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.53
Std Dev	3.118

Gaus mean : 44.9837

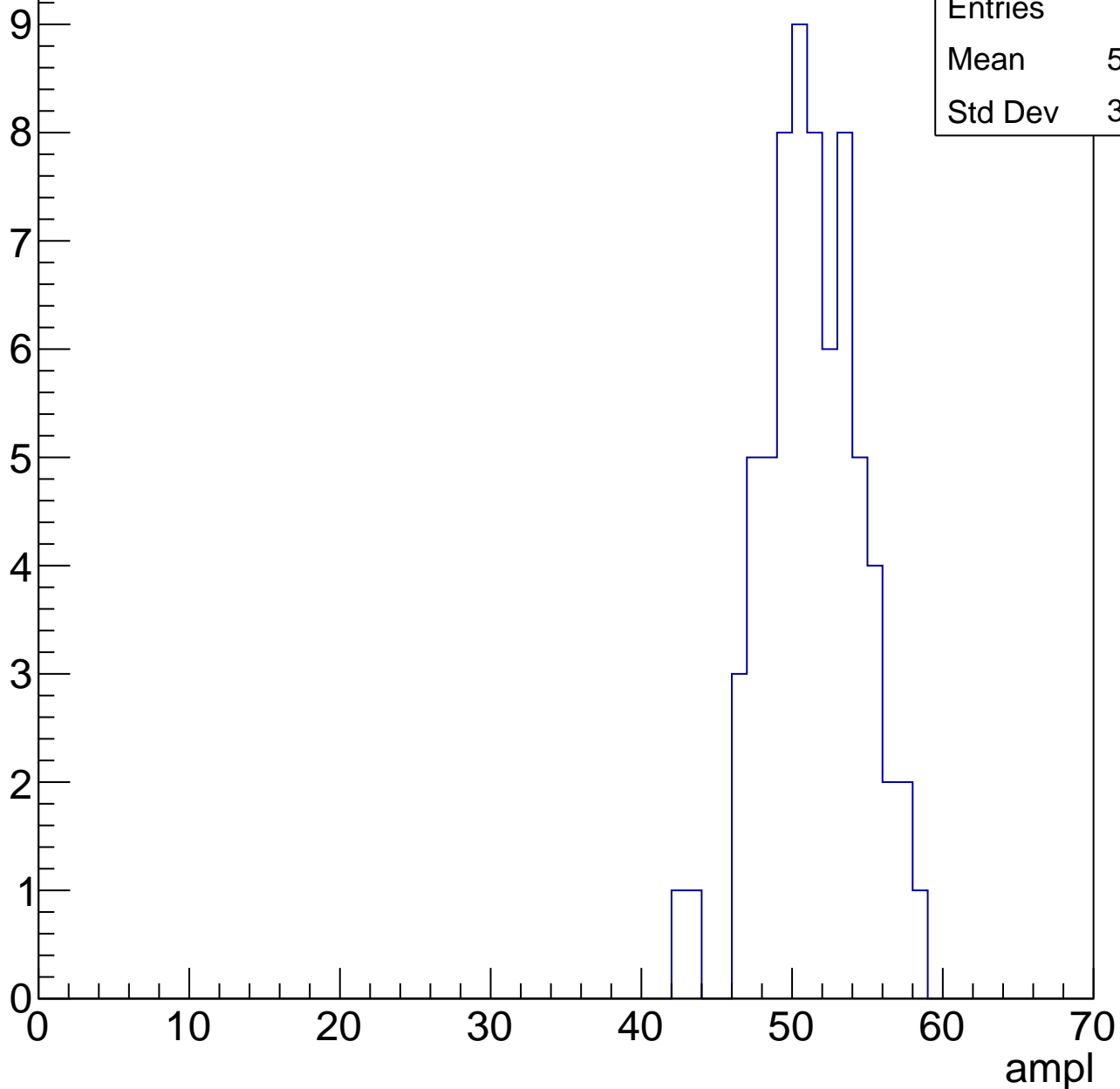
Gaus Width: 3.2614



B0L001S, U18-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

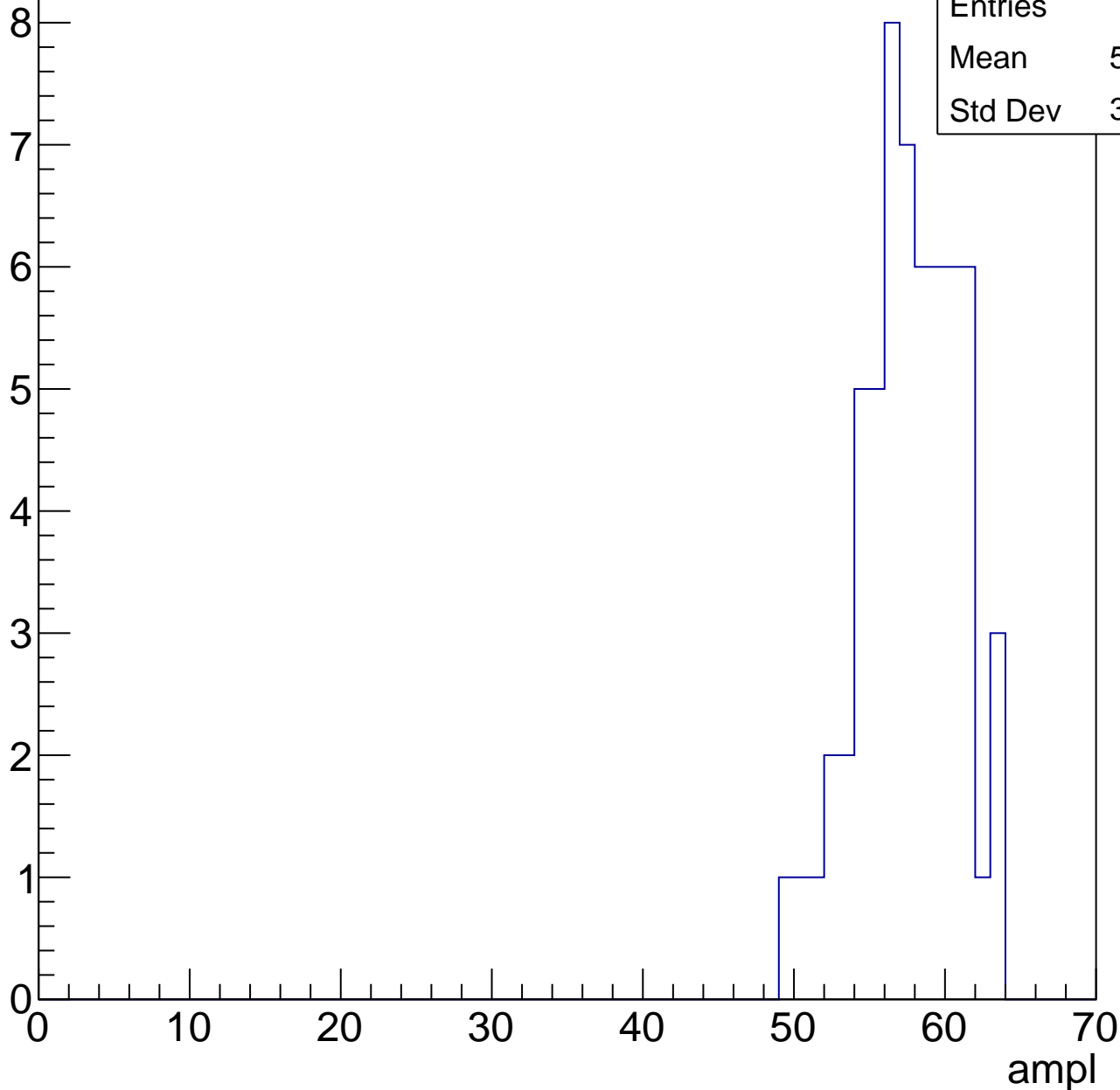


B0L001S, U18-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.18
Std Dev	3.202

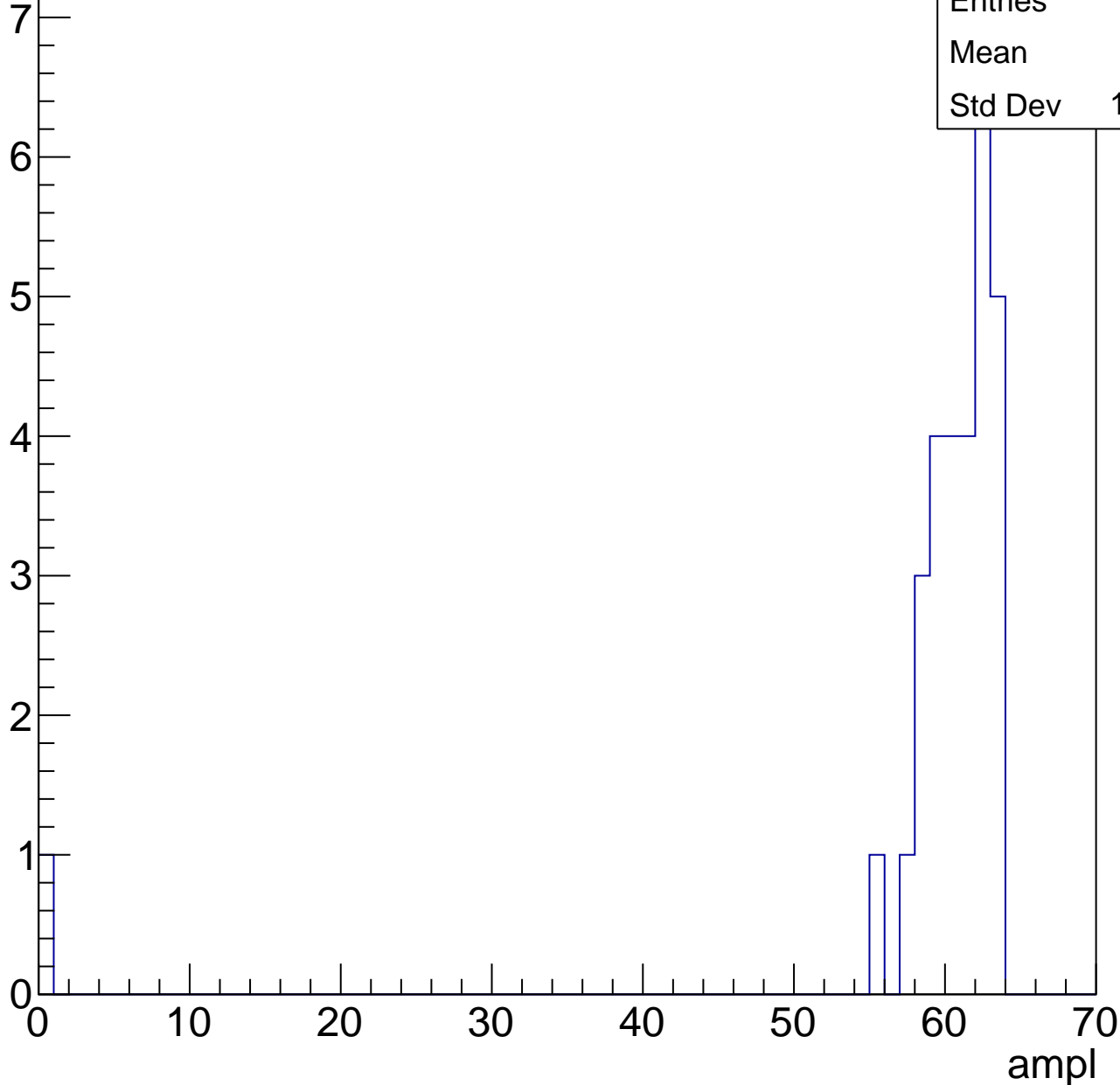


B0L001S, U18-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.5
Std Dev	11.04



B0L001S, U18-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch32, adc0

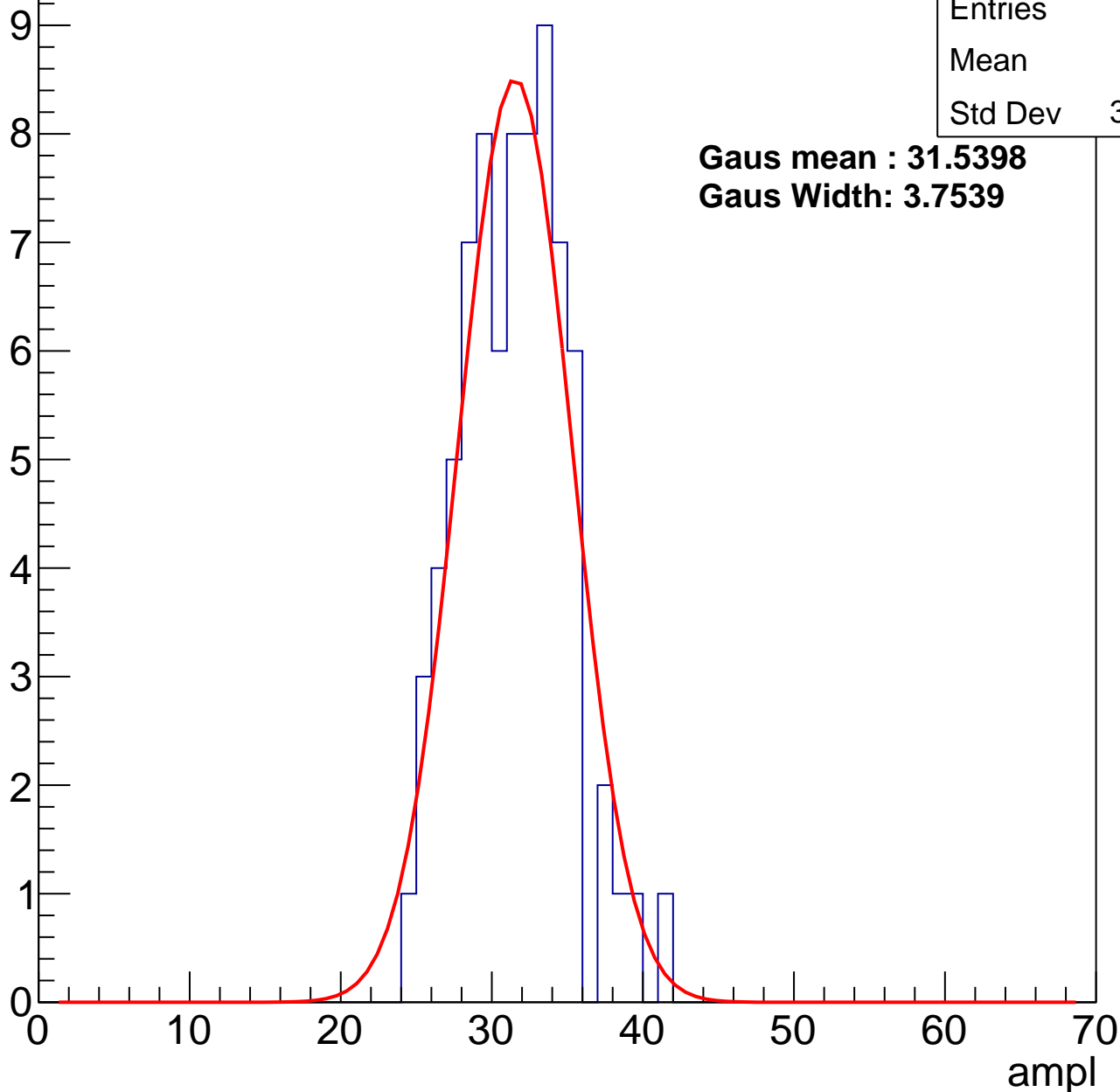
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	31
Std Dev	3.468

Gaus mean : 31.5398

Gaus Width: 3.7539



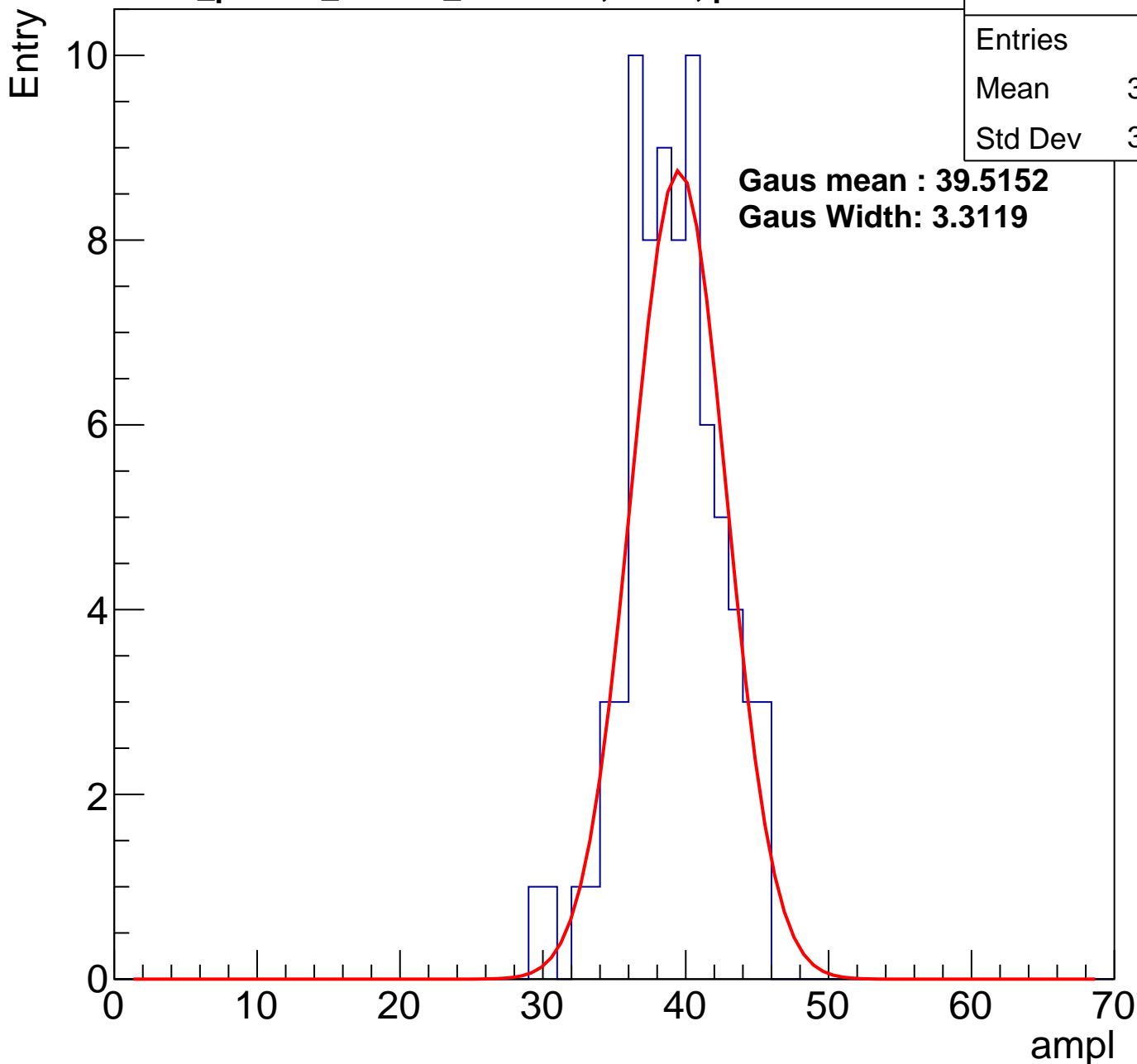
B0L001S, U18-ch32, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	38.63
Std Dev	3.316

Gaus mean : 39.5152

Gaus Width: 3.3119



B0L001S, U18-ch32, adc2

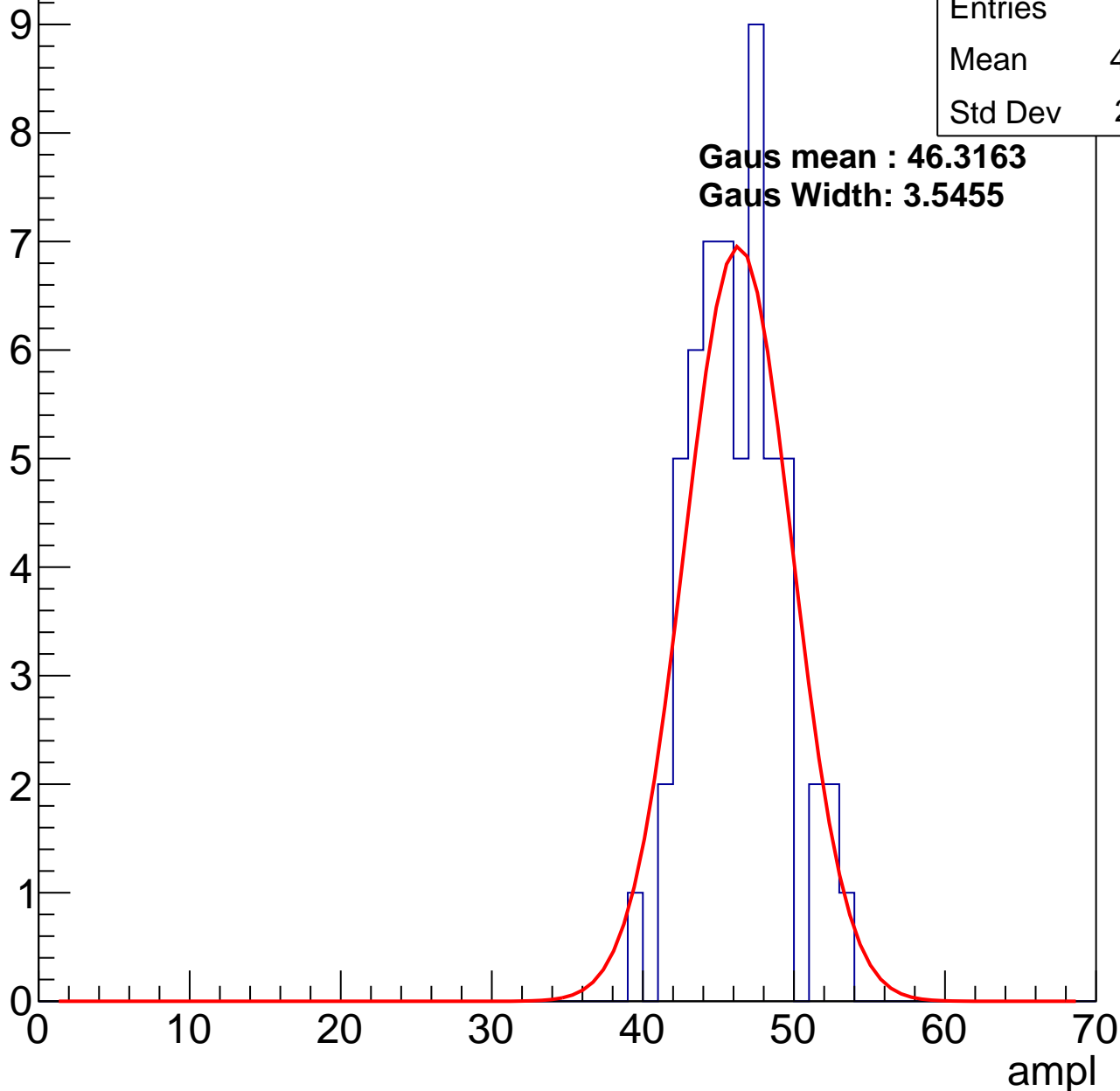
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.77
Std Dev	2.991

Gaus mean : 46.3163

Gaus Width: 3.5455

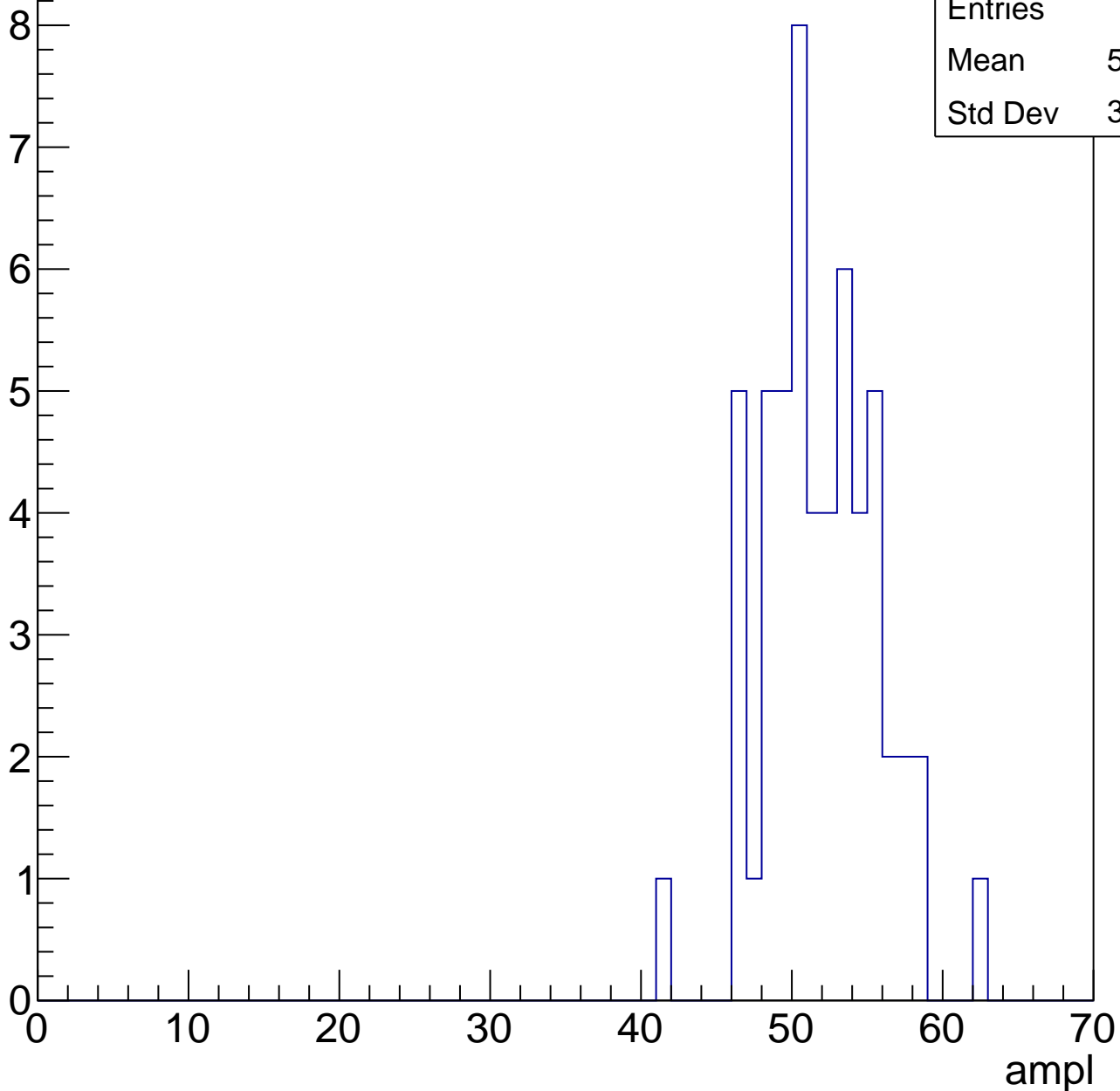


B0L001S, U18-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.42
Std Dev	3.788

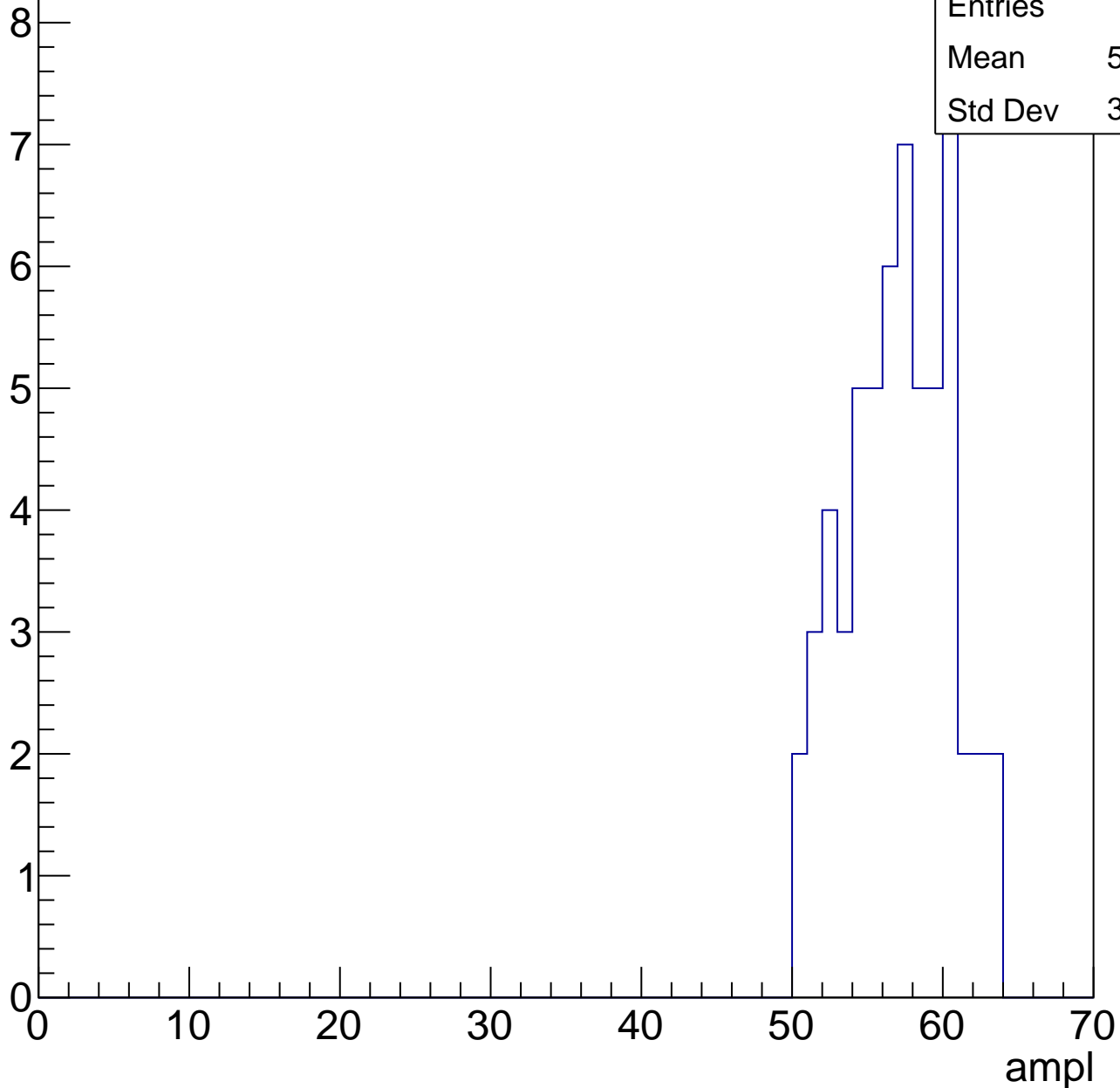


B0L001S, U18-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.56
Std Dev	3.356

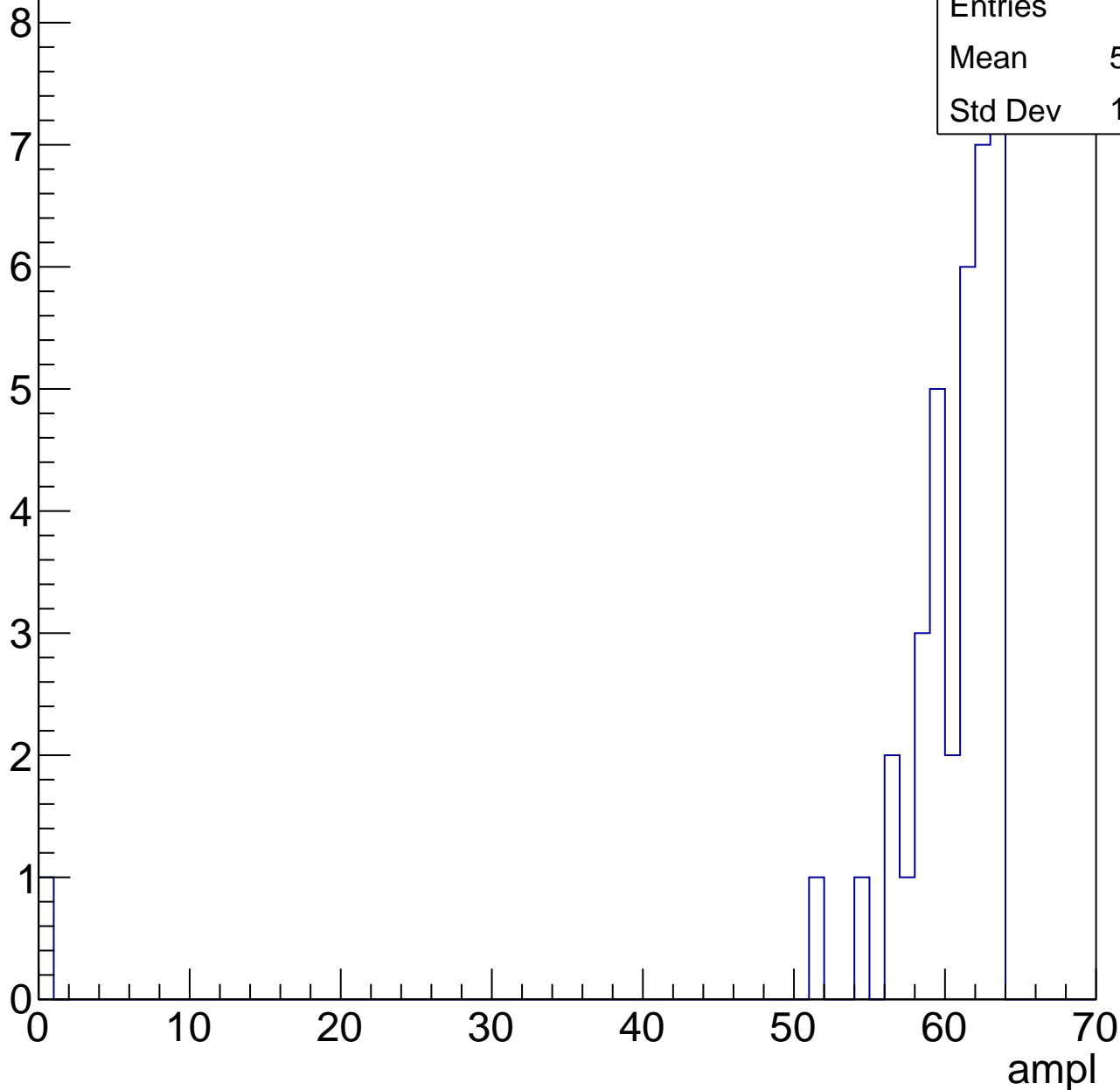


B0L001S, U18-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

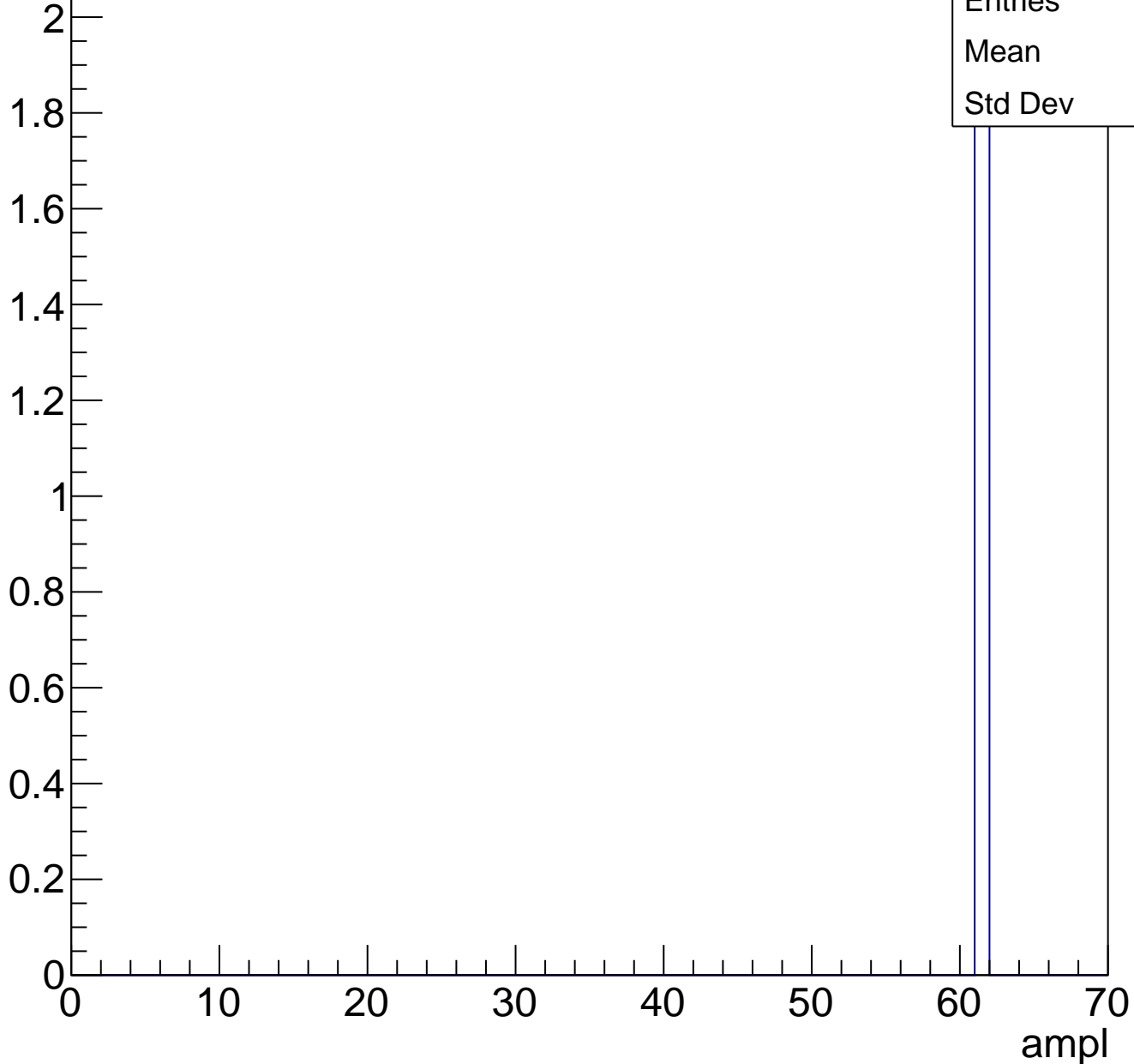
Entries	37
Mean	58.57
Std Dev	10.14



B0L001S, U18-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch33, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	30.52
Std Dev	4.954

Gaus mean : 32.3921

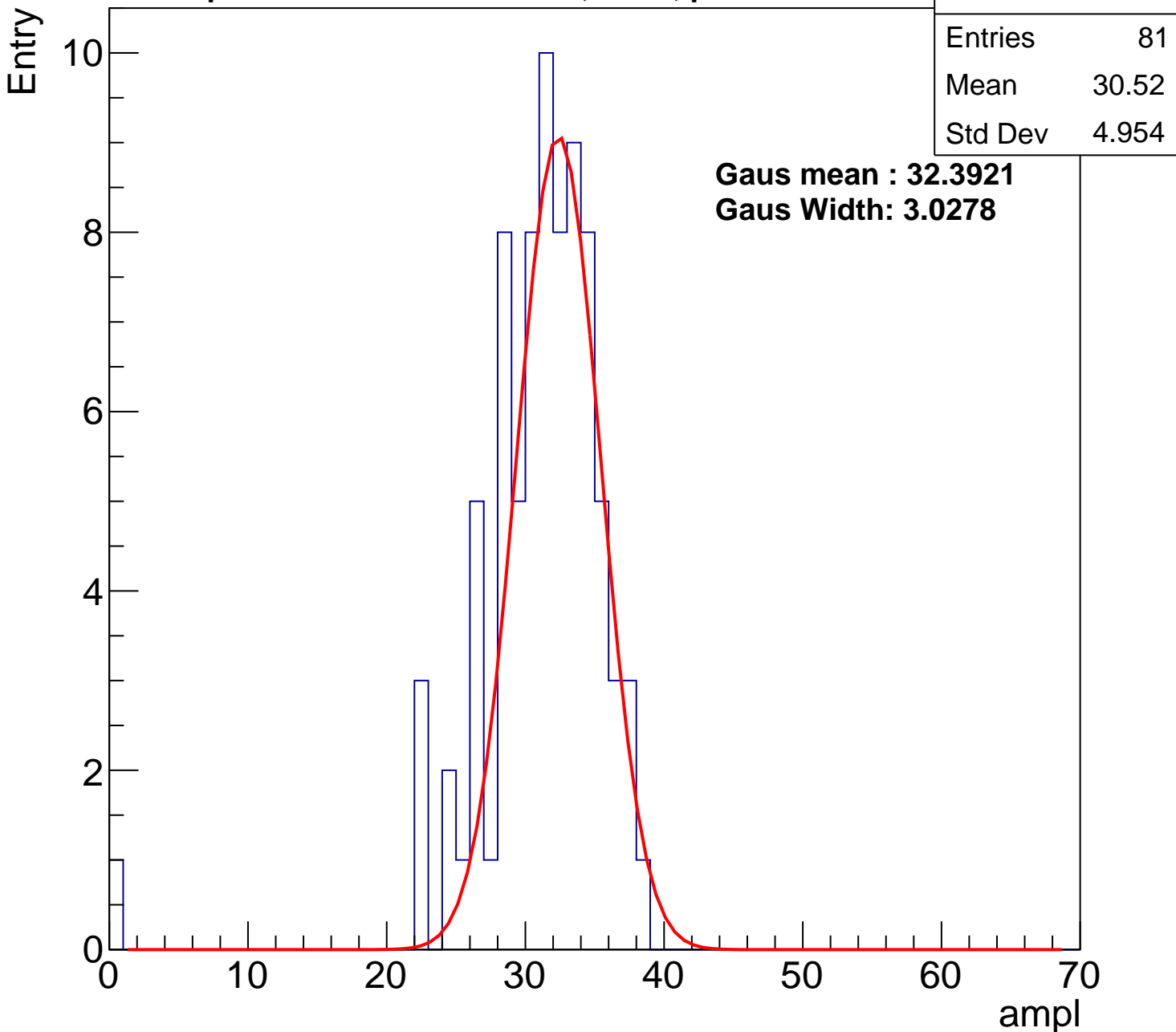
Gaus Width: 3.0278

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch33, adc1

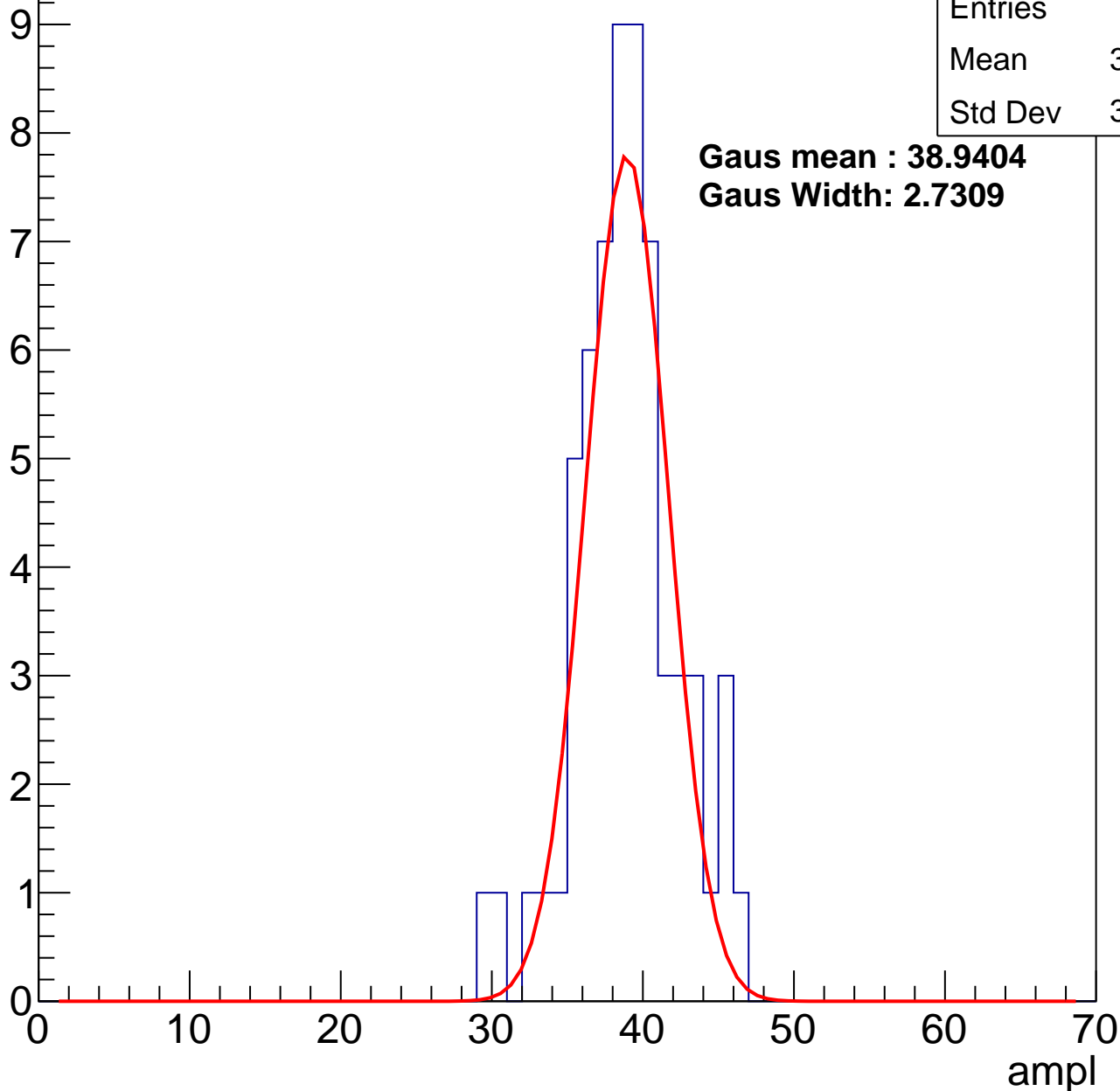
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	38.45
Std Dev	3.406

Gaus mean : 38.9404

Gaus Width: 2.7309



B0L001S, U18-ch33, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	45.01
Std Dev	3.283

Gaus mean : 45.2498

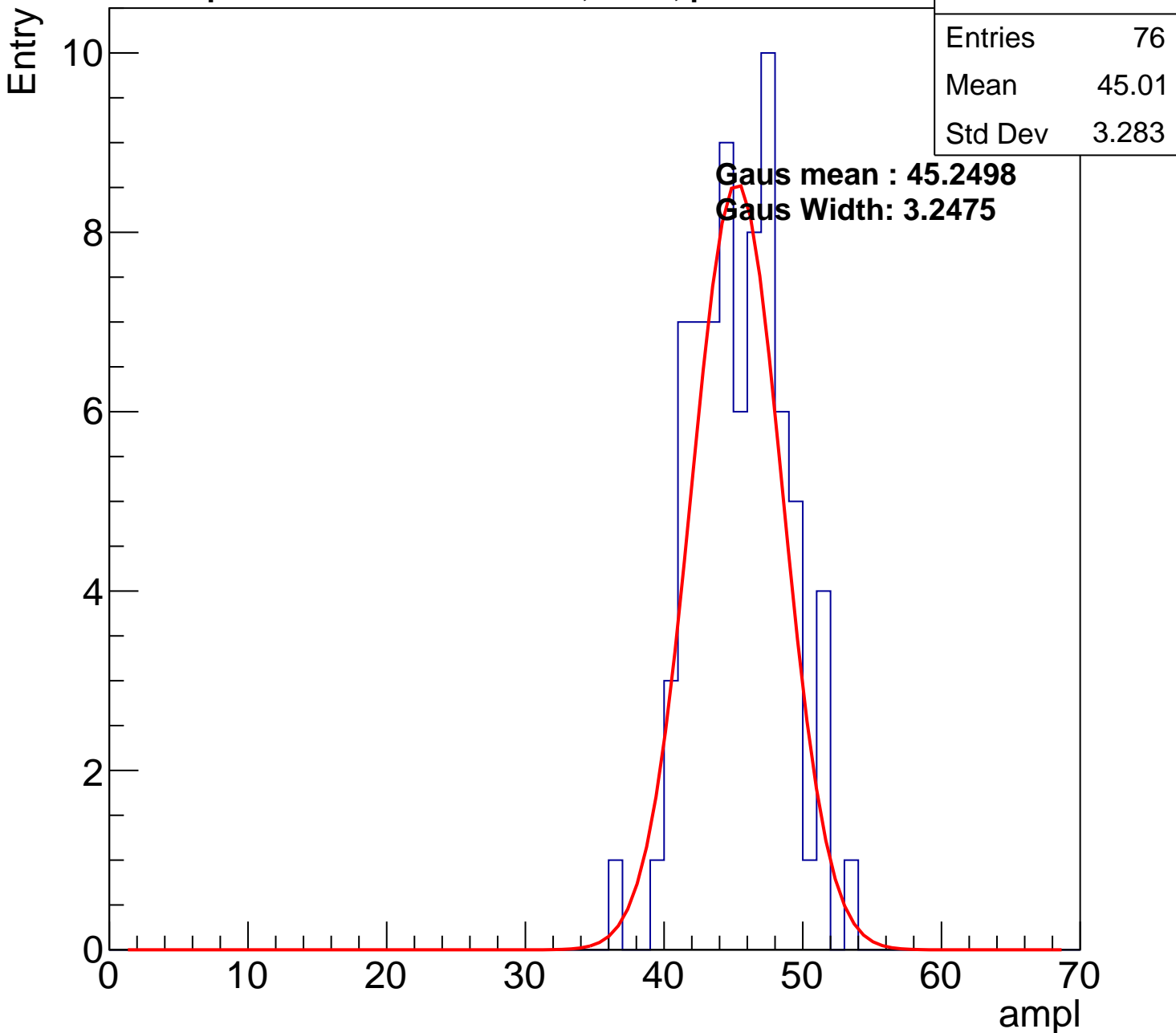
Gaus Width: 3.2475

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

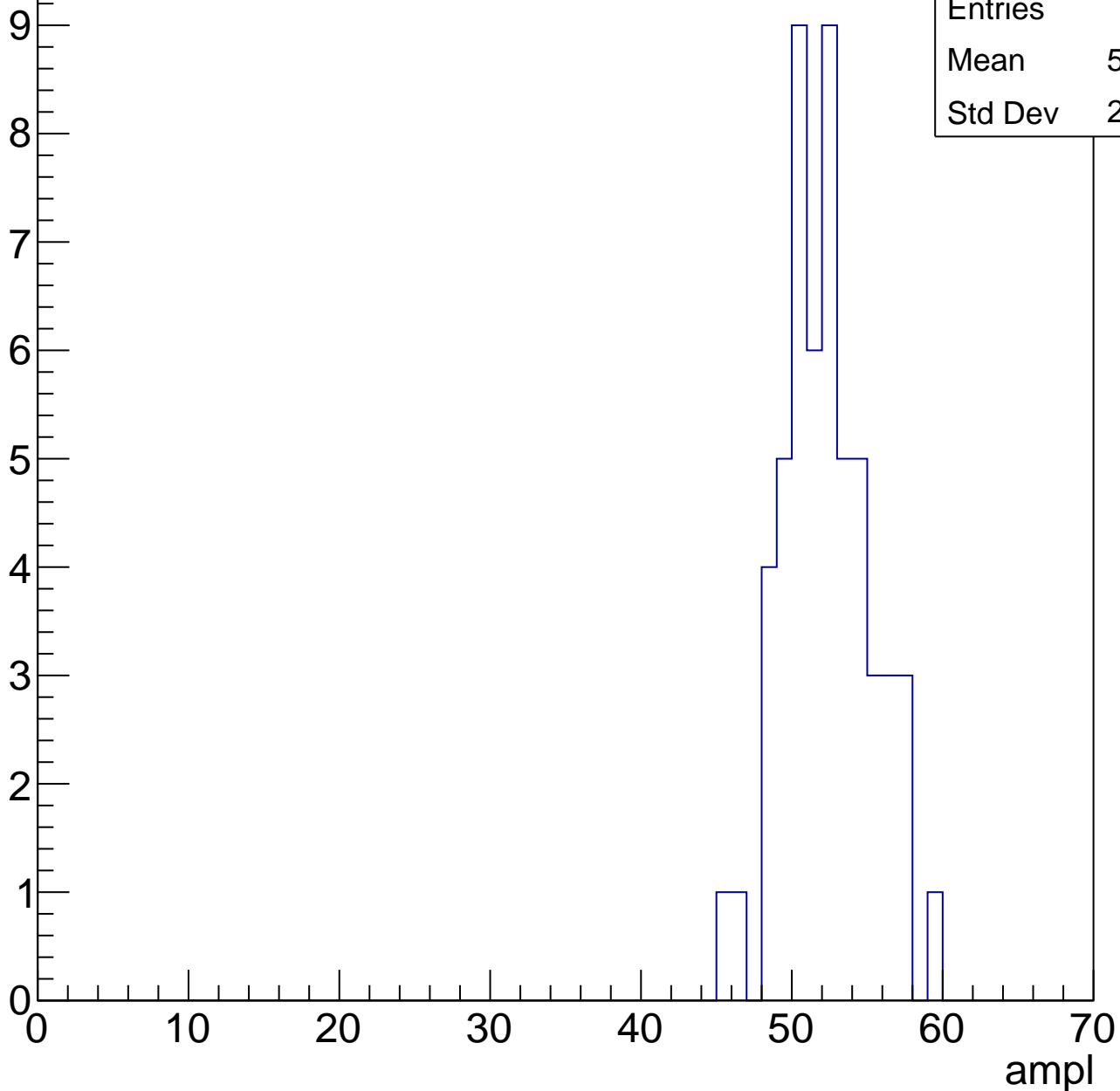


B0L001S, U18-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.82
Std Dev	2.886

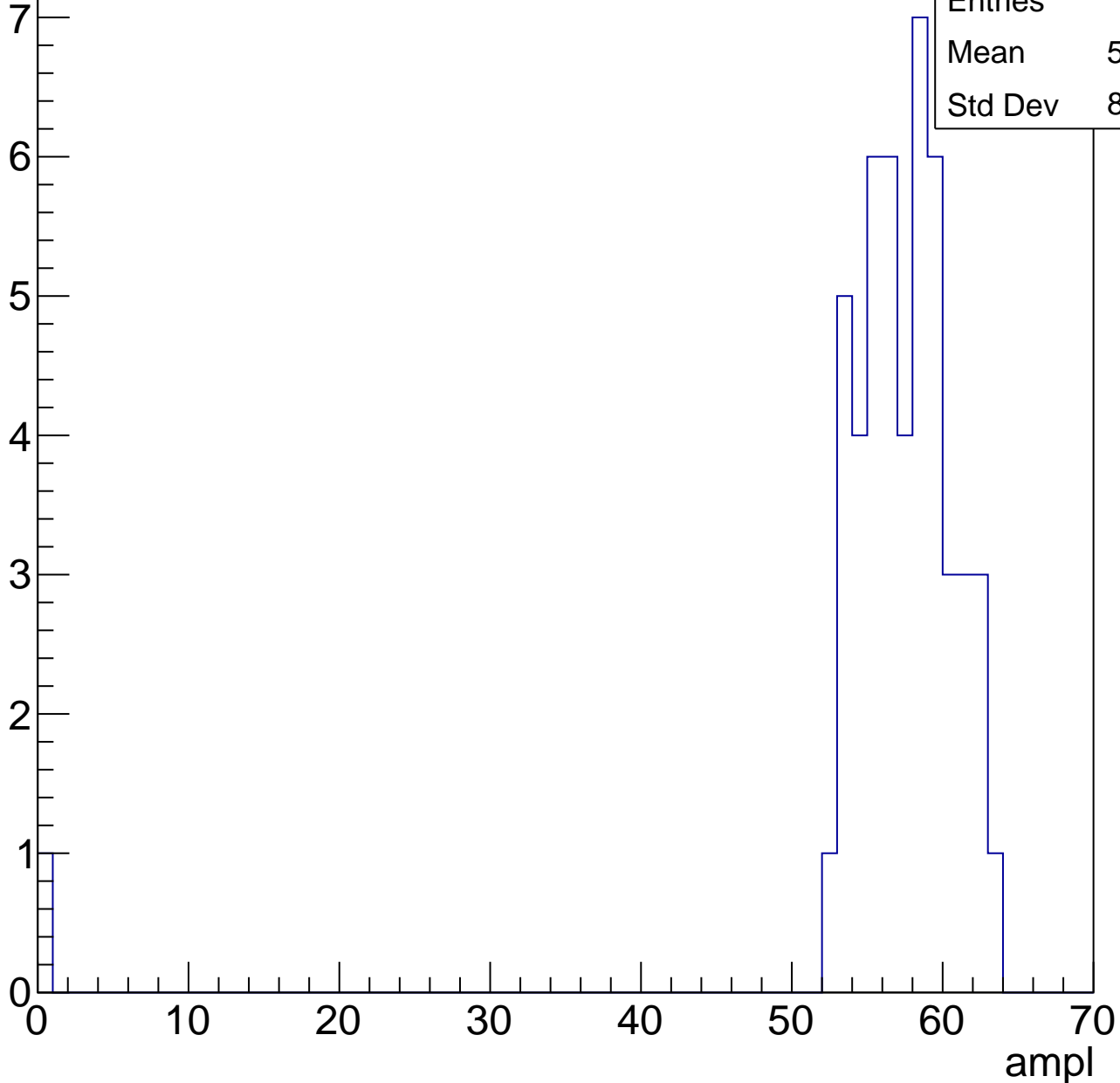


B0L001S, U18-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	55.98
Std Dev	8.468



B0L001S, U18-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.25
Std Dev	2.343

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L001S, U18-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch34, adc0

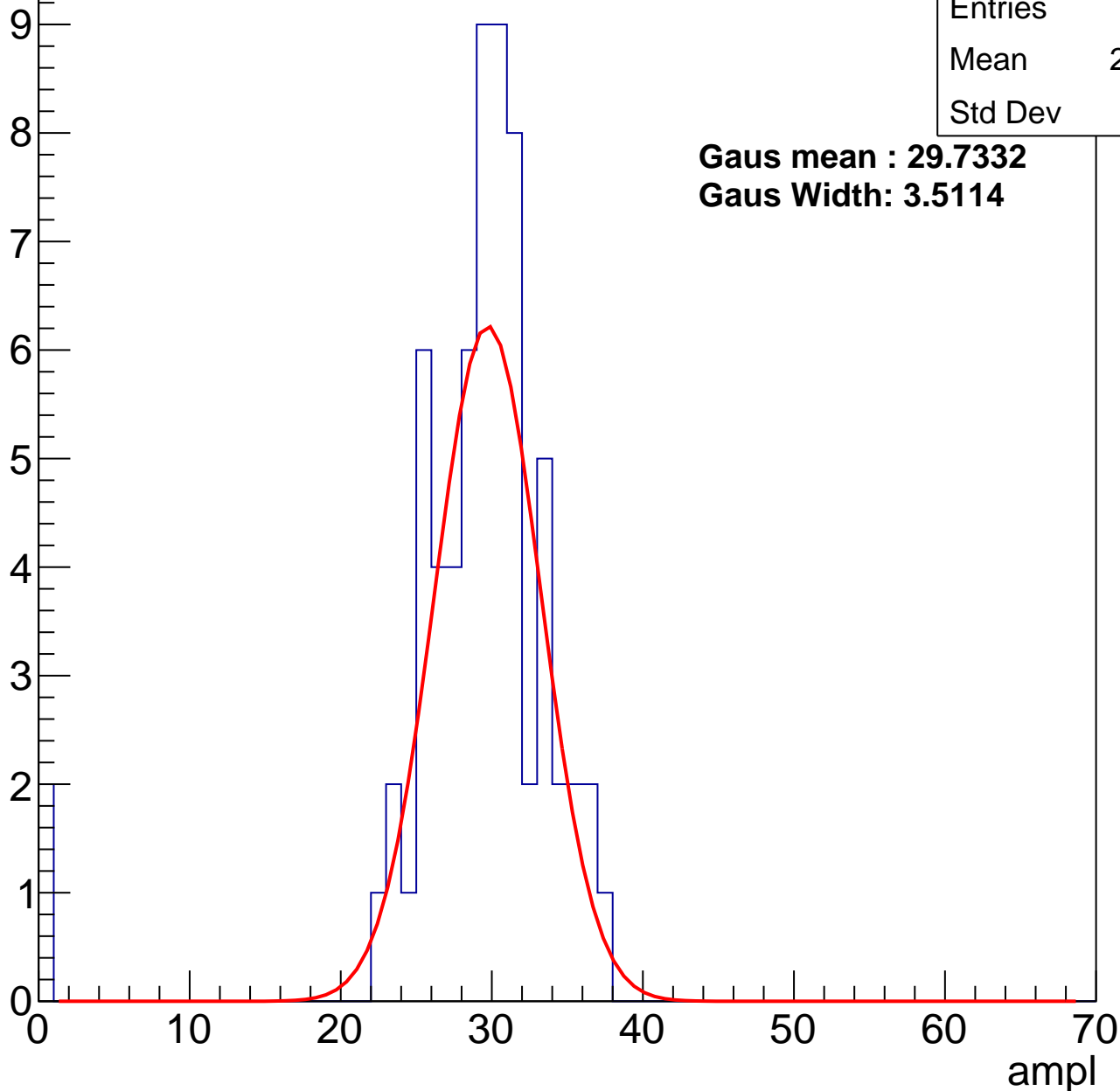
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	28.44
Std Dev	6.01

Gaus mean : 29.7332

Gaus Width: 3.5114



B0L001S, U18-ch34, adc1

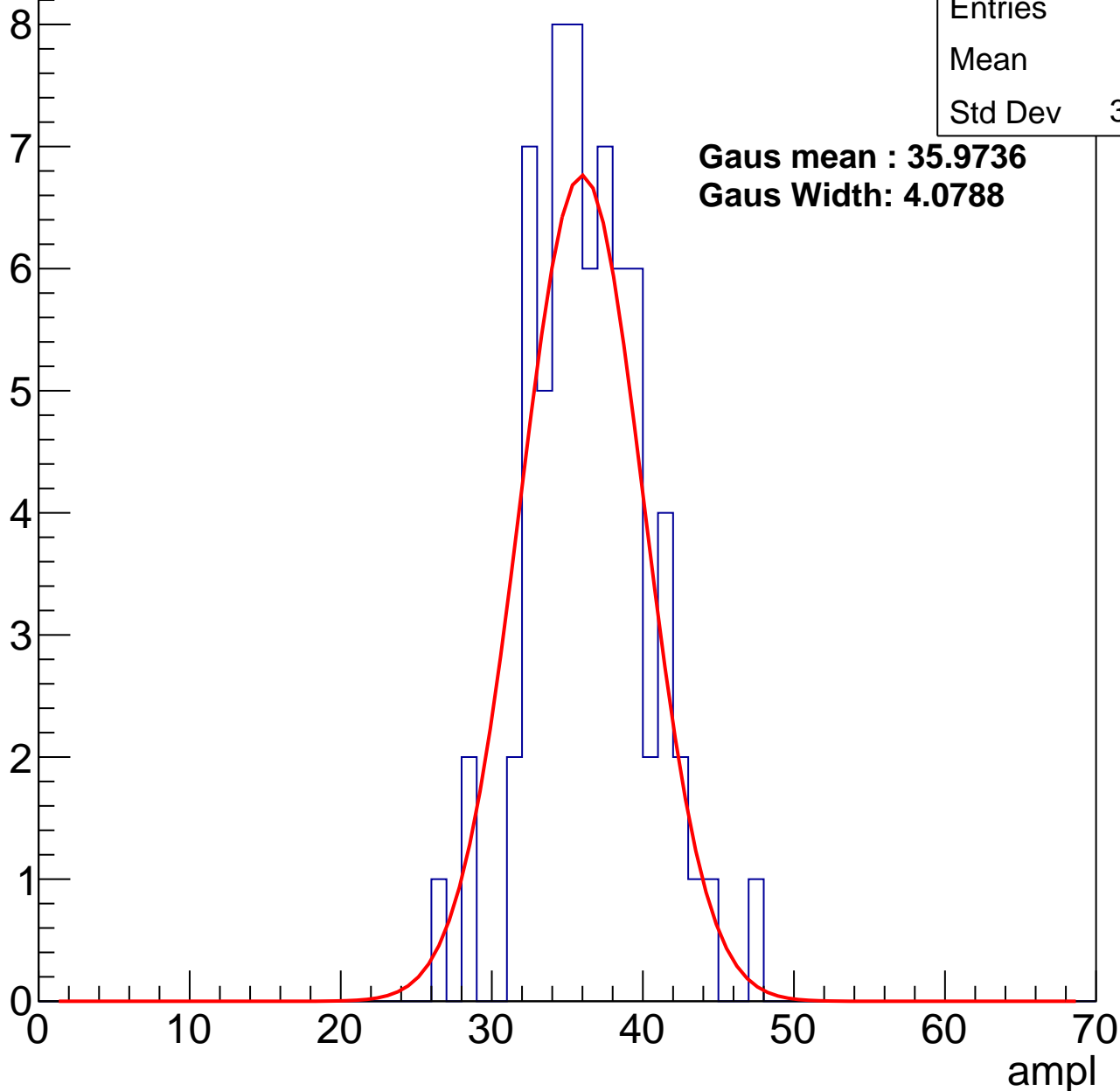
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36
Std Dev	3.803

Gaus mean : 35.9736

Gaus Width: 4.0788



B0L001S, U18-ch34, adc2

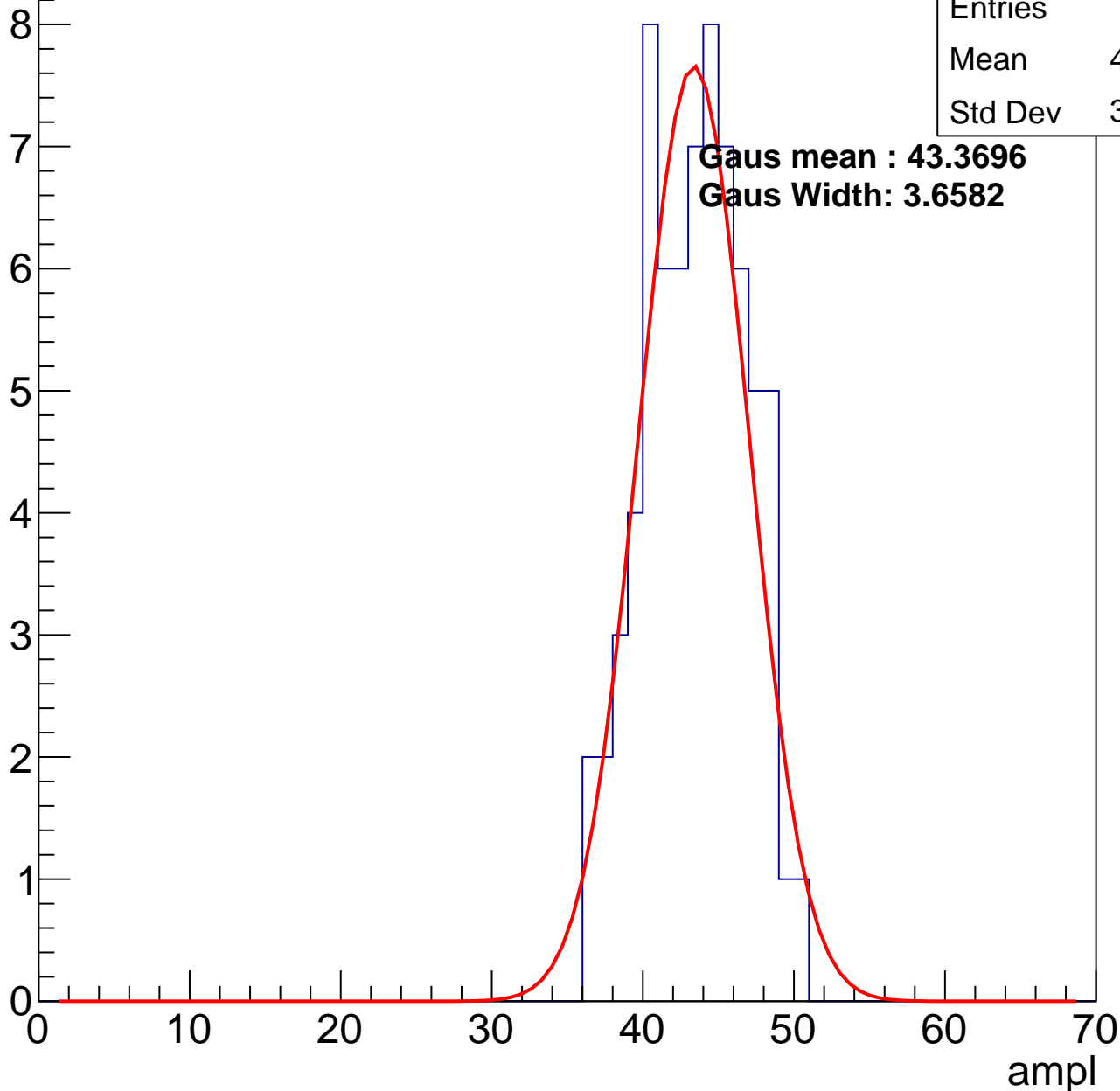
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	42.99
Std Dev	3.338

Gaus mean : 43.3696

Gaus Width: 3.6582

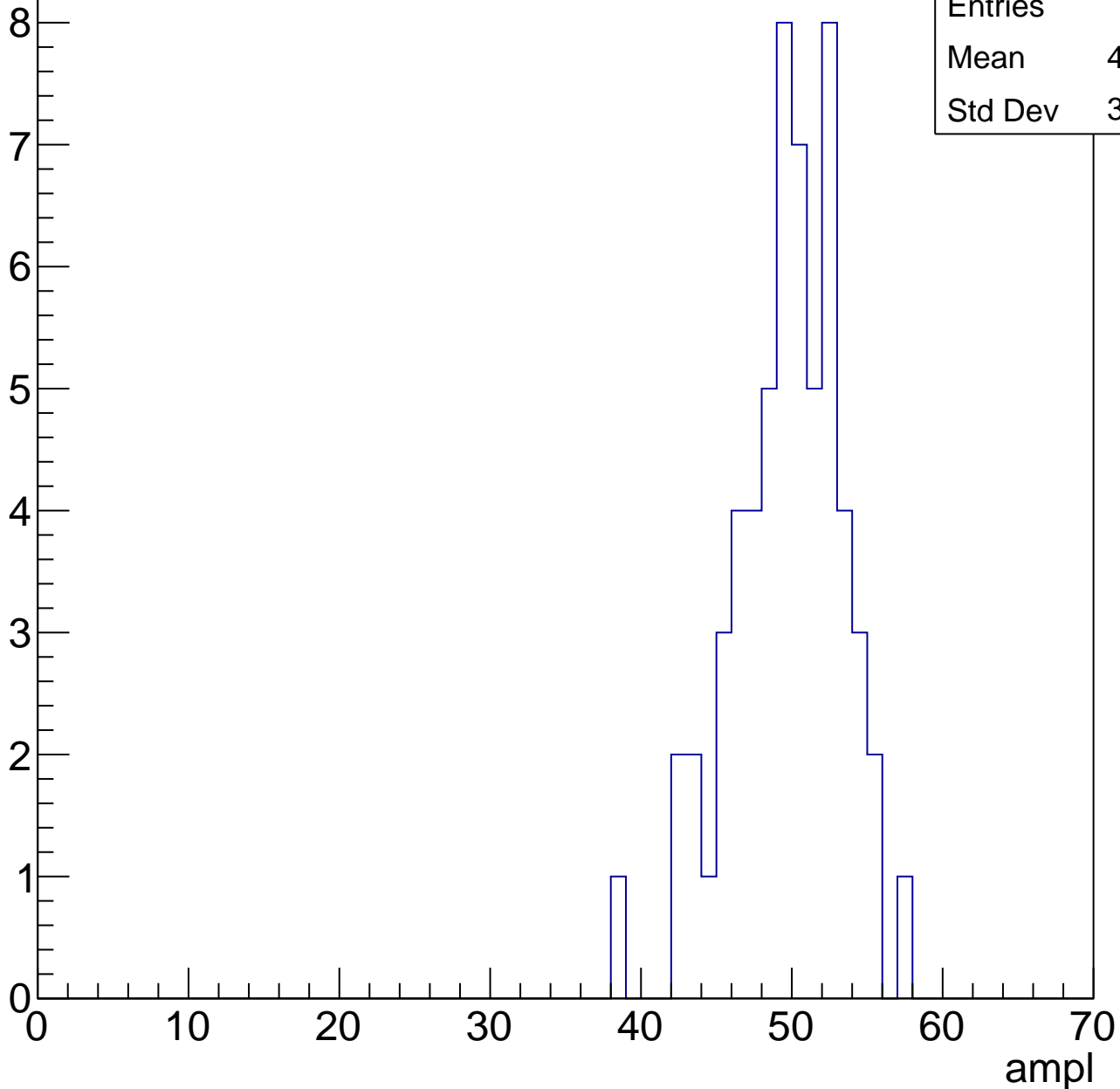


B0L001S, U18-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	49.22
Std Dev	3.634

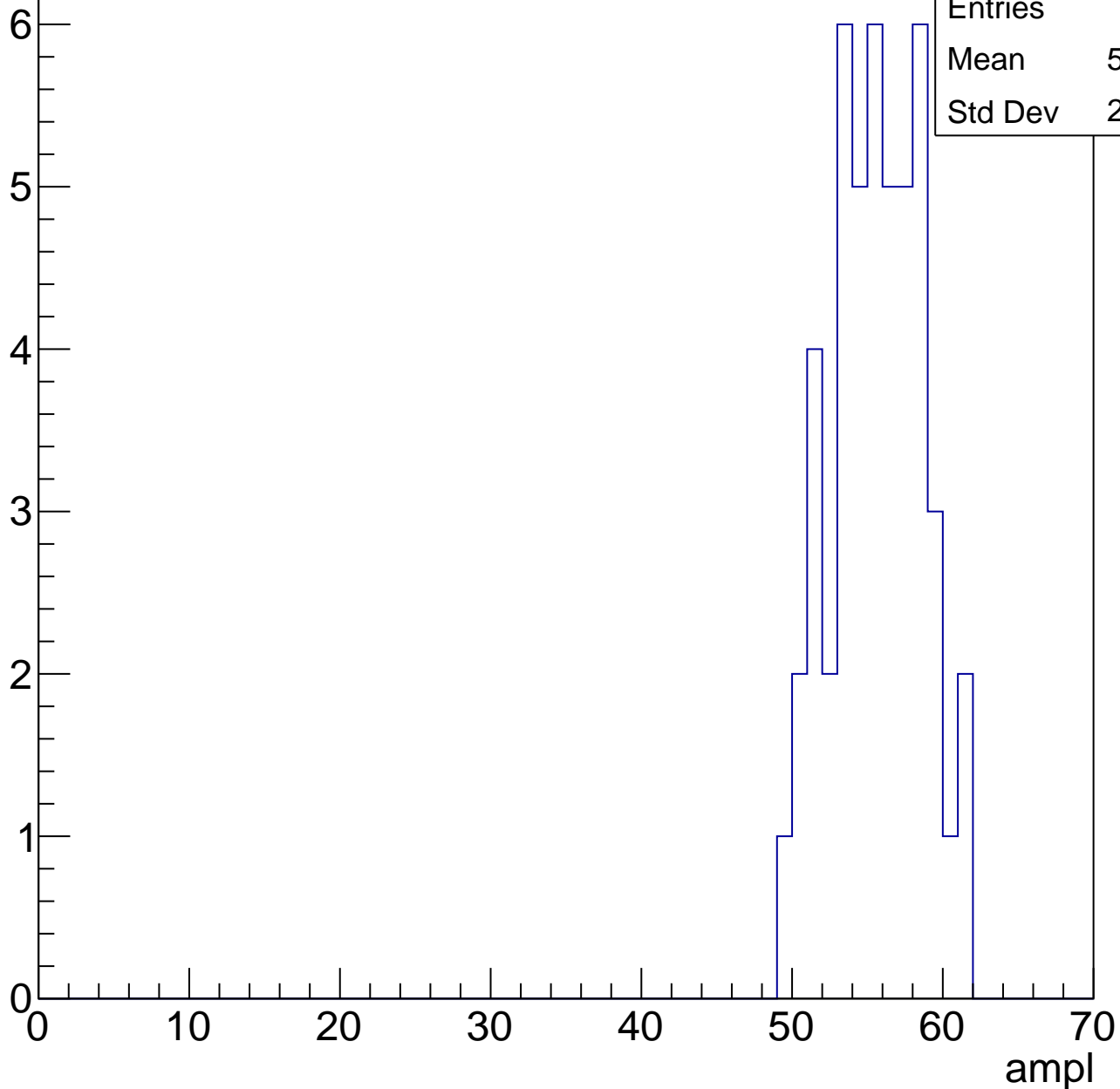


B0L001S, U18-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

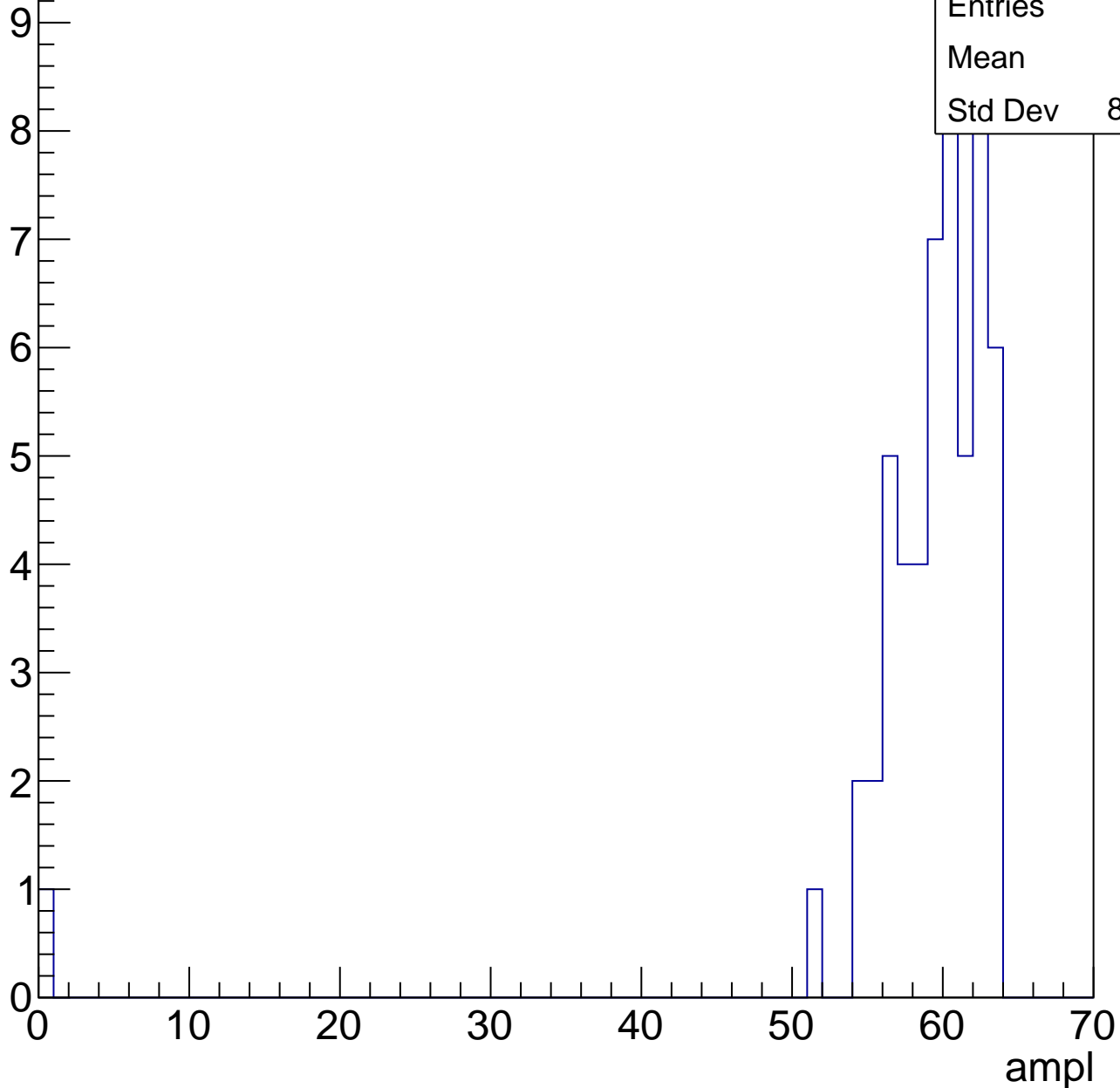
Entries	48
Mean	55.15
Std Dev	2.958



B0L001S, U18-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

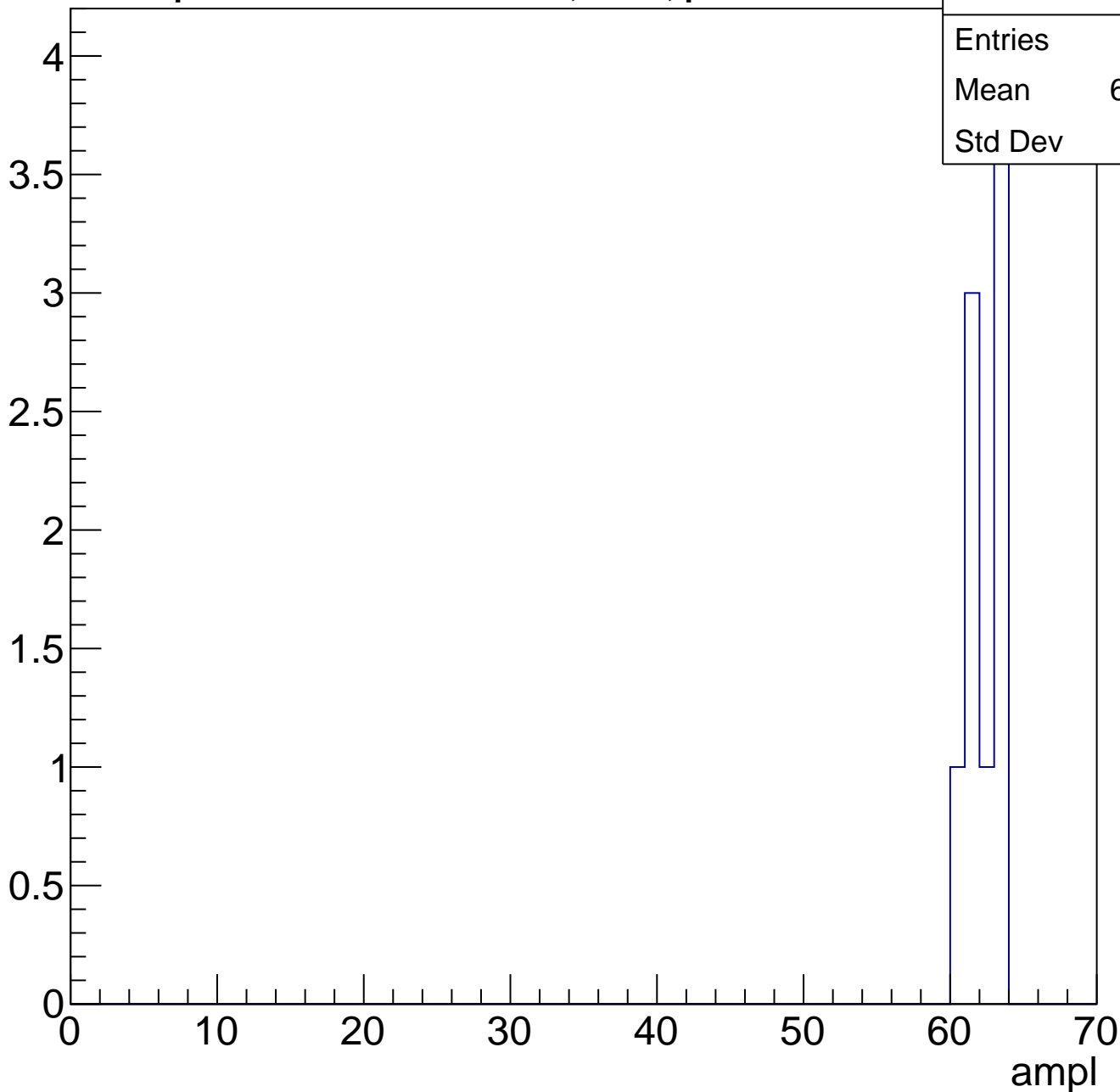
Entry



B0L001S, U18-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch35, adc0

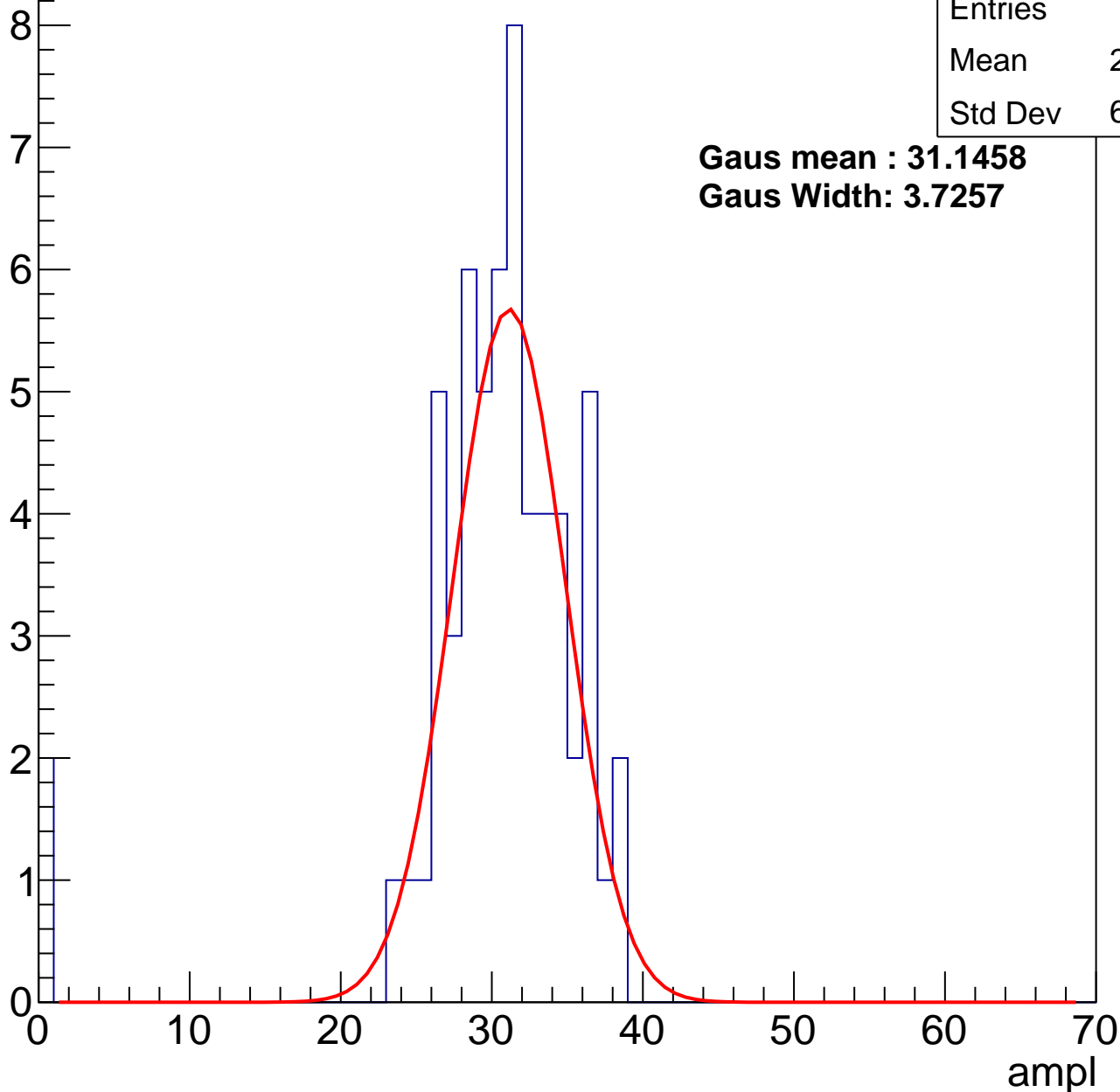
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.72
Std Dev	6.547

Gaus mean : 31.1458

Gaus Width: 3.7257



B0L001S, U18-ch35, adc1

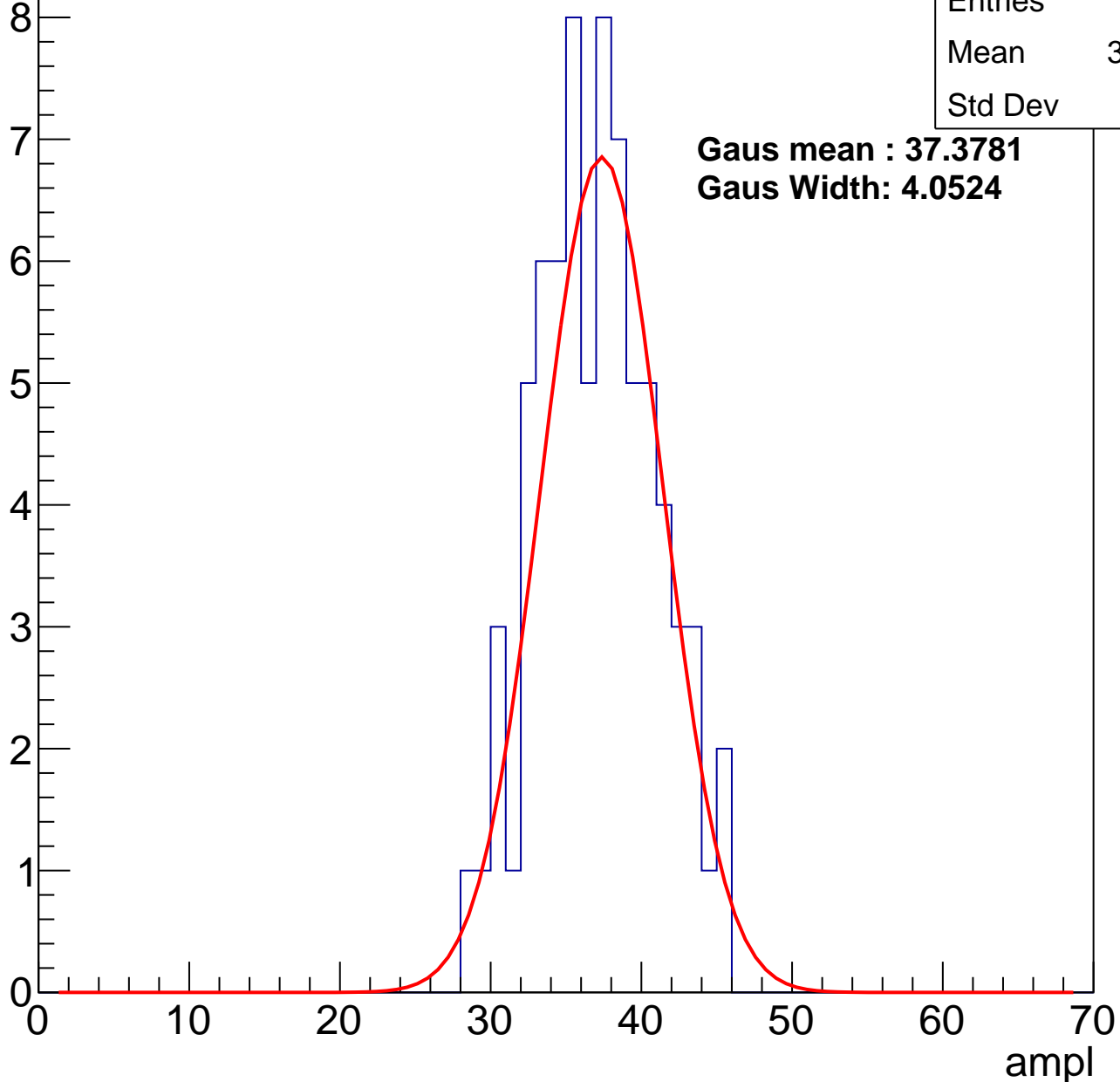
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	36.62
Std Dev	3.9

Gaus mean : 37.3781

Gaus Width: 4.0524



B0L001S, U18-ch35, adc2

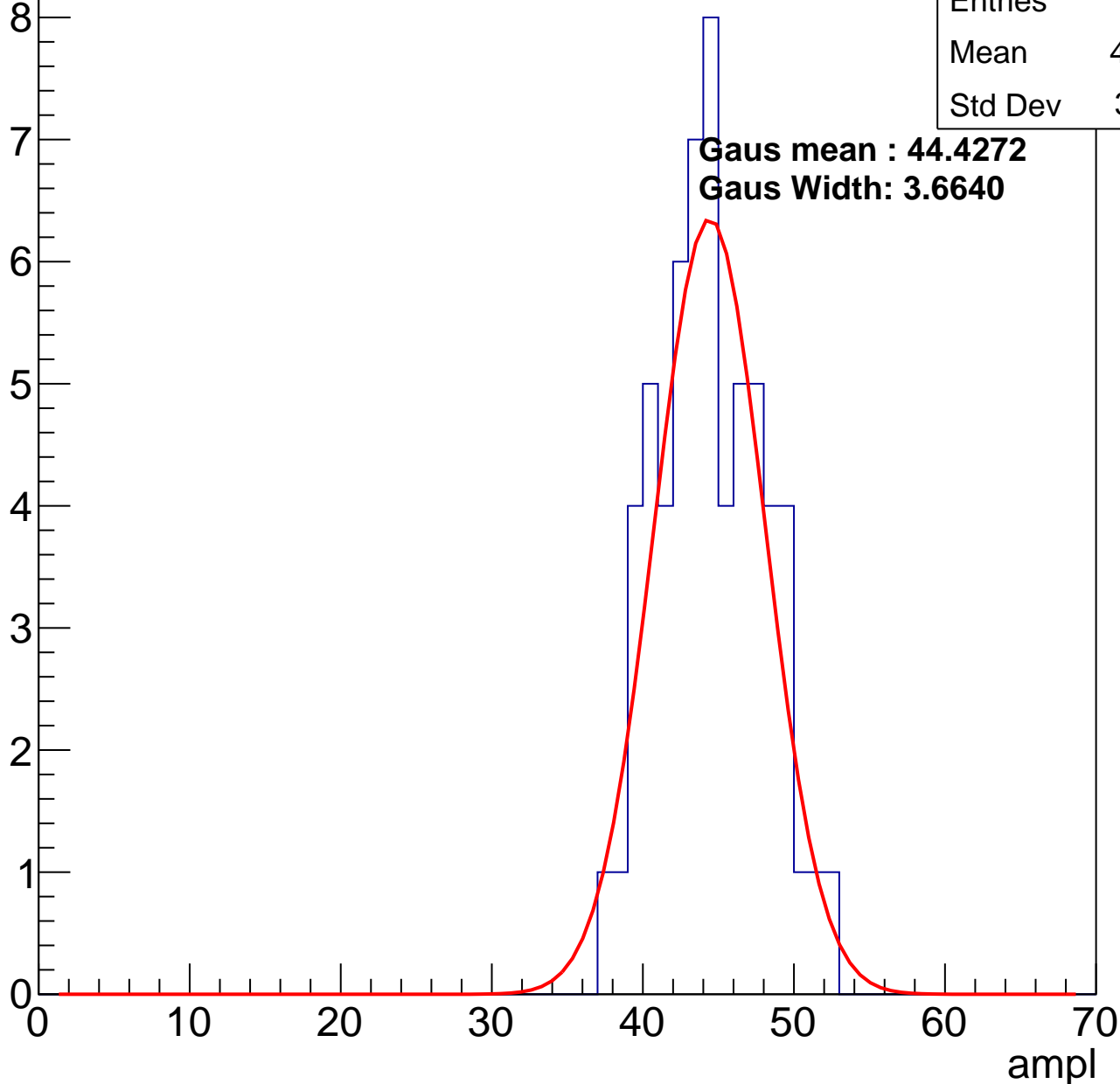
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.03
Std Dev	3.421

Gaus mean : 44.4272

Gaus Width: 3.6640

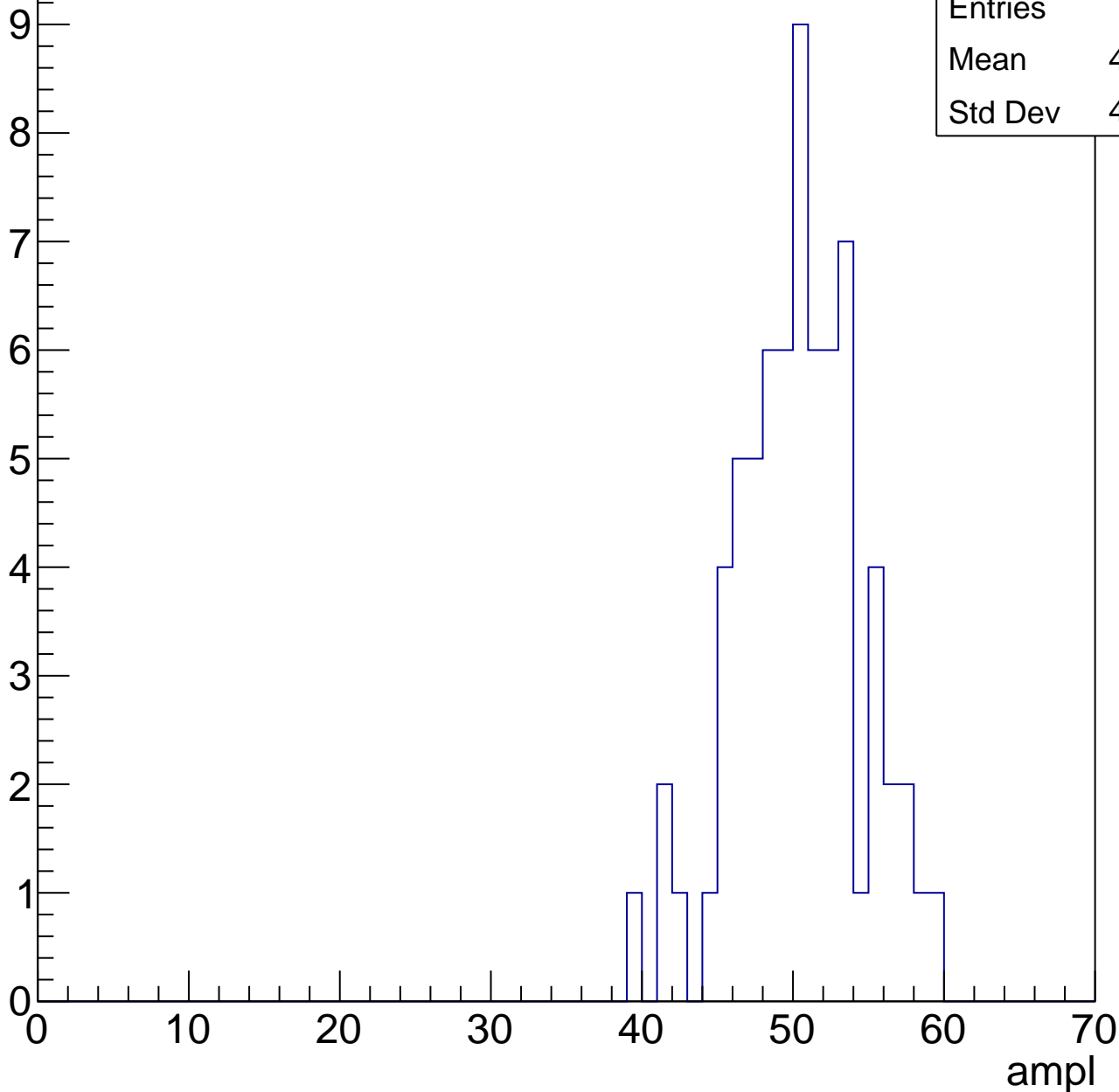


B0L001S, U18-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.86
Std Dev	4.072

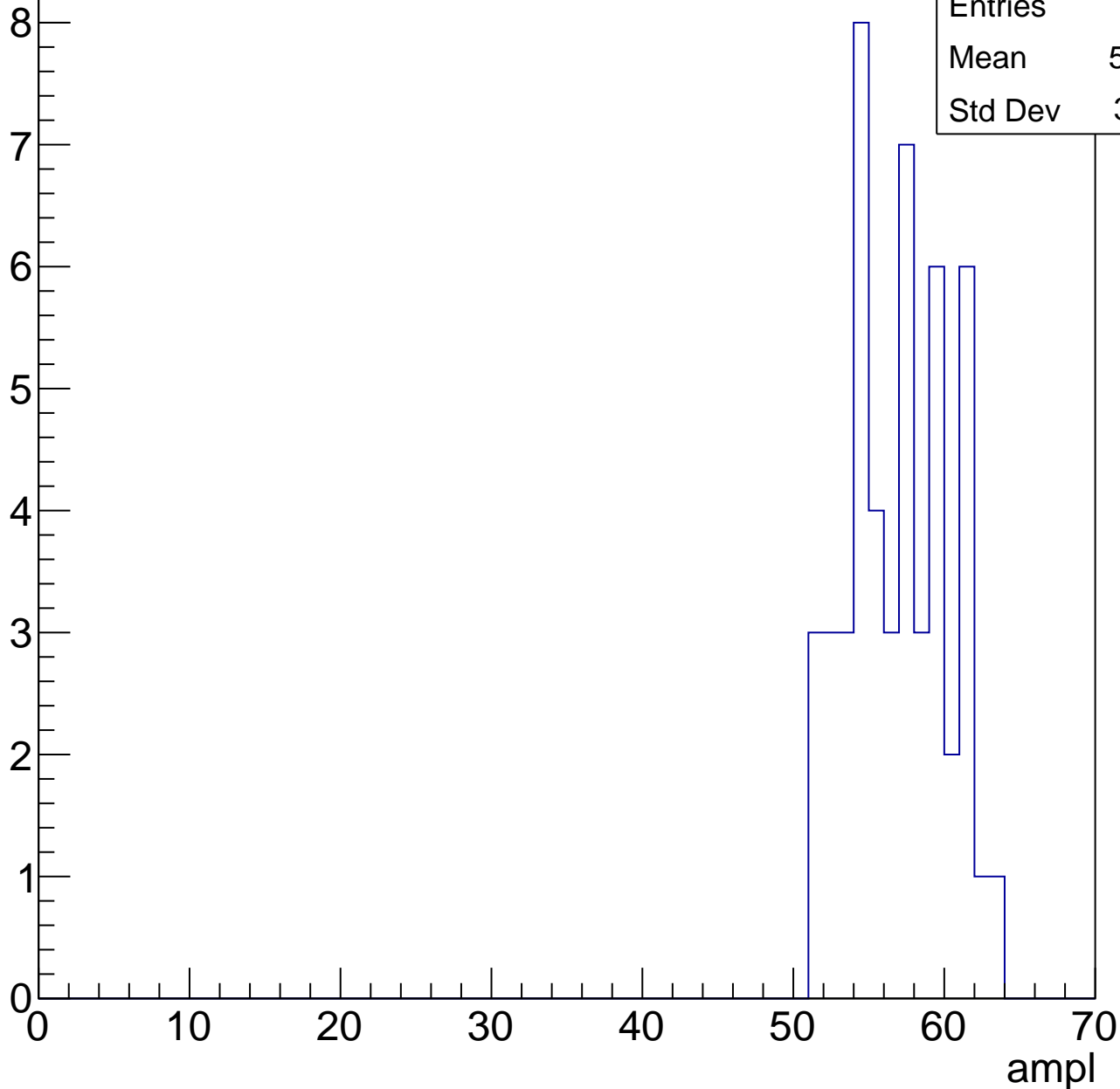


B0L001S, U18-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.52
Std Dev	3.201

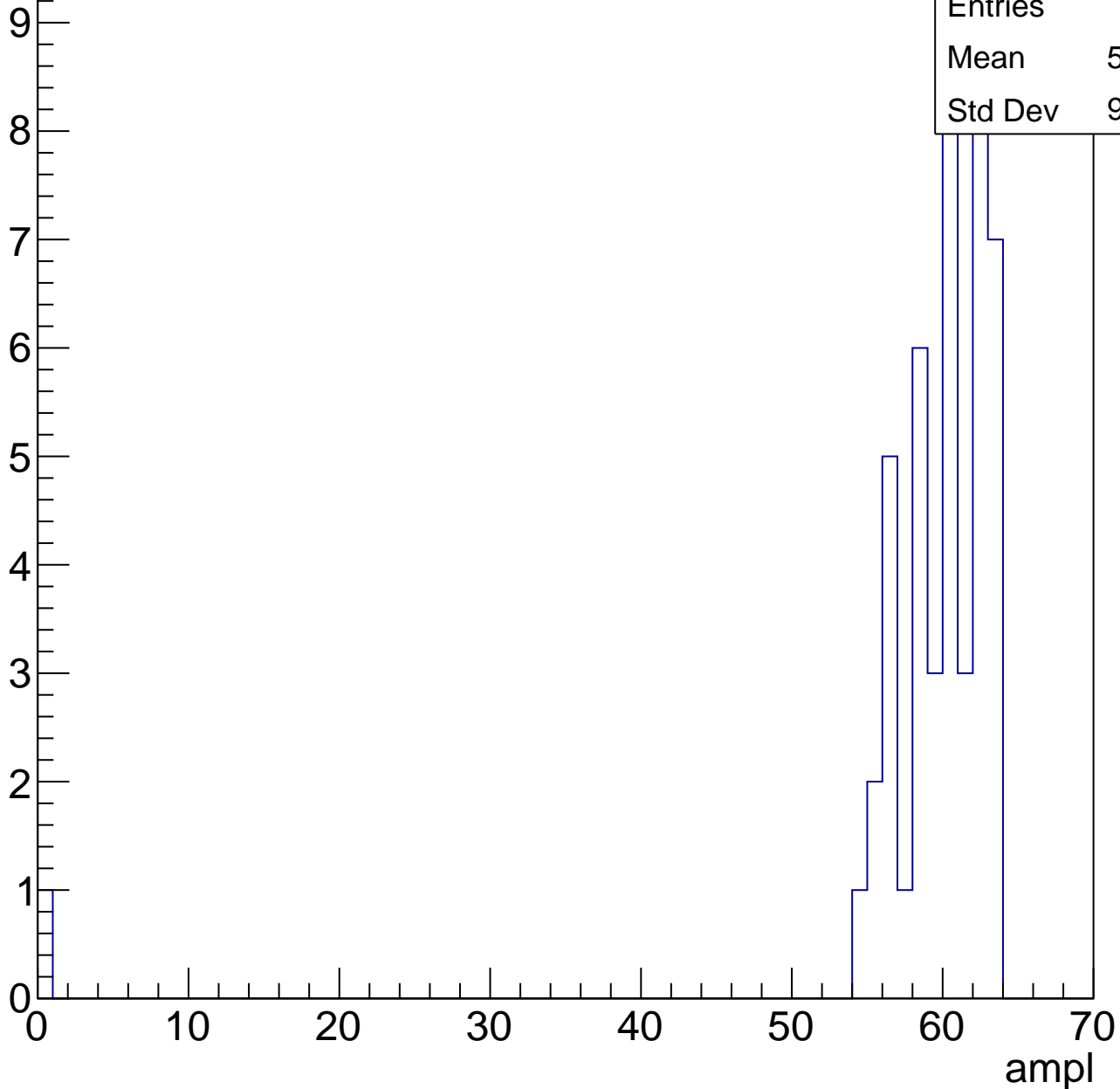


B0L001S, U18-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.43
Std Dev	9.079



B0L001S, U18-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch36, adc0

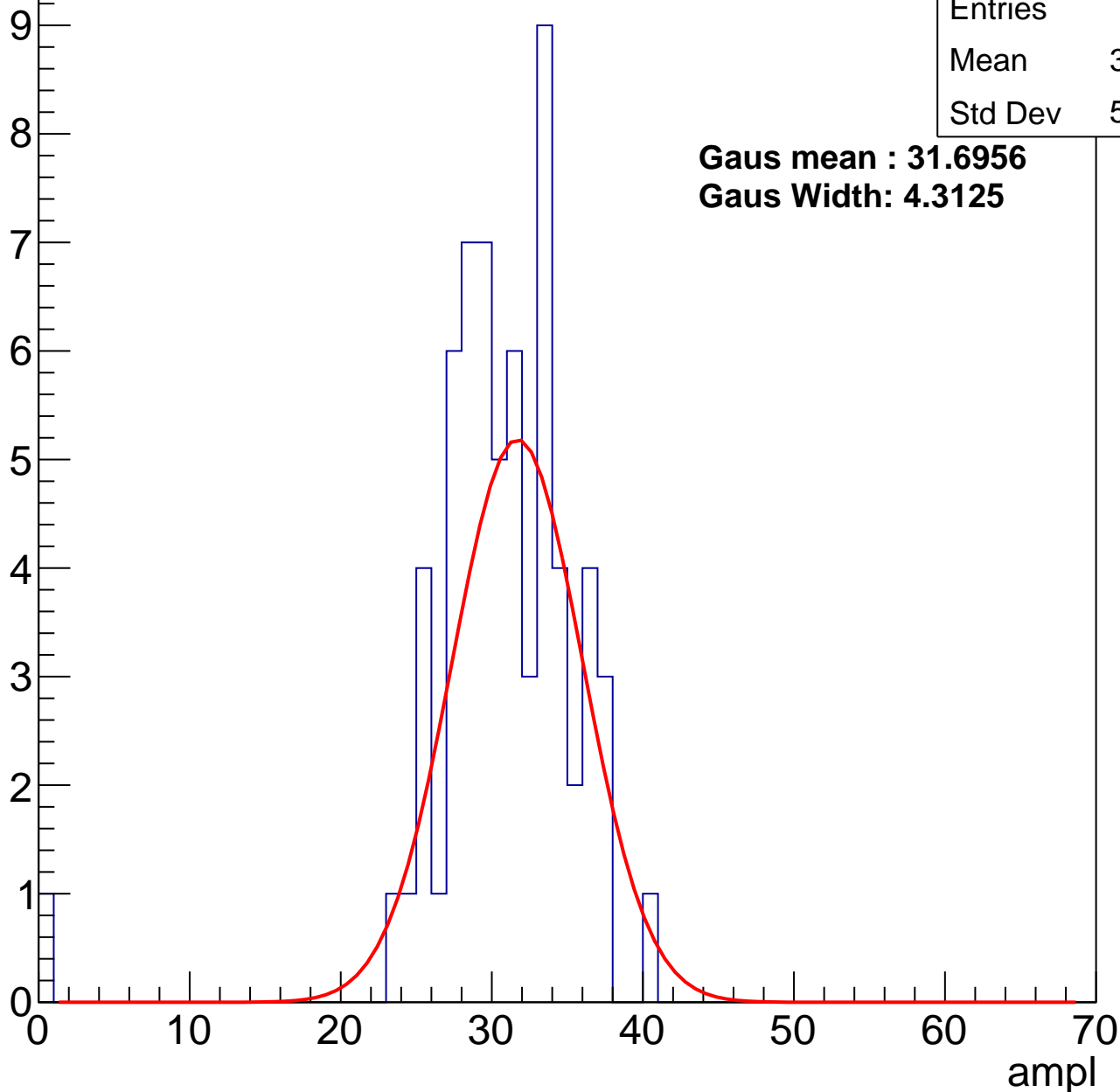
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.22
Std Dev	5.249

Gaus mean : 31.6956

Gaus Width: 4.3125



B0L001S, U18-ch36, adc1

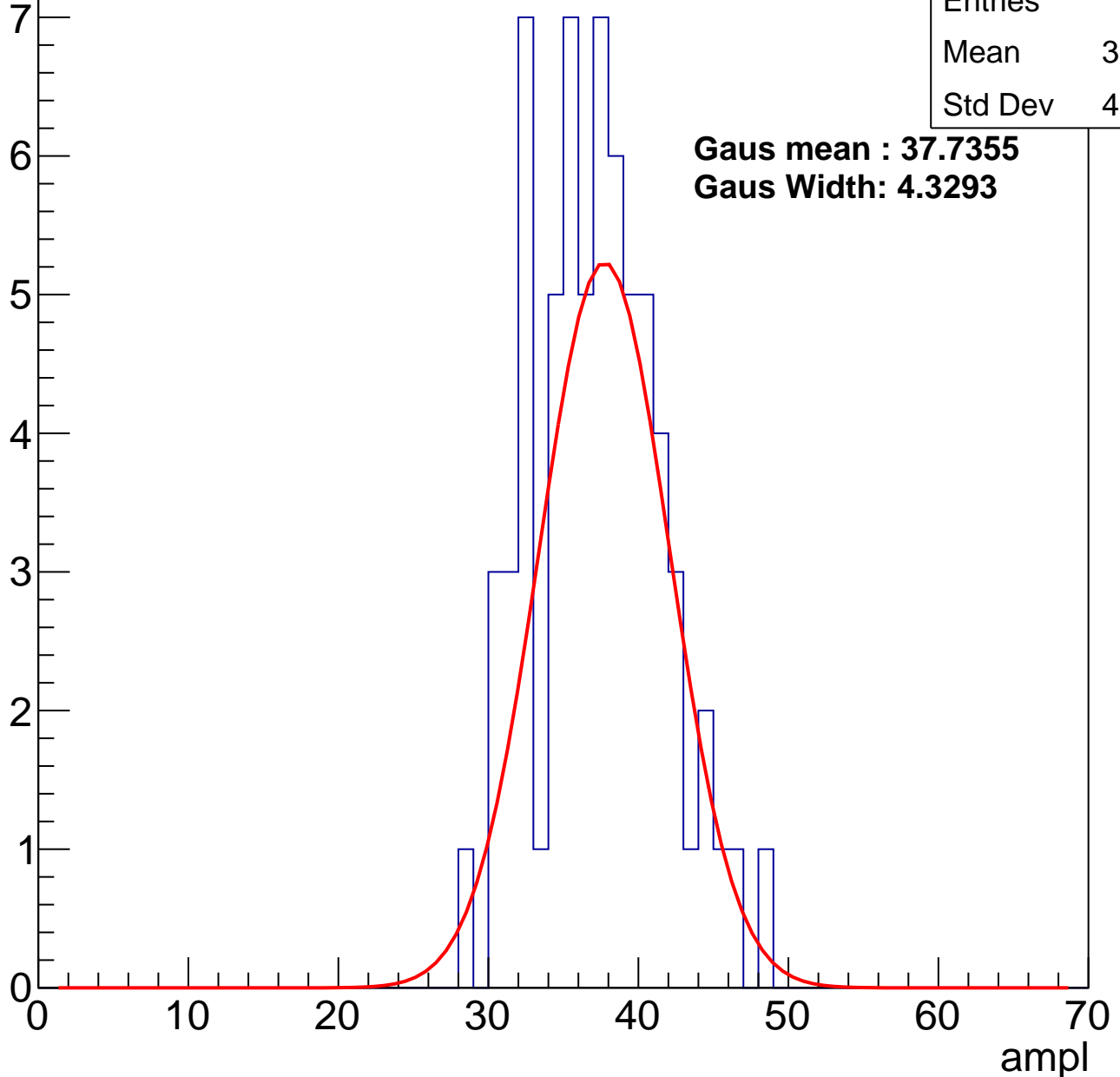
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.84
Std Dev	4.224

Gaus mean : 37.7355

Gaus Width: 4.3293



B0L001S, U18-ch36, adc2

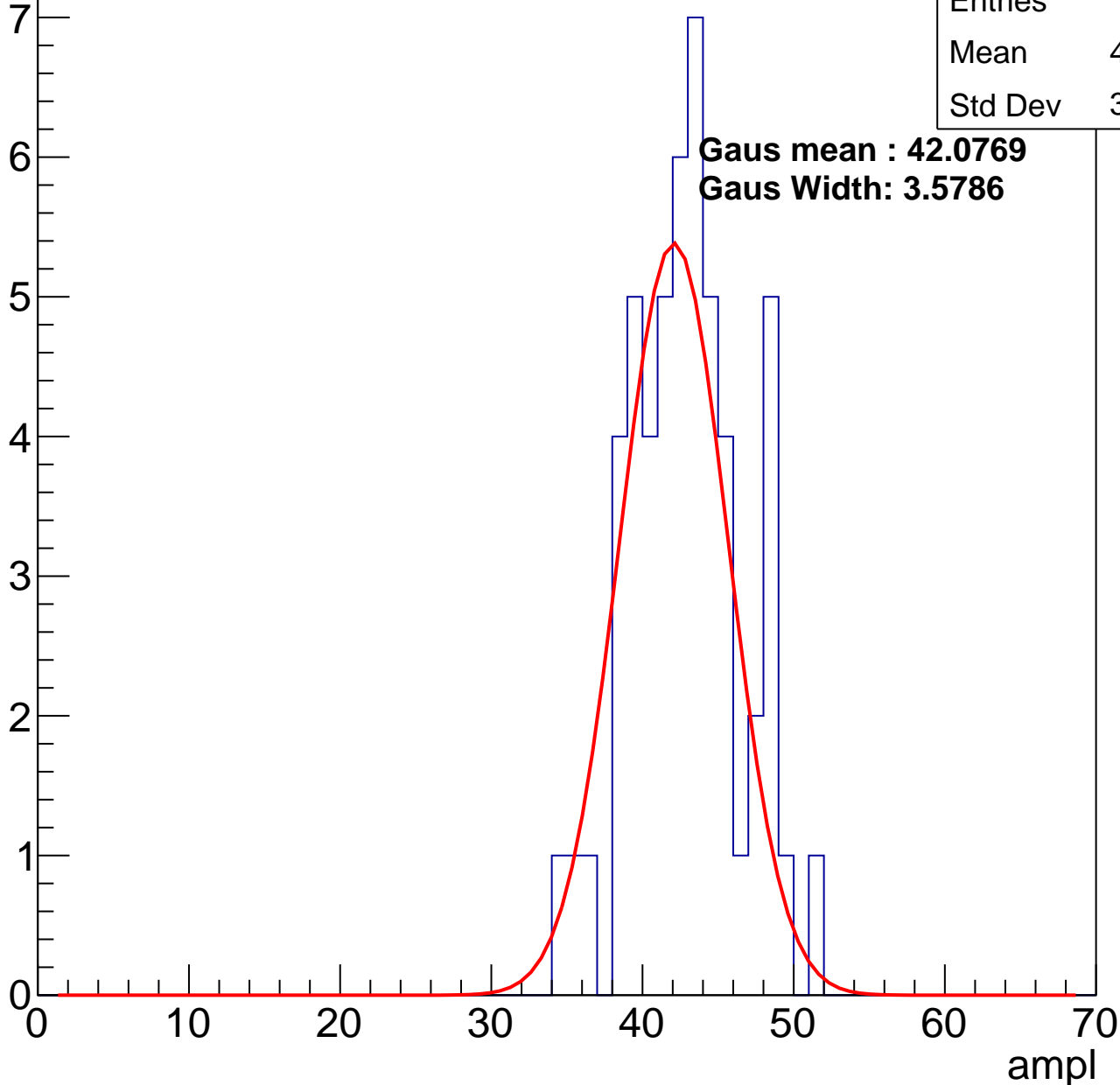
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.45
Std Dev	3.653

Gaus mean : 42.0769

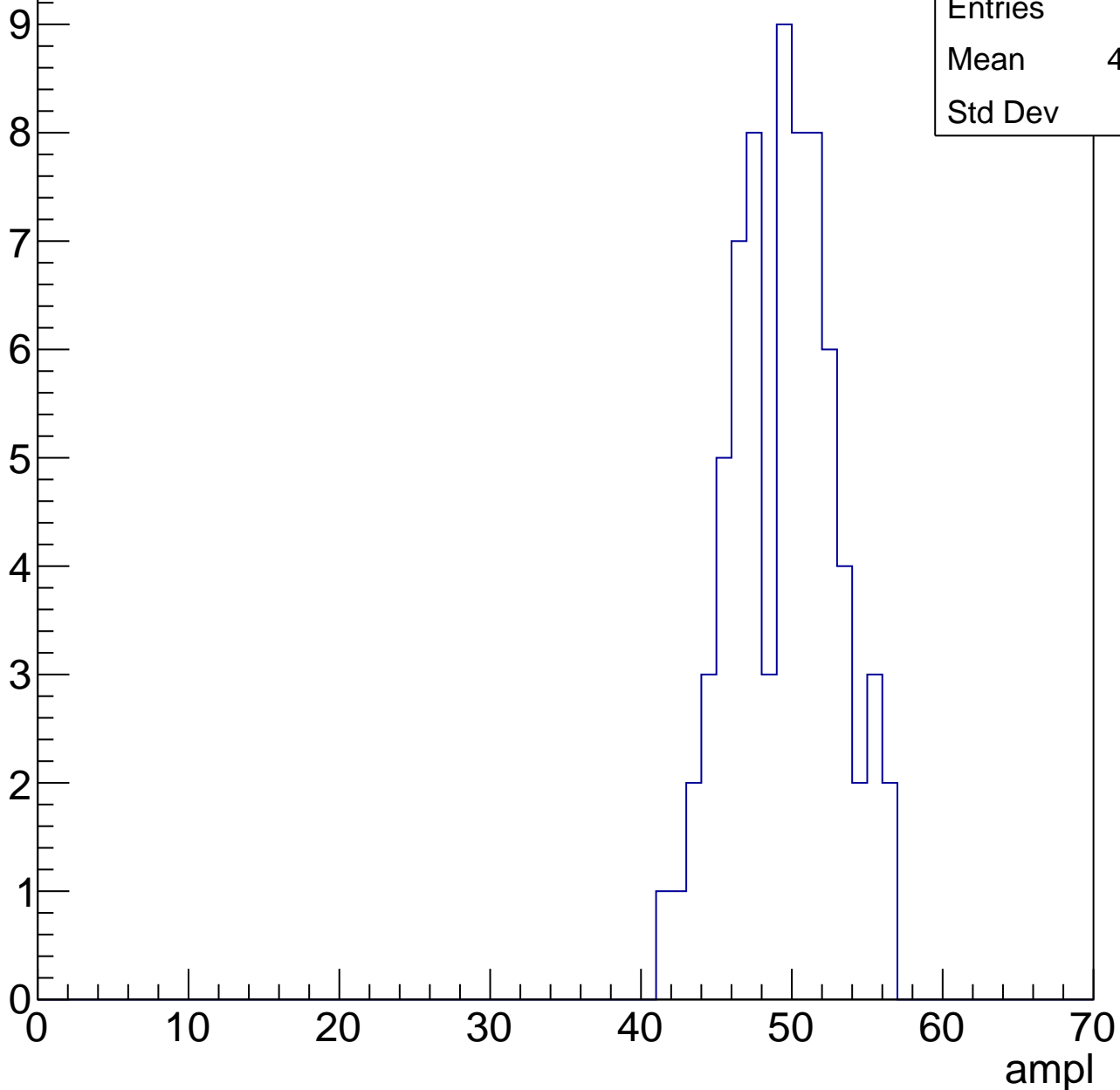
Gaus Width: 3.5786



B0L001S, U18-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



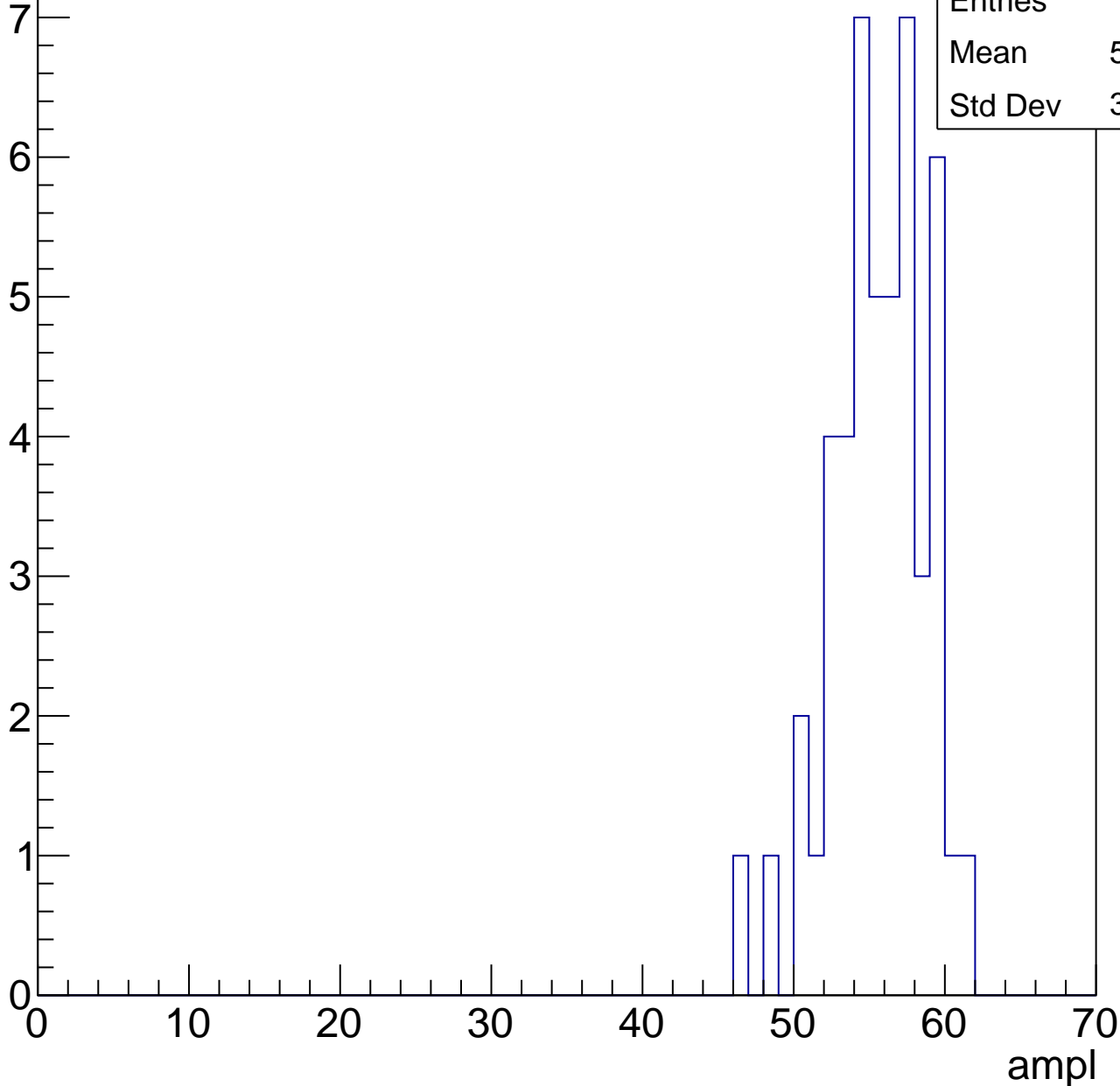
Entries	72
Mean	48.97
Std Dev	3.44

B0L001S, U18-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

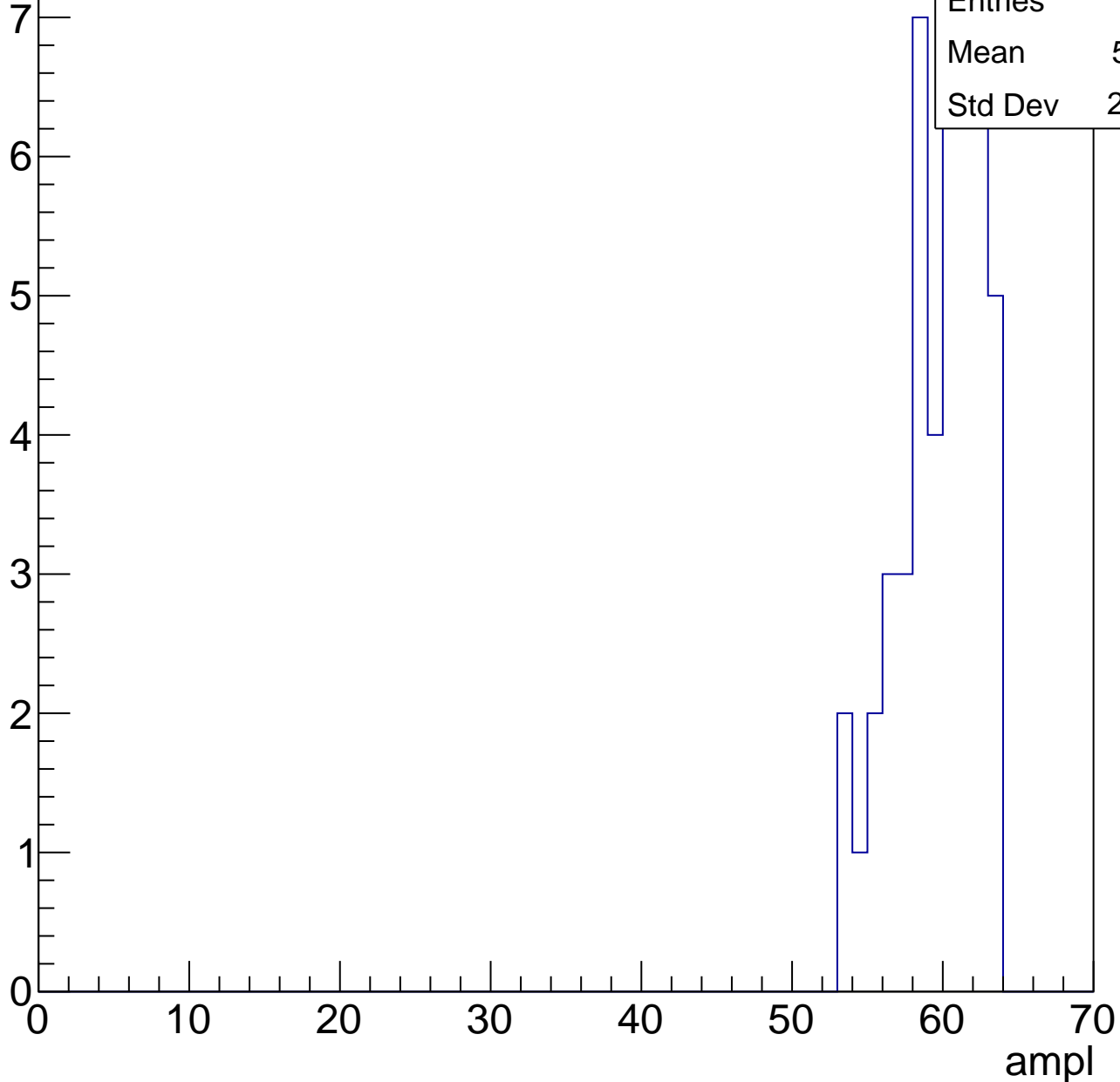
Entries	48
Mean	55.12
Std Dev	3.133



B0L001S, U18-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



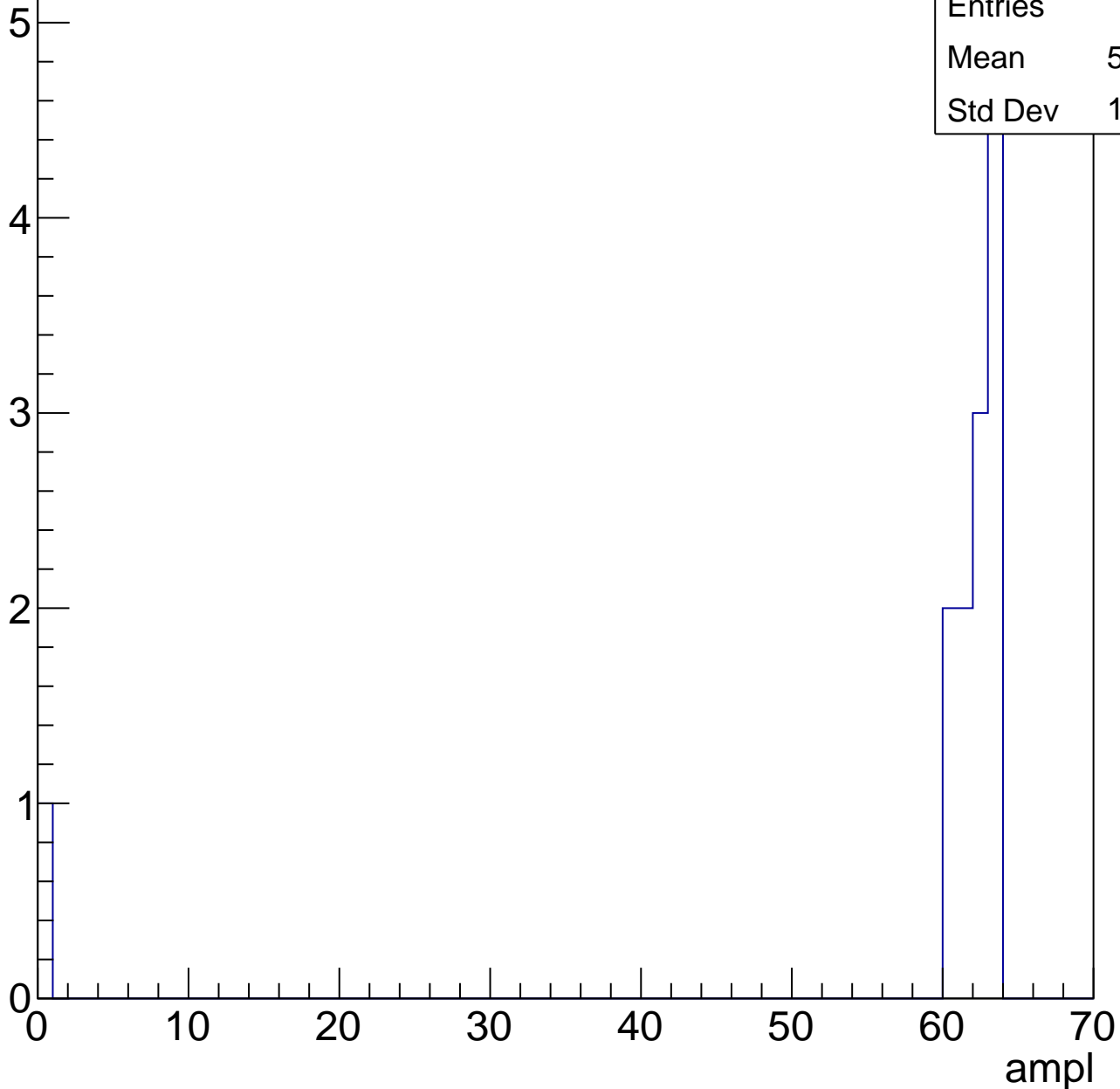
Entries	48
Mean	59.31
Std Dev	2.694

B0L001S, U18-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	13
Mean	57.15
Std Dev	16.53



B0L001S, U18-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch37, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	28.94
Std Dev	4.818

Gaus mean : 30.2383

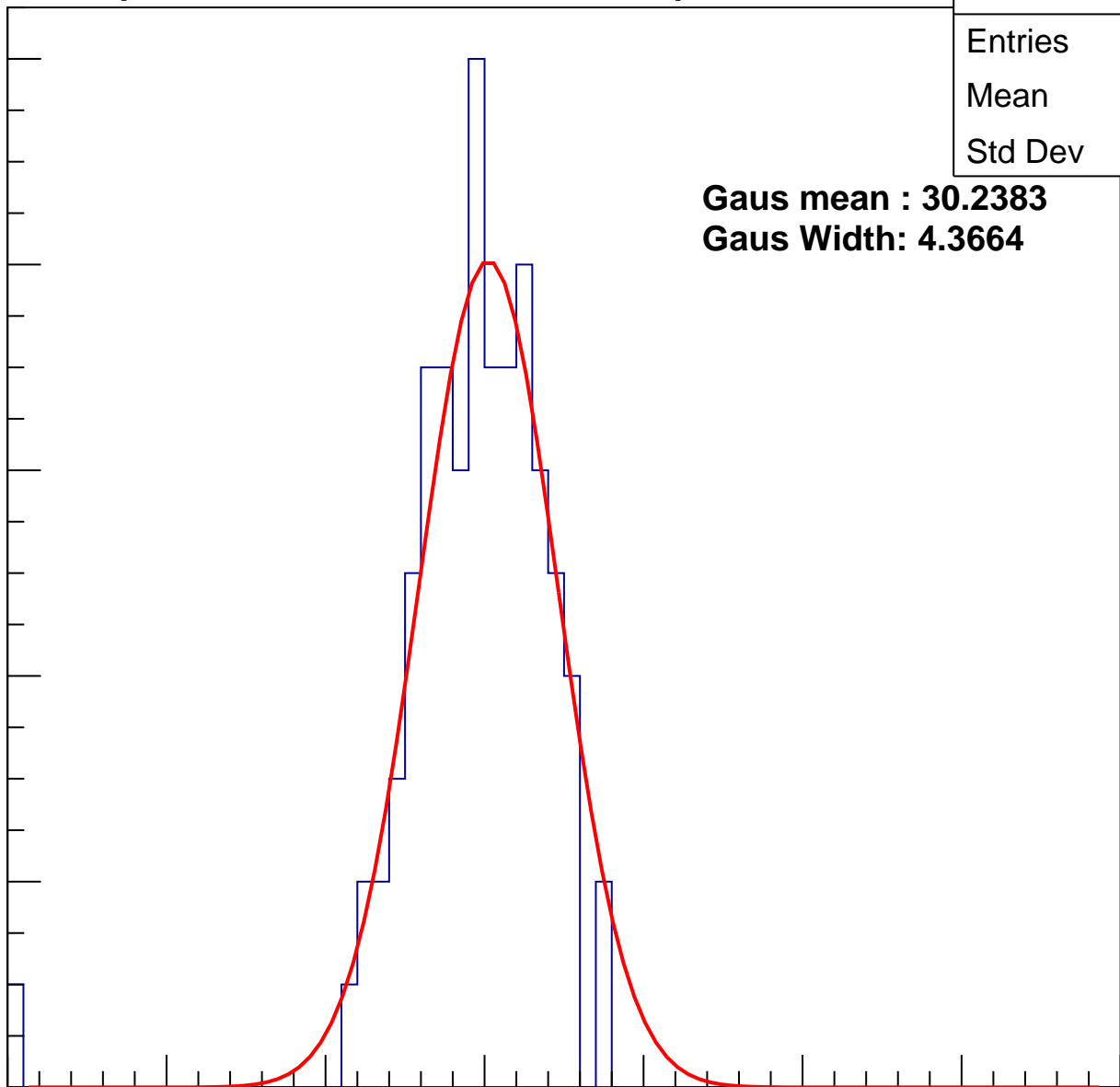
Gaus Width: 4.3664

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



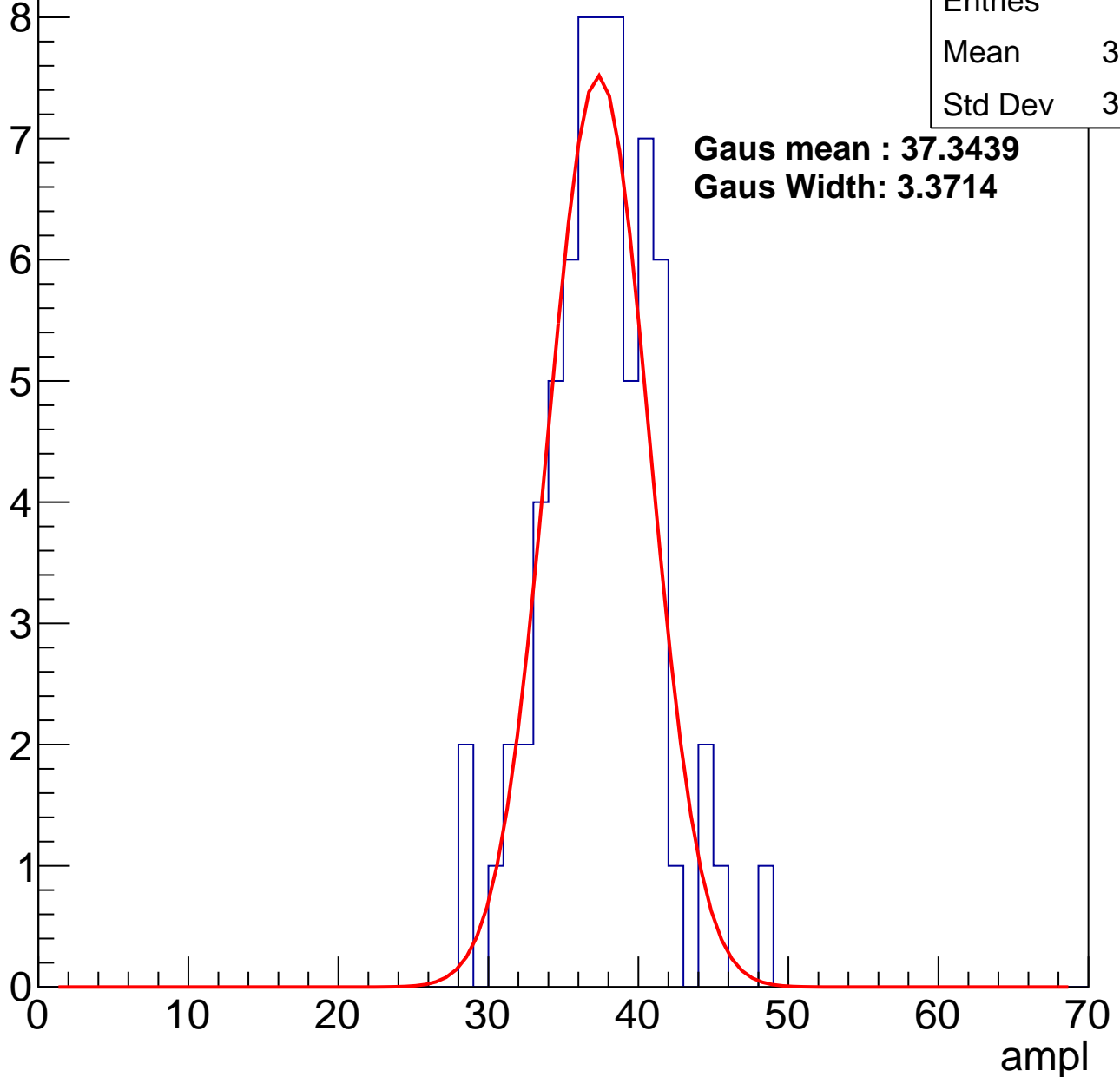
B0L001S, U18-ch37, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.04
Std Dev	3.747

Gaus mean : 37.3439
Gaus Width: 3.3714



B0L001S, U18-ch37, adc2

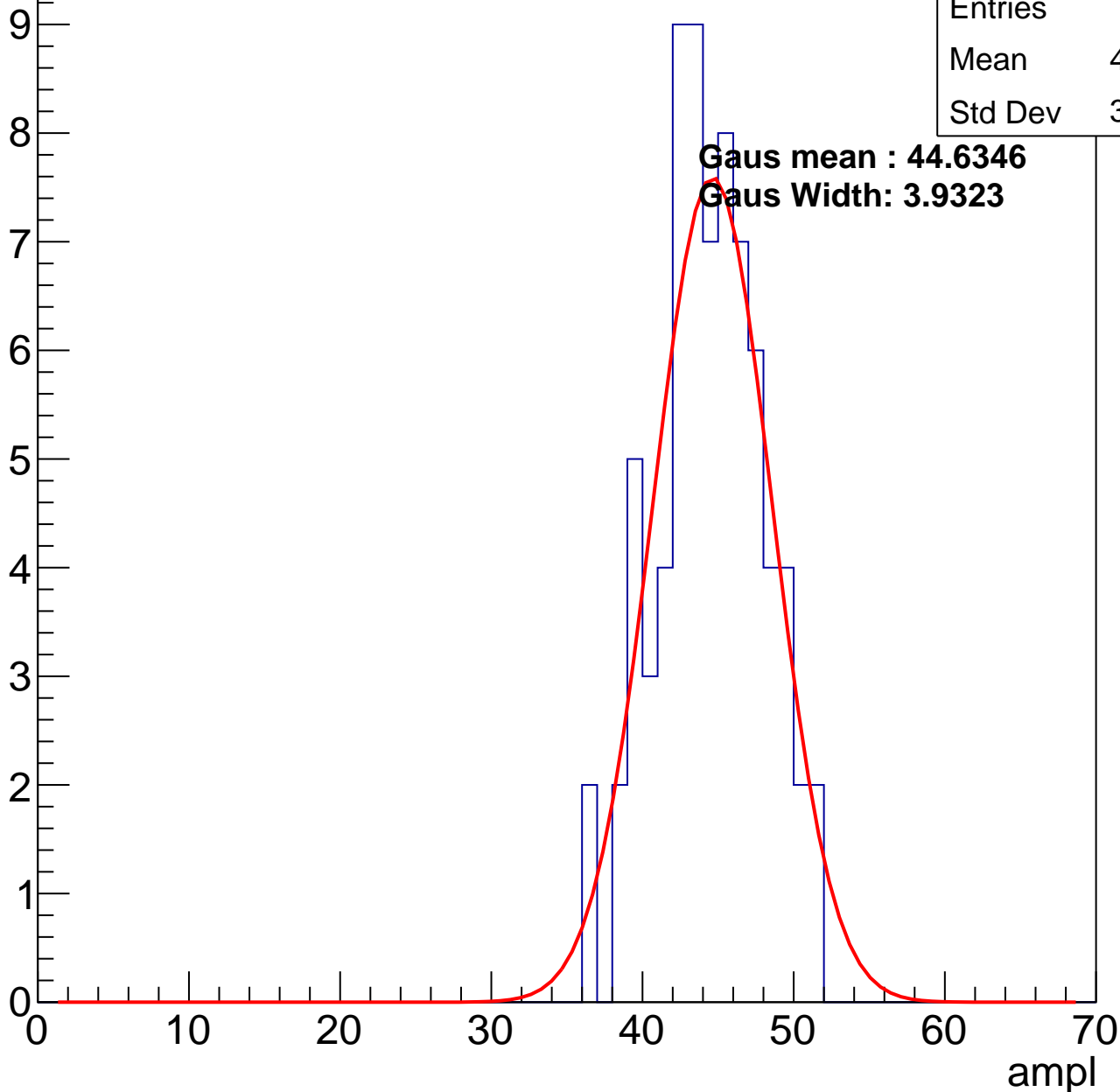
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	43.97
Std Dev	3.444

Gaus mean : 44.6346

Gaus Width: 3.9323

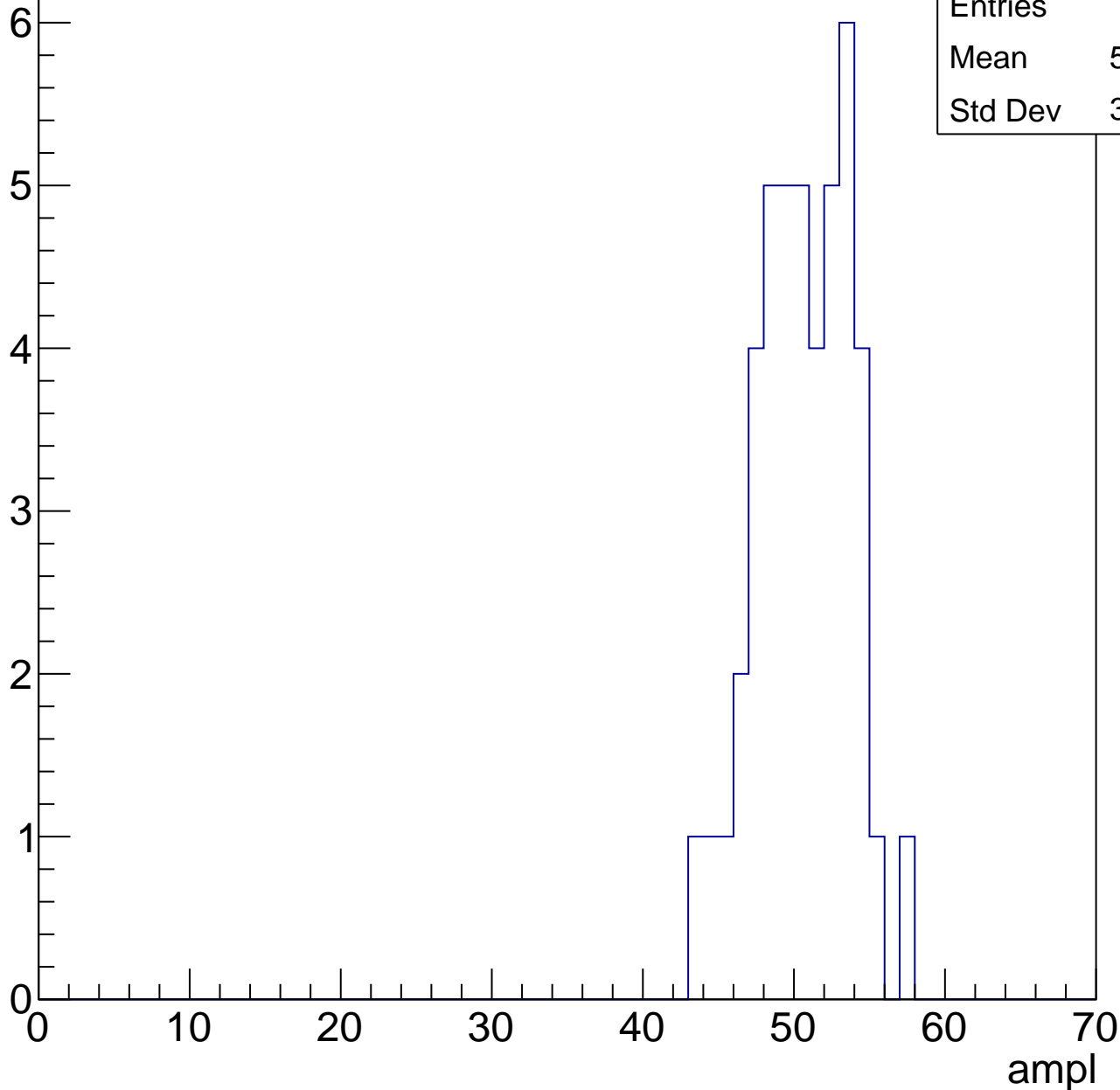


B0L001S, U18-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

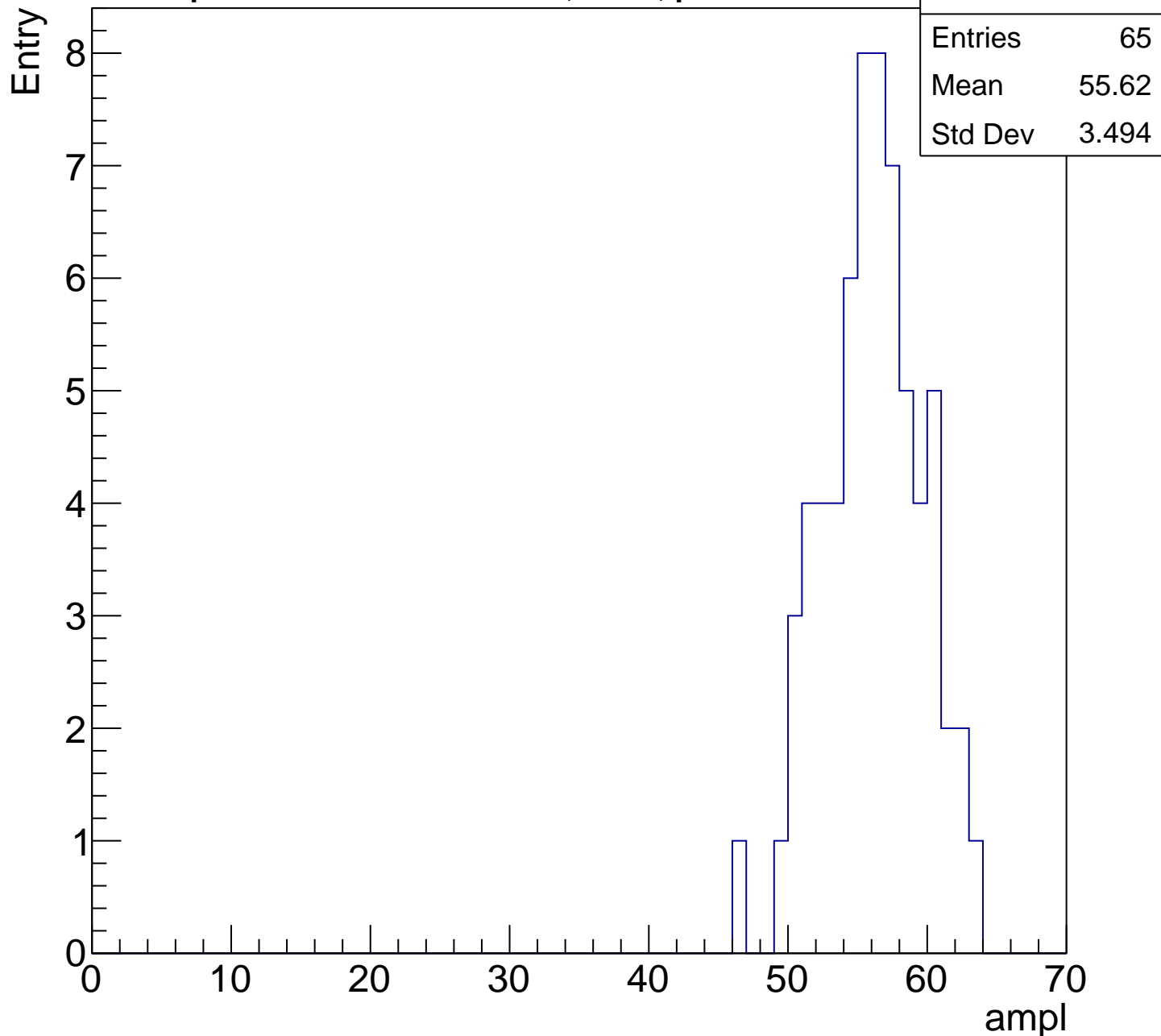
Entry

Entries	45
Mean	50.16
Std Dev	3.047



B0L001S, U18-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

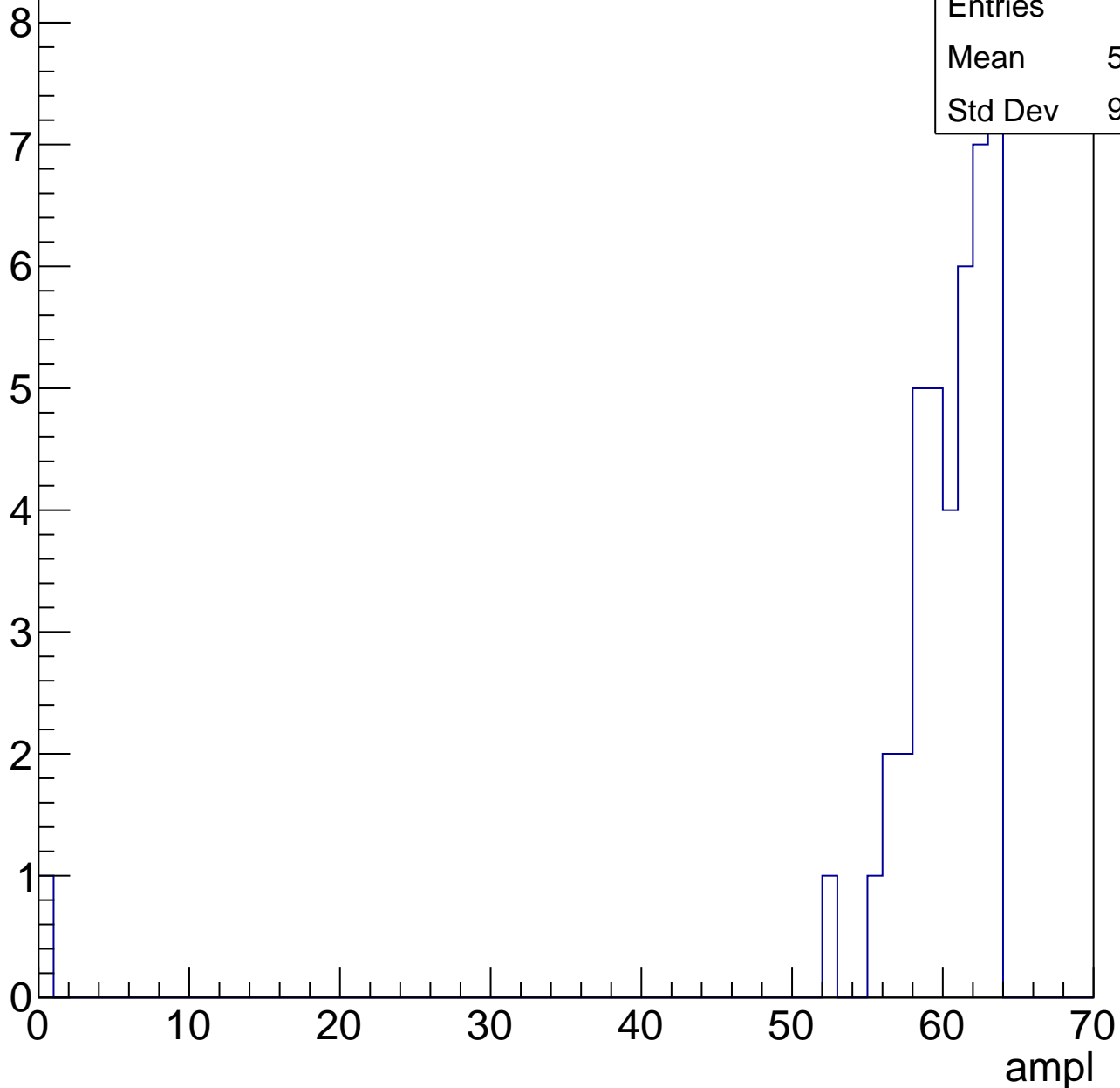


B0L001S, U18-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

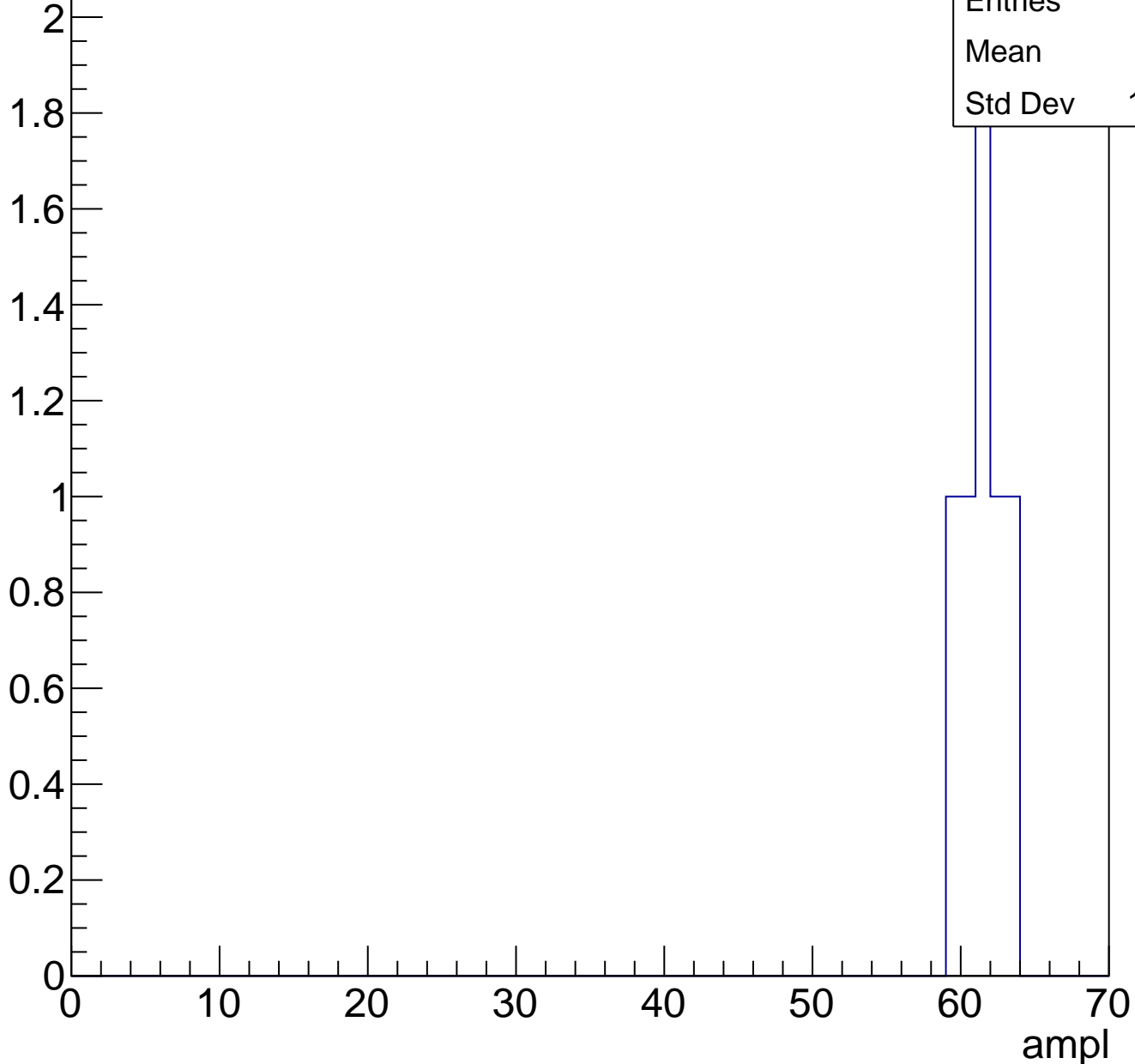
Entries	42
Mean	58.62
Std Dev	9.499



B0L001S, U18-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

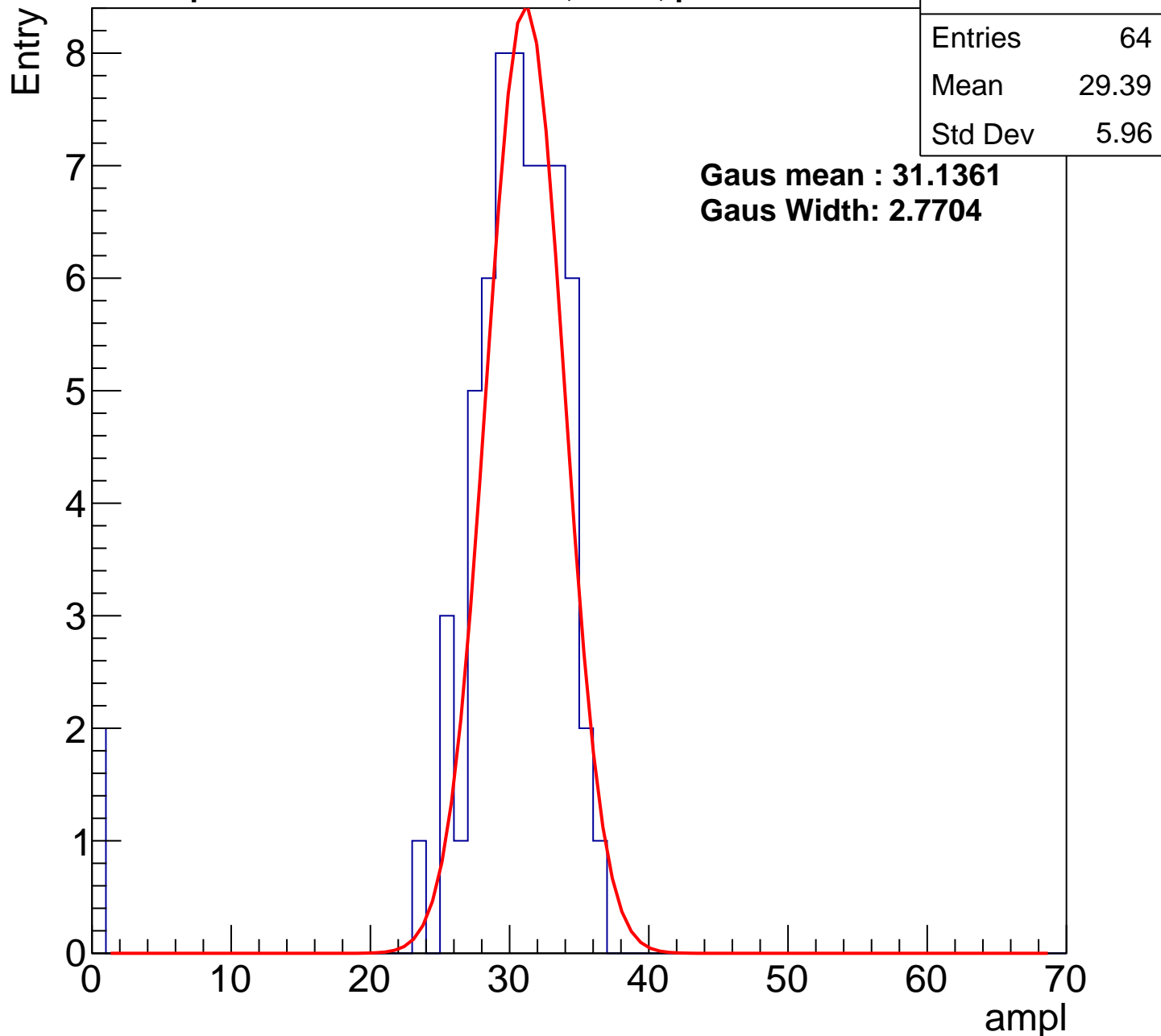
Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch38, adc0

calib_packv5_042523_0143.root, FC#9, port A1

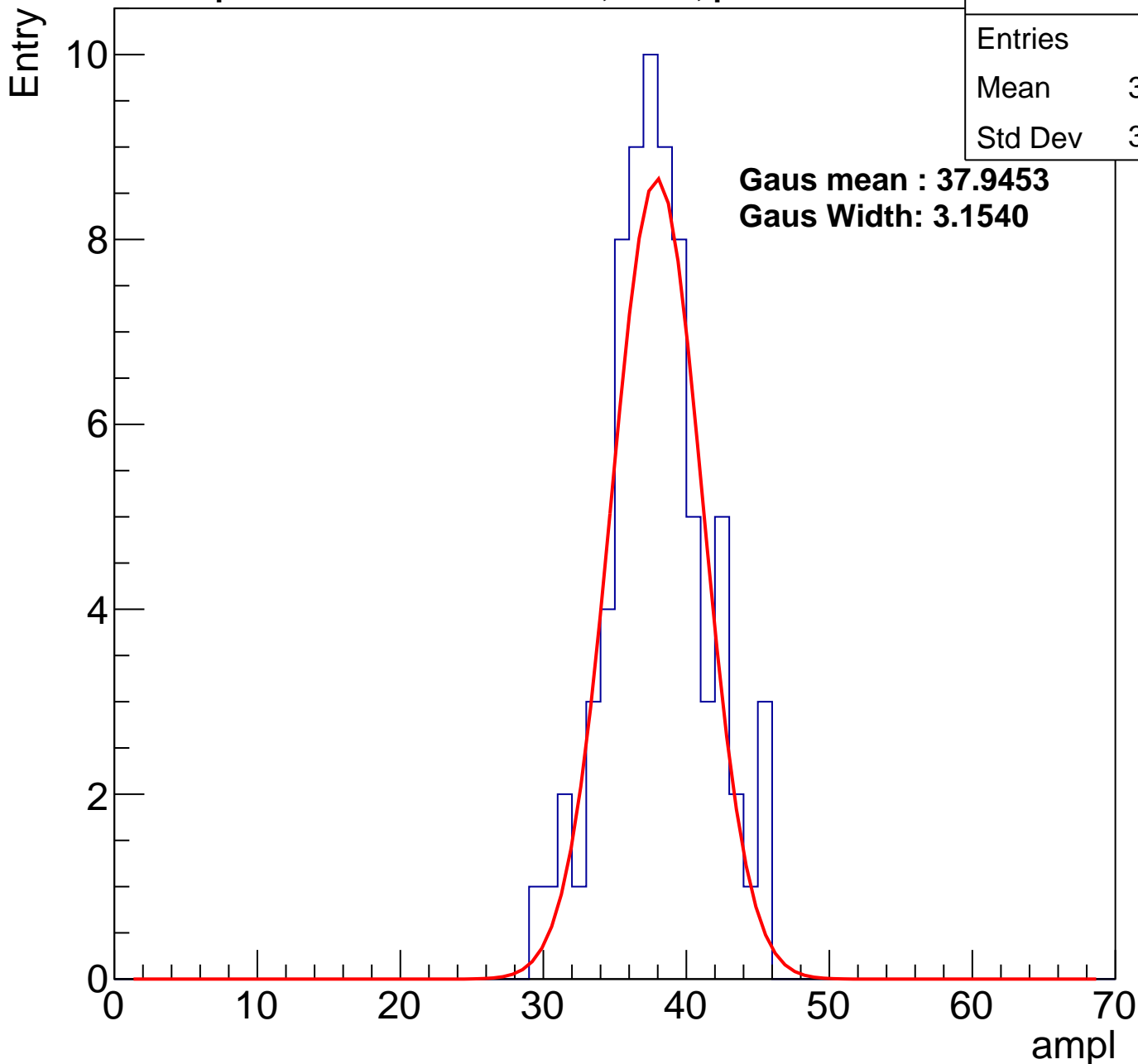


B0L001S, U18-ch38, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	37.52
Std Dev	3.438

Gaus mean : 37.9453
Gaus Width: 3.1540



B0L001S, U18-ch38, adc2

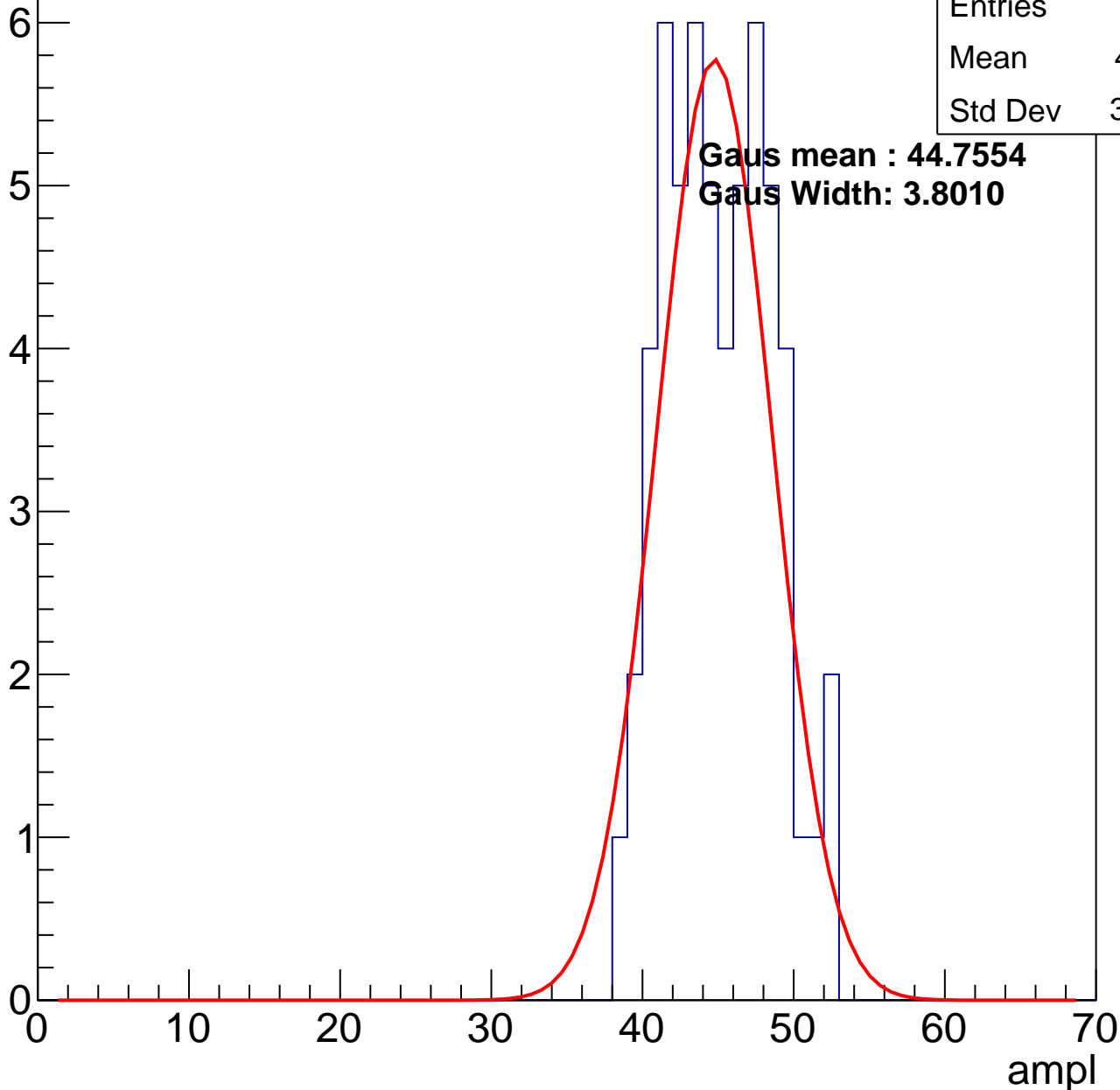
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.61
Std Dev	3.453

Gaus mean : 44.7554

Gaus Width: 3.8010

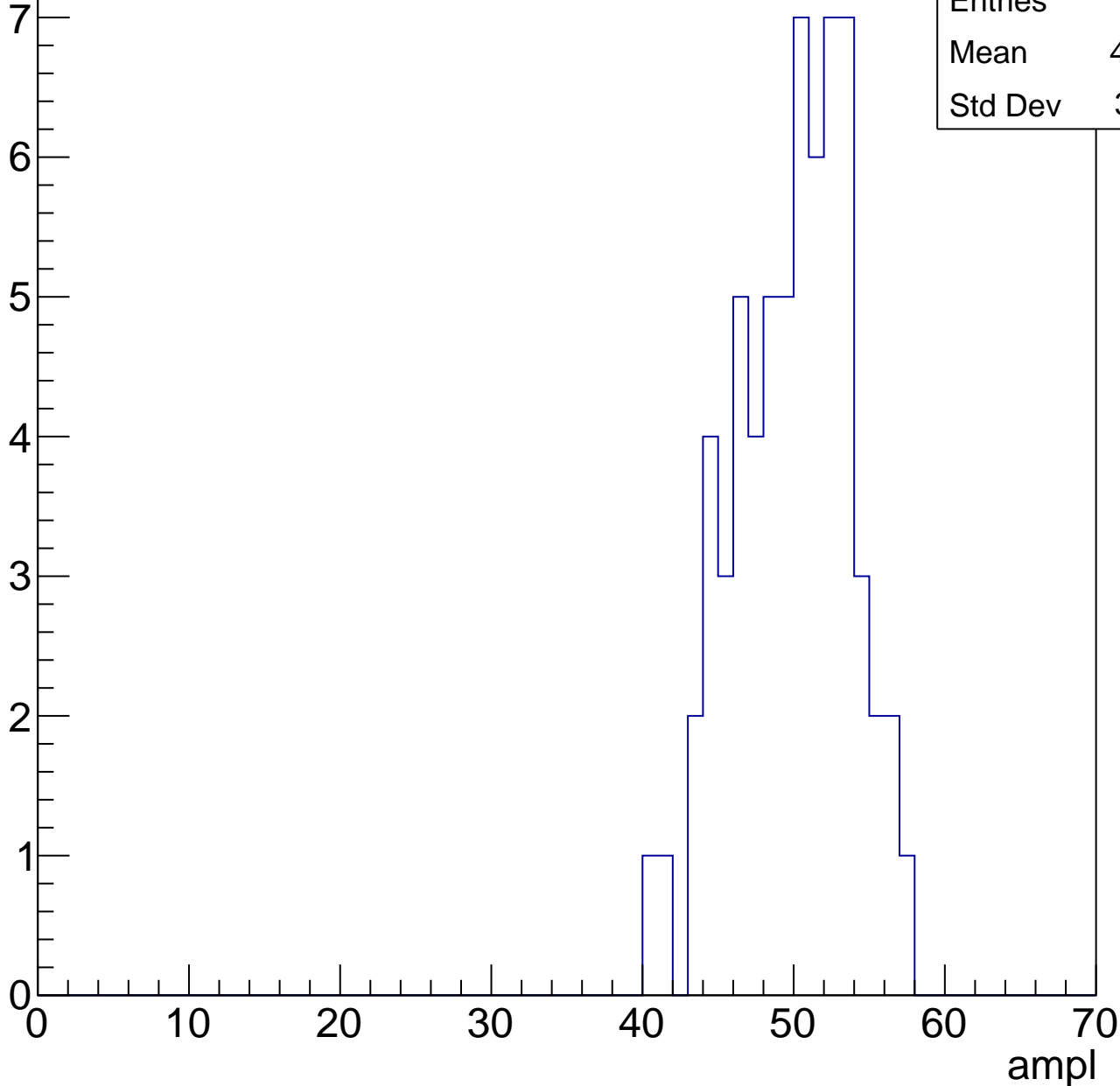


B0L001S, U18-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

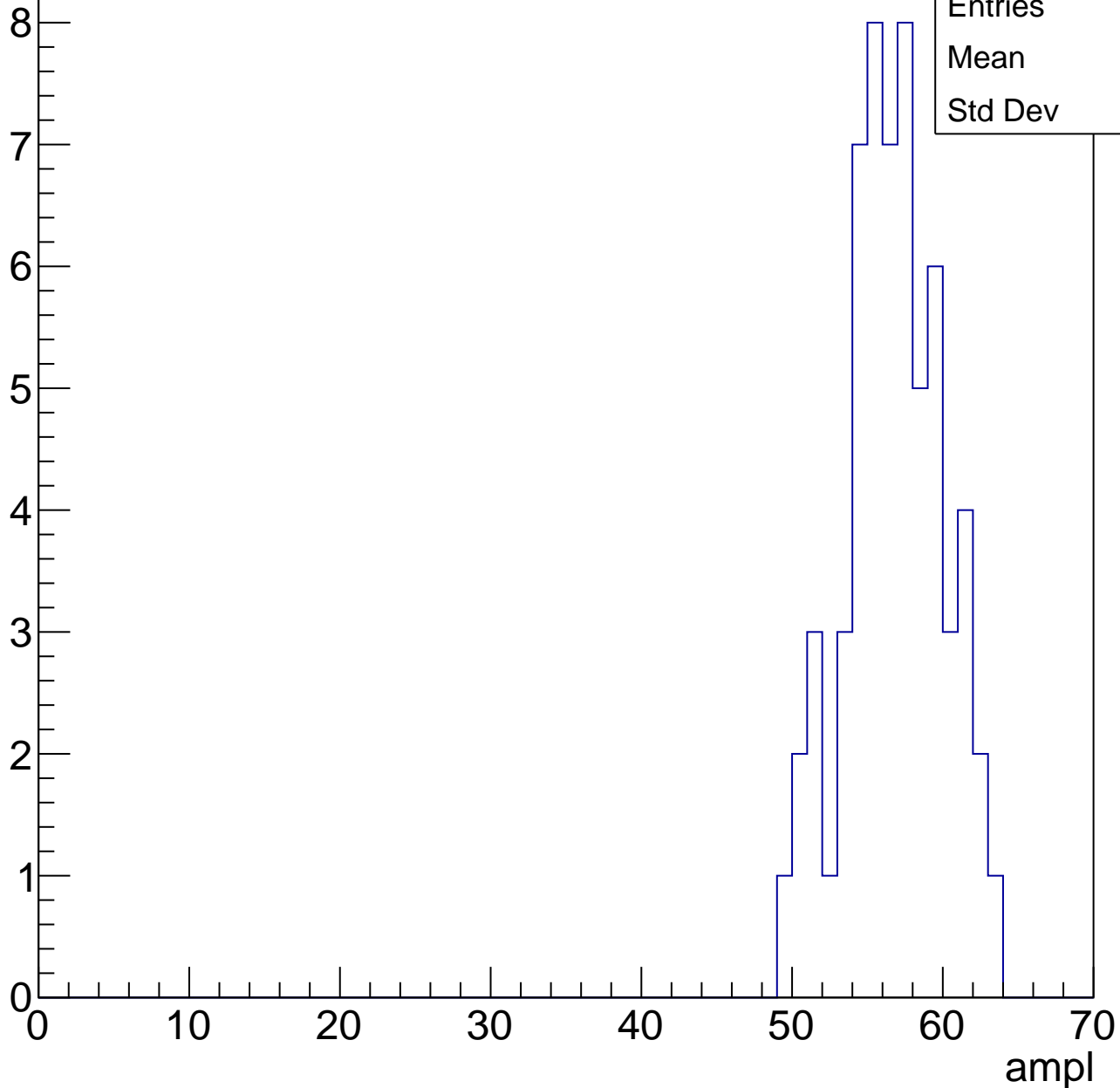
Entries	65
Mean	49.43
Std Dev	3.791



B0L001S, U18-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

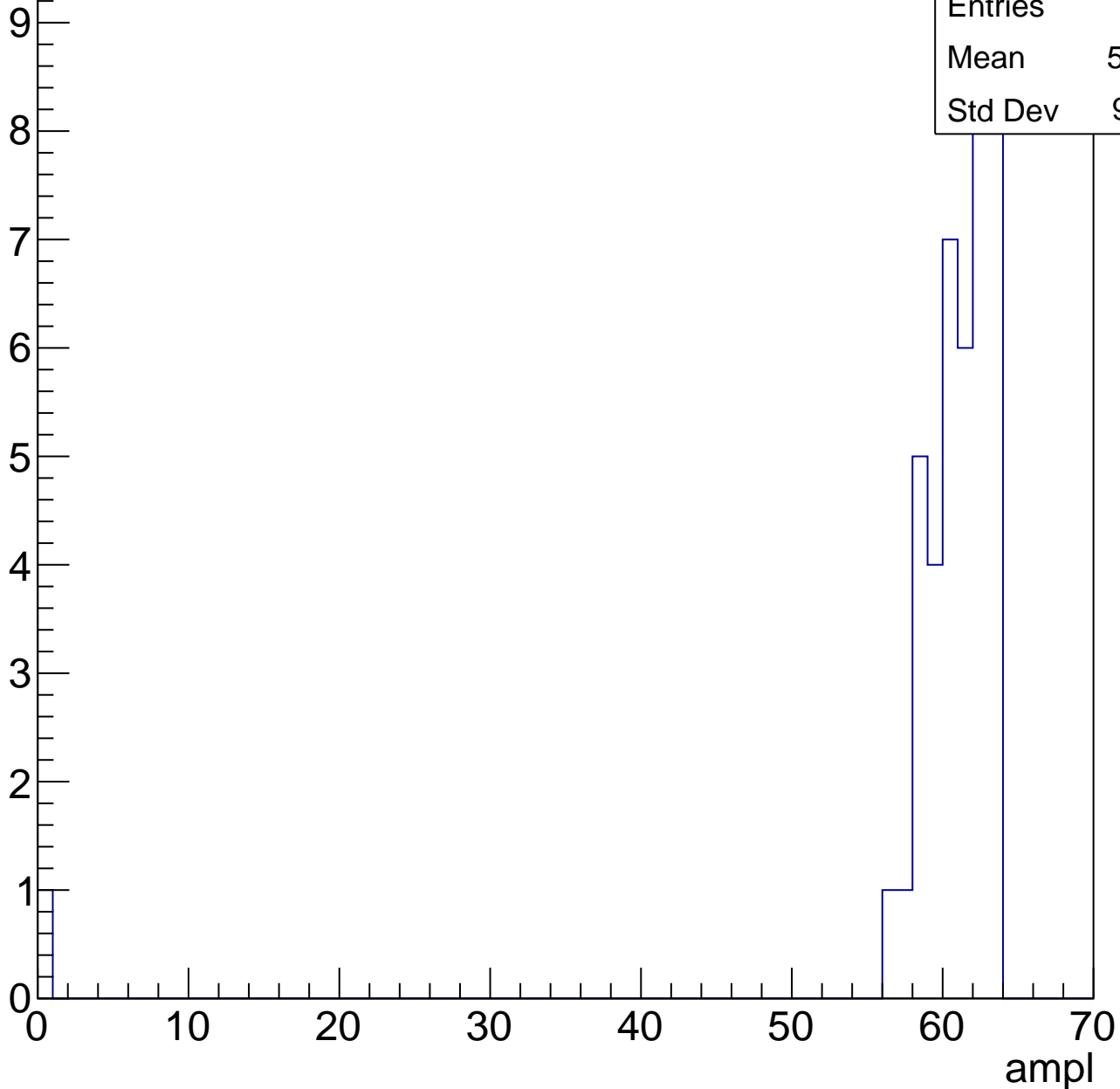


B0L001S, U18-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.24
Std Dev	9.441



B0L001S, U18-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch39, adc0

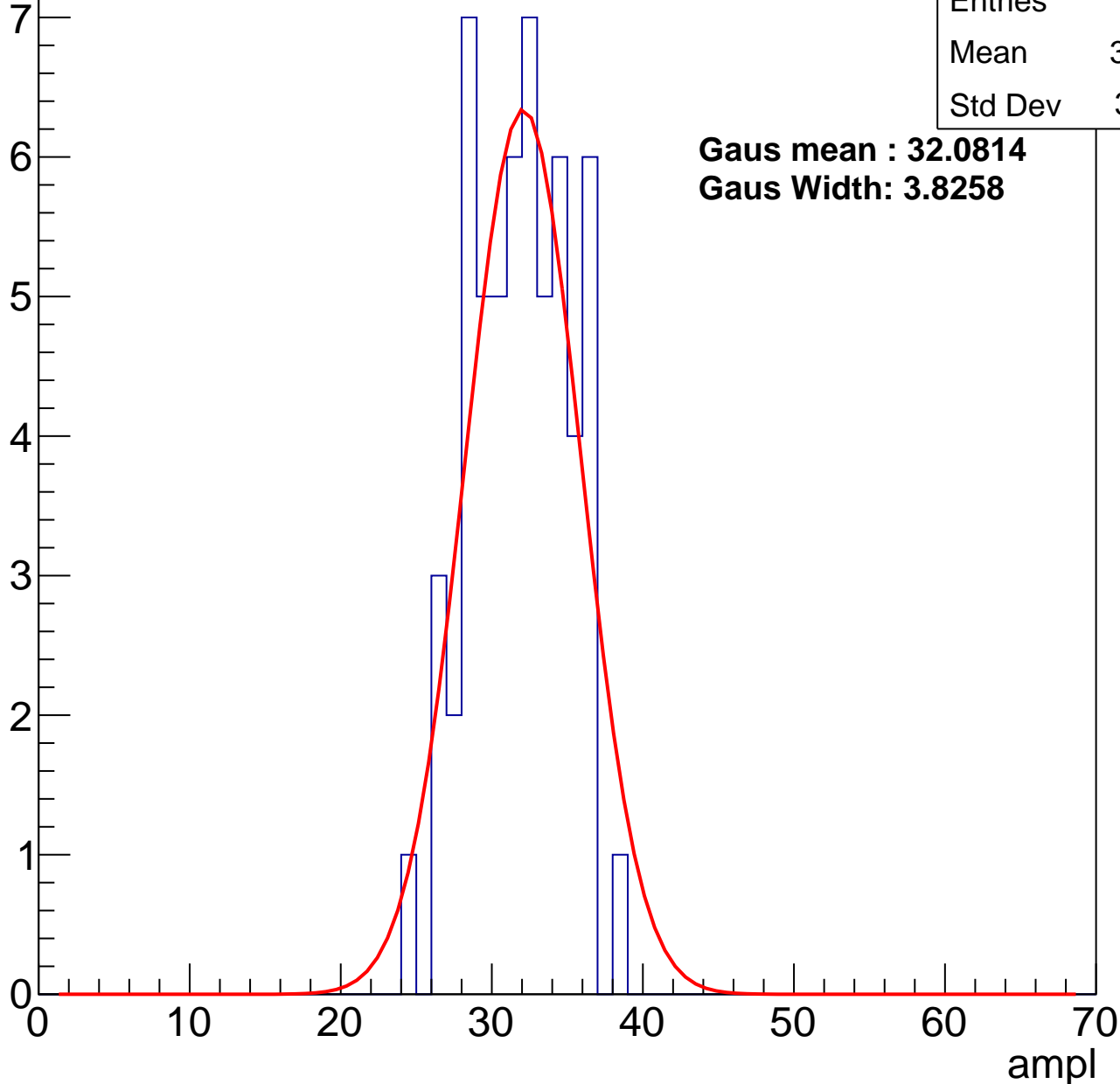
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.38
Std Dev	3.161

Gaus mean : 32.0814

Gaus Width: 3.8258



B0L001S, U18-ch39, adc1

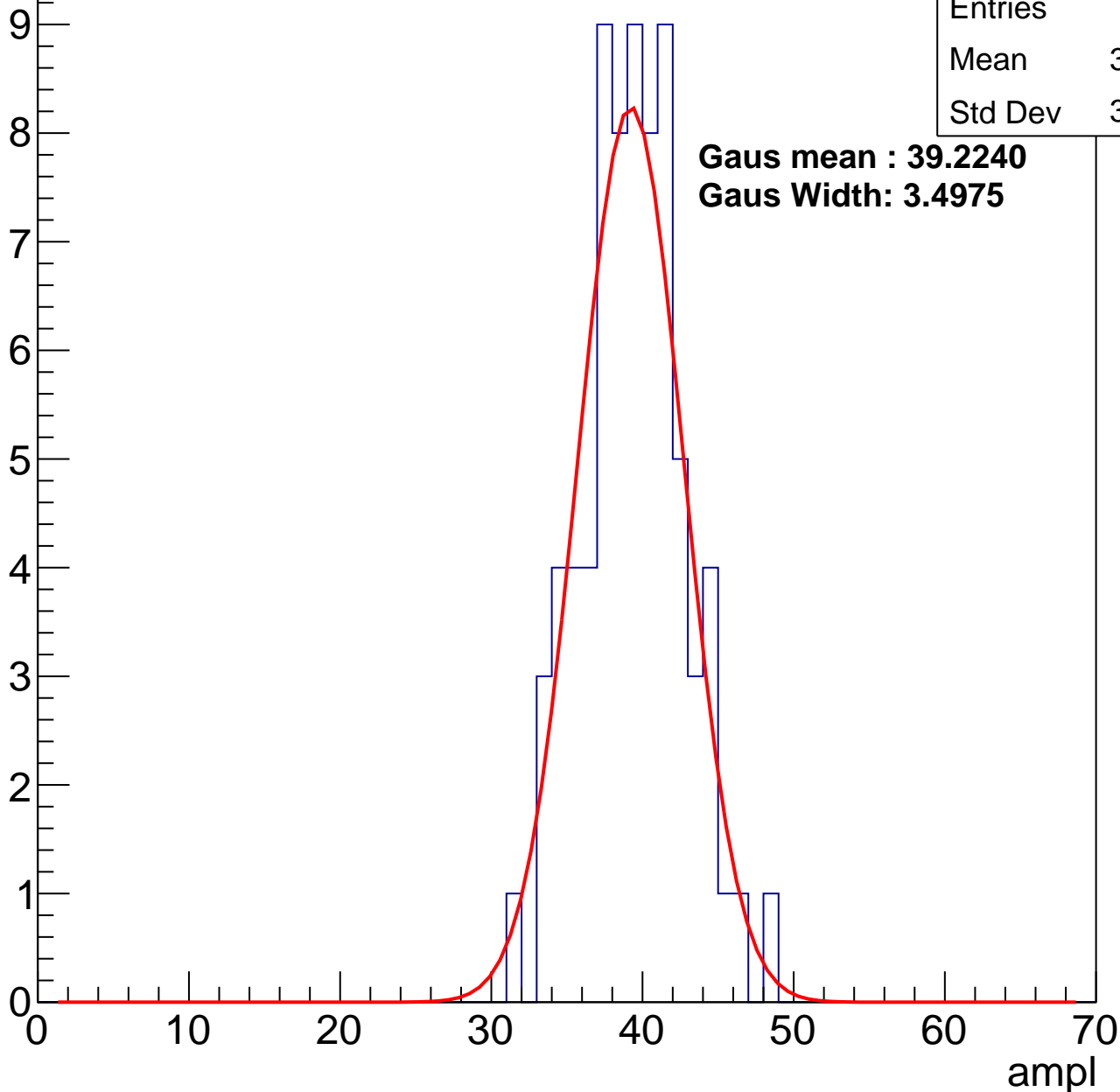
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.93
Std Dev	3.326

Gaus mean : 39.2240

Gaus Width: 3.4975



B0L001S, U18-ch39, adc2

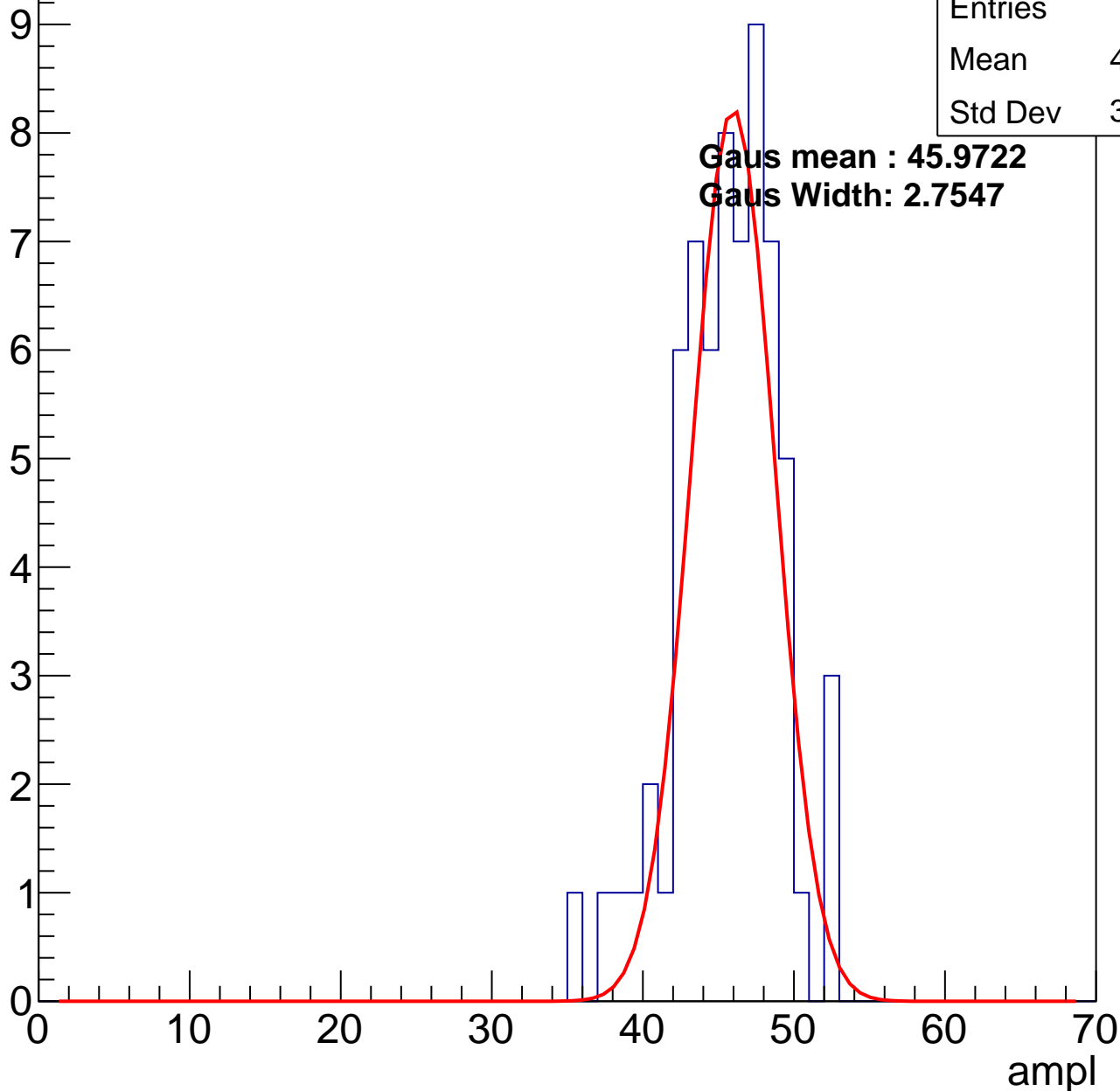
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.14
Std Dev	3.393

Gaus mean : 45.9722

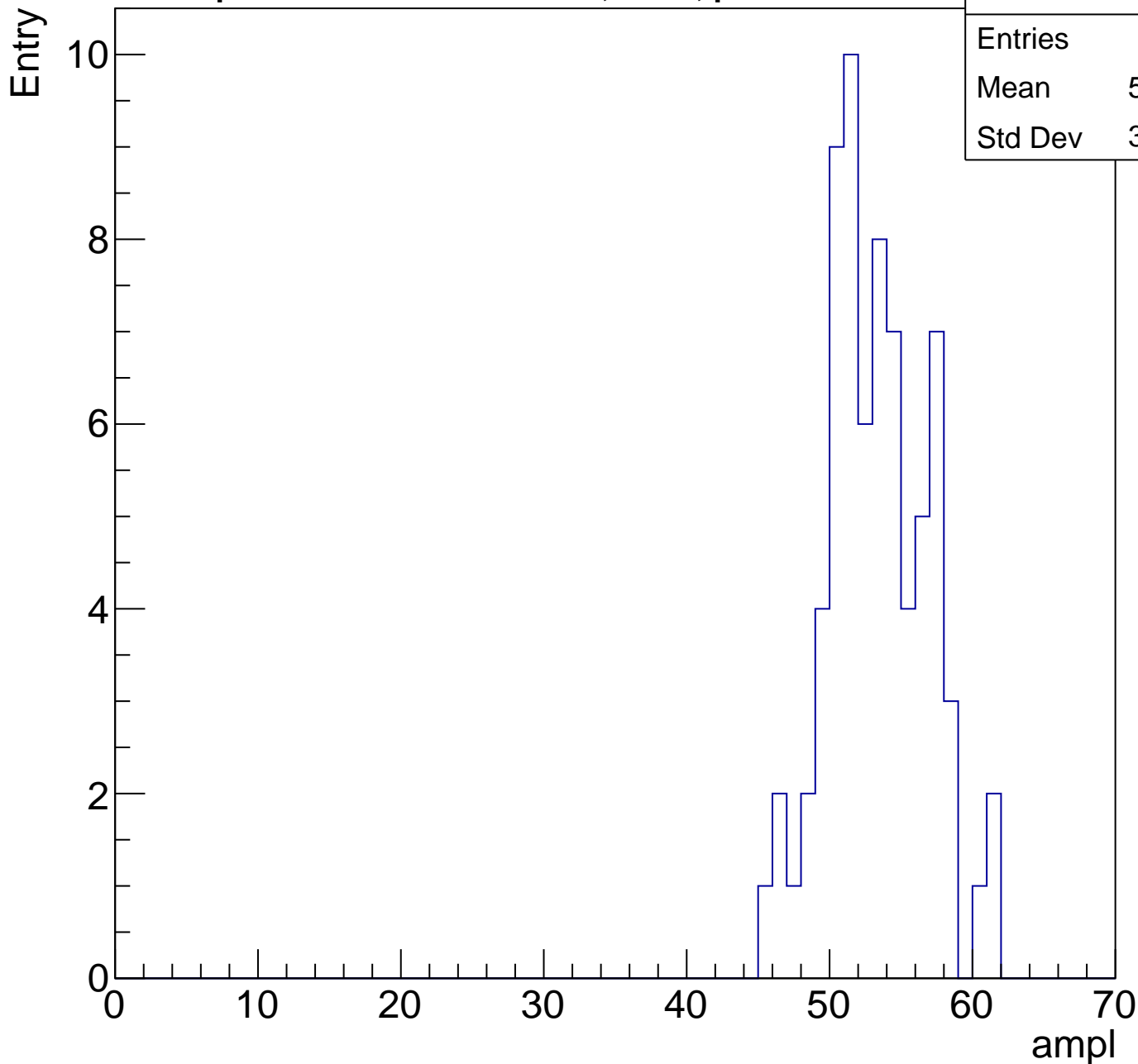
Gaus Width: 2.7547



B0L001S, U18-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

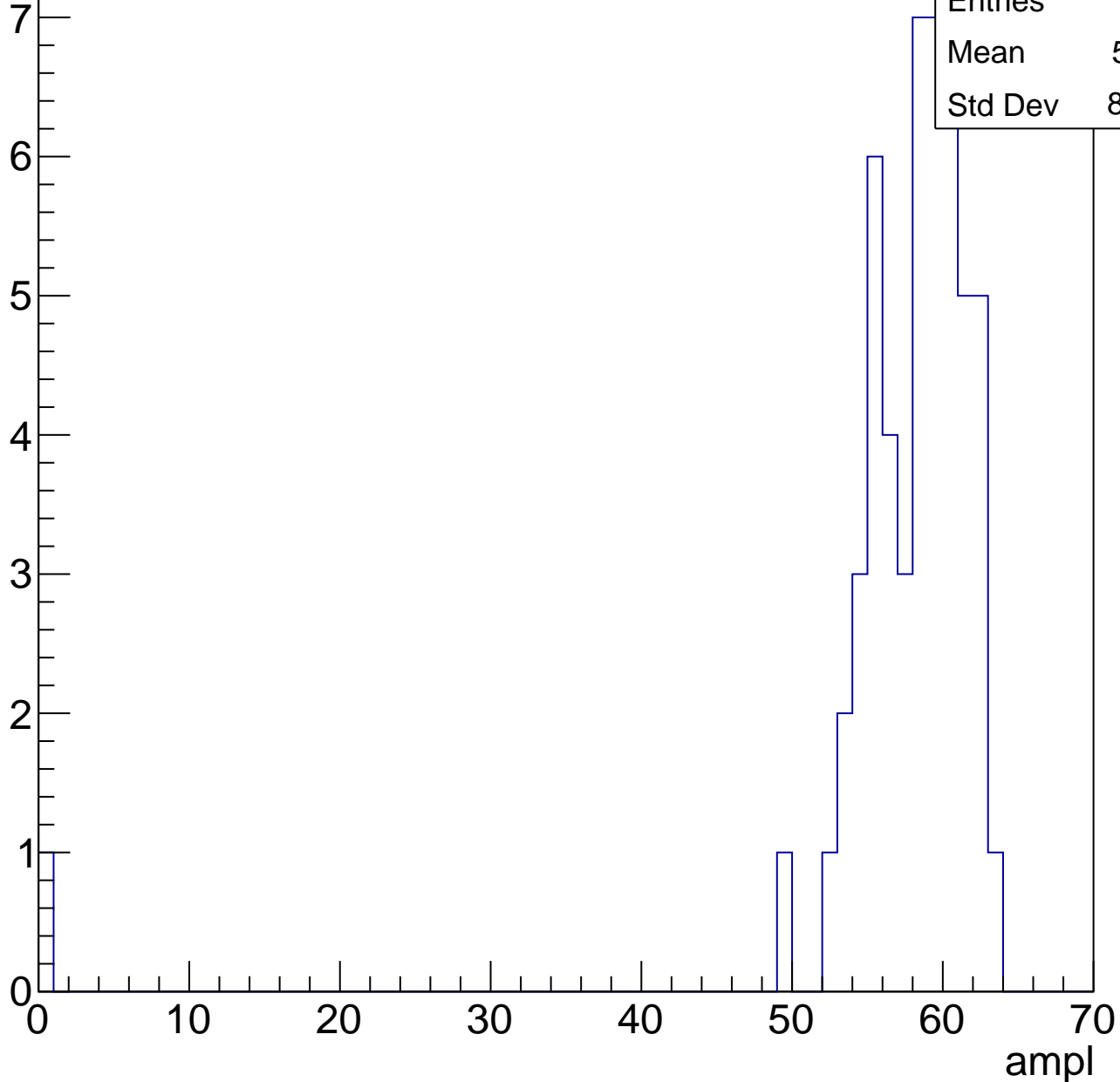
Entries	72
Mean	52.85
Std Dev	3.475



B0L001S, U18-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

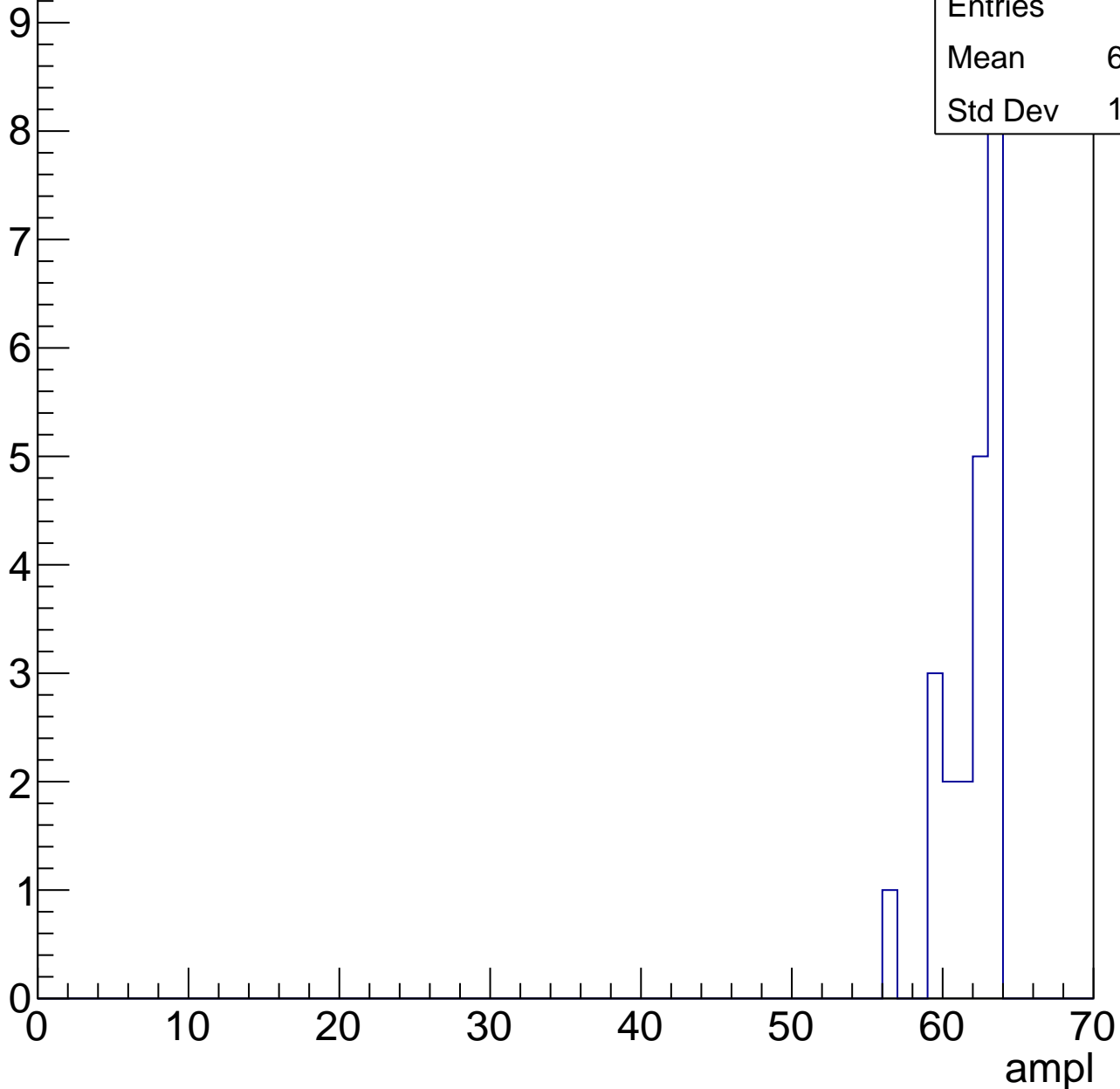


B0L001S, U18-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	61.45
Std Dev	1.852



B0L001S, U18-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch40, adc0

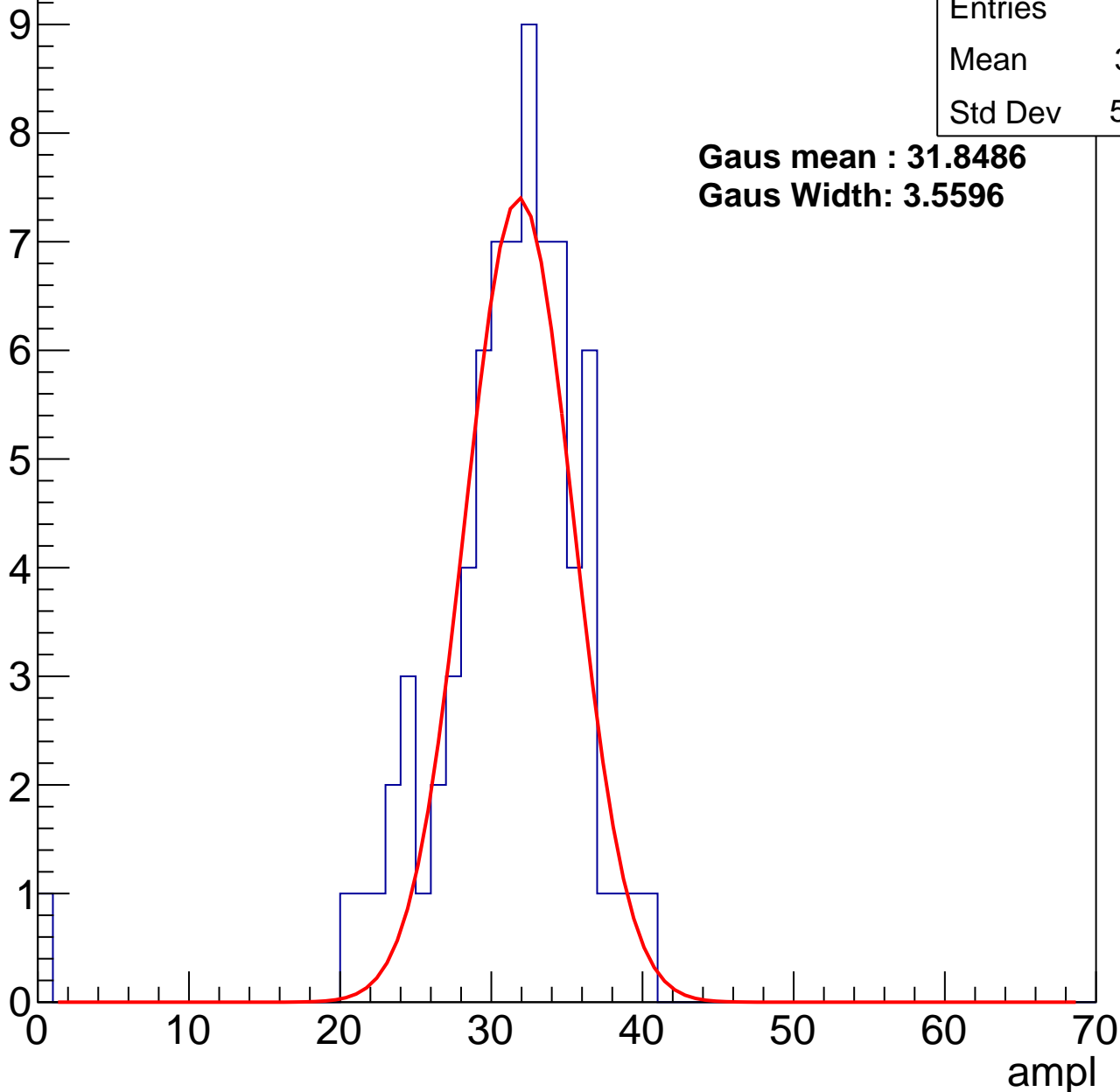
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.51
Std Dev	5.459

Gaus mean : 31.8486

Gaus Width: 3.5596



B0L001S, U18-ch40, adc1

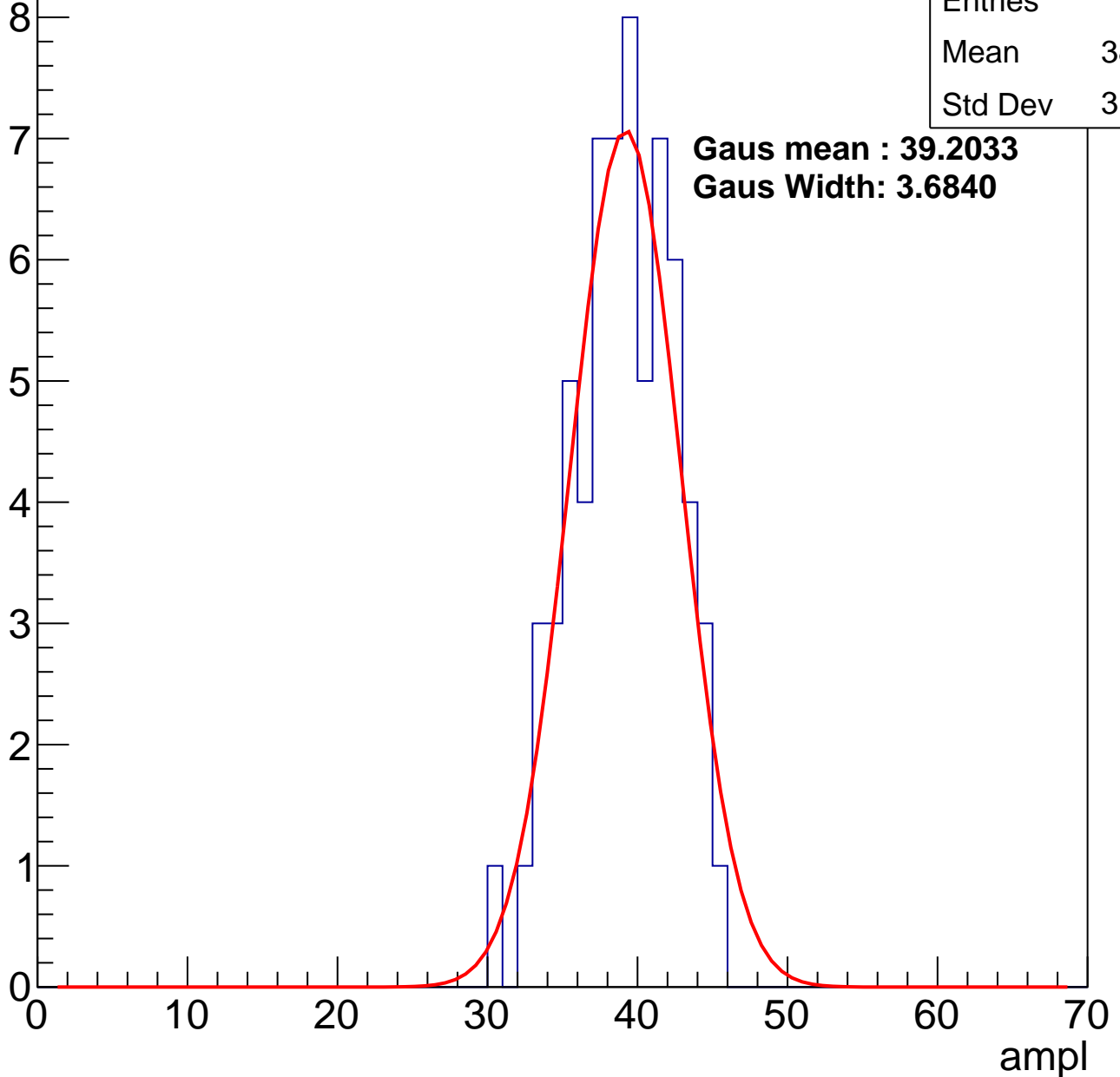
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.57
Std Dev	3.314

Gaus mean : 39.2033

Gaus Width: 3.6840



B0L001S, U18-ch40, adc2

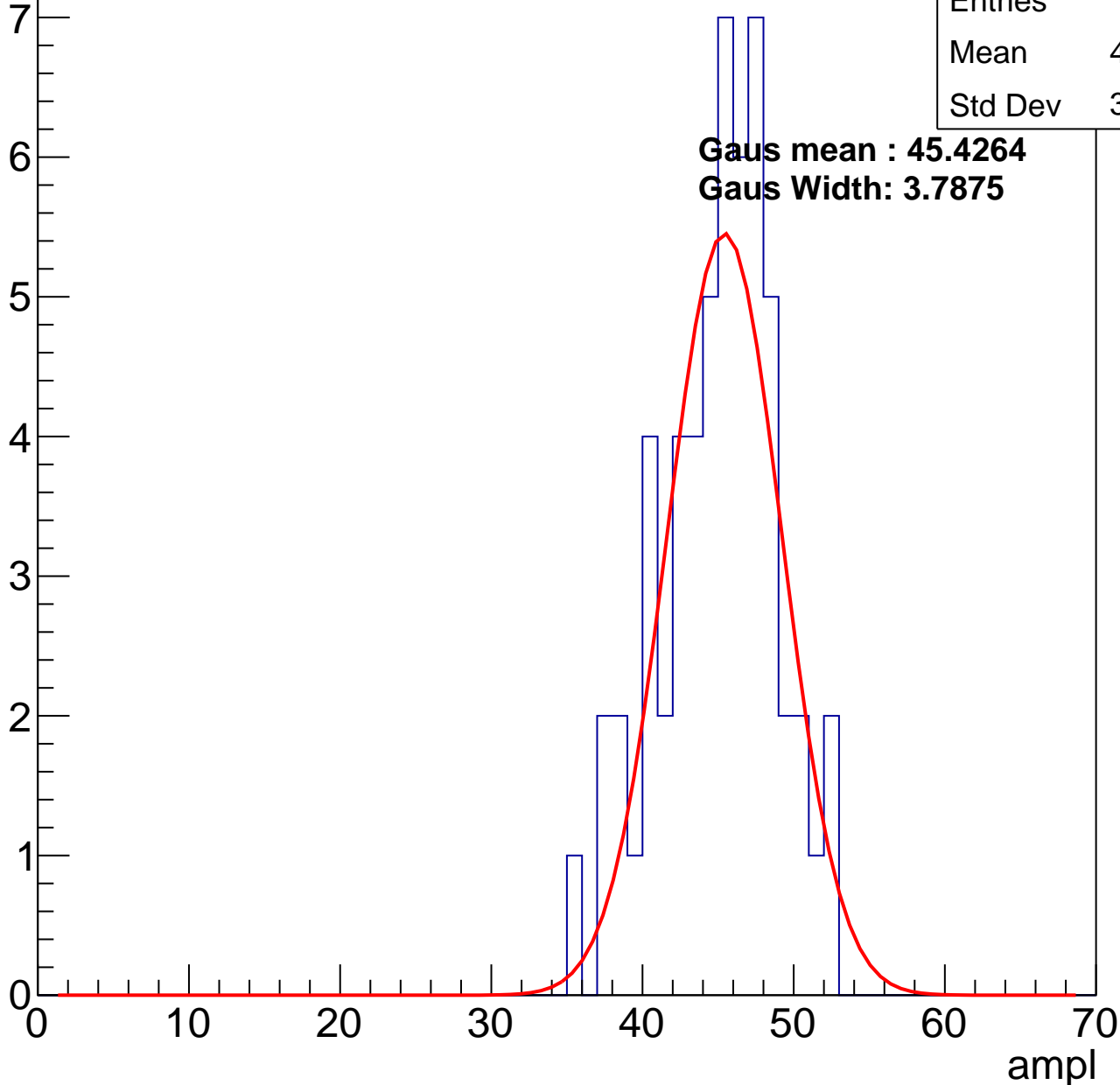
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.54
Std Dev	3.825

Gaus mean : 45.4264

Gaus Width: 3.7875

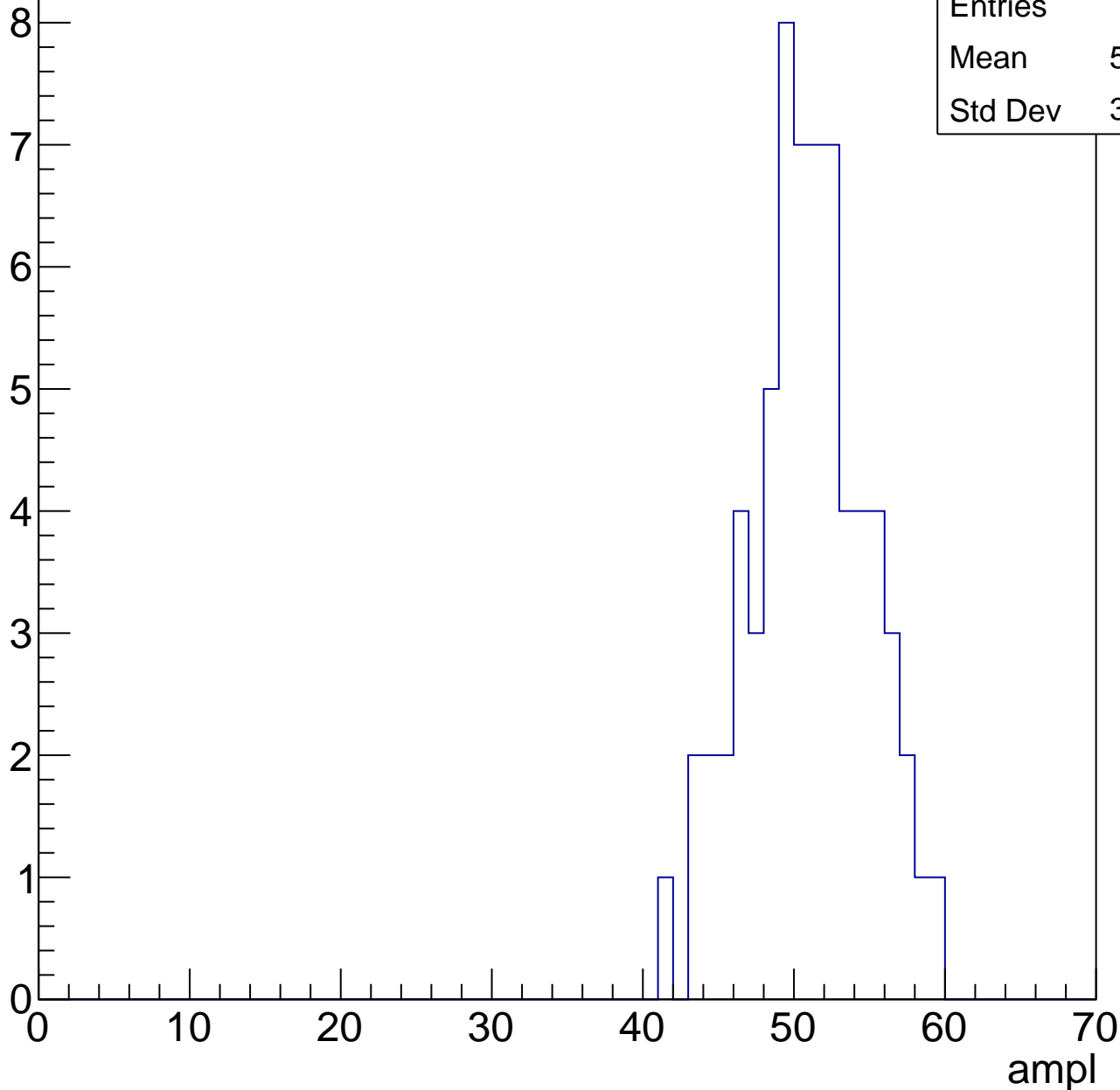


B0L001S, U18-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.45
Std Dev	3.849

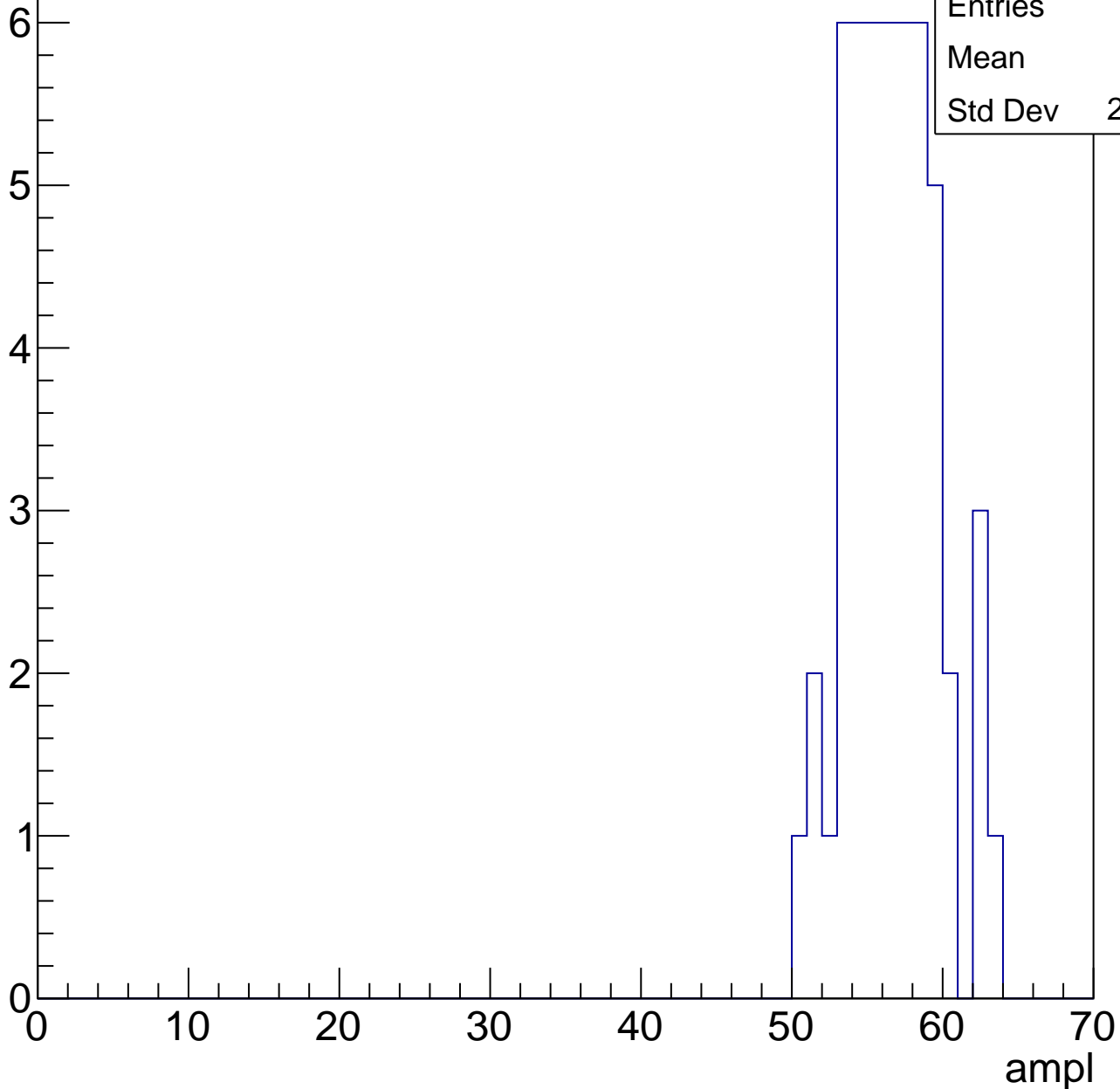


B0L001S, U18-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.2
Std Dev	2.964

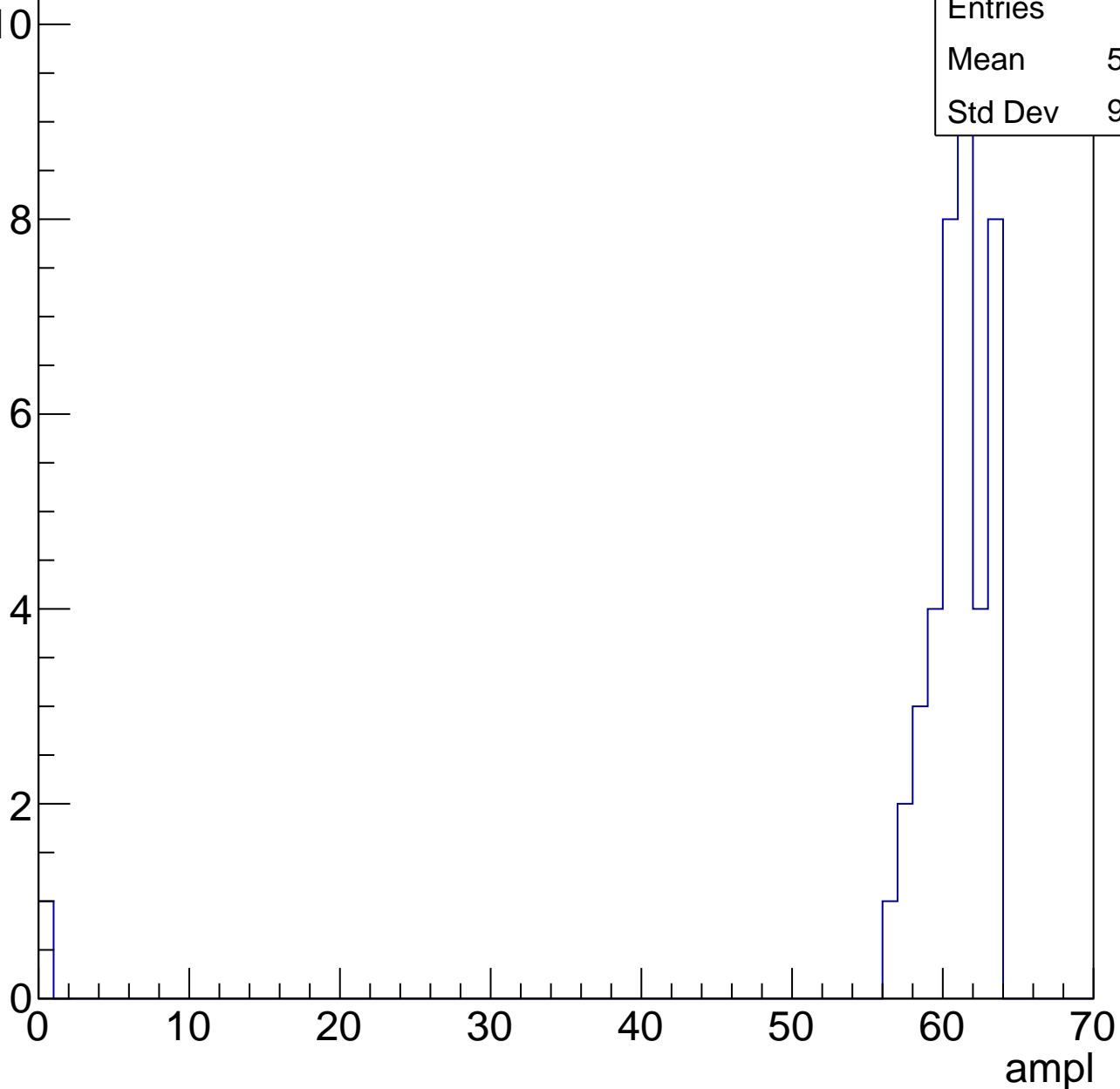


B0L001S, U18-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	59.07
Std Dev	9.516



B0L001S, U18-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch41, adc0

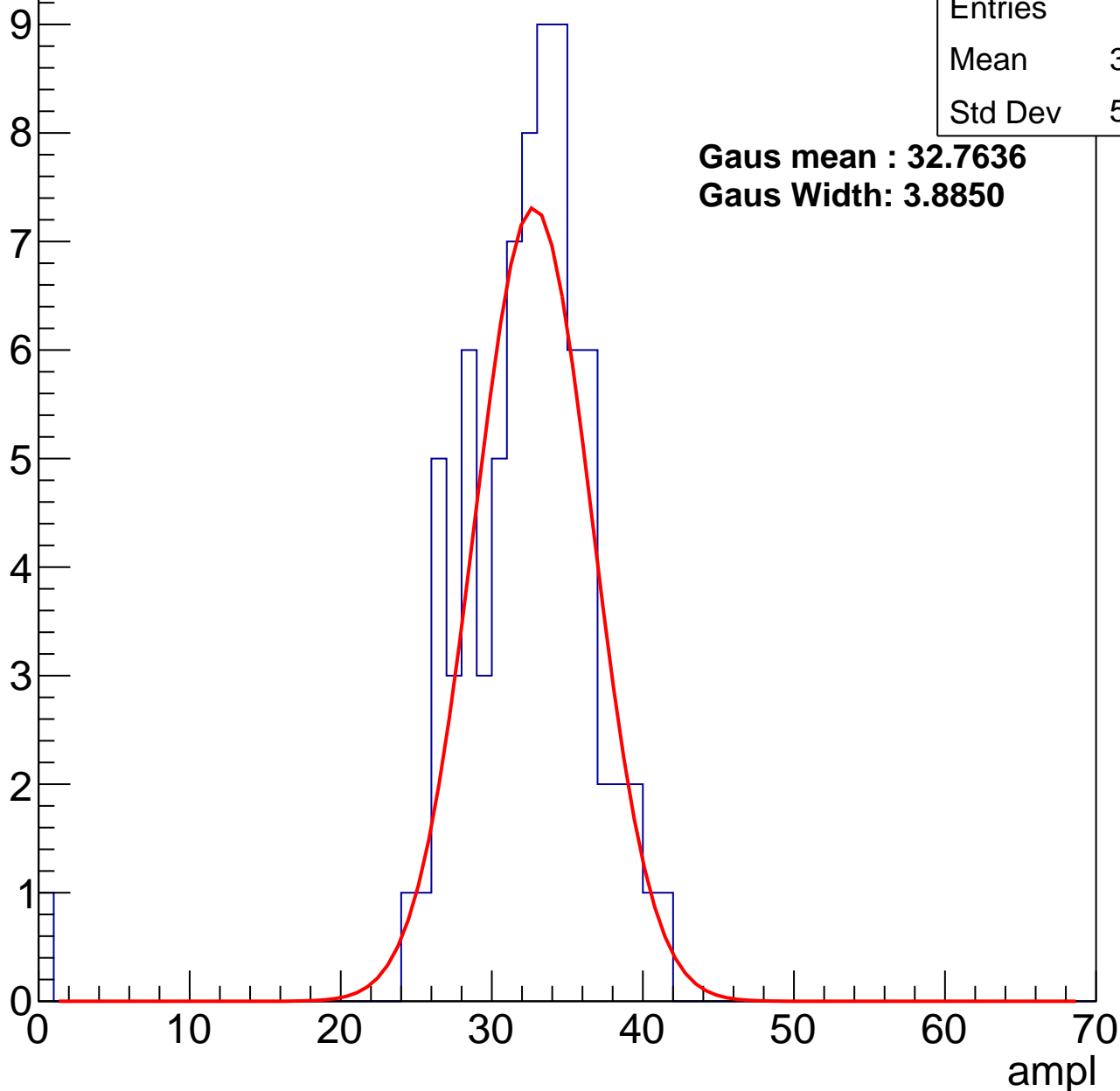
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	31.74
Std Dev	5.185

Gaus mean : 32.7636

Gaus Width: 3.8850



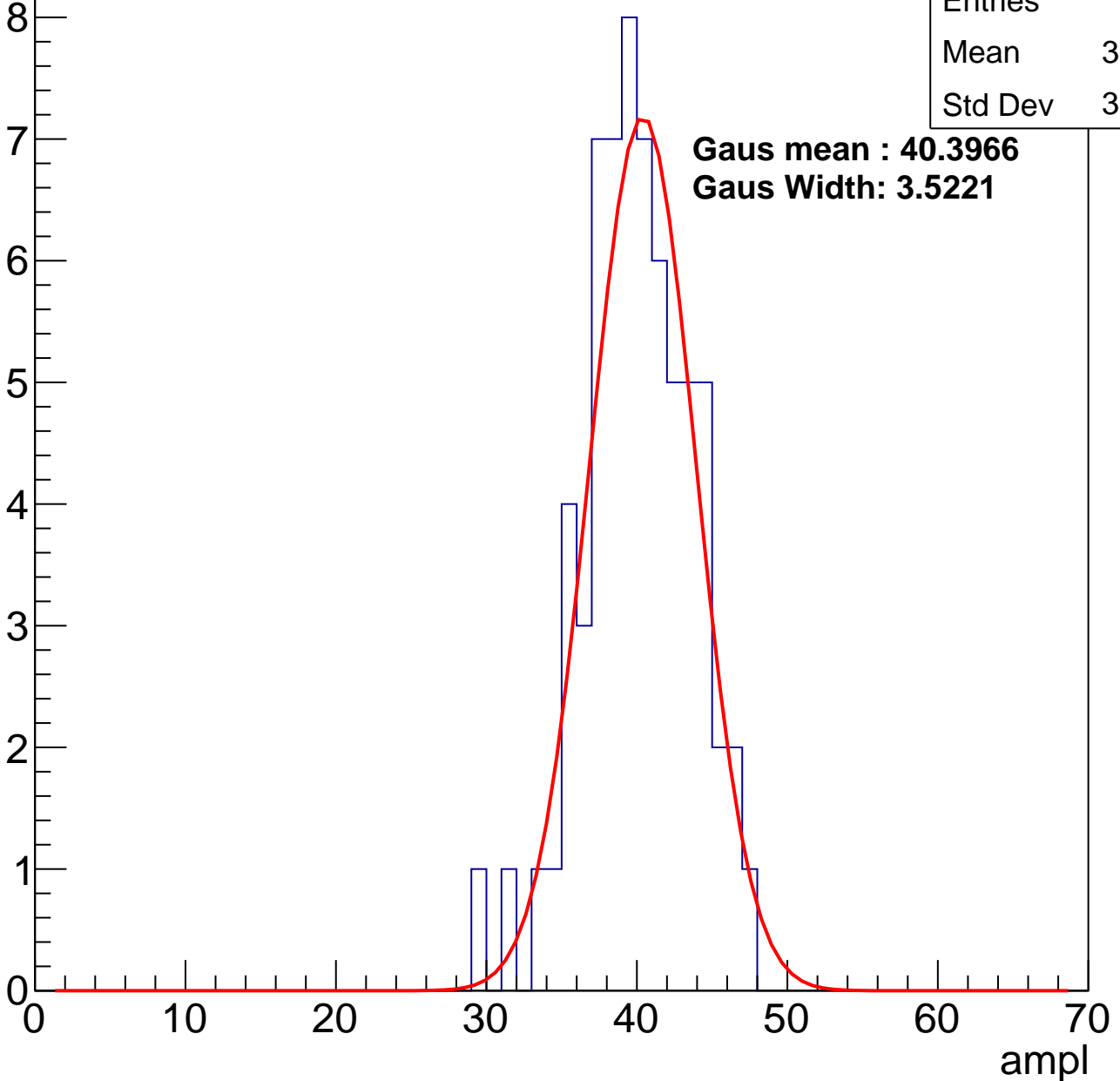
B0L001S, U18-ch41, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	39.58
Std Dev	3.576

Gaus mean : 40.3966
Gaus Width: 3.5221



B0L001S, U18-ch41, adc2

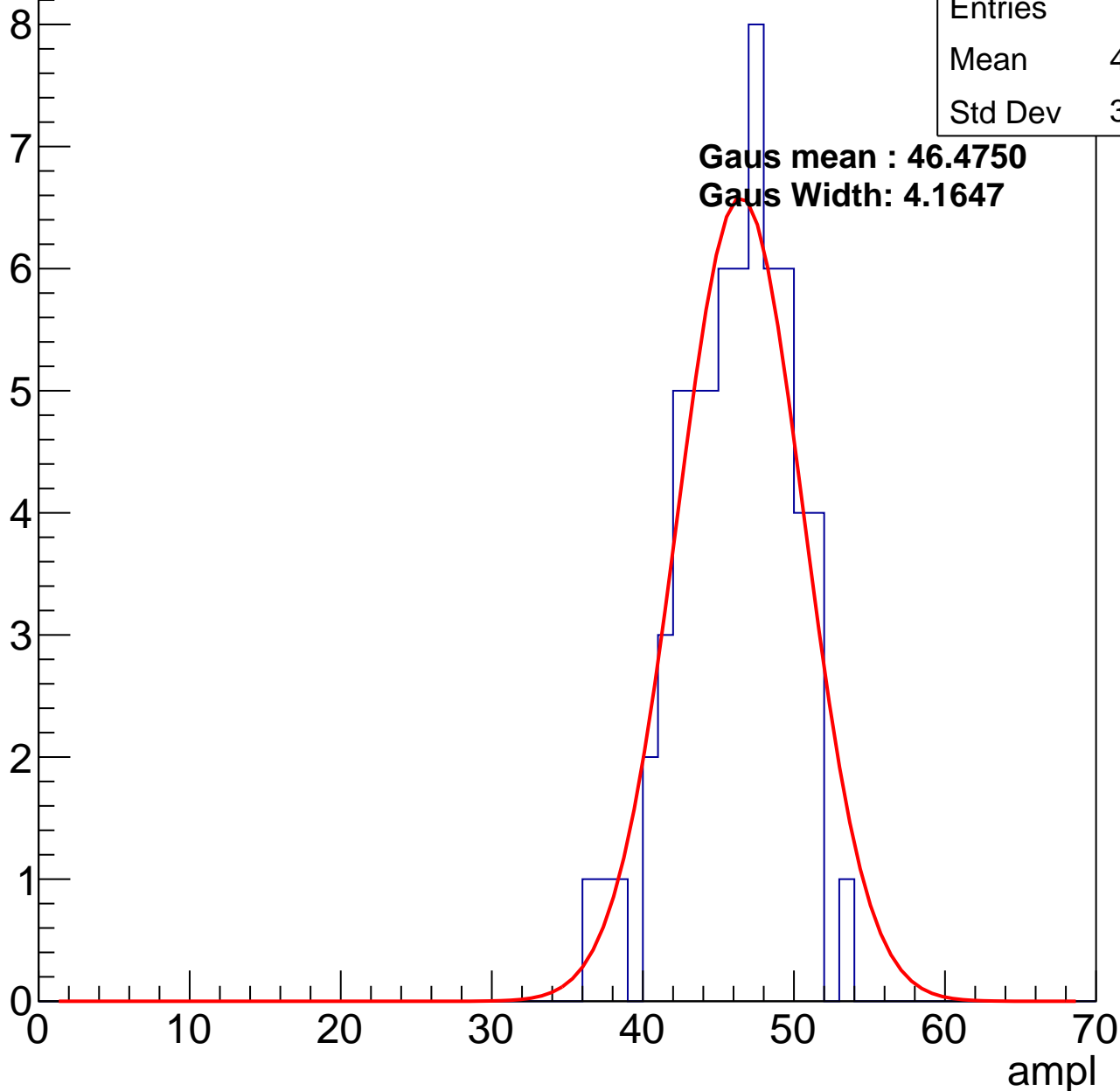
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.62
Std Dev	3.608

Gaus mean : 46.4750

Gaus Width: 4.1647

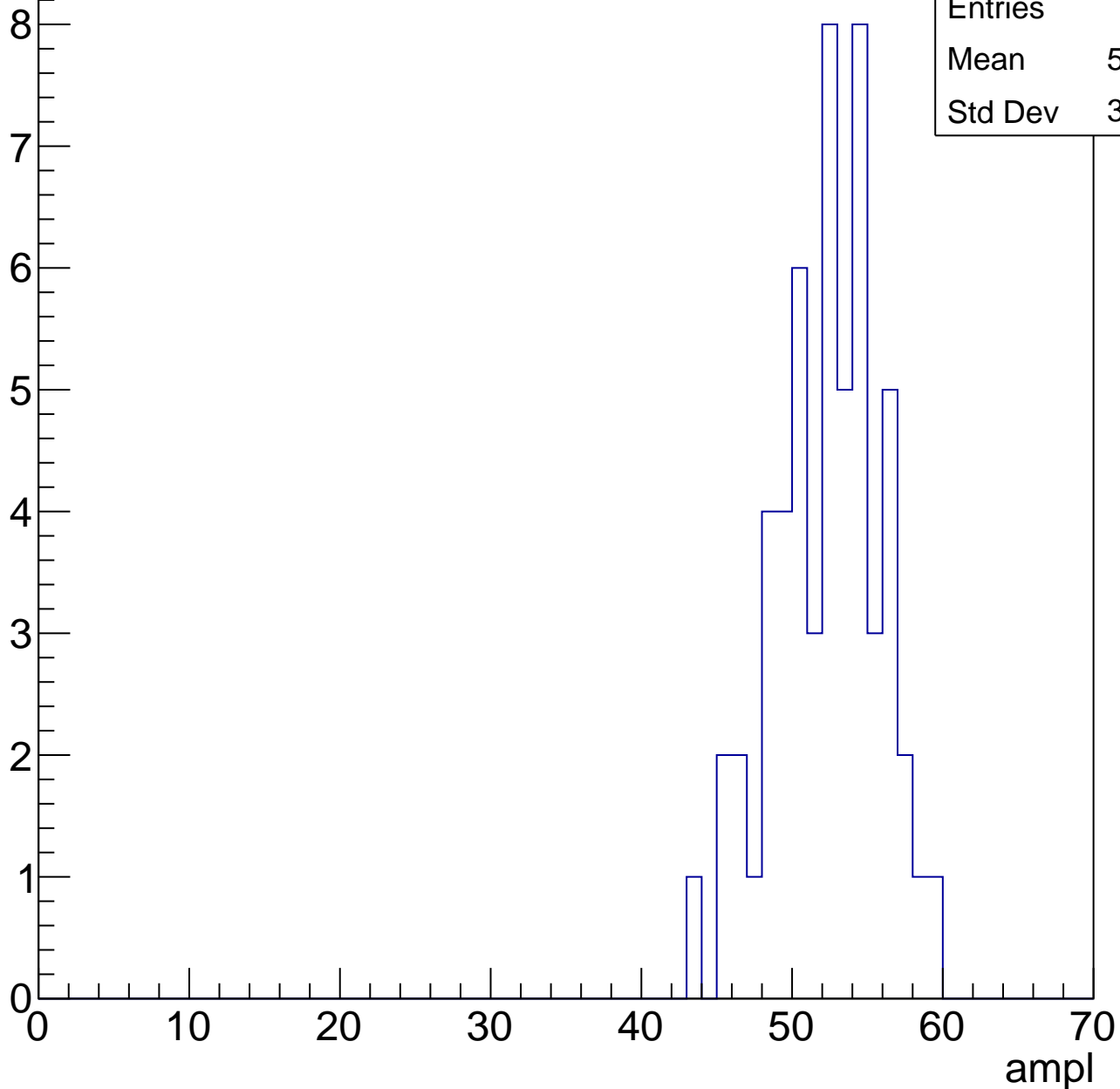


B0L001S, U18-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	51.82
Std Dev	3.485

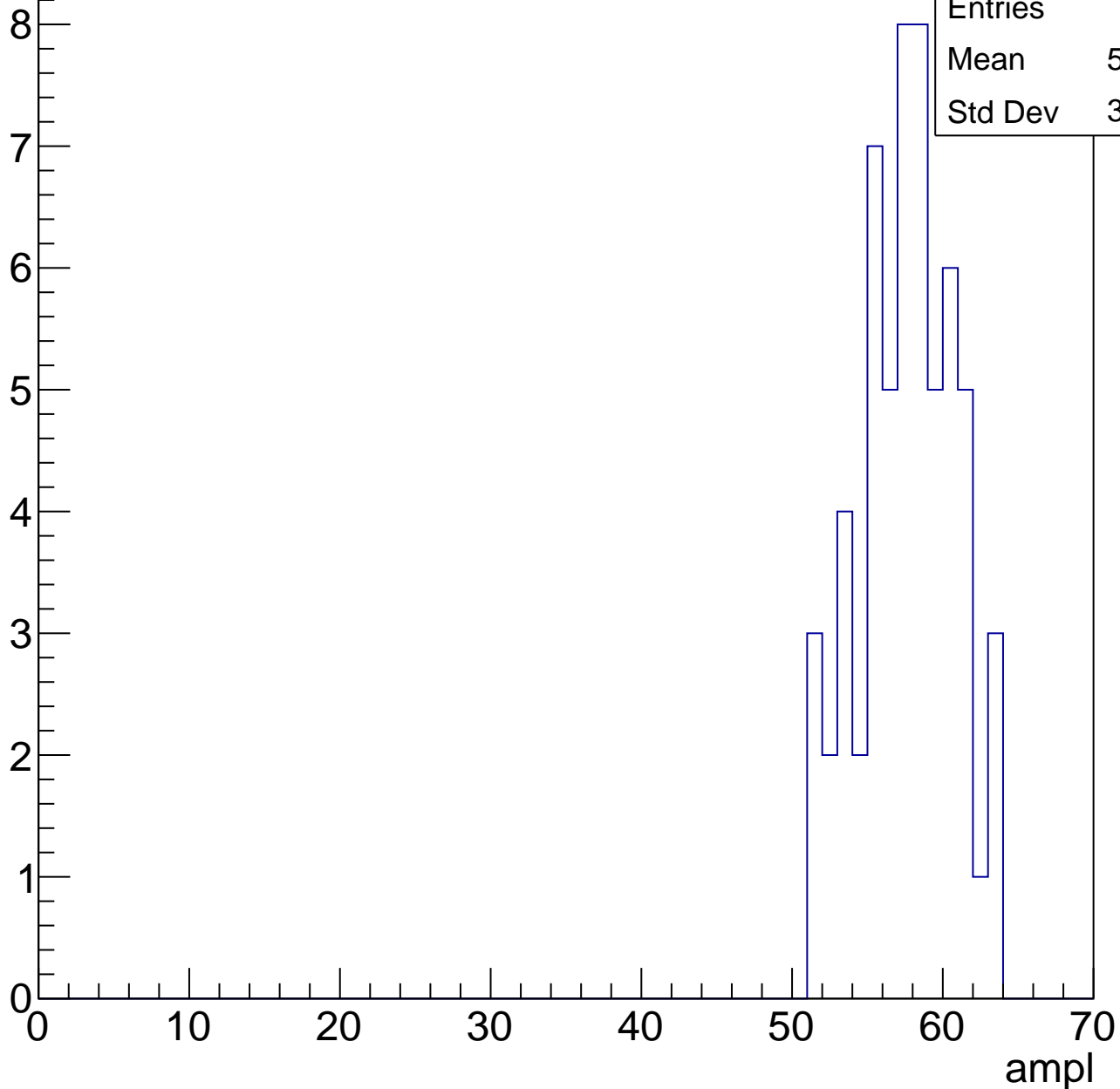


B0L001S, U18-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.17
Std Dev	3.098

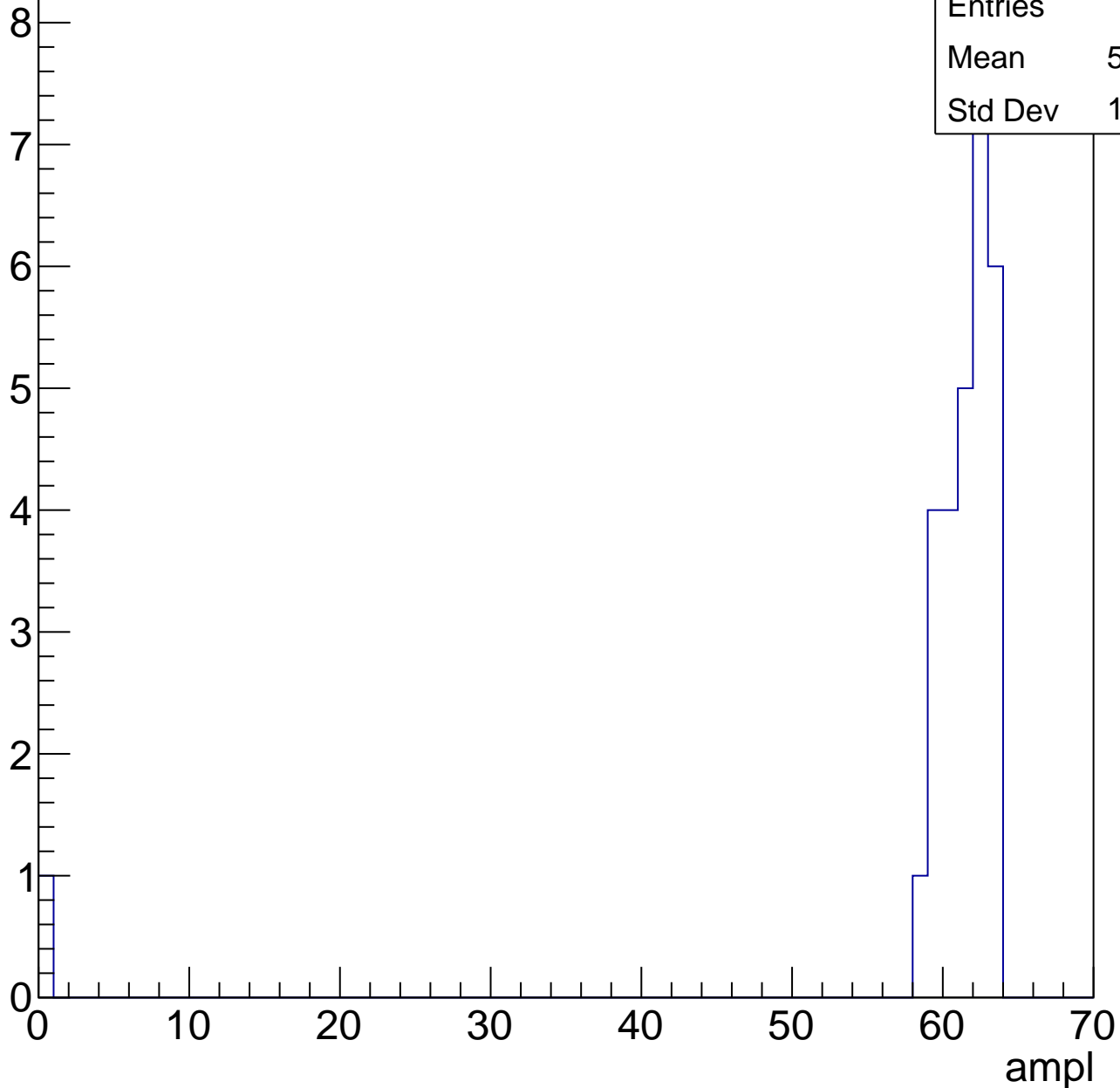


B0L001S, U18-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59.07
Std Dev	11.26



B0L001S, U18-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch42, adc0

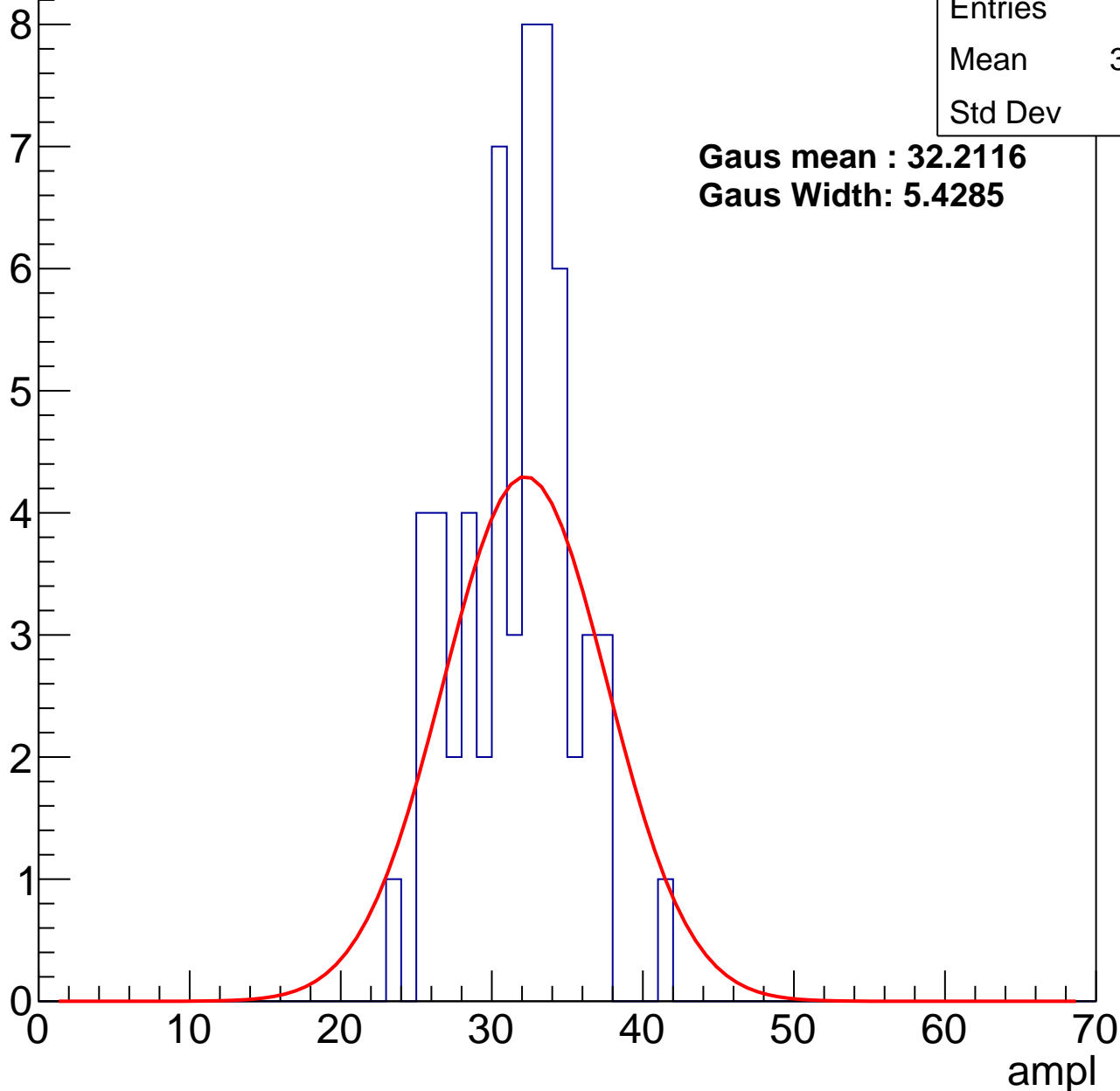
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.17
Std Dev	3.71

Gaus mean : 32.2116

Gaus Width: 5.4285



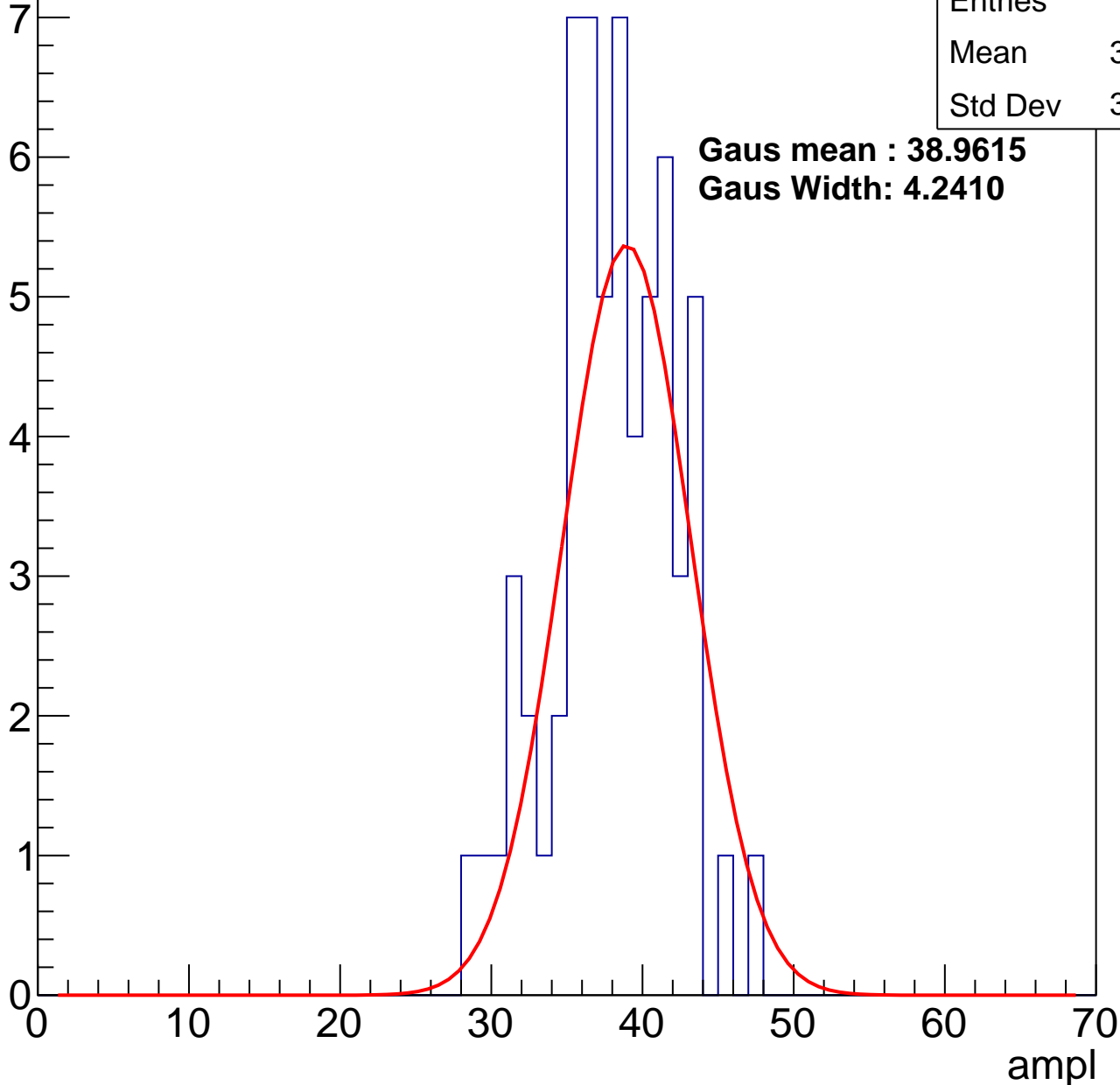
B0L001S, U18-ch42, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.55
Std Dev	3.987

Gaus mean : 38.9615
Gaus Width: 4.2410



B0L001S, U18-ch42, adc2

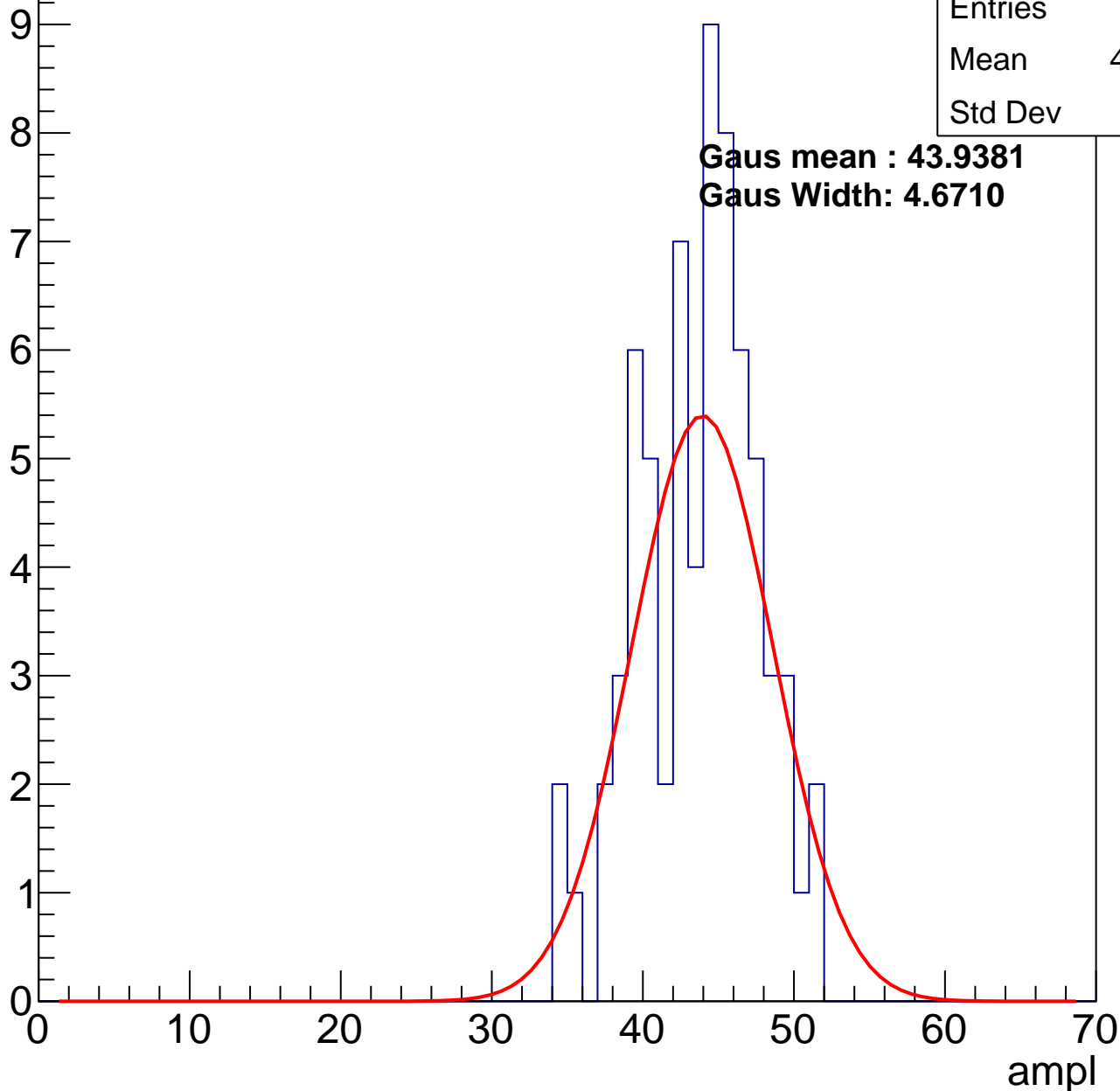
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.23
Std Dev	3.92

Gaus mean : 43.9381

Gaus Width: 4.6710

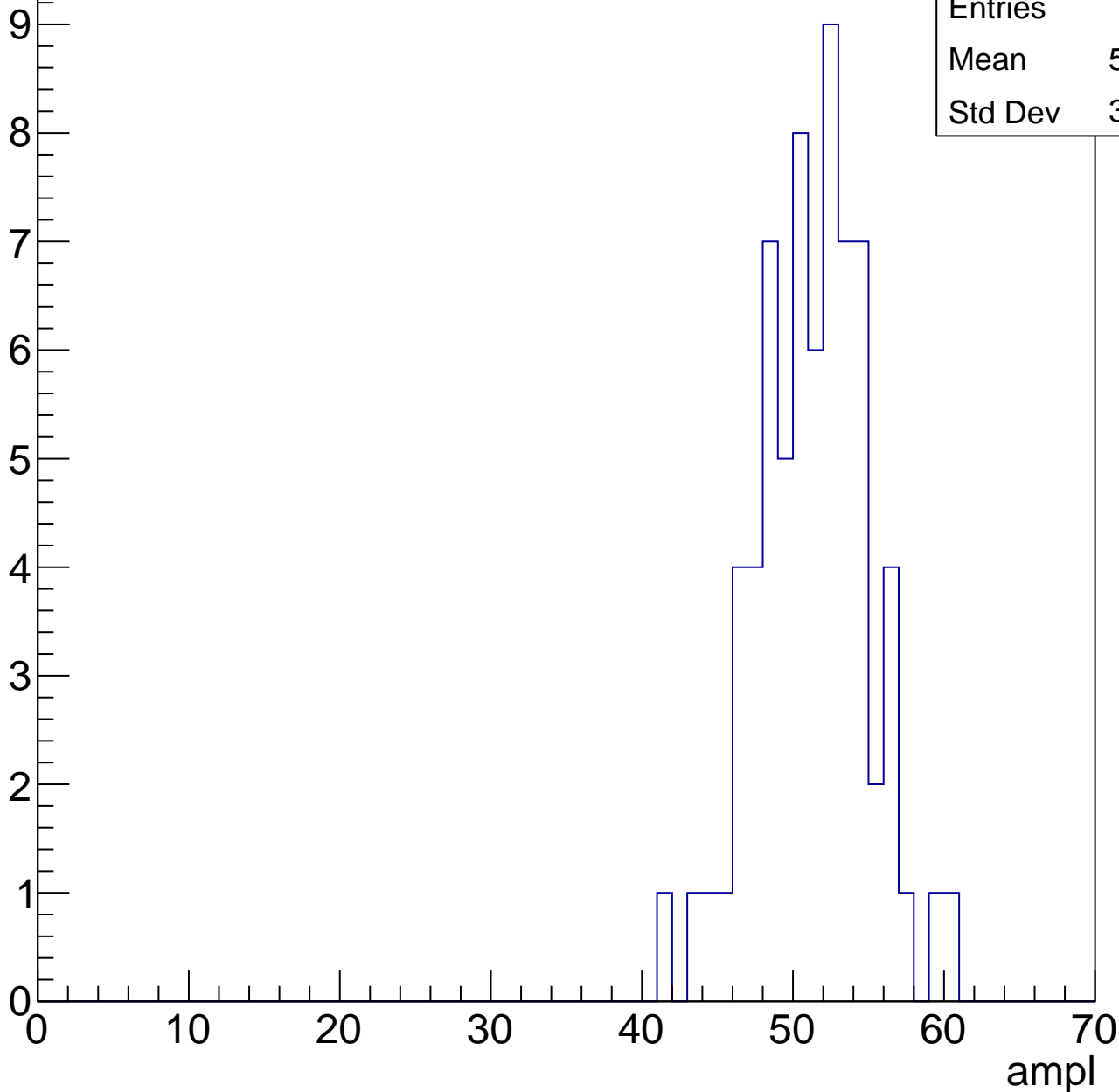


B0L001S, U18-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.84
Std Dev	3.616



B0L001S, U18-ch42, adc4

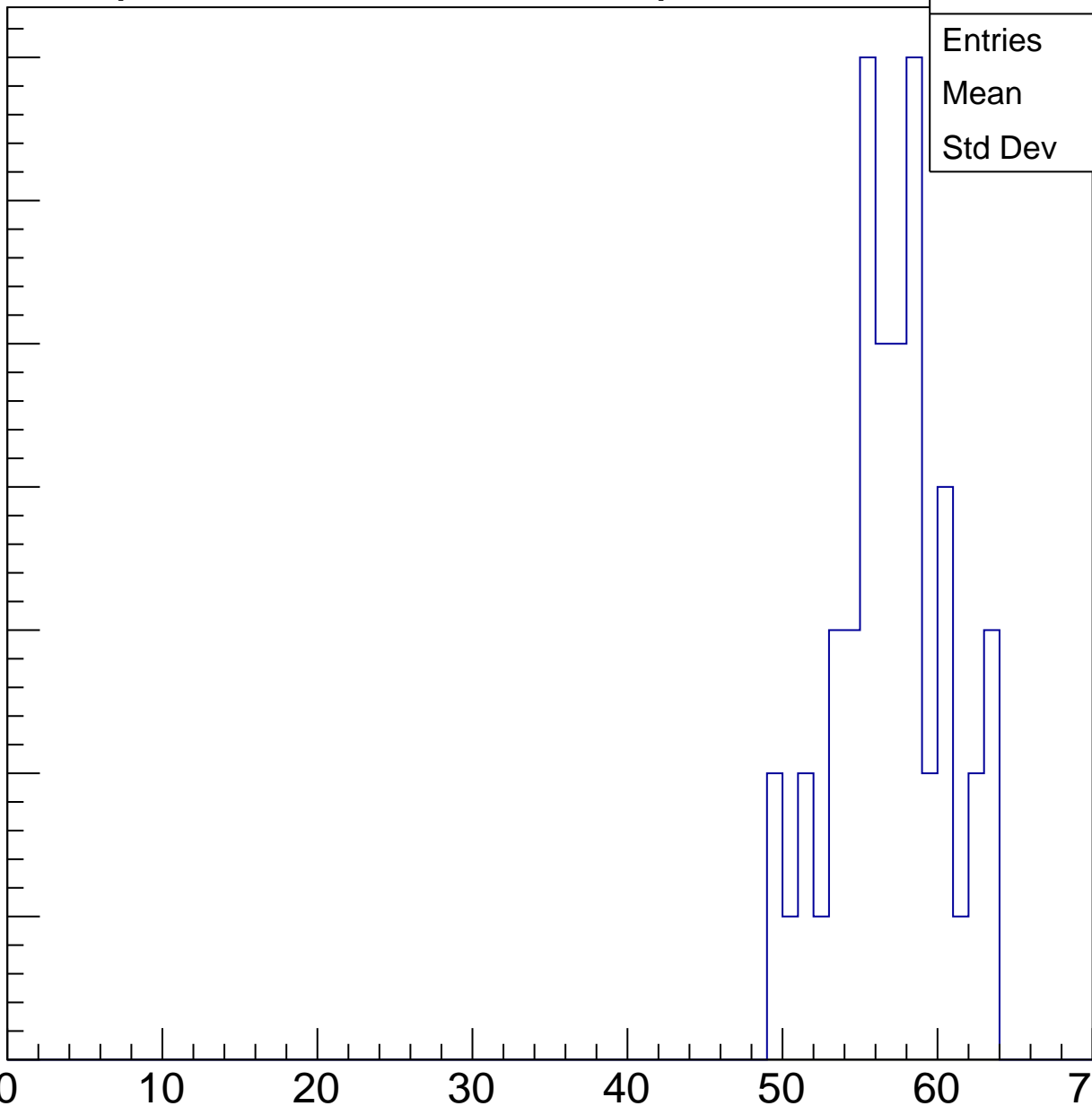
calib_packv5_042523_0143.root, FC#9, port A1

Entry

7
6
5
4
3
2
1
0

Entries	48
Mean	56.48
Std Dev	3.518

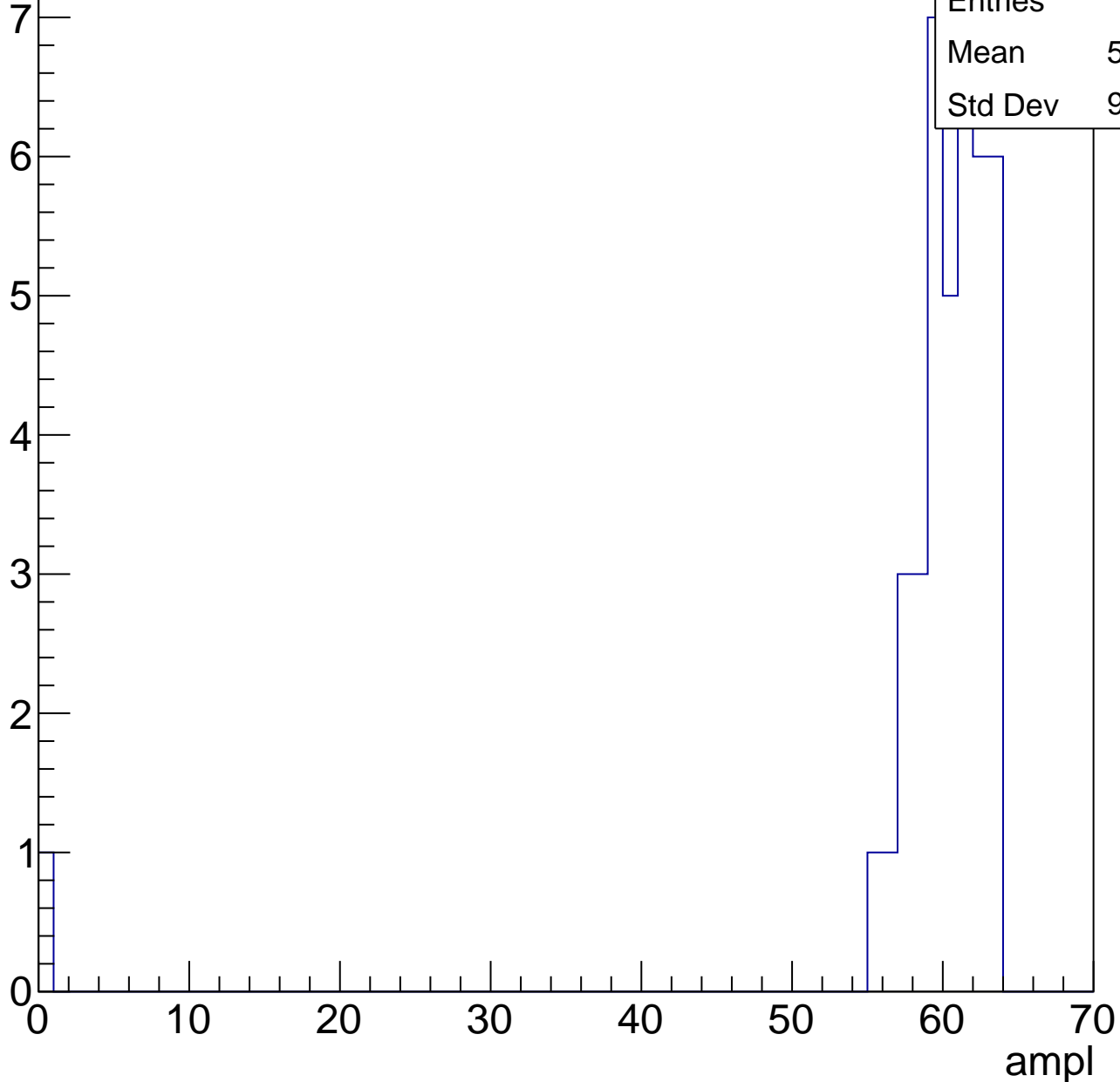
ampl



B0L001S, U18-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

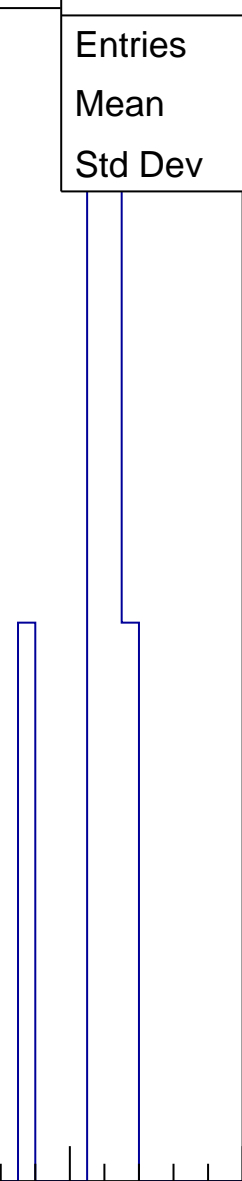
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61
Std Dev	1.915

0 10 20 30 40 50 60 70

ampl



B0L001S, U18-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch43, adc0

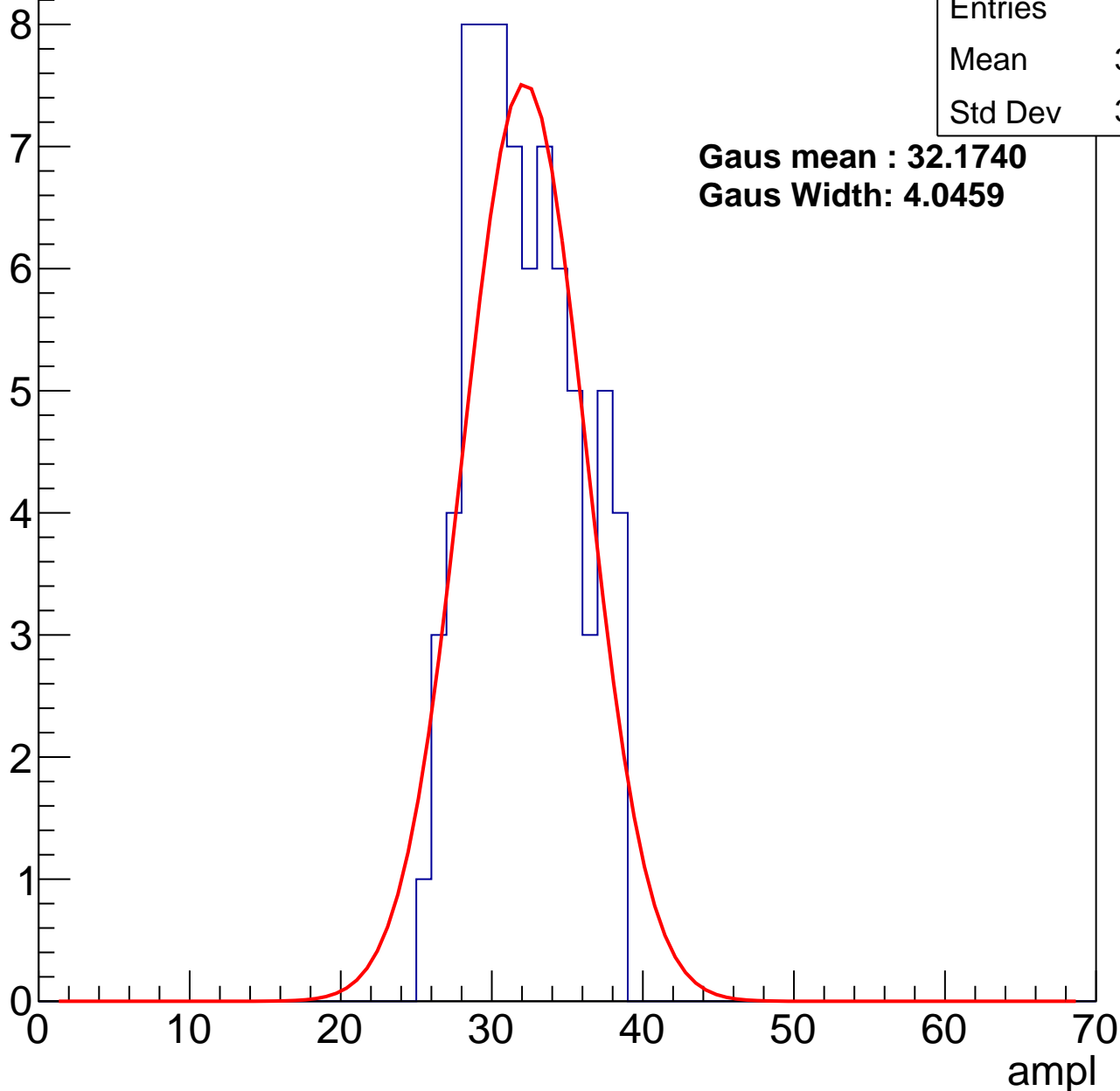
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	31.61
Std Dev	3.421

Gaus mean : 32.1740

Gaus Width: 4.0459



B0L001S, U18-ch43, adc1

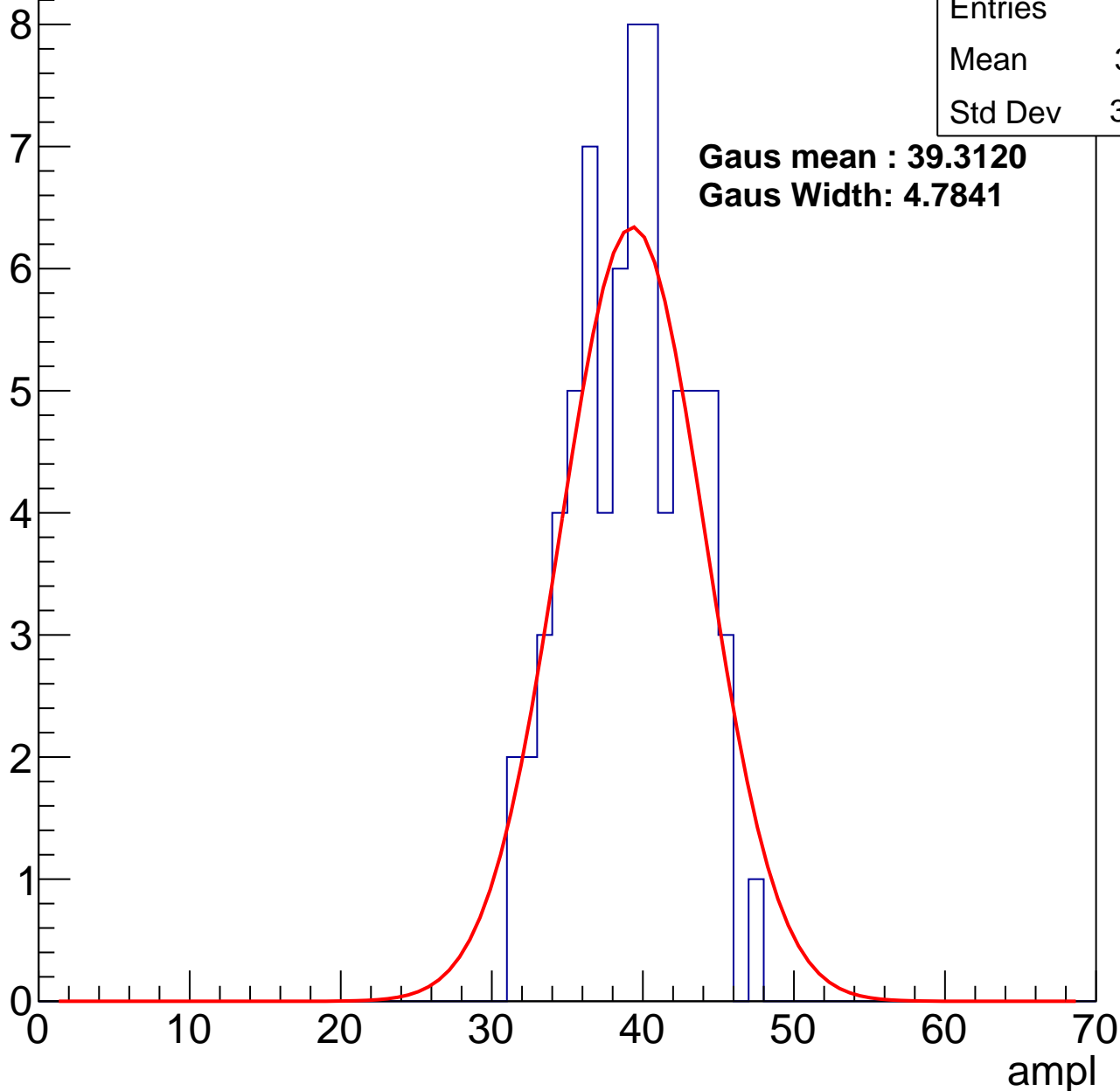
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.71
Std Dev	3.799

Gaus mean : 39.3120

Gaus Width: 4.7841

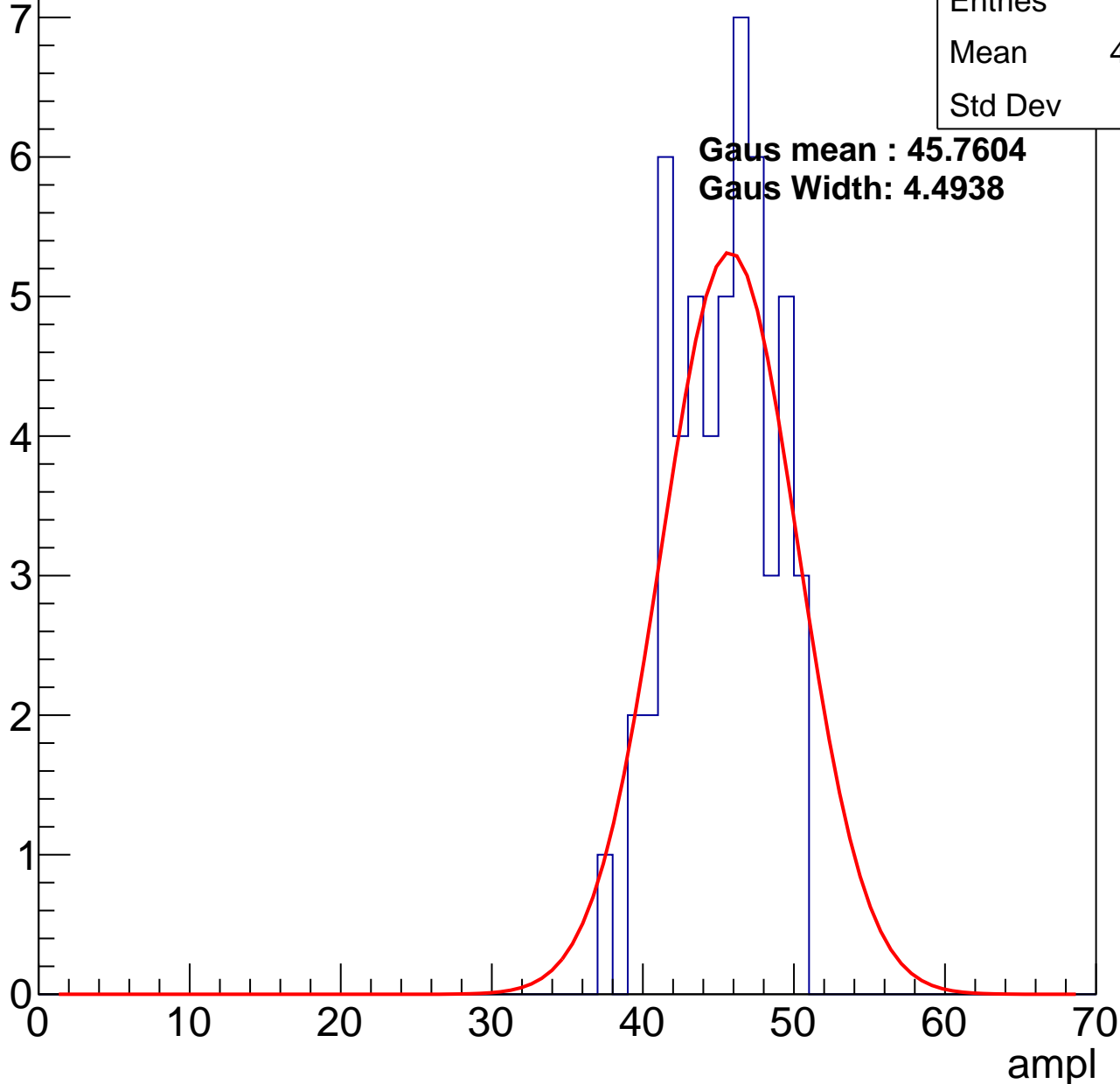


B0L001S, U18-ch43, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.68
Std Dev	3.22

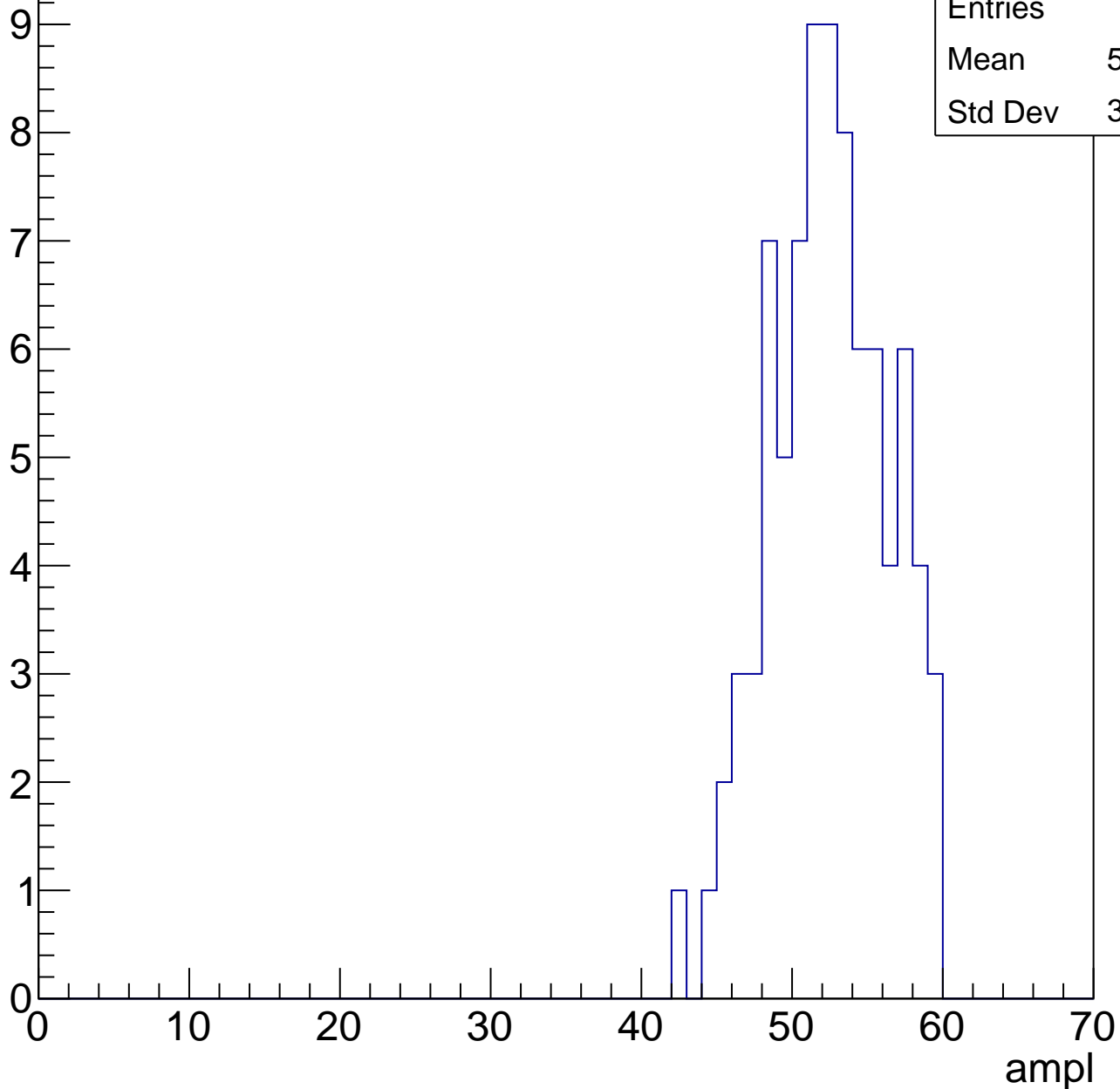


B0L001S, U18-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	51.98
Std Dev	3.827

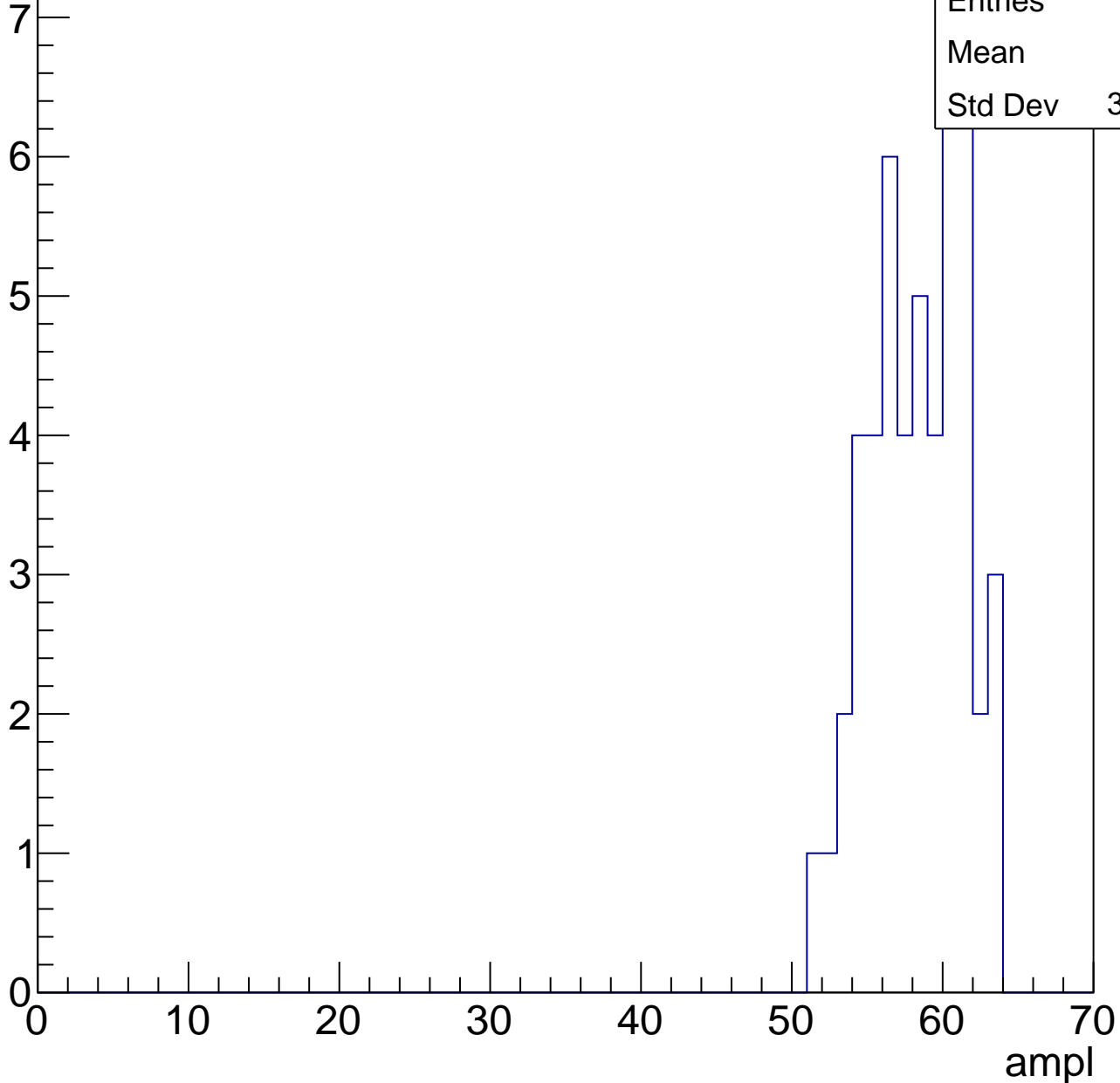


B0L001S, U18-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.9
Std Dev	3.048

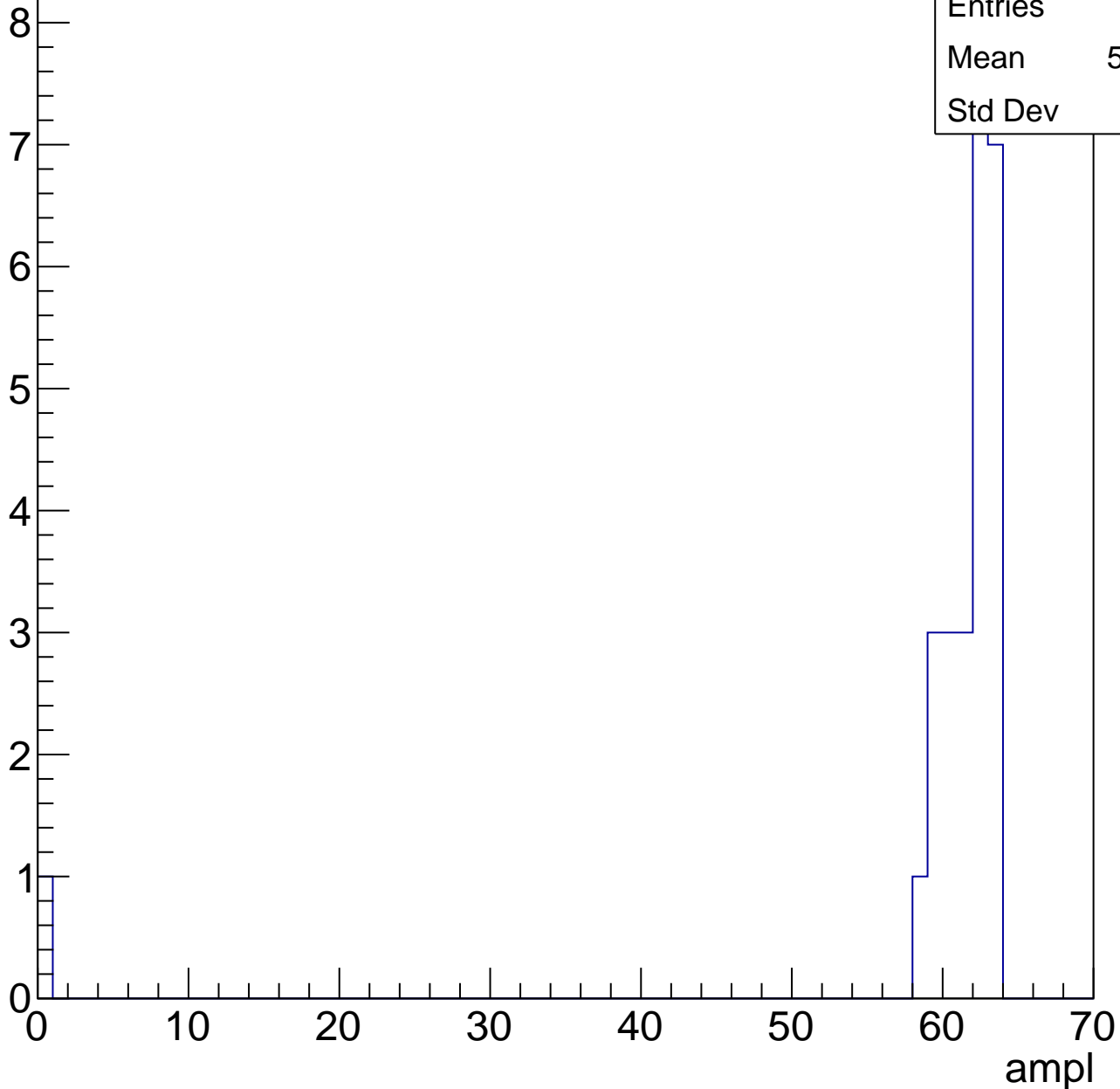


B0L001S, U18-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	59.04
Std Dev	11.9



B0L001S, U18-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch44, adc0

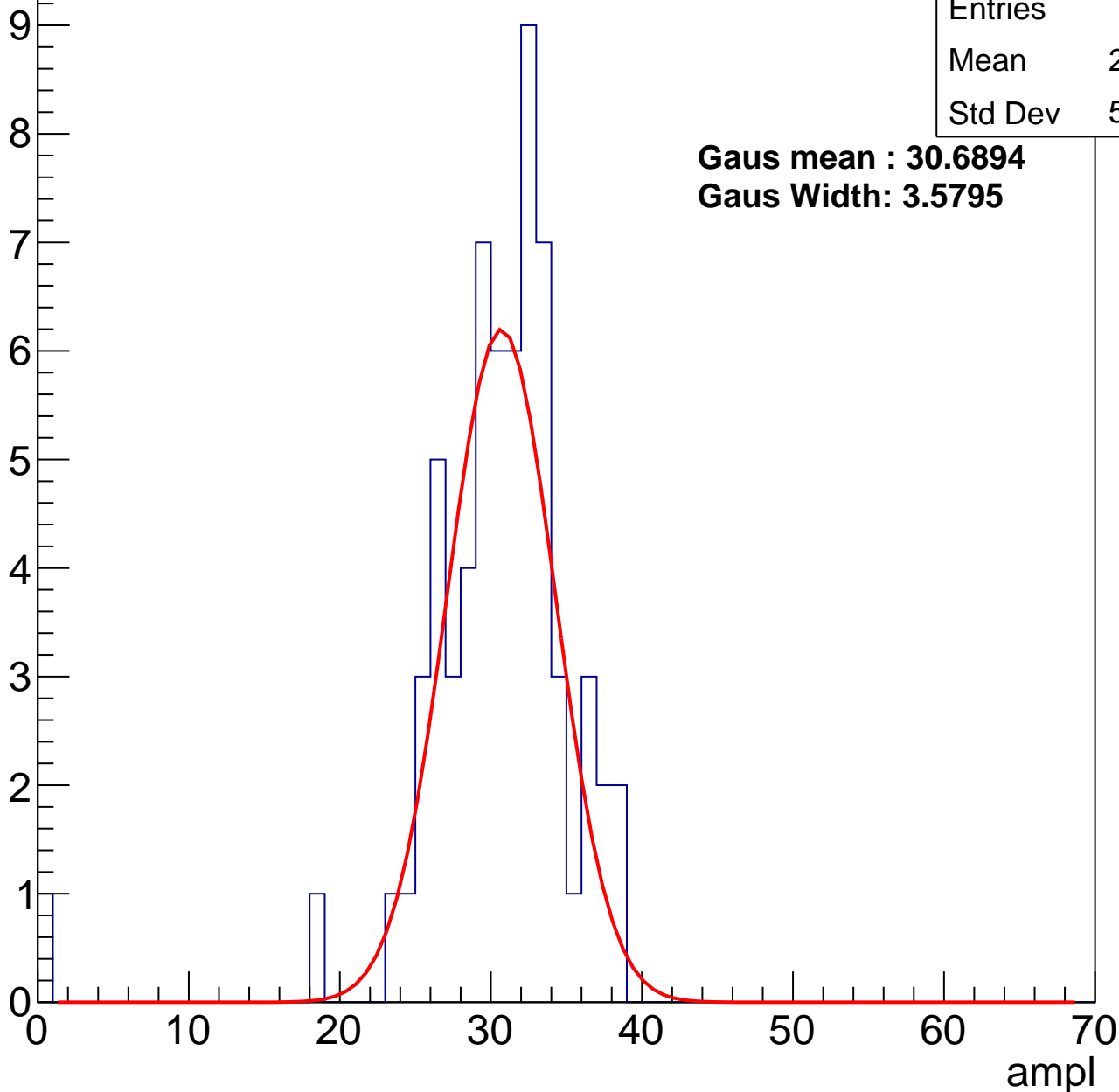
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.94
Std Dev	5.329

Gaus mean : 30.6894

Gaus Width: 3.5795



B0L001S, U18-ch44, adc1

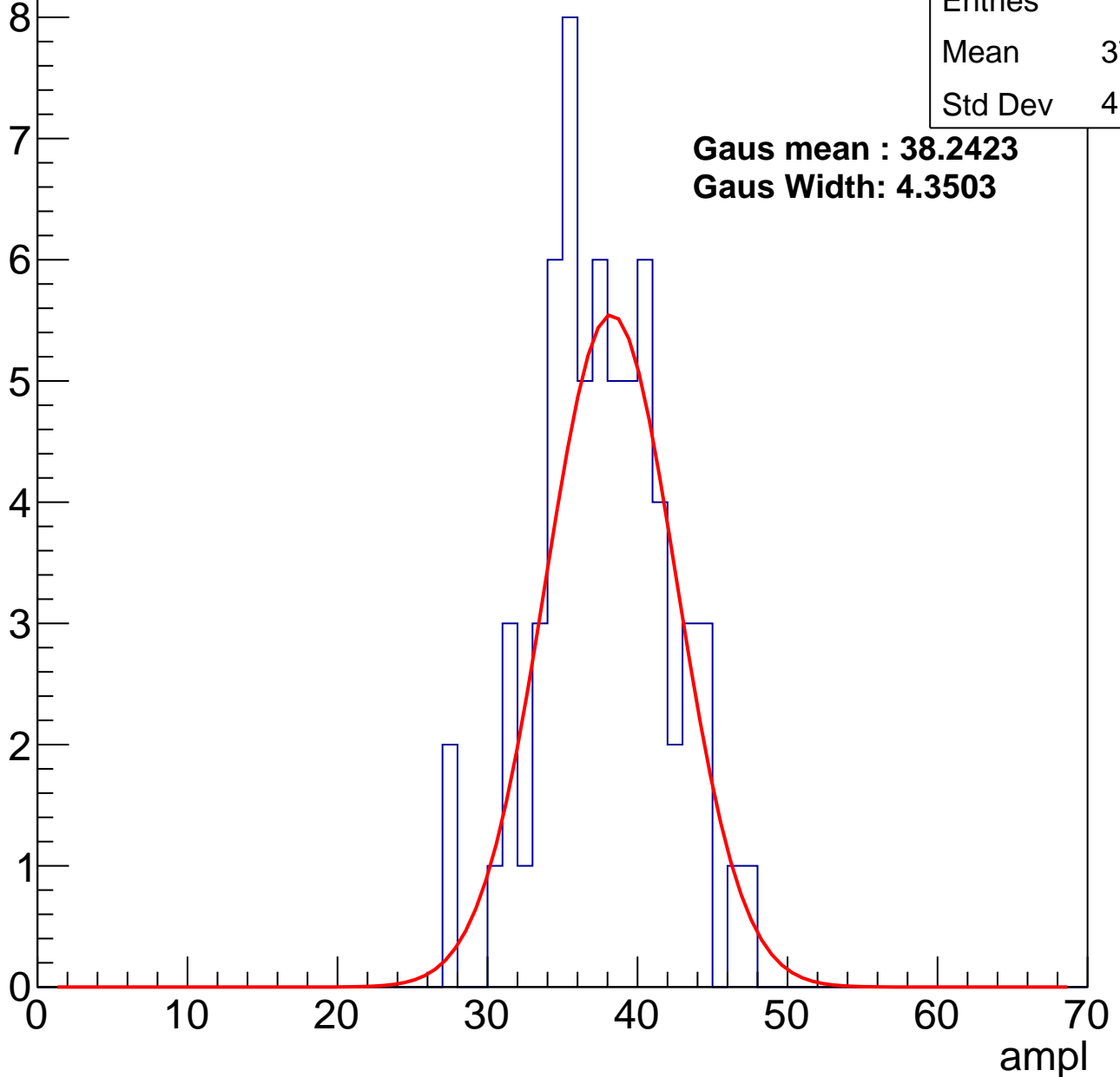
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.25
Std Dev	4.199

Gaus mean : 38.2423

Gaus Width: 4.3503



B0L001S, U18-ch44, adc2

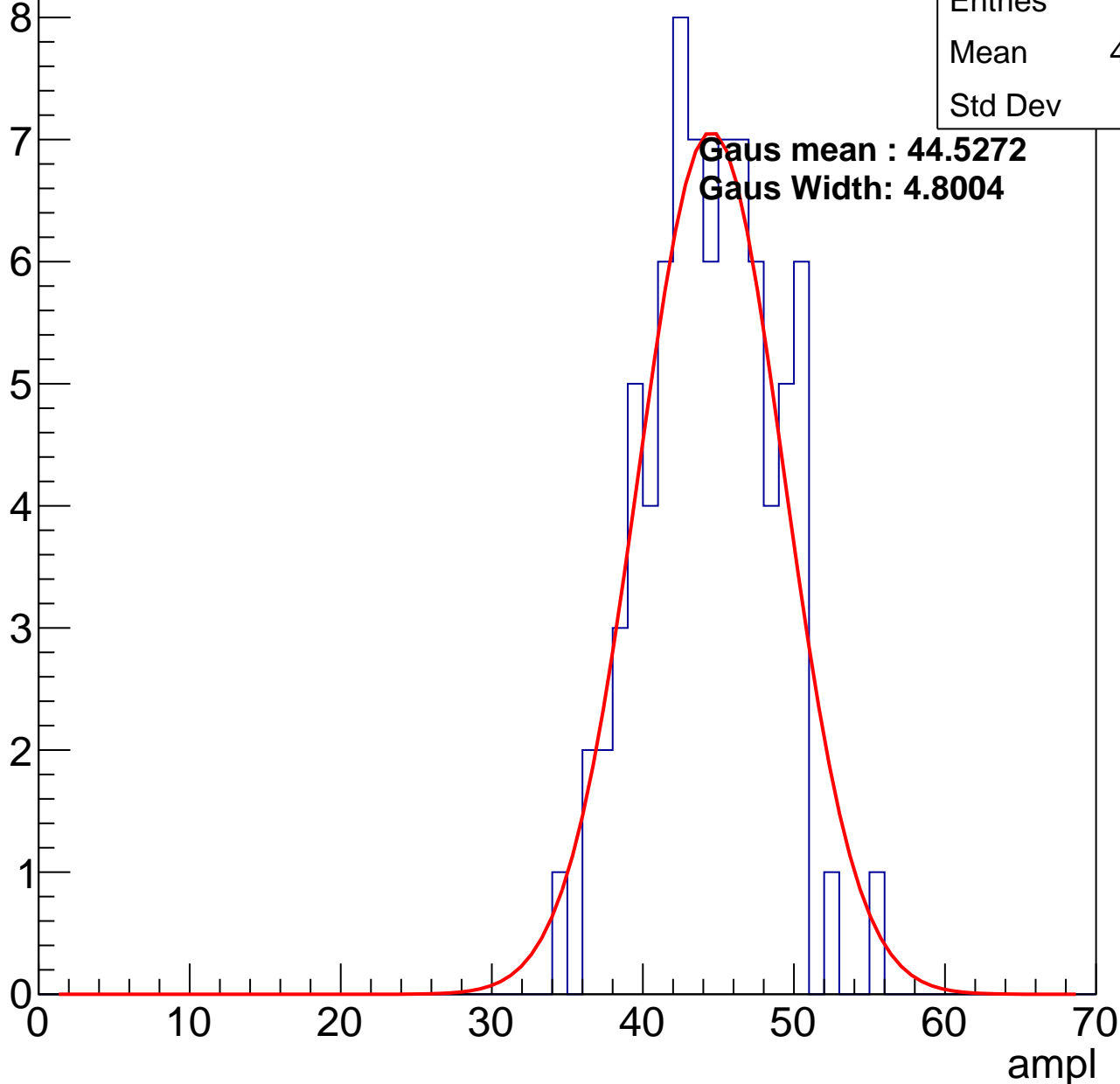
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	43.94
Std Dev	4.15

Gaus mean : 44.5272

Gaus Width: 4.8004

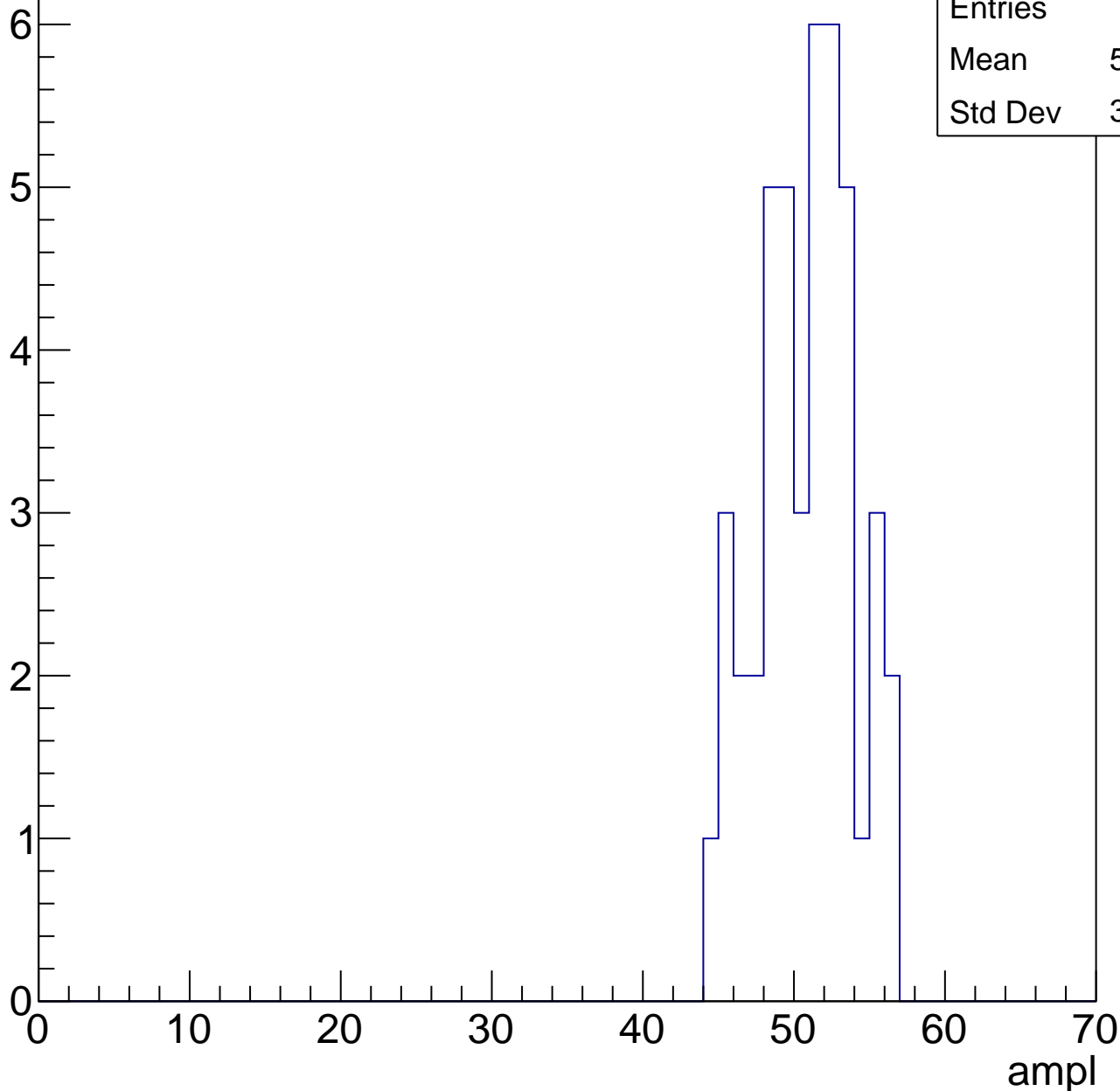


B0L001S, U18-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	50.32
Std Dev	3.088

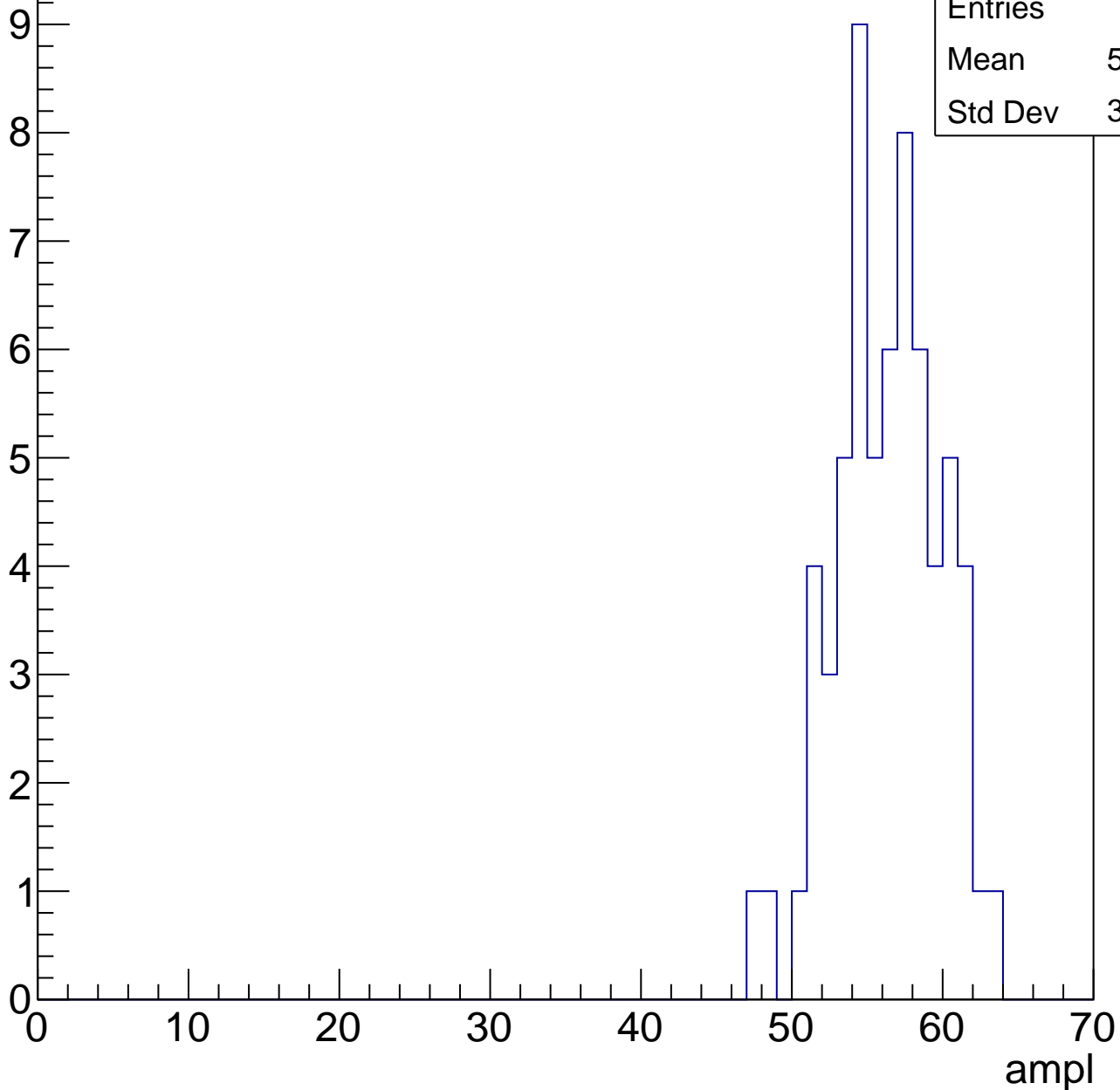


B0L001S, U18-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.88
Std Dev	3.416

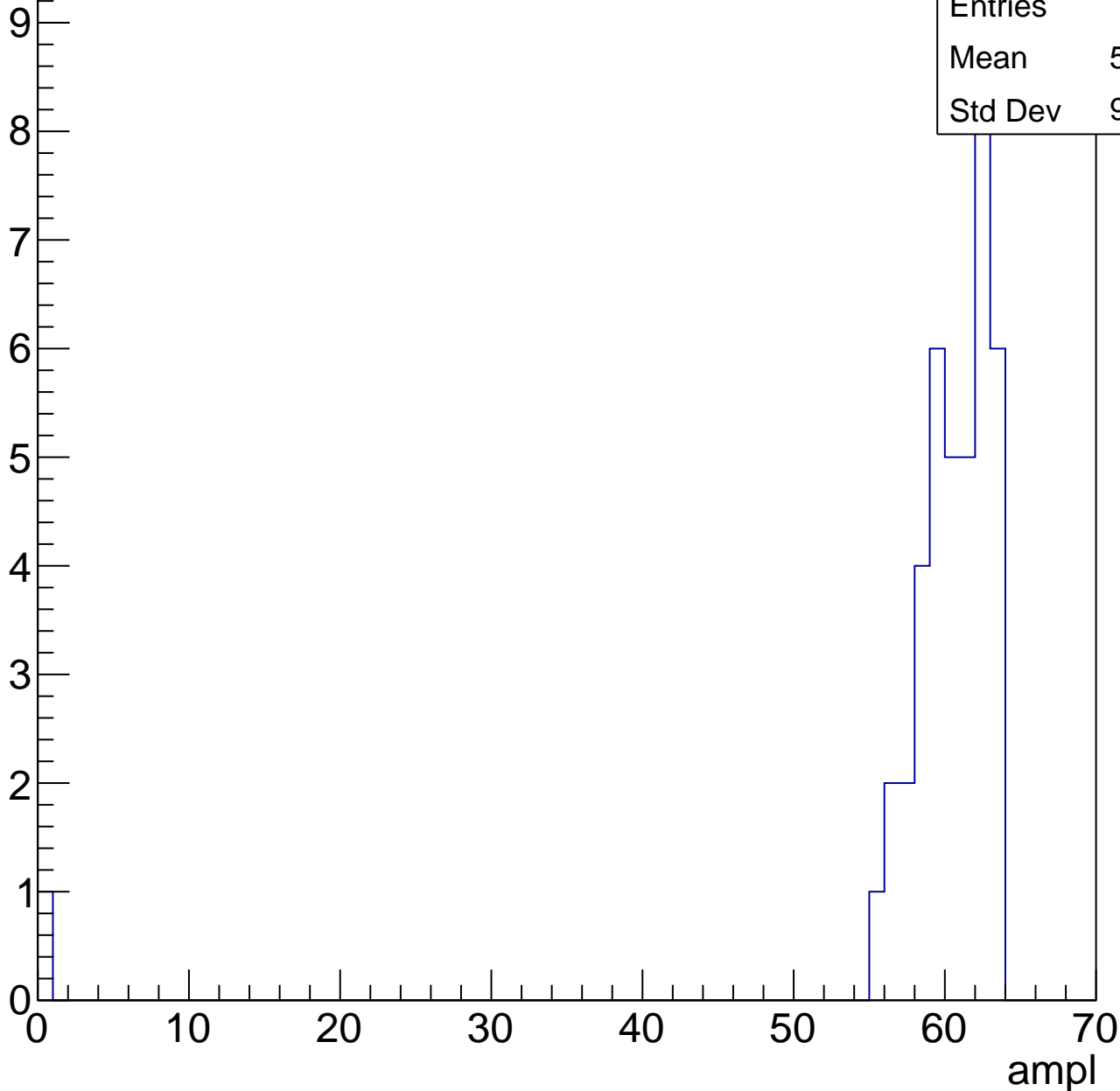


B0L001S, U18-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.73
Std Dev	9.533



B0L001S, U18-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch45, adc0

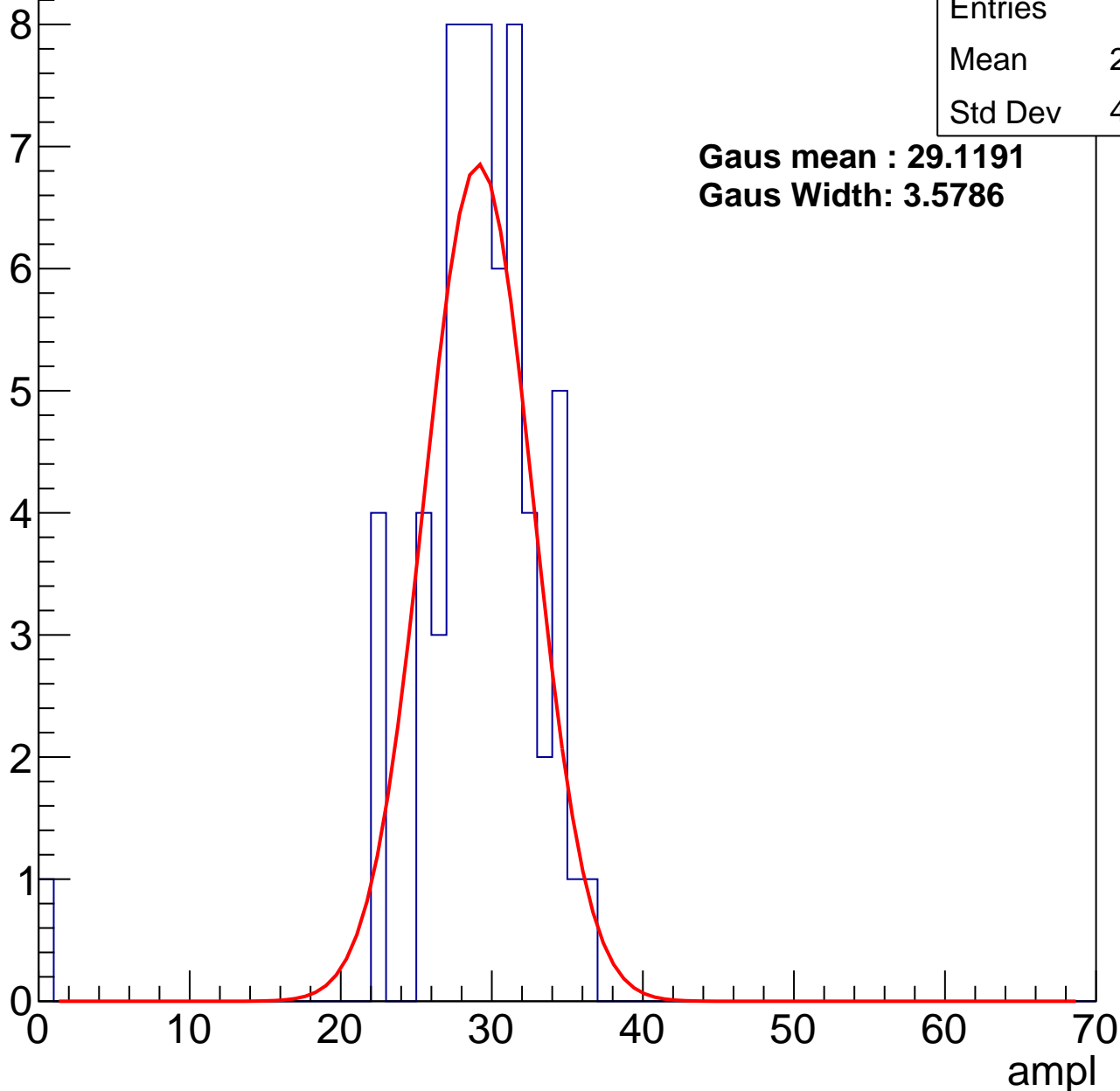
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	28.59
Std Dev	4.836

Gaus mean : 29.1191

Gaus Width: 3.5786

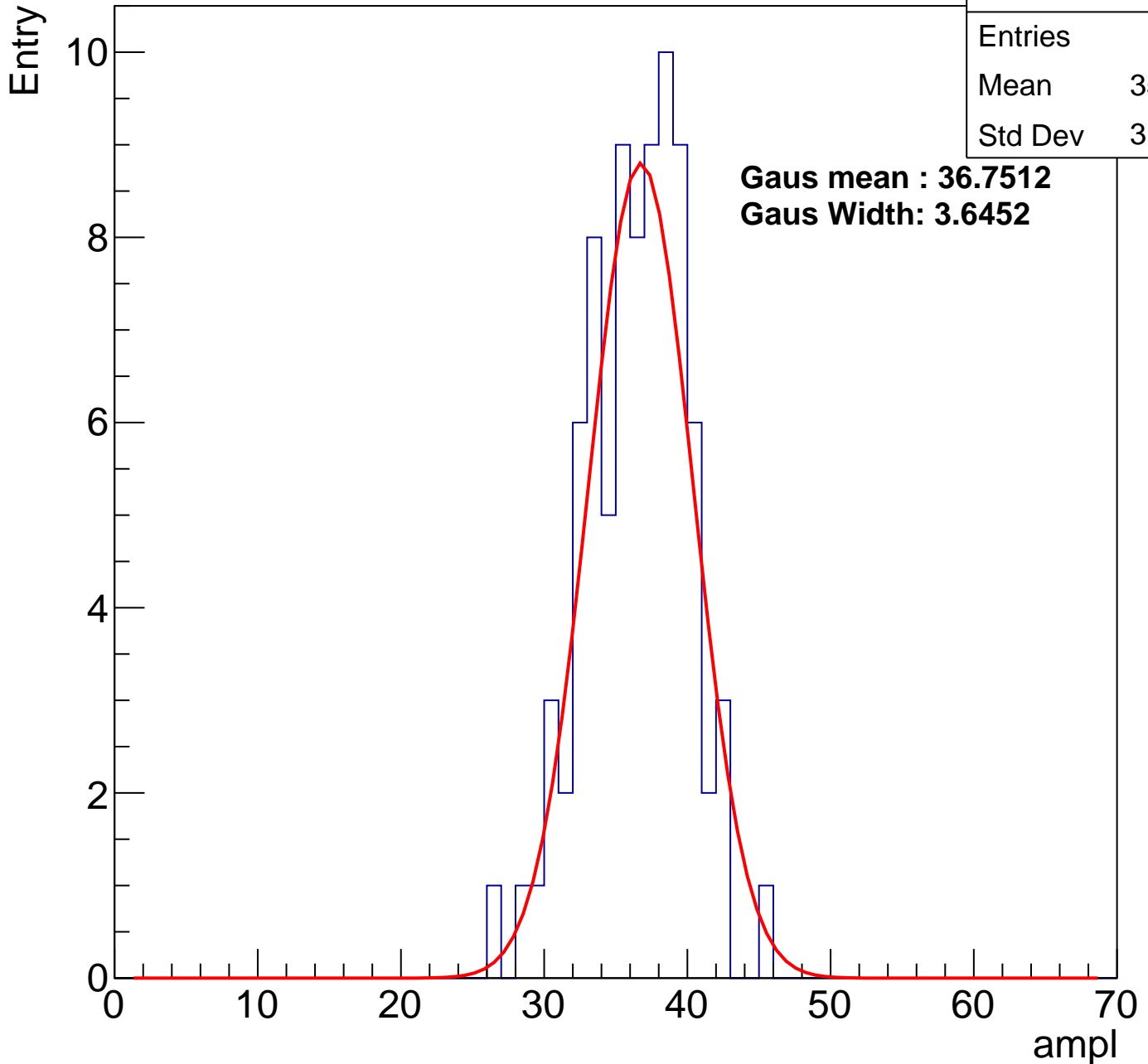


B0L001S, U18-ch45, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	35.96
Std Dev	3.507

Gaus mean : 36.7512
Gaus Width: 3.6452



B0L001S, U18-ch45, adc2

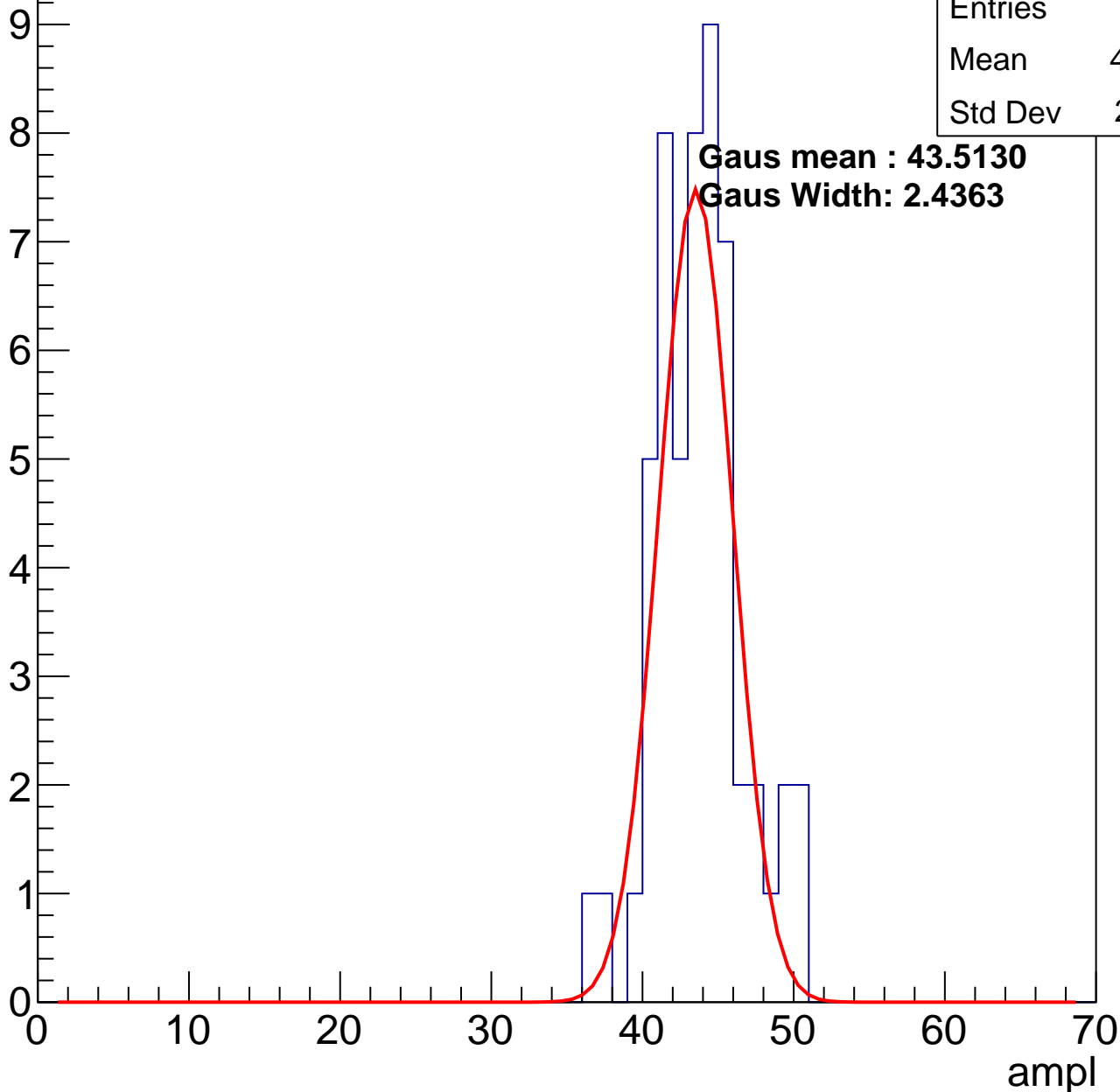
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.28
Std Dev	2.921

Gaus mean : 43.5130

Gaus Width: 2.4363



B0L001S, U18-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	49.98
Std Dev	3.813

Entry

10

8

6

4

2

0

0

10

20

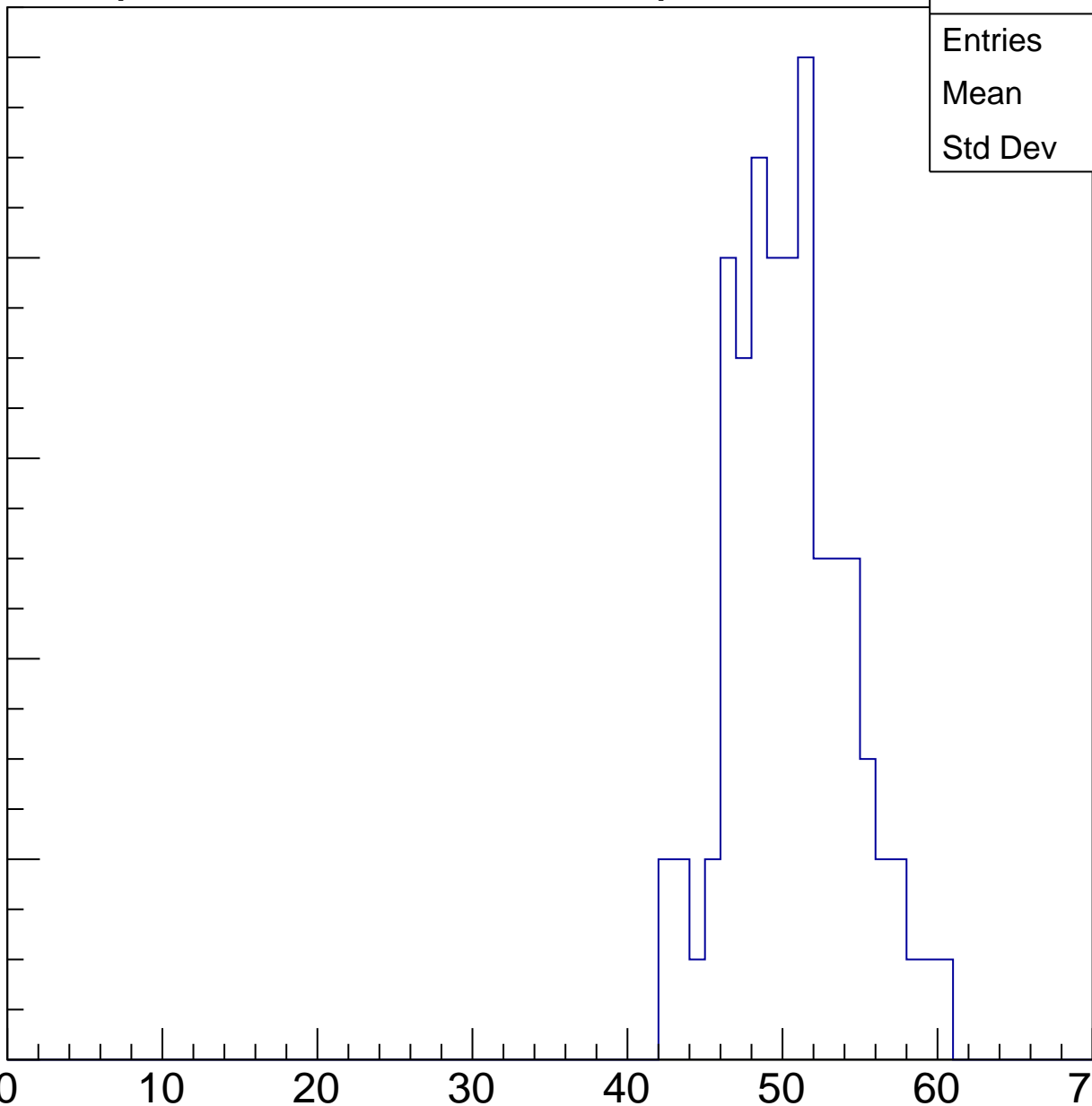
30

40

50

60

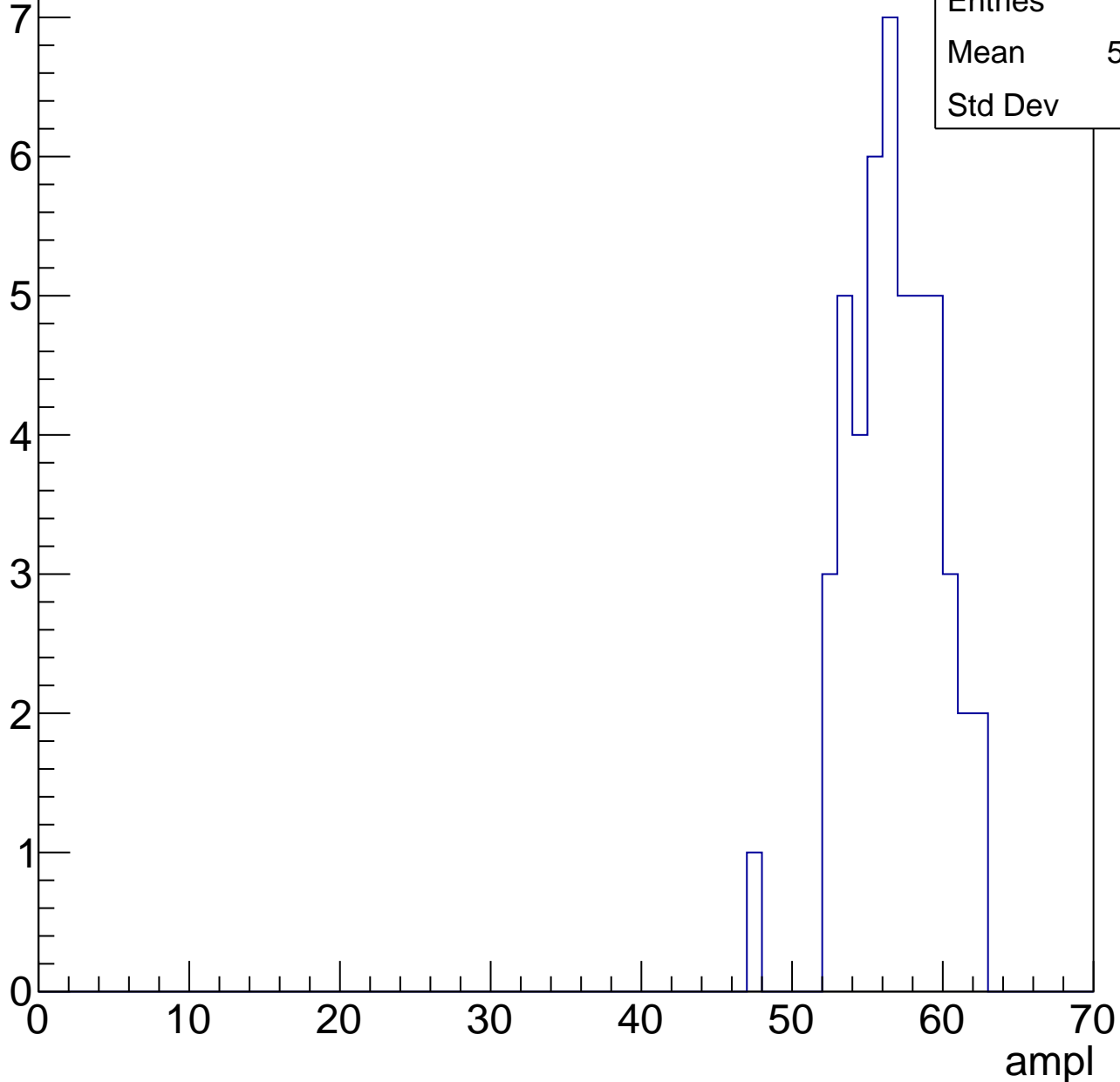
ampl



B0L001S, U18-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

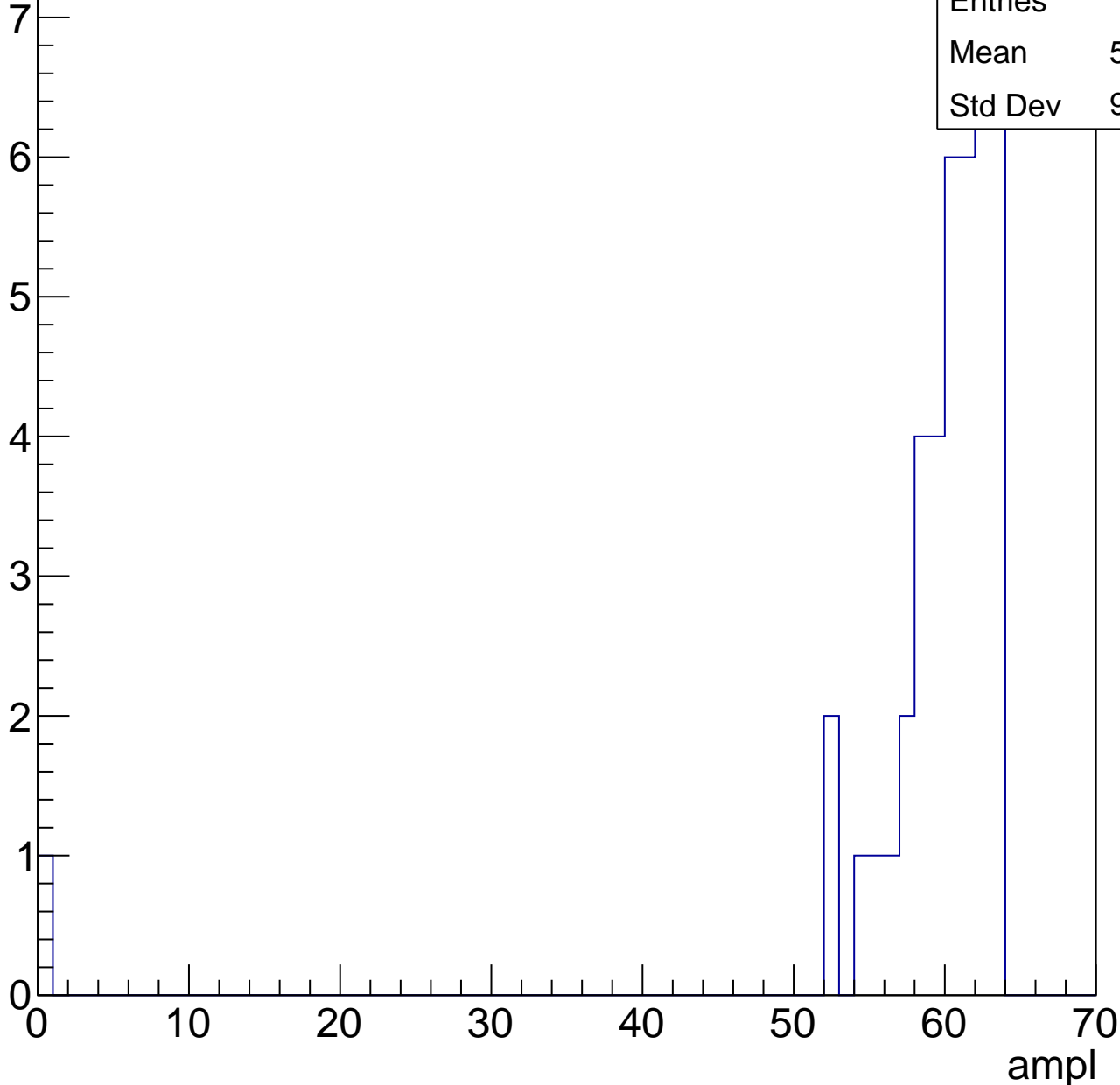


B0L001S, U18-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

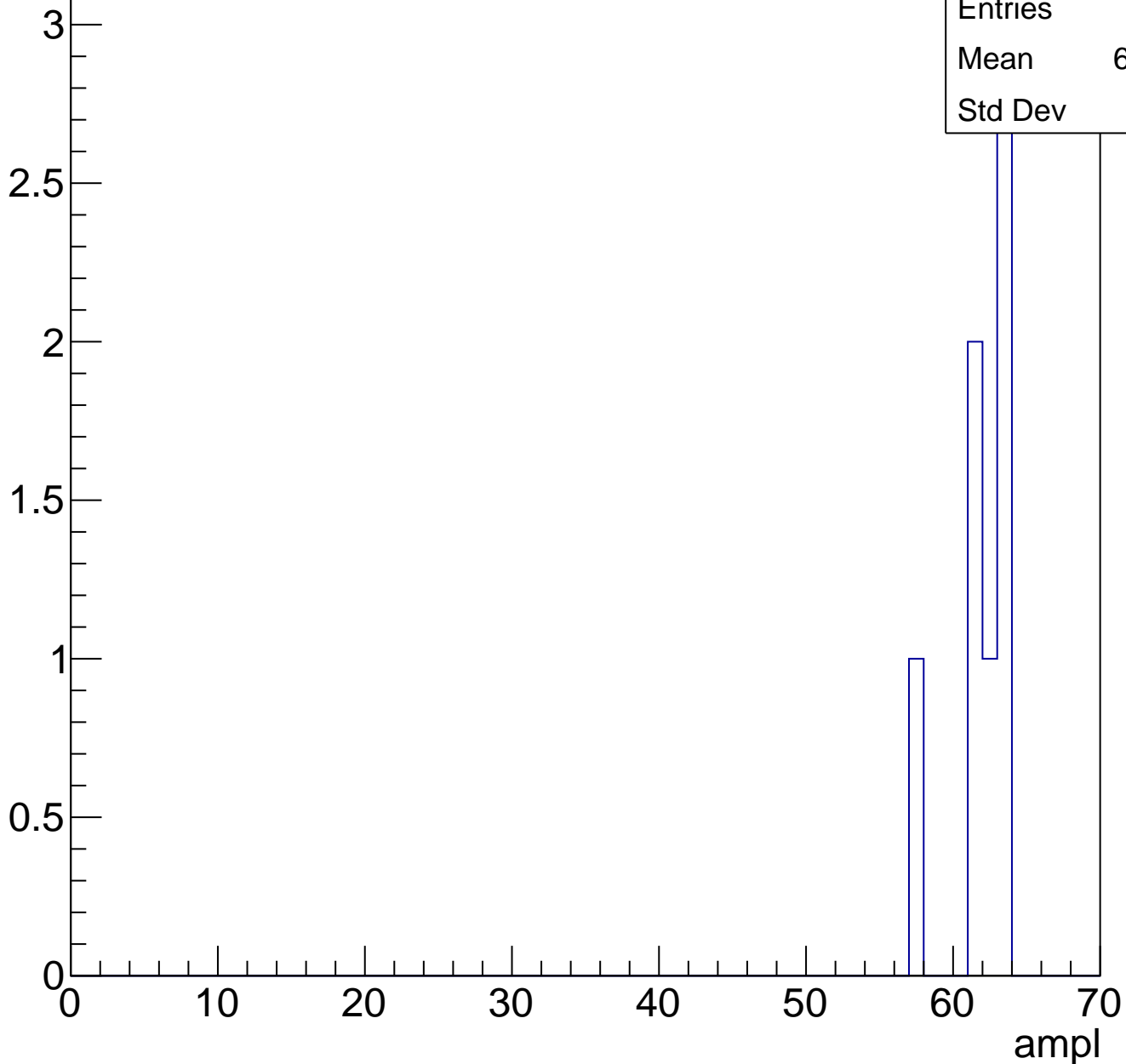
Entries	42
Mean	58.38
Std Dev	9.549



B0L001S, U18-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U18-ch46, adc0

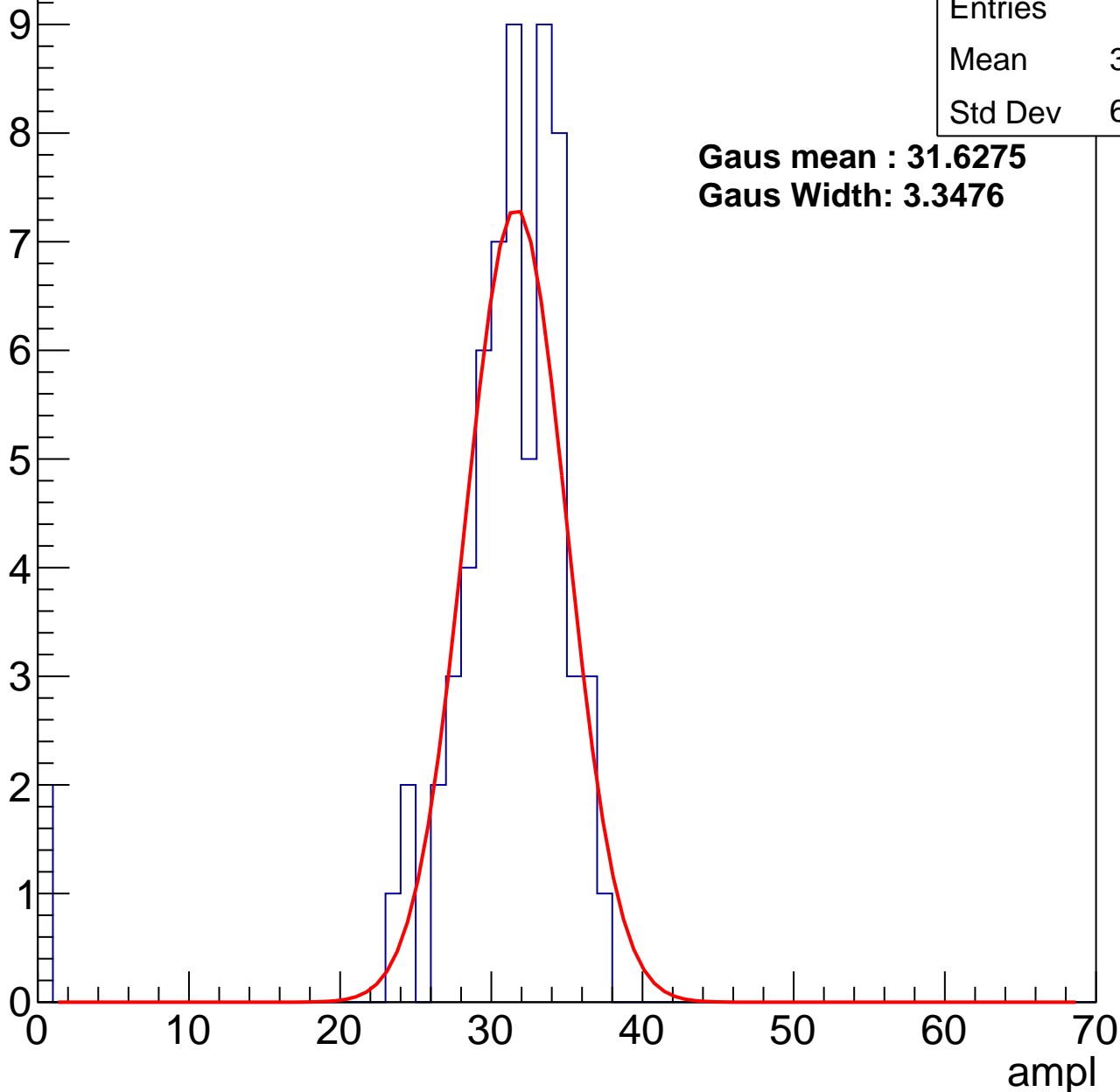
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.12
Std Dev	6.166

Gaus mean : 31.6275

Gaus Width: 3.3476



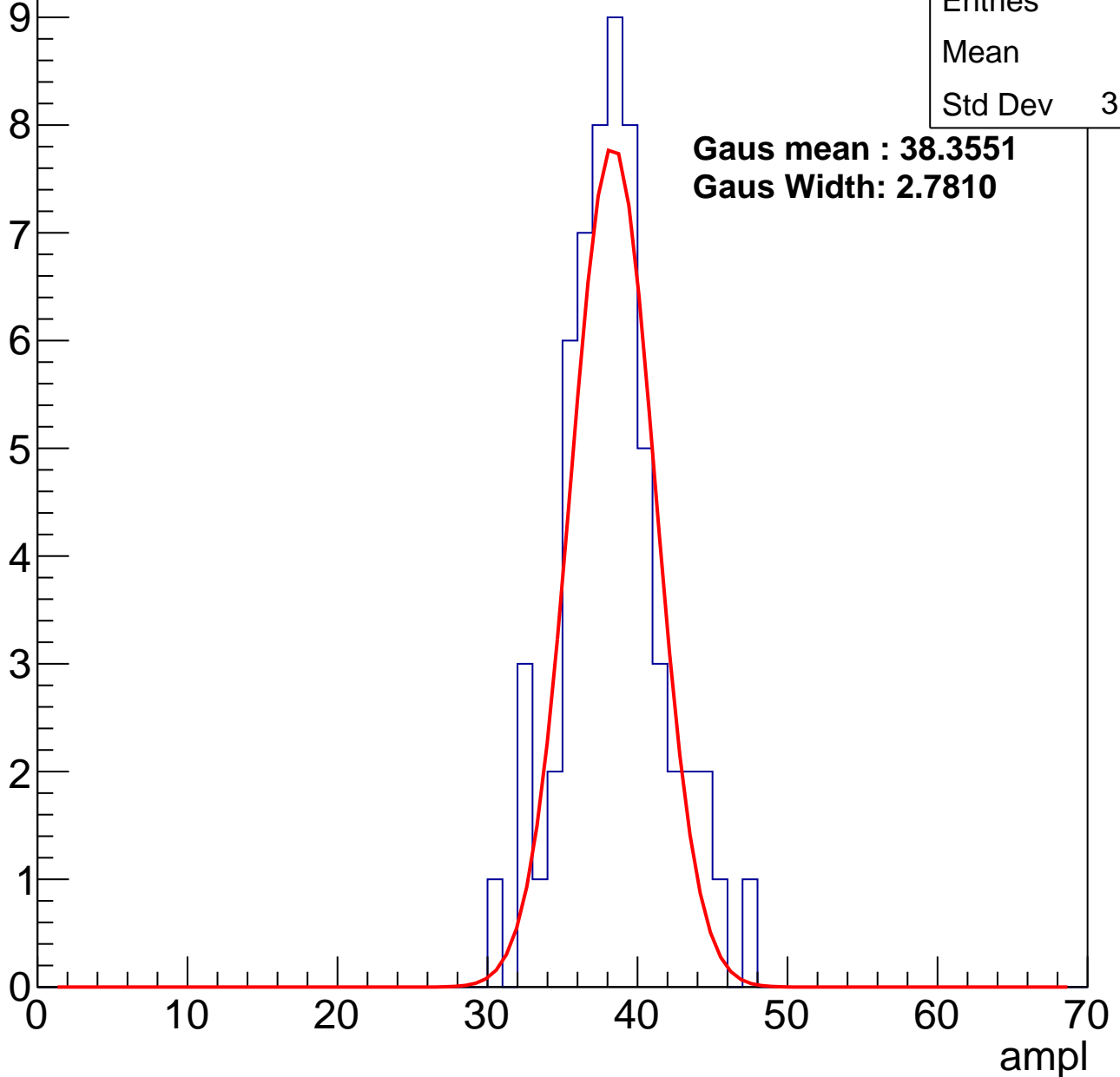
B0L001S, U18-ch46, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.9
Std Dev	3.283

Gaus mean : 38.3551
Gaus Width: 2.7810



B0L001S, U18-ch46, adc2

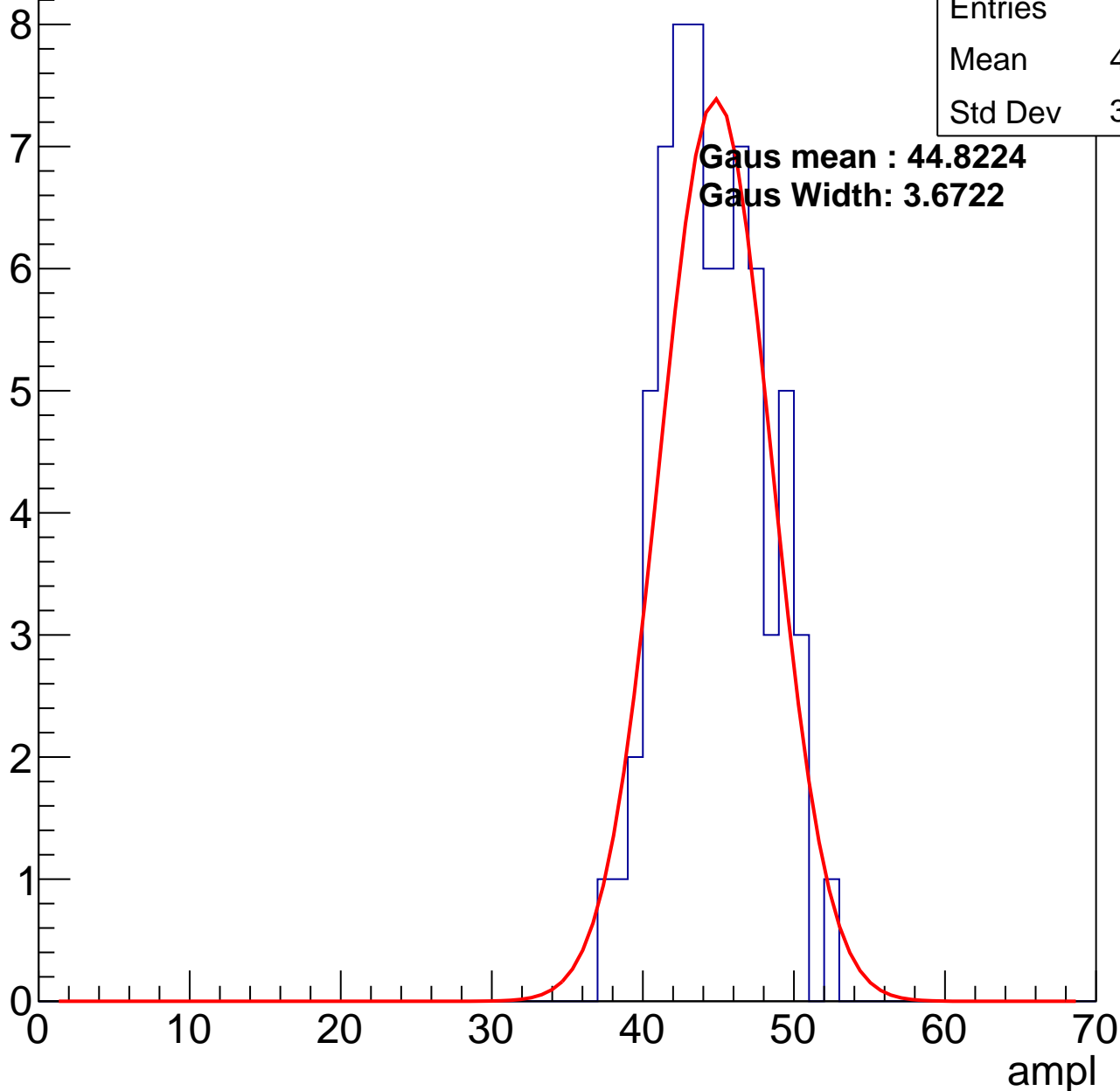
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.19
Std Dev	3.294

Gaus mean : 44.8224

Gaus Width: 3.6722



B0L001S, U18-ch46, adc3

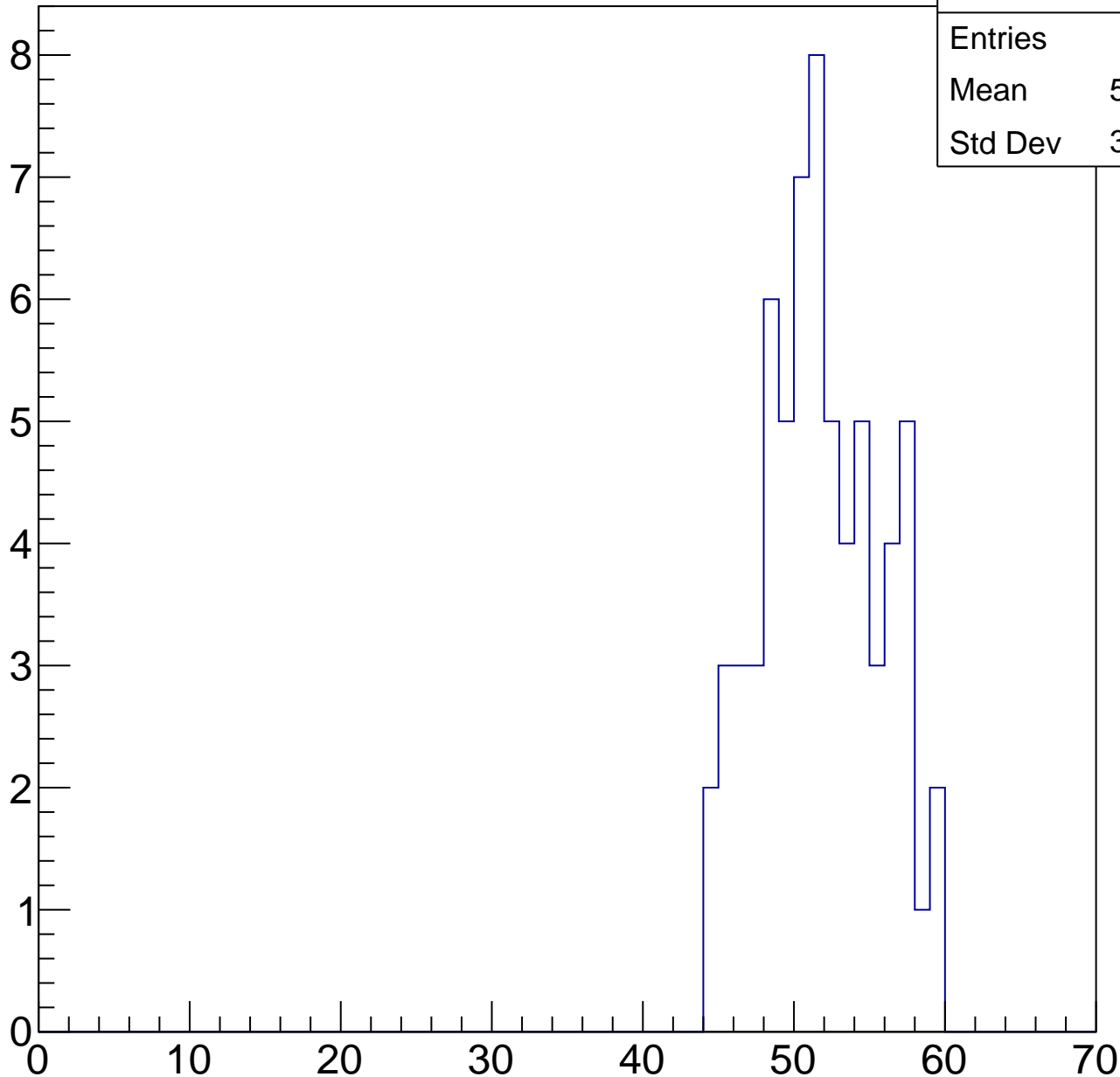
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	66
Mean	51.29
Std Dev	3.845

ampl

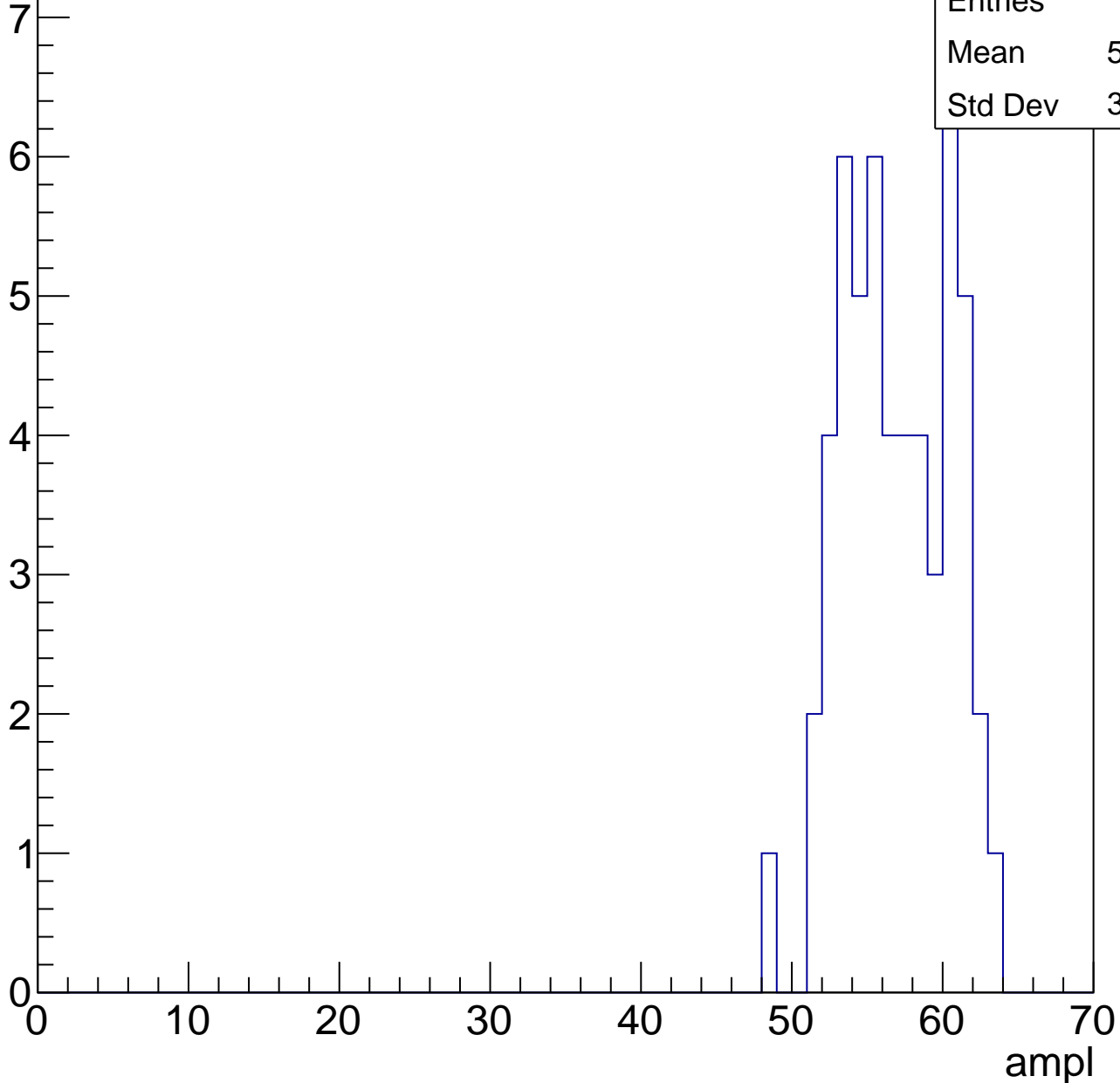


B0L001S, U18-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.46
Std Dev	3.473

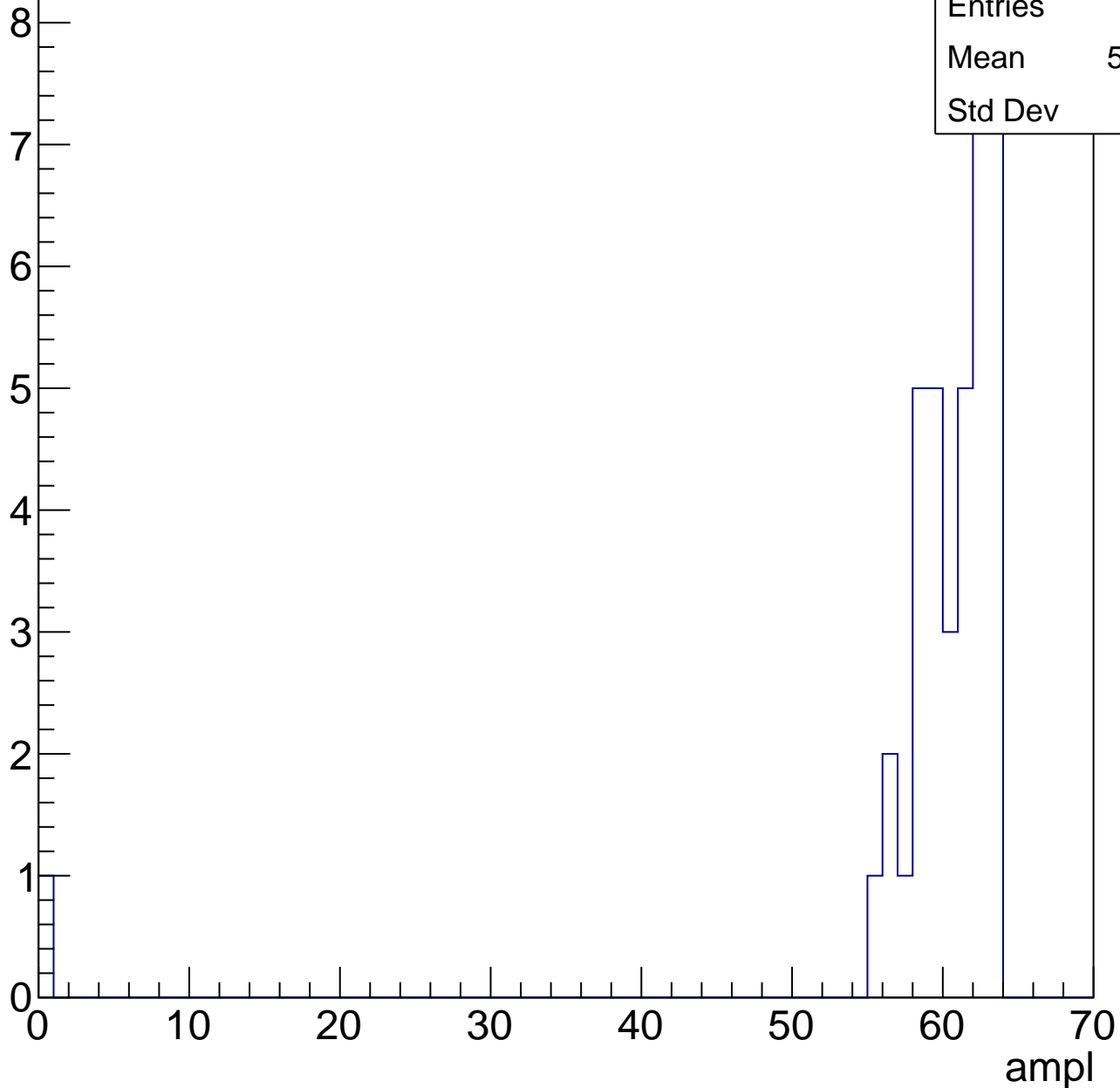


B0L001S, U18-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.82
Std Dev	9.8



B0L001S, U18-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch47, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	28.81
Std Dev	8.651

Gaus mean : 30.9657

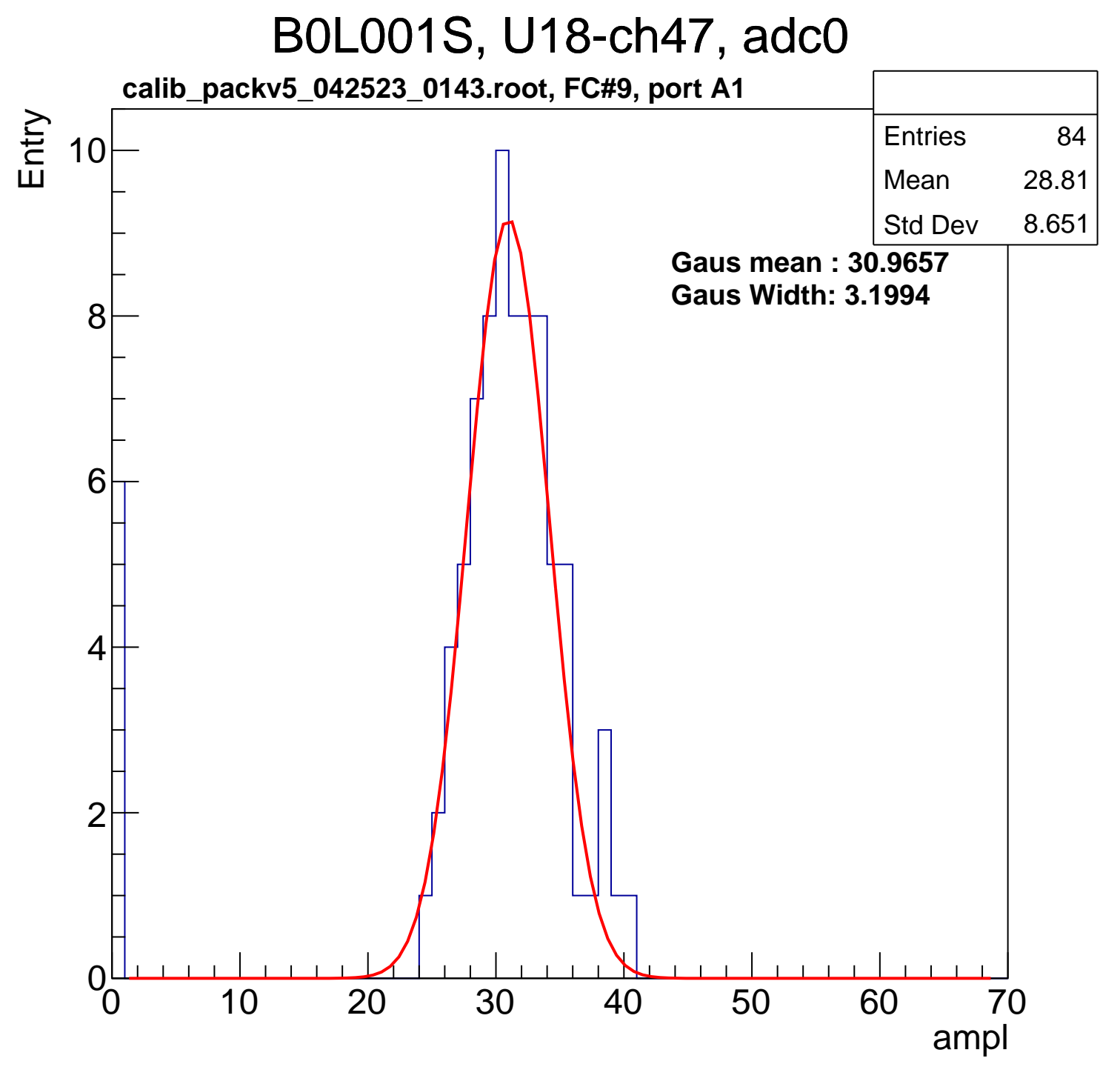
Gaus Width: 3.1994

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



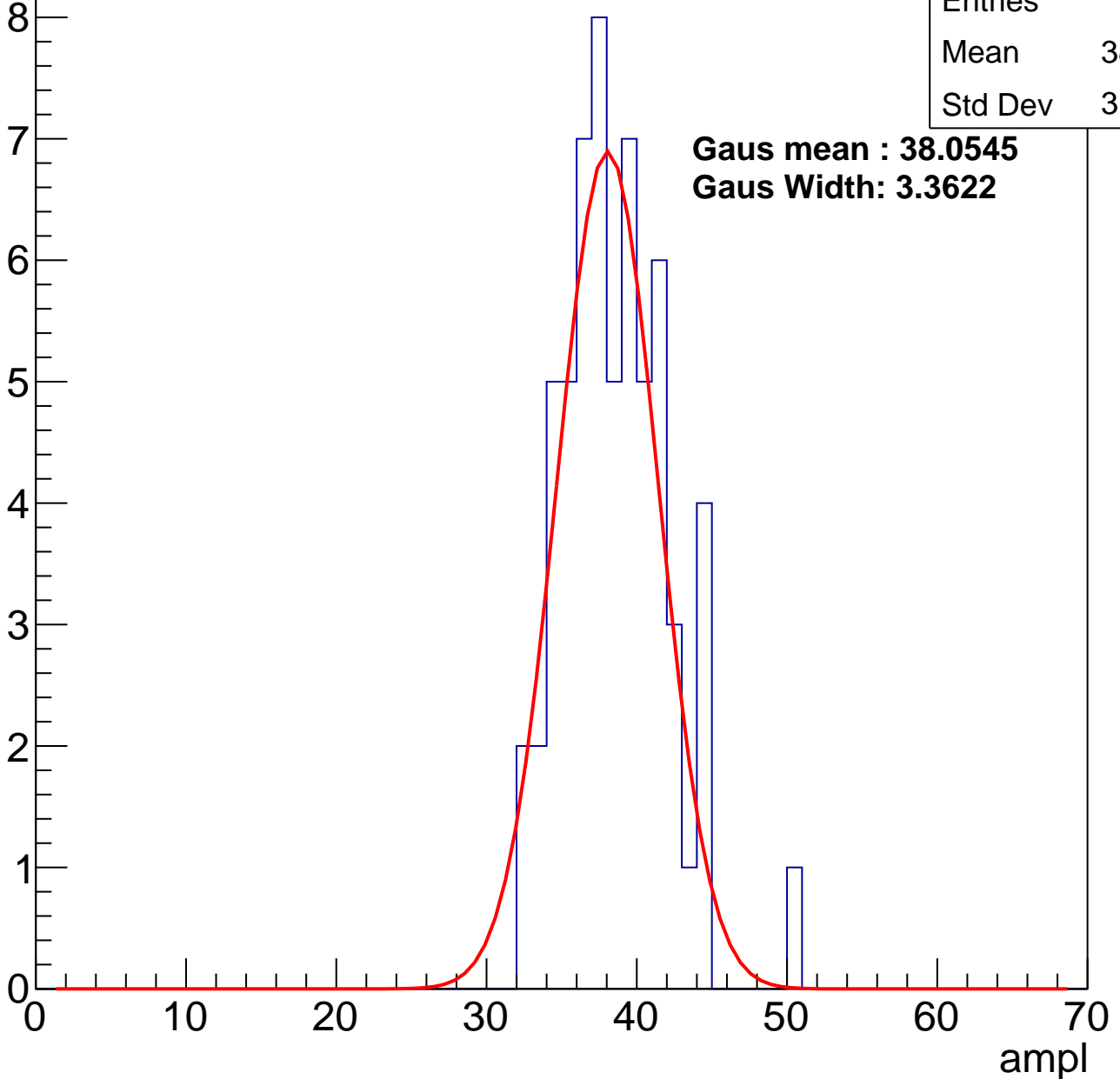
B0L001S, U18-ch47, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.15
Std Dev	3.444

Gaus mean : 38.0545
Gaus Width: 3.3622



B0L001S, U18-ch47, adc2

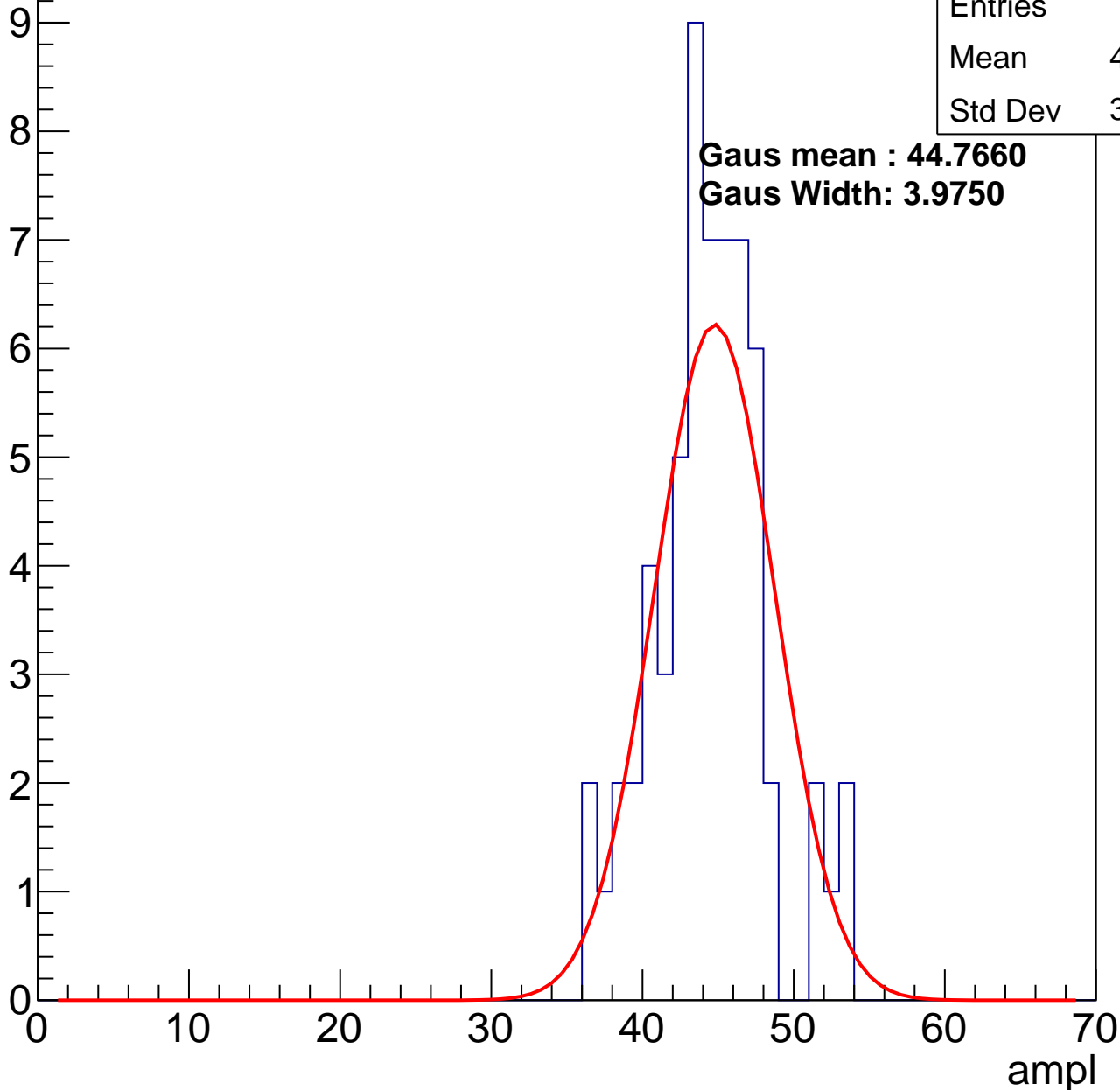
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.97
Std Dev	3.733

Gaus mean : 44.7660

Gaus Width: 3.9750

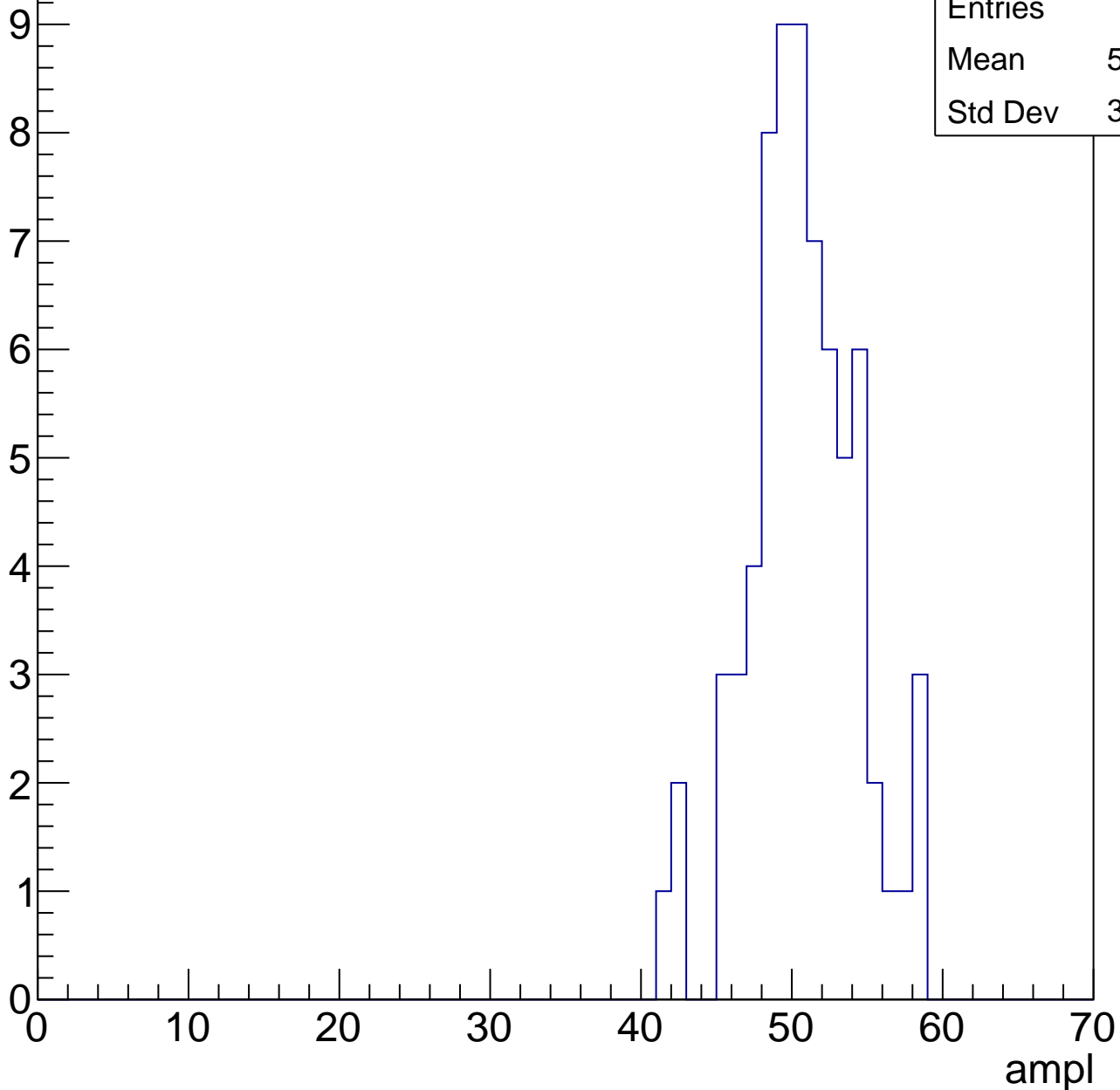


B0L001S, U18-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

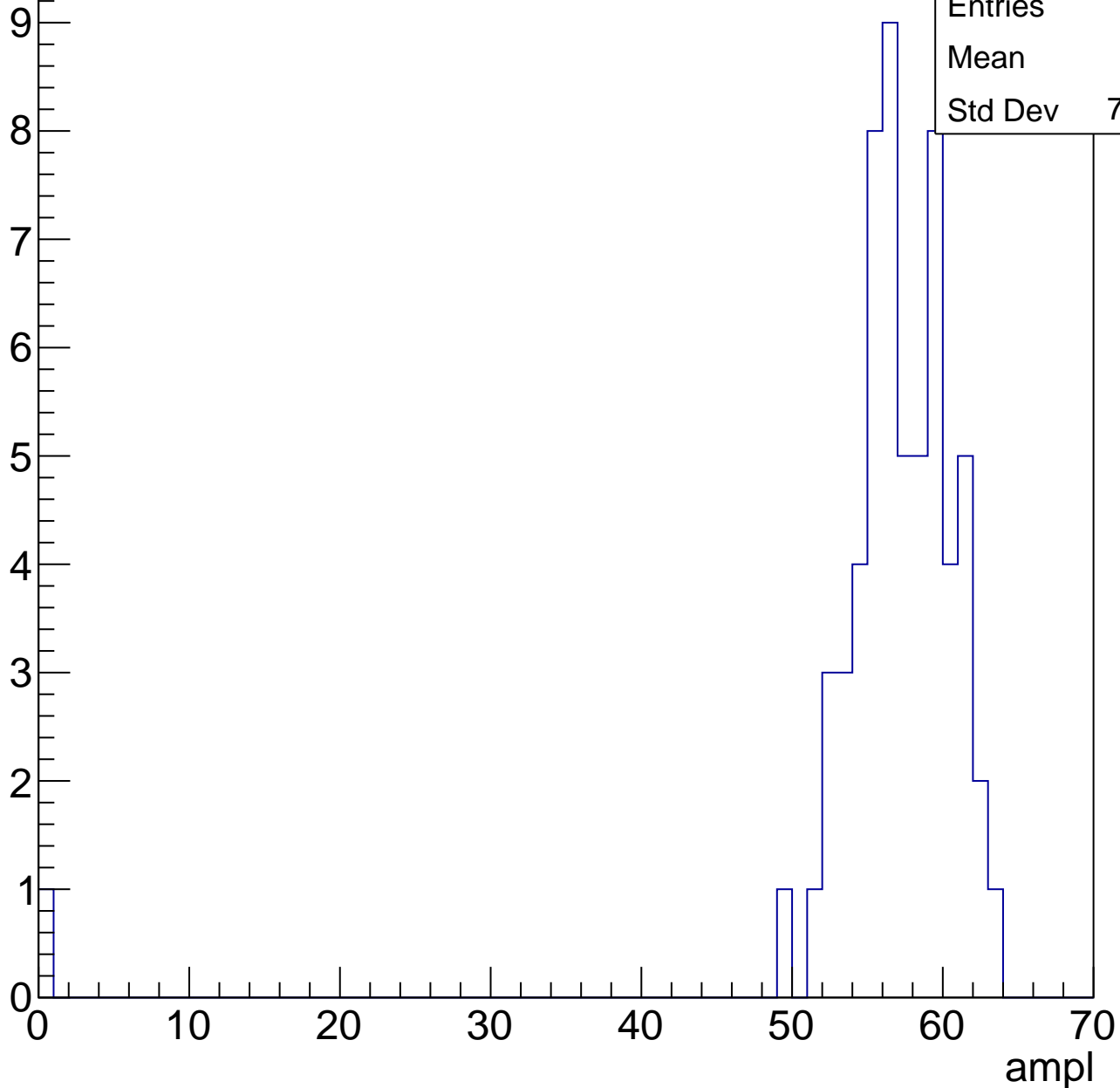
Entries	70
Mean	50.23
Std Dev	3.594



B0L001S, U18-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

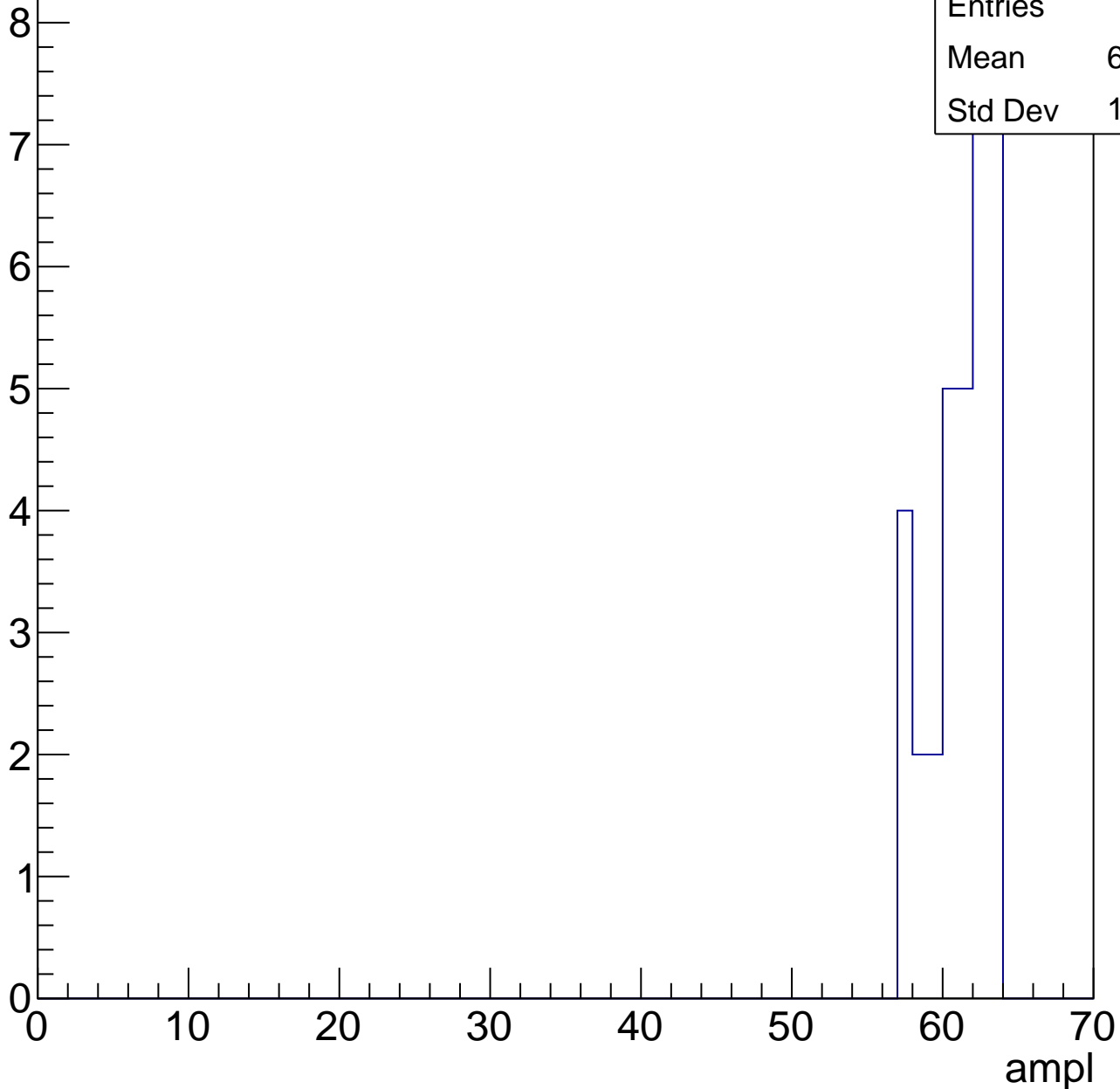


B0L001S, U18-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	60.79
Std Dev	1.982



B0L001S, U18-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

61.5

Std Dev

1.5

B0L001S, U18-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch48, adc0

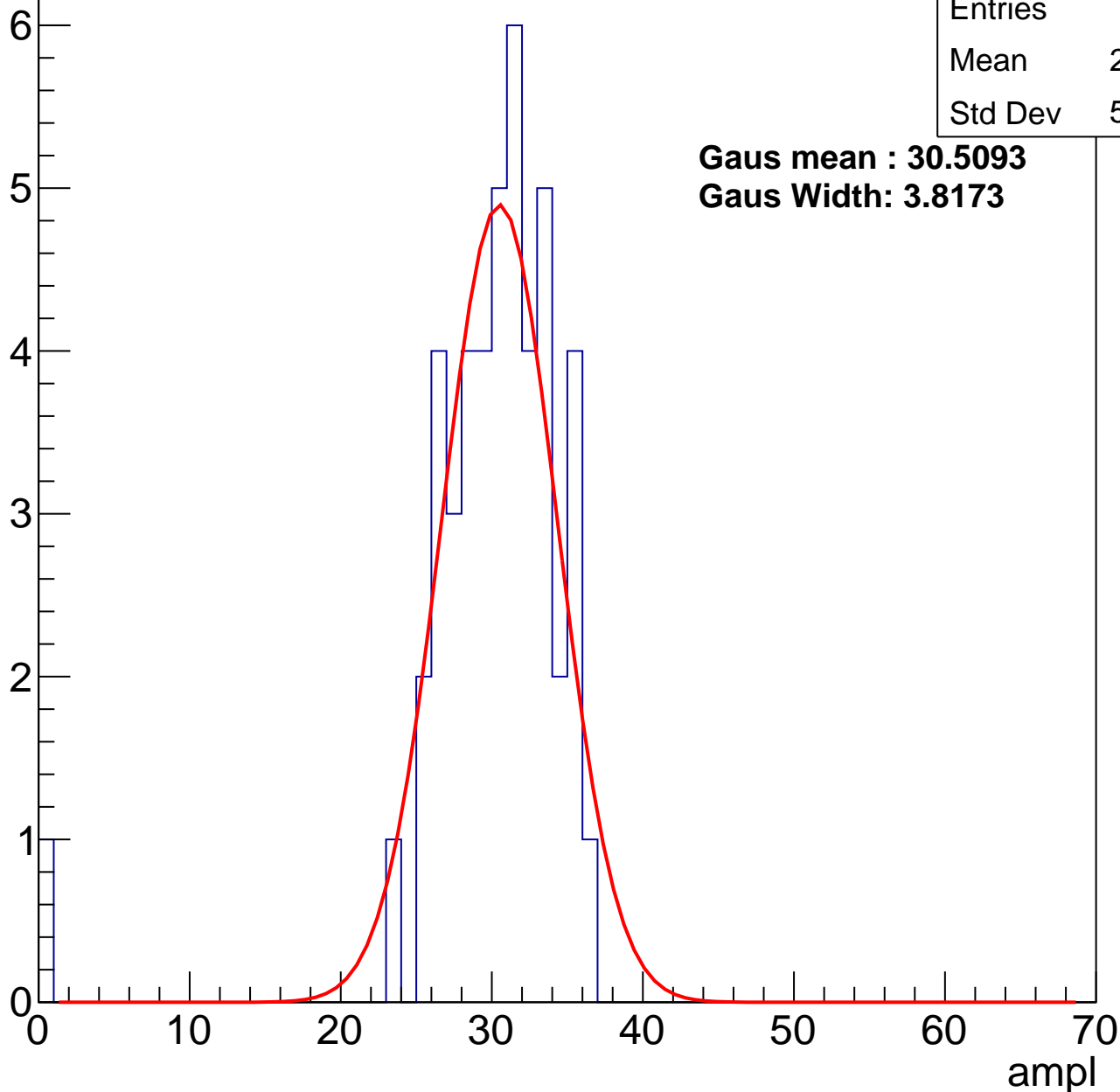
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	29.54
Std Dev	5.388

Gaus mean : 30.5093

Gaus Width: 3.8173

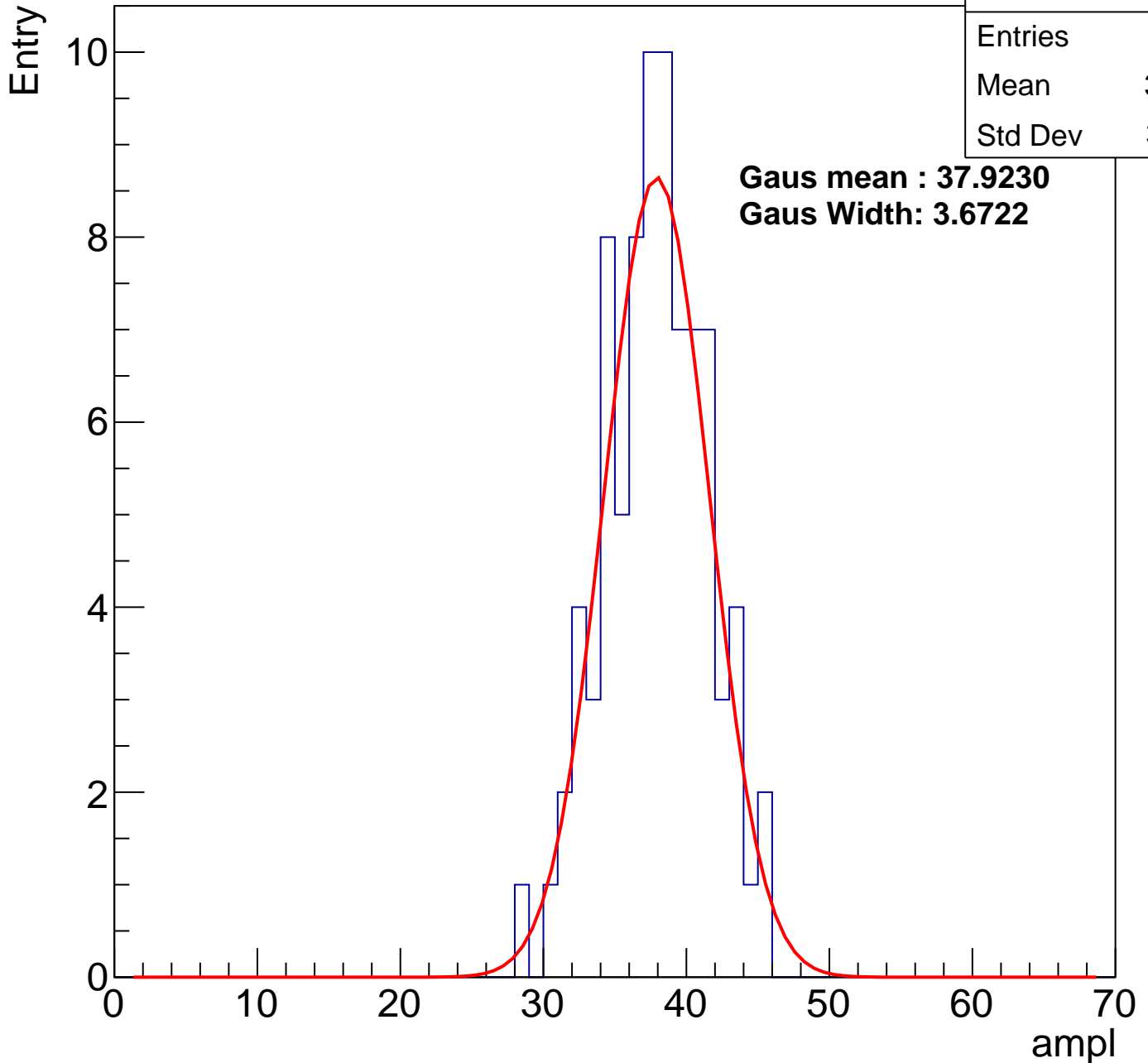


B0L001S, U18-ch48, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	37.4
Std Dev	3.55

Gaus mean : 37.9230
Gaus Width: 3.6722



B0L001S, U18-ch48, adc2

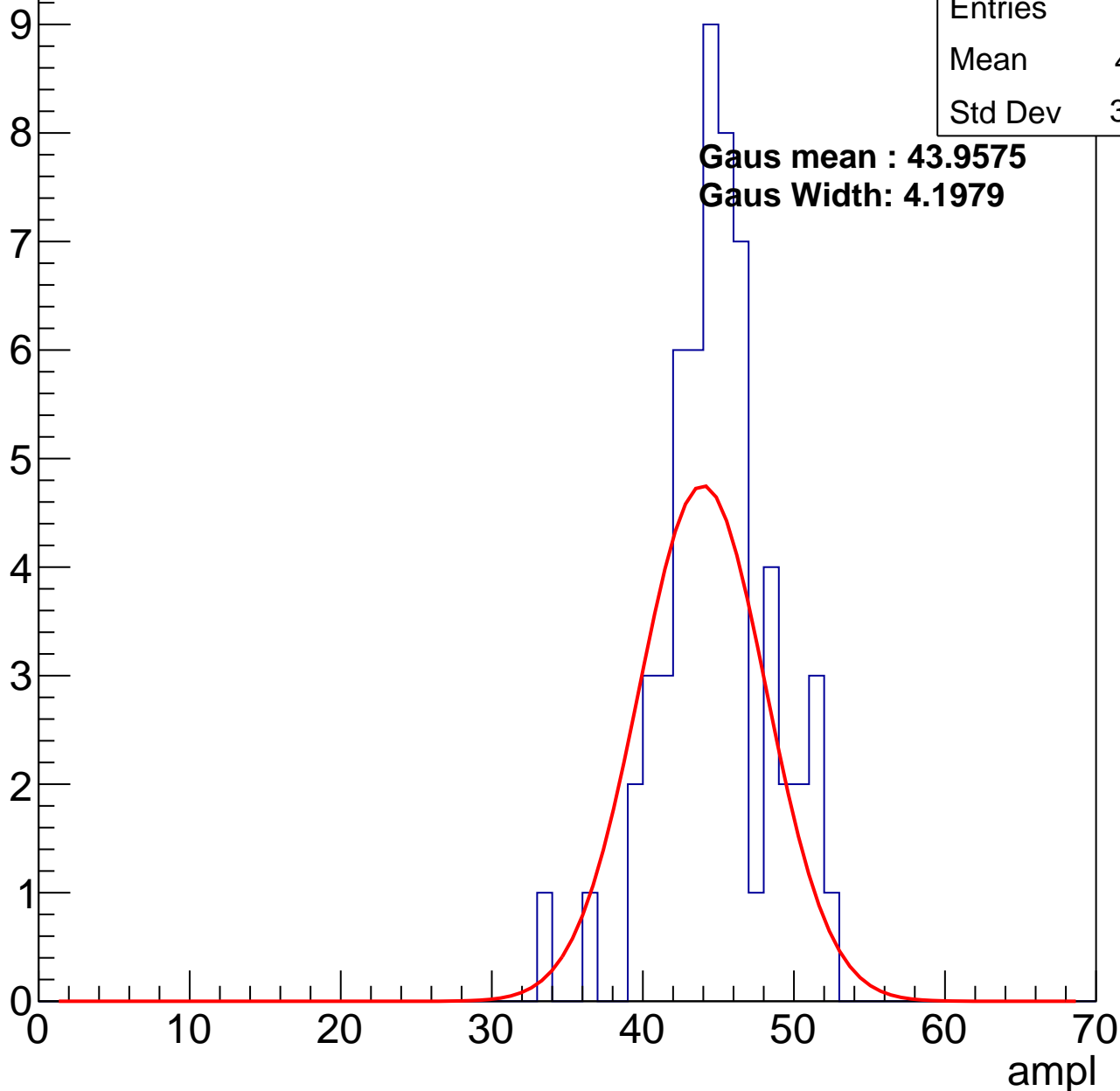
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.41
Std Dev	3.618

Gaus mean : 43.9575

Gaus Width: 4.1979

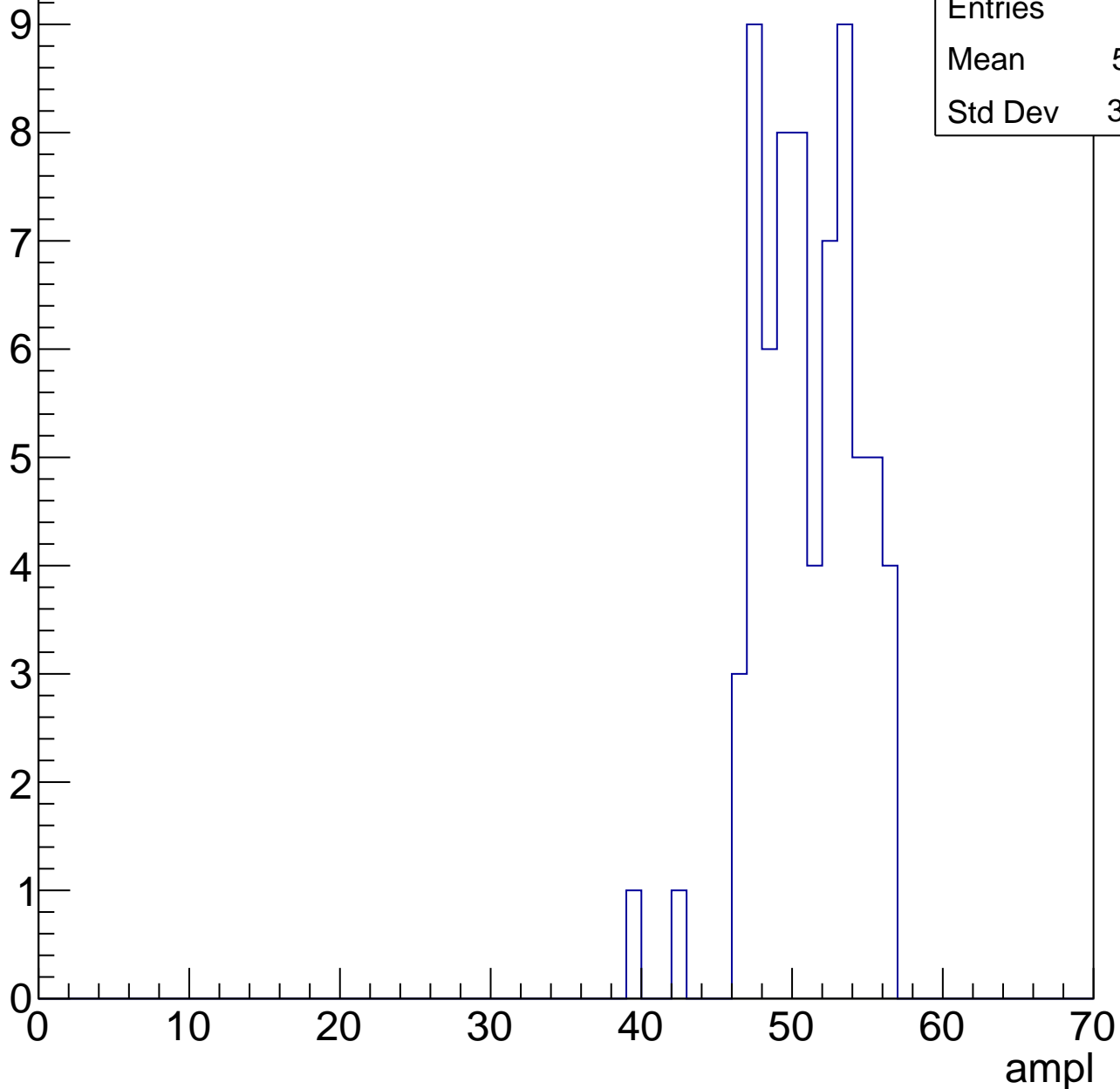


B0L001S, U18-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.51
Std Dev	3.358



B0L001S, U18-ch48, adc4

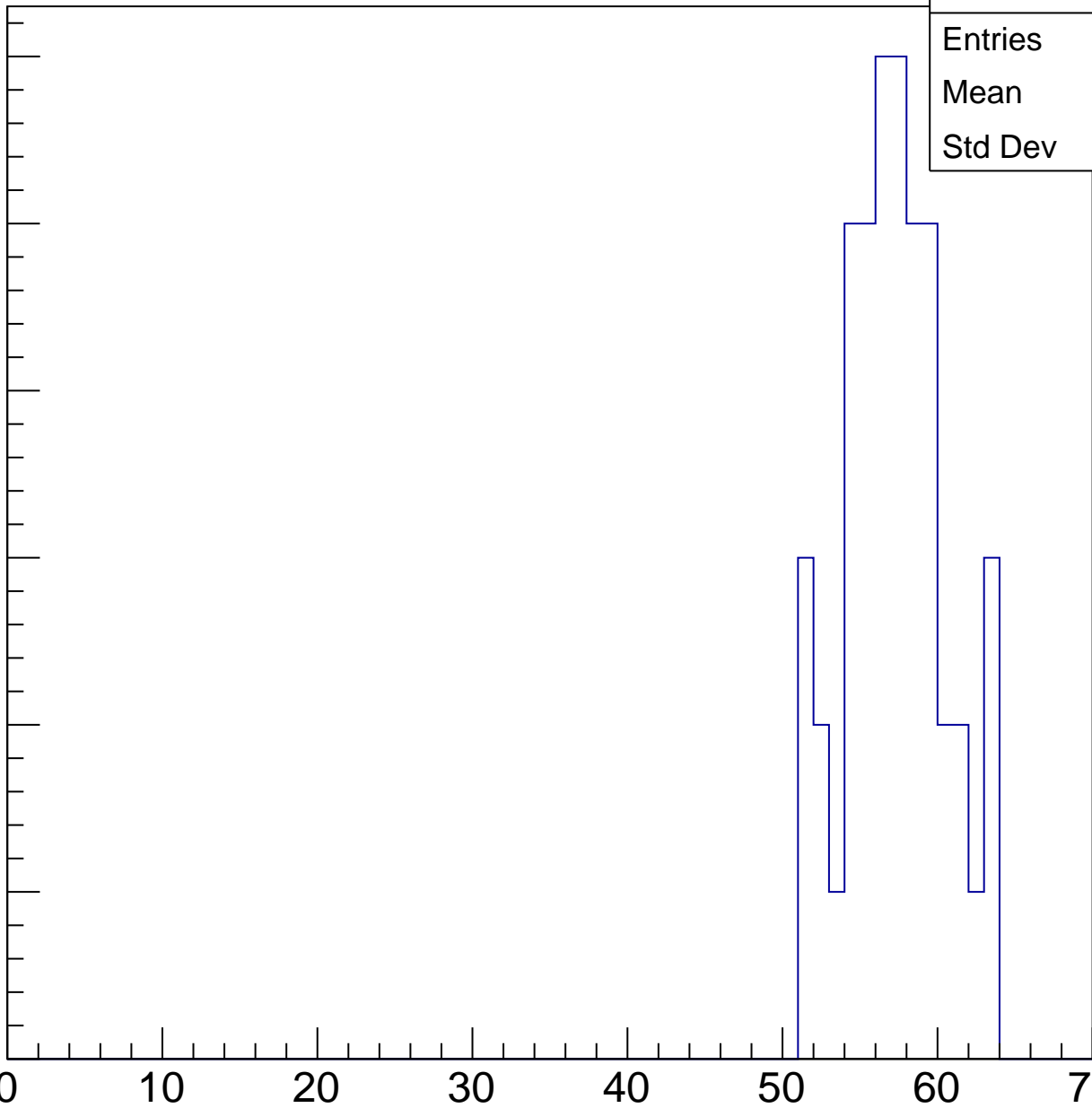
calib_packv5_042523_0143.root, FC#9, port A1

Entry

6
5
4
3
2
1
0

Entries	46
Mean	56.76
Std Dev	3.129

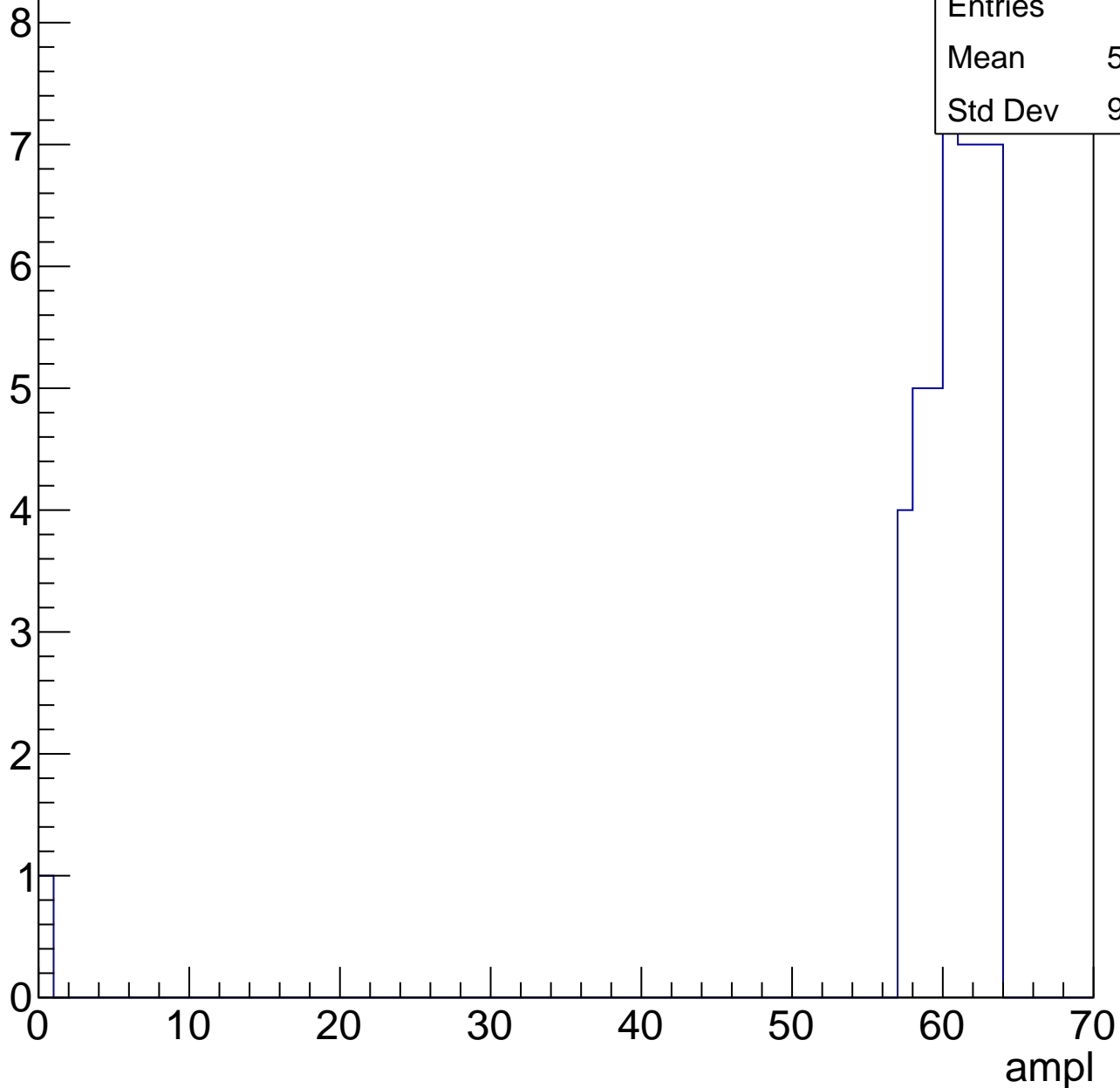
ampl



B0L001S, U18-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch49, adc0

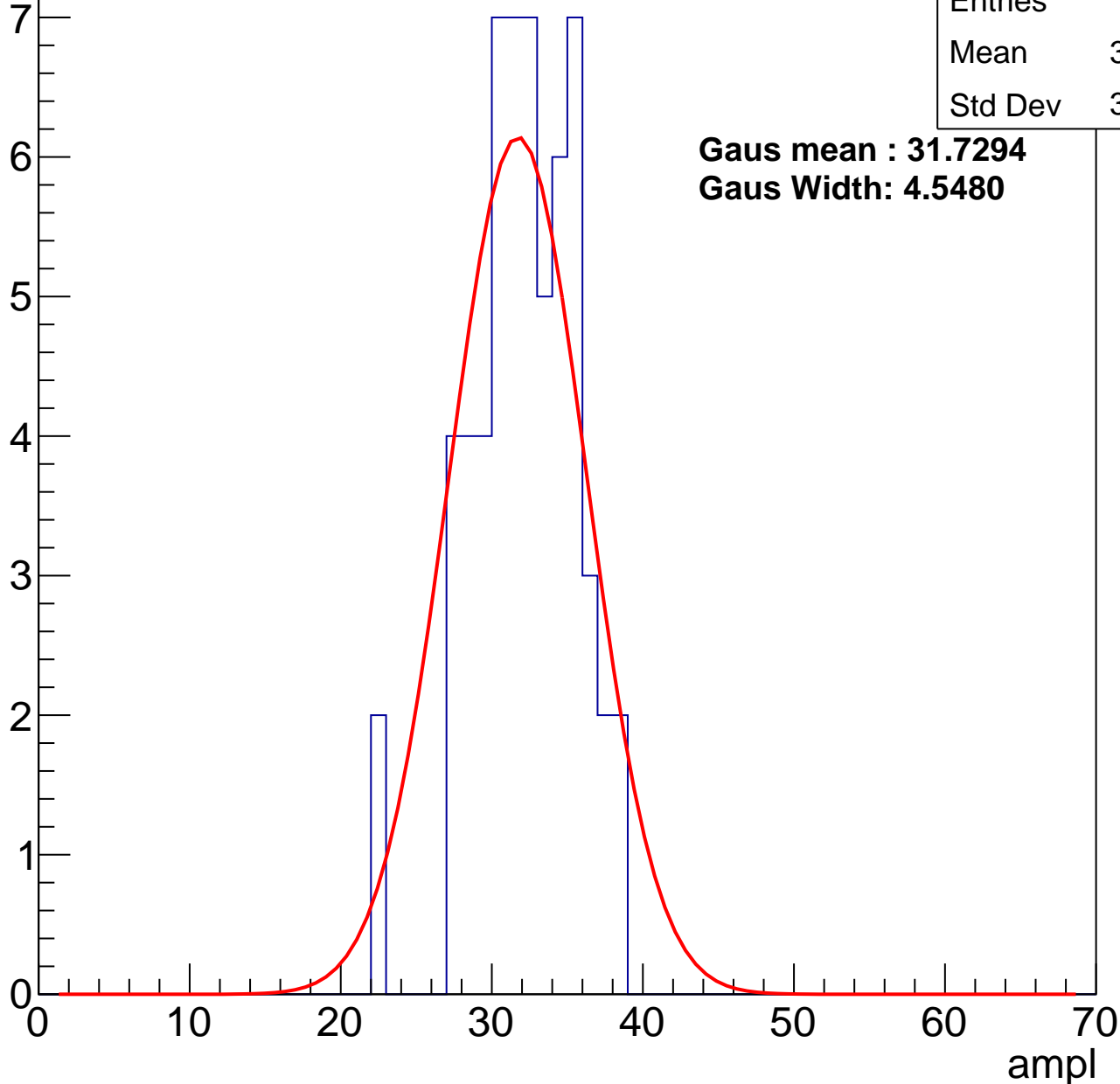
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.72
Std Dev	3.397

Gaus mean : 31.7294

Gaus Width: 4.5480



B0L001S, U18-ch49, adc1

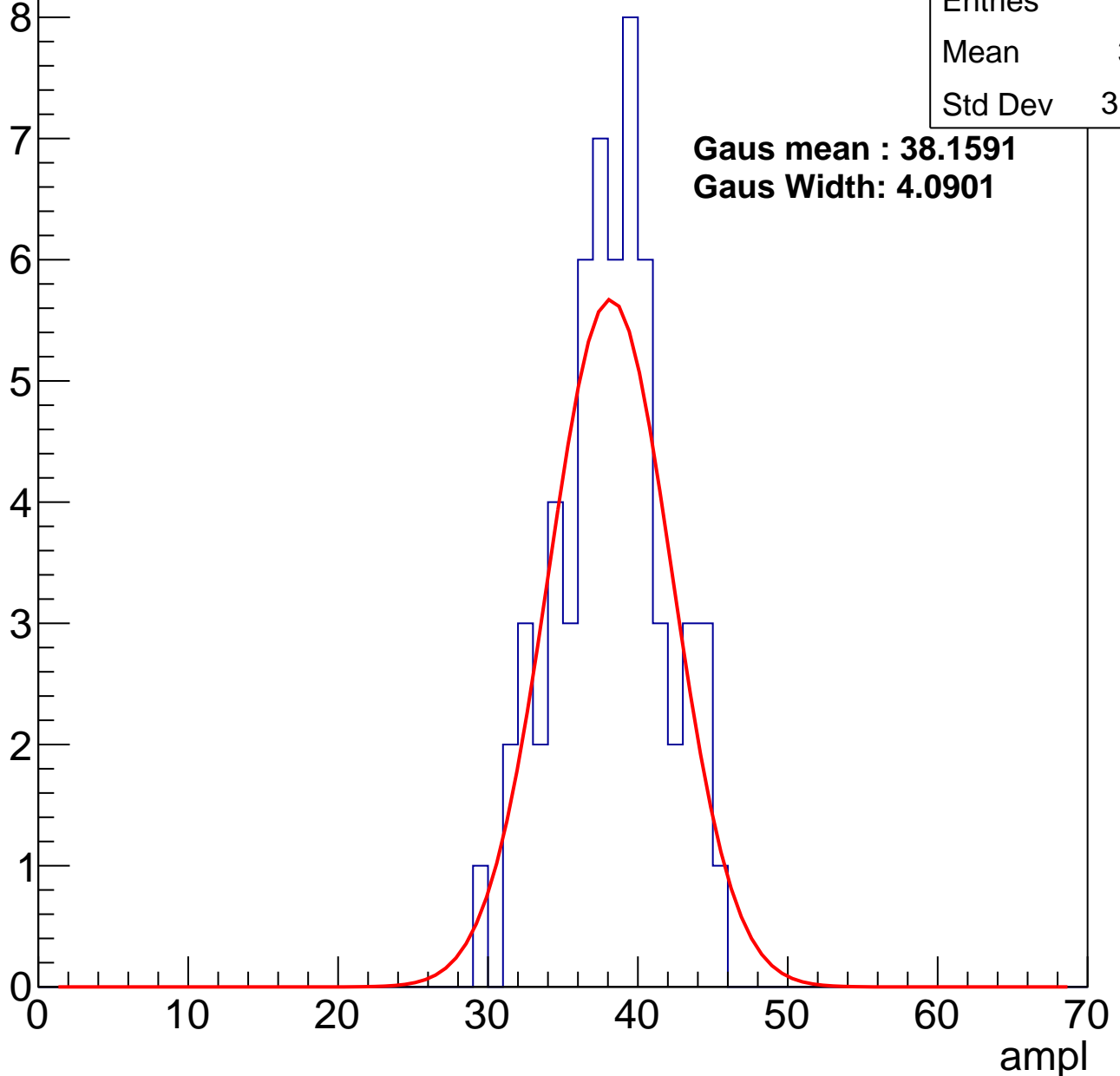
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.7
Std Dev	3.598

Gaus mean : 38.1591

Gaus Width: 4.0901



B0L001S, U18-ch49, adc2

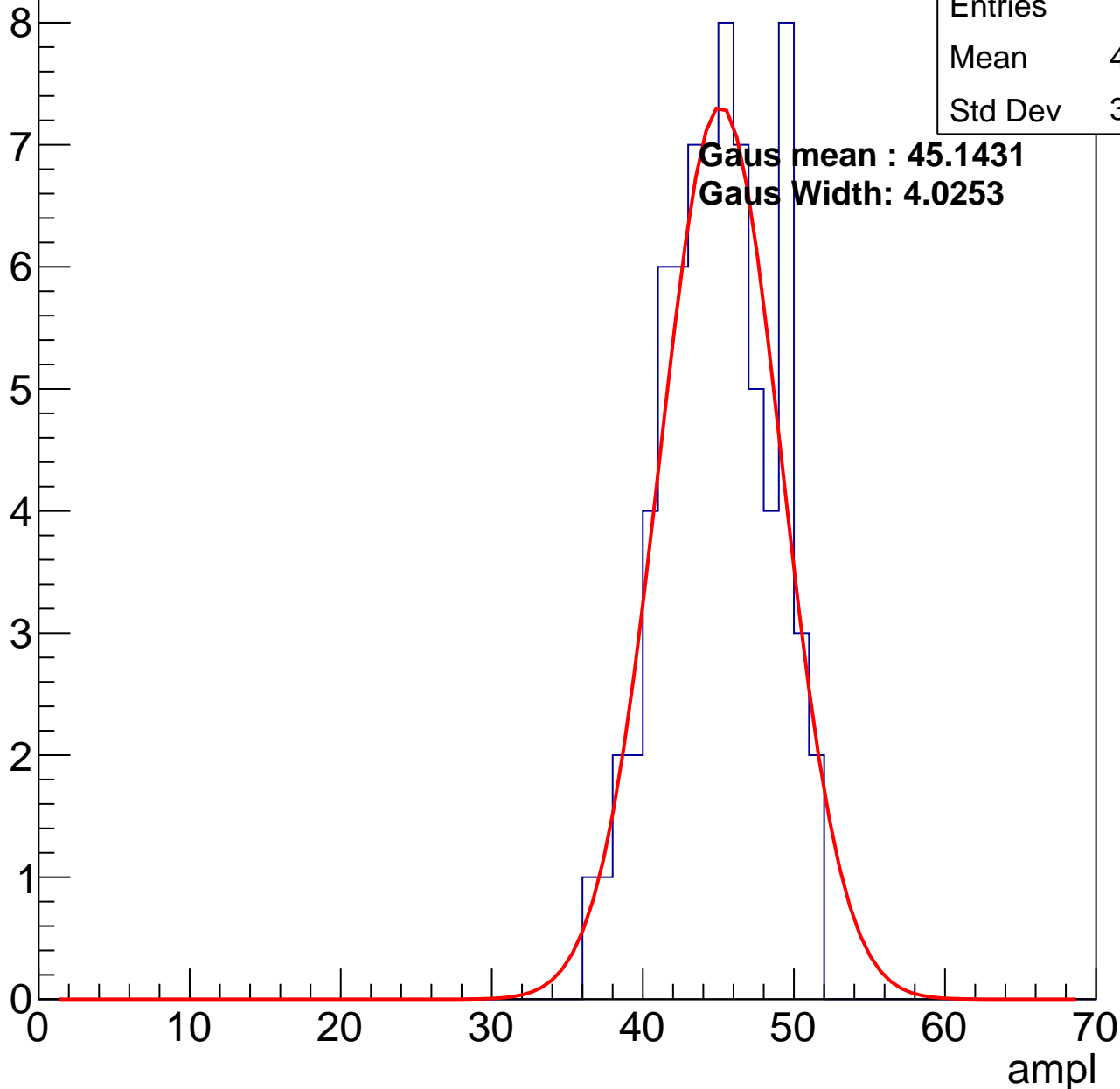
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.48
Std Dev	3.547

Gaus mean : 45.1431

Gaus Width: 4.0253

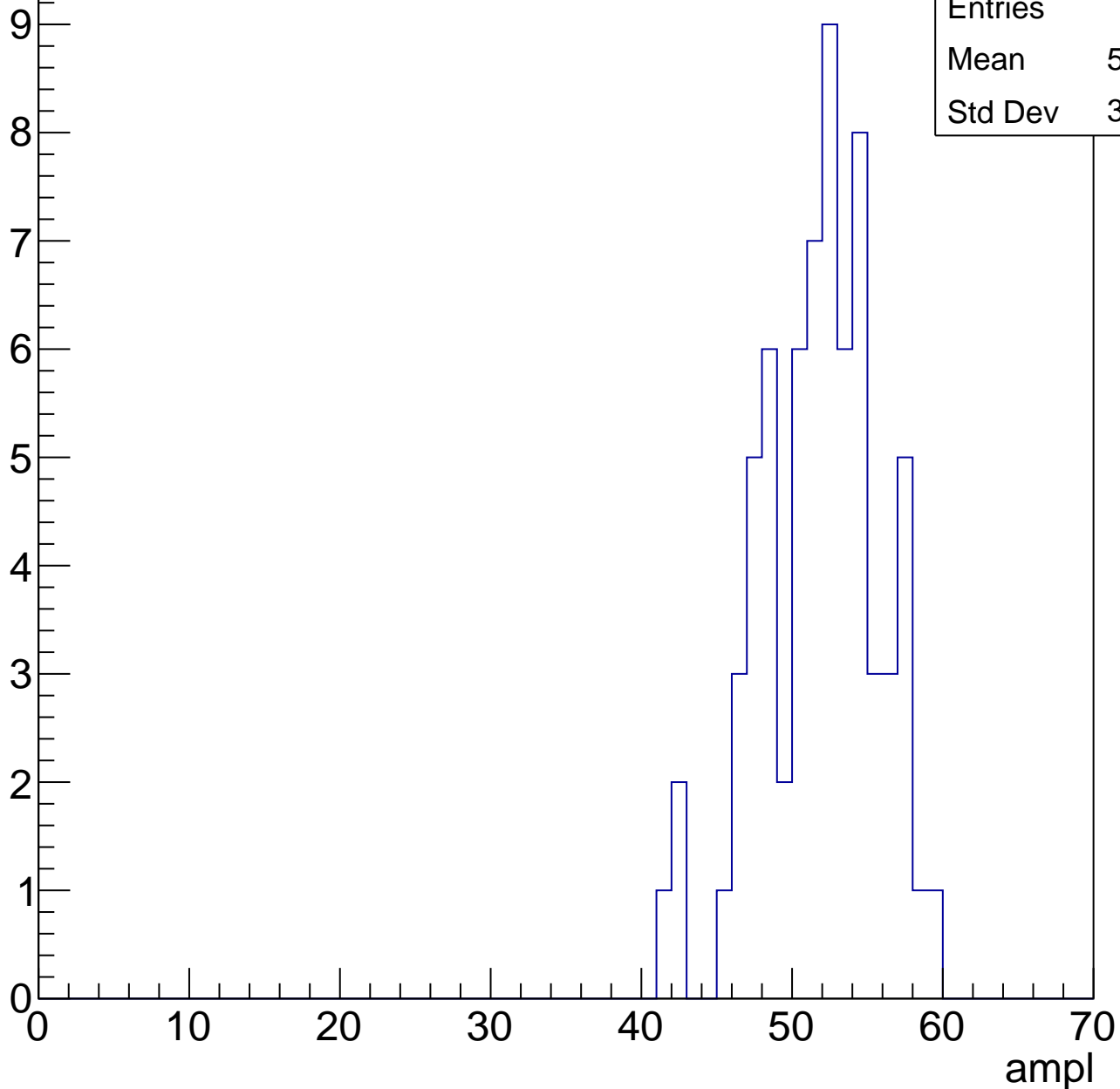


B0L001S, U18-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.29
Std Dev	3.875

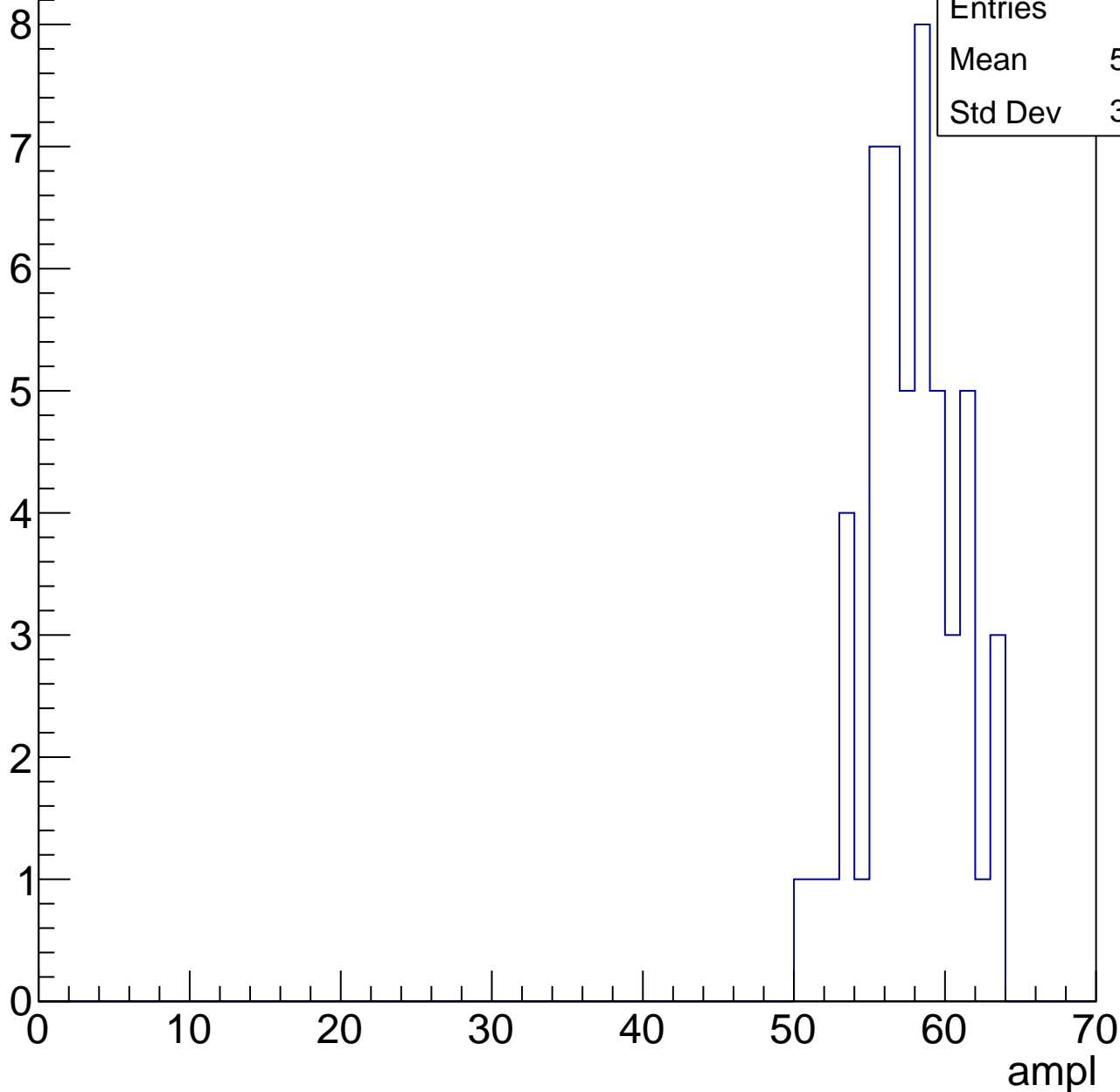


B0L001S, U18-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	57.23
Std Dev	3.048



B0L001S, U18-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 33

Mean 59.03

Std Dev 10.6

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

7

B0L001S, U18-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch50, adc0

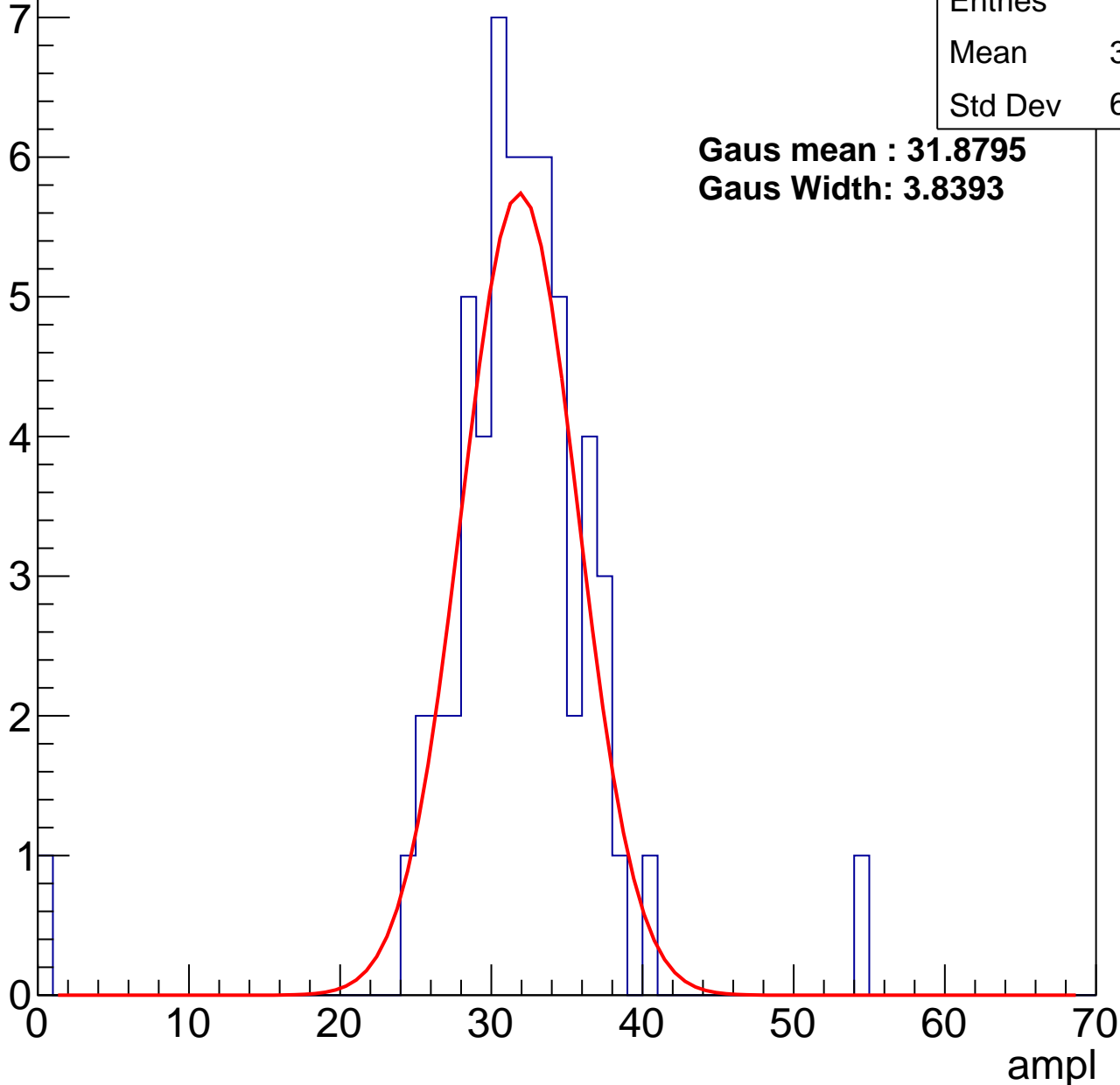
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.34
Std Dev	6.105

Gaus mean : 31.8795

Gaus Width: 3.8393



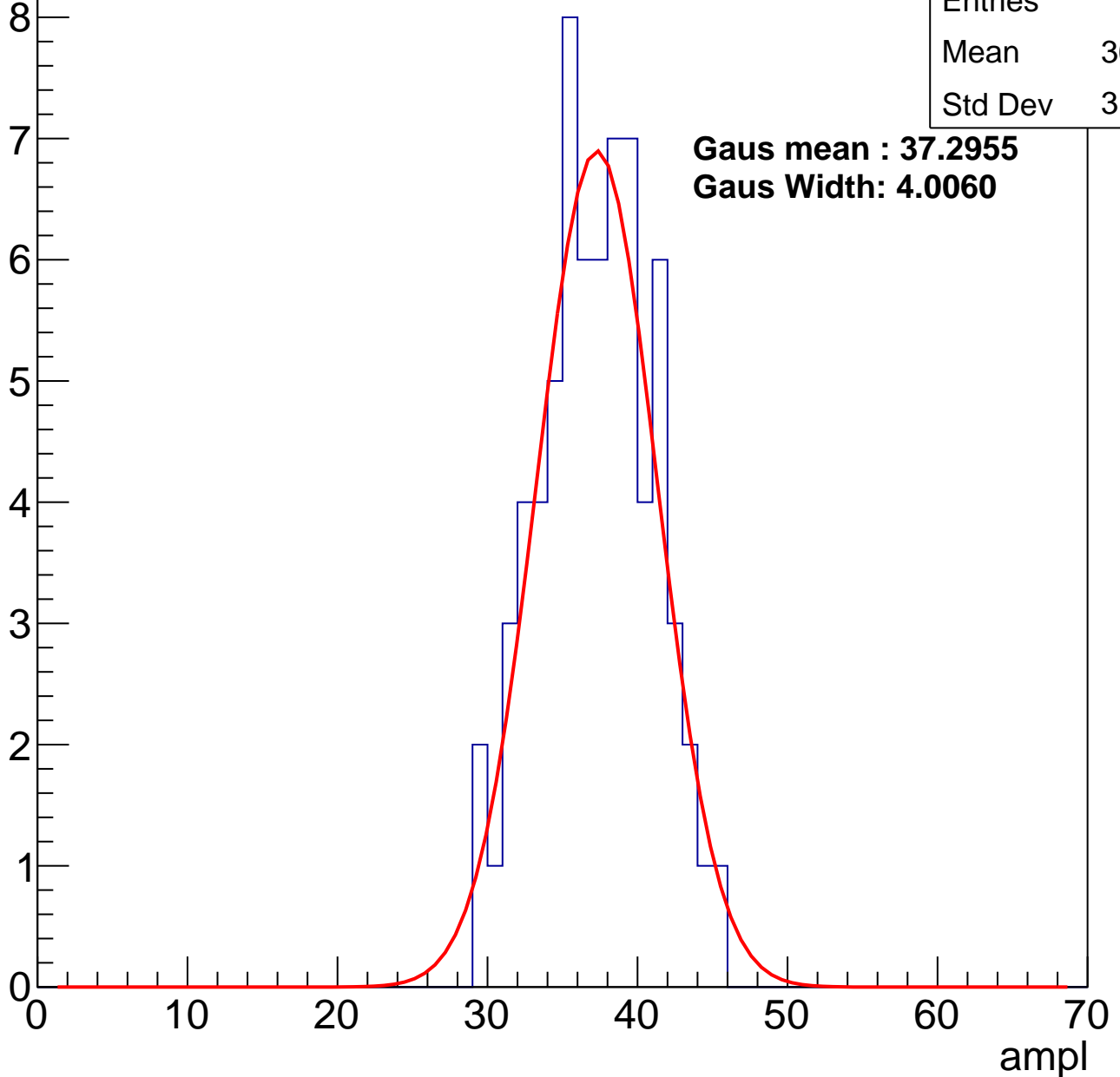
B0L001S, U18-ch50, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.79
Std Dev	3.695

Gaus mean : 37.2955
Gaus Width: 4.0060



B0L001S, U18-ch50, adc2

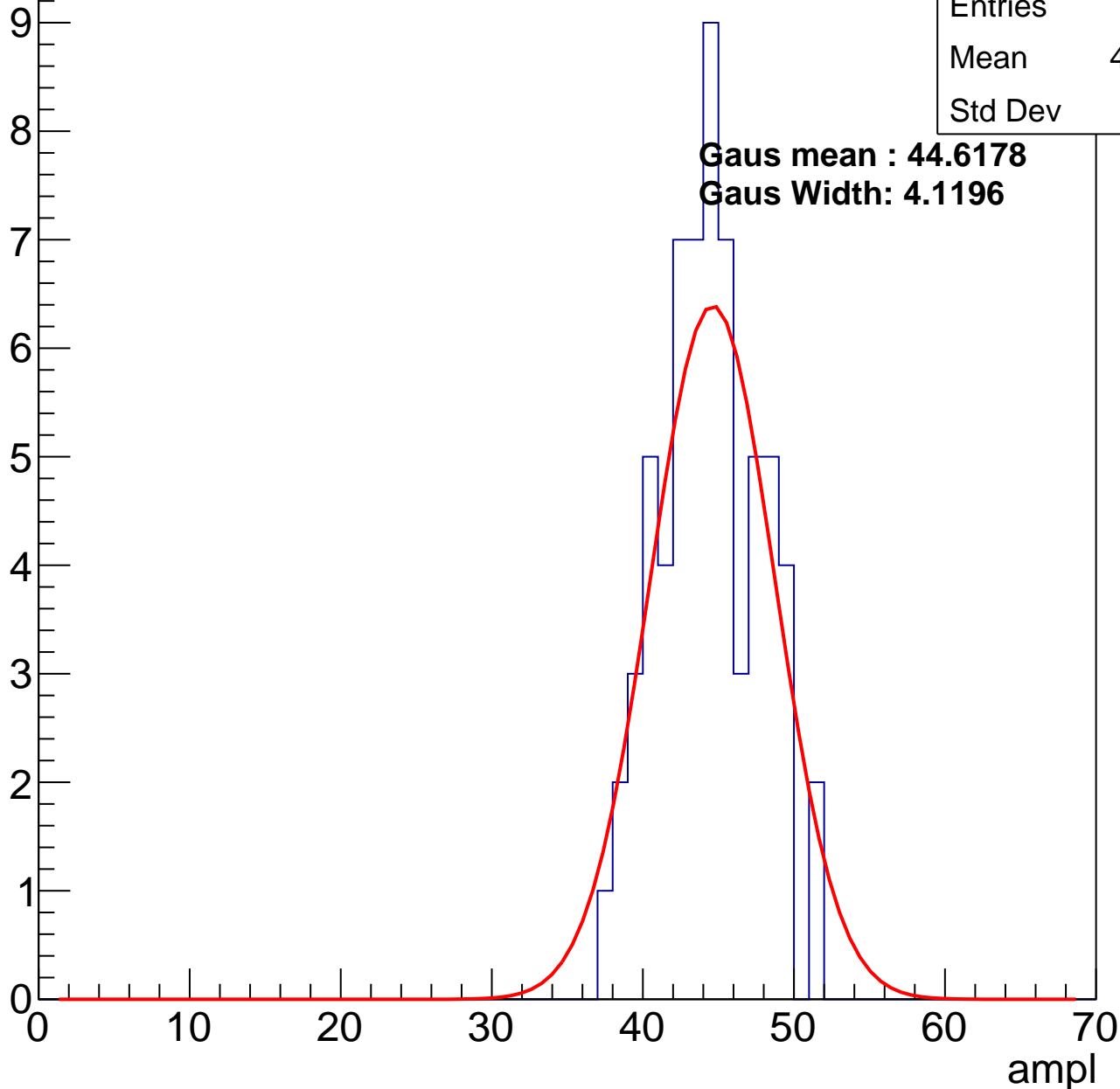
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.92
Std Dev	3.28

Gaus mean : 44.6178

Gaus Width: 4.1196

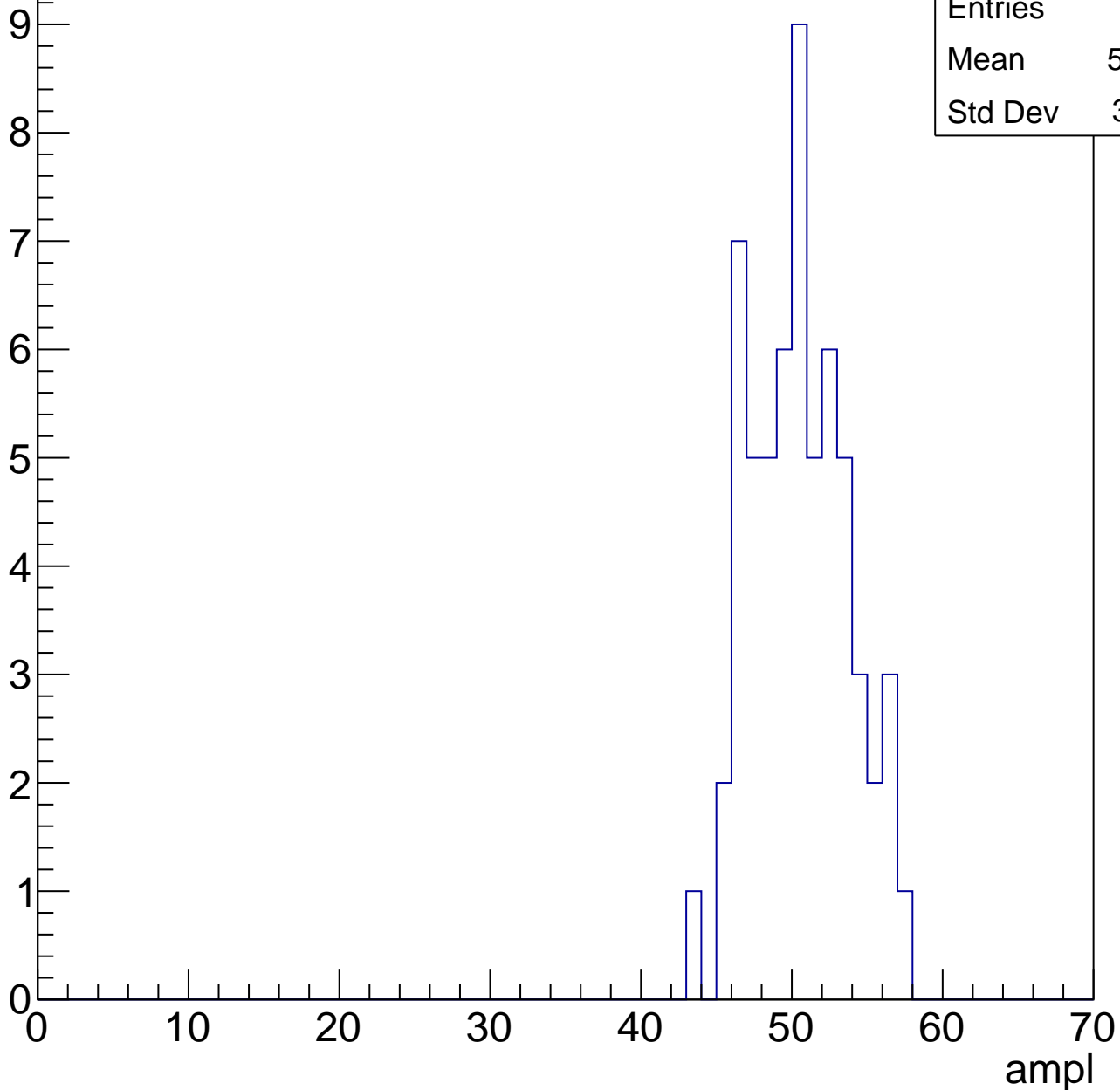


B0L001S, U18-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

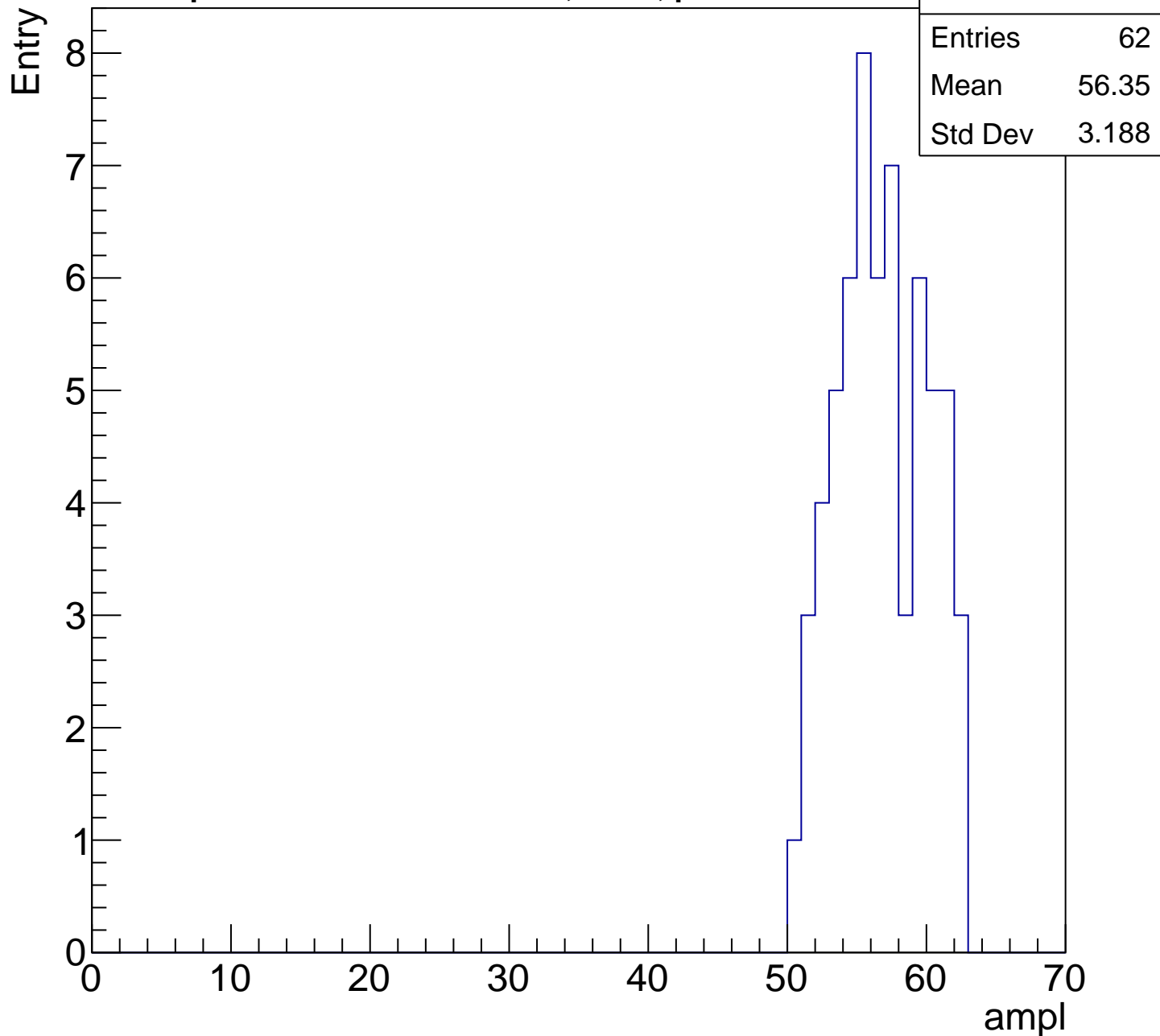
Entry

Entries	60
Mean	50.05
Std Dev	3.191



B0L001S, U18-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

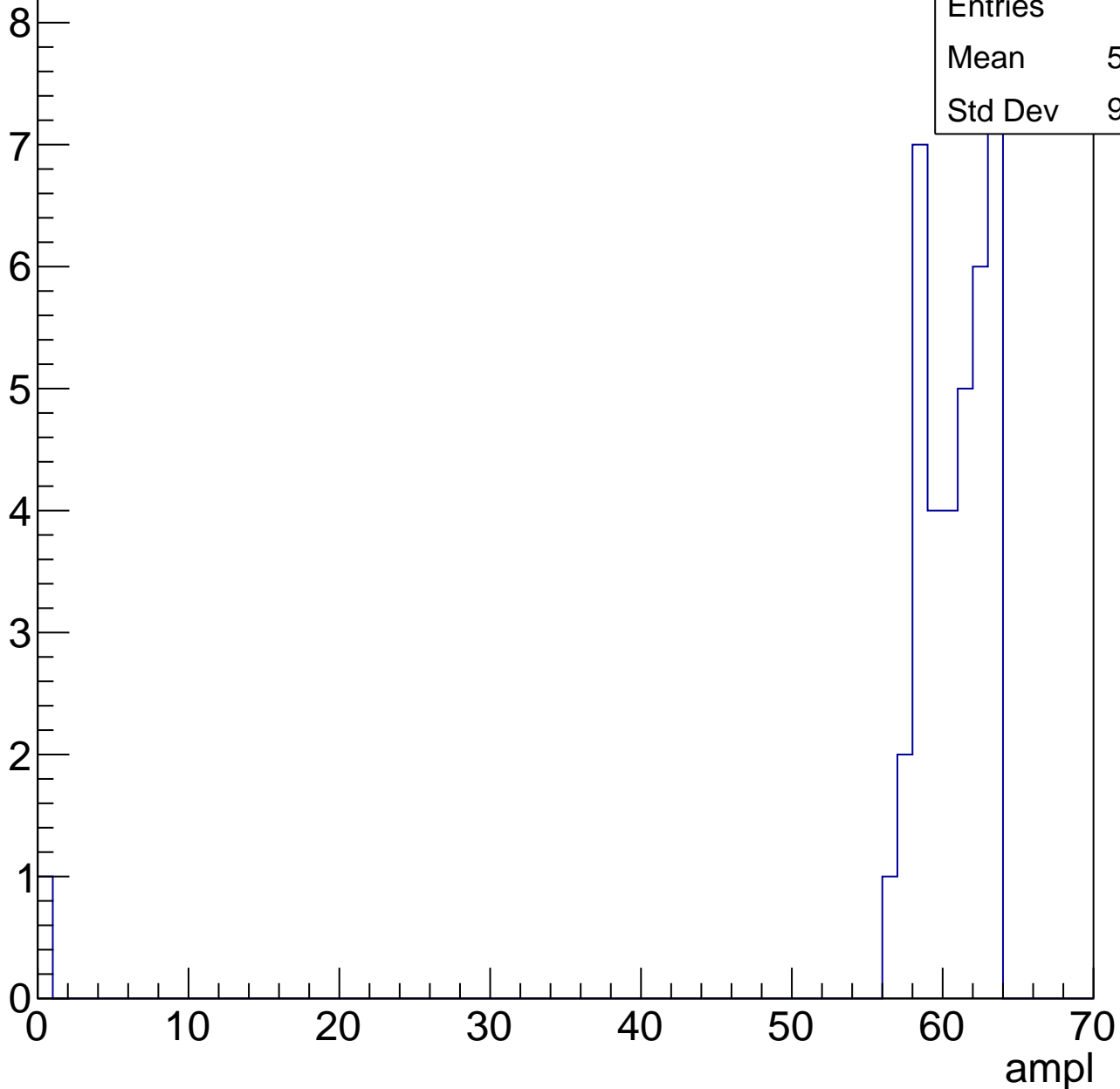


B0L001S, U18-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.76
Std Dev	9.879



B0L001S, U18-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U18-ch51, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	31.79
Std Dev	5.217

Gaus mean : 32.9572

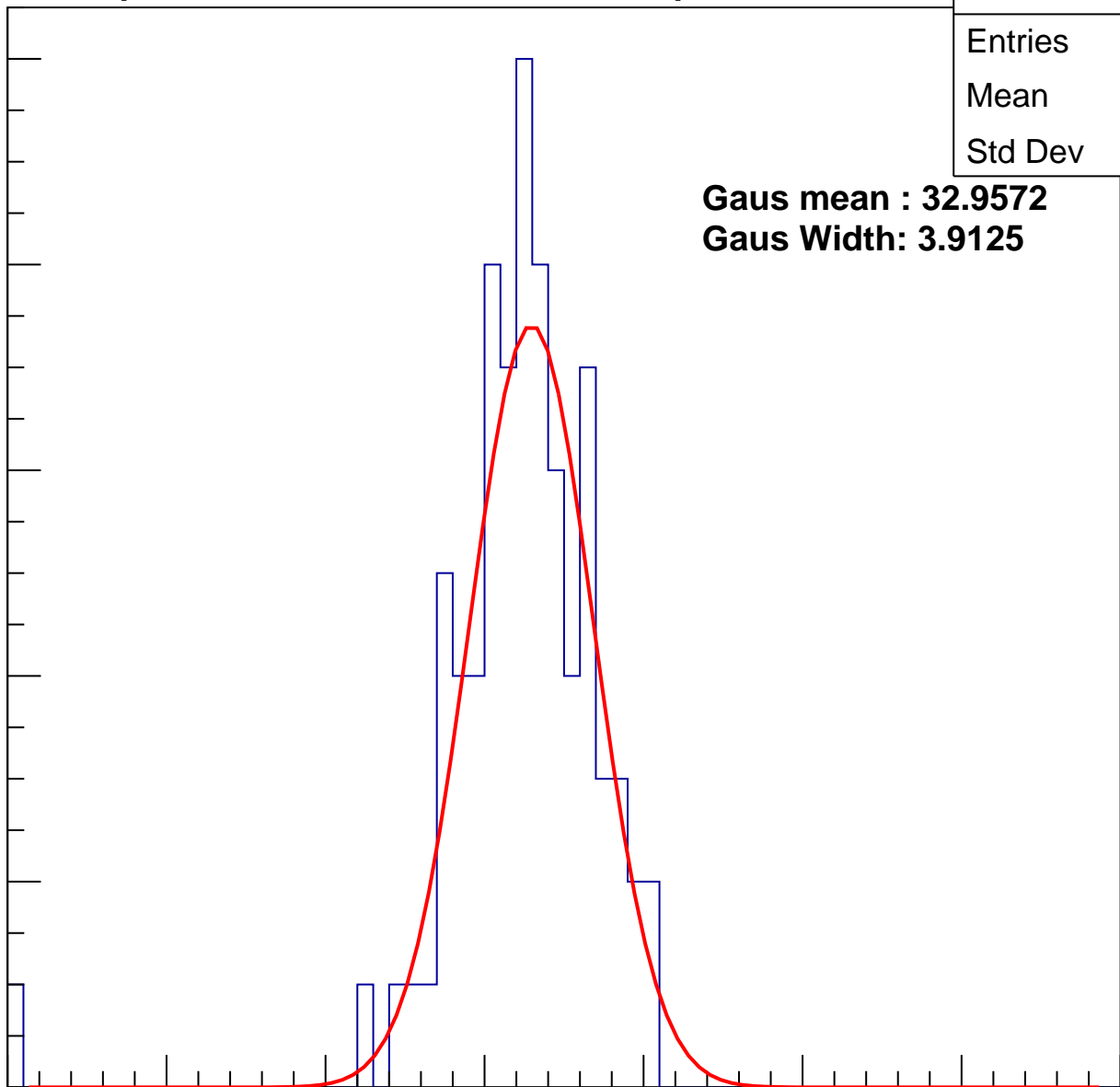
Gaus Width: 3.9125

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch51, adc1

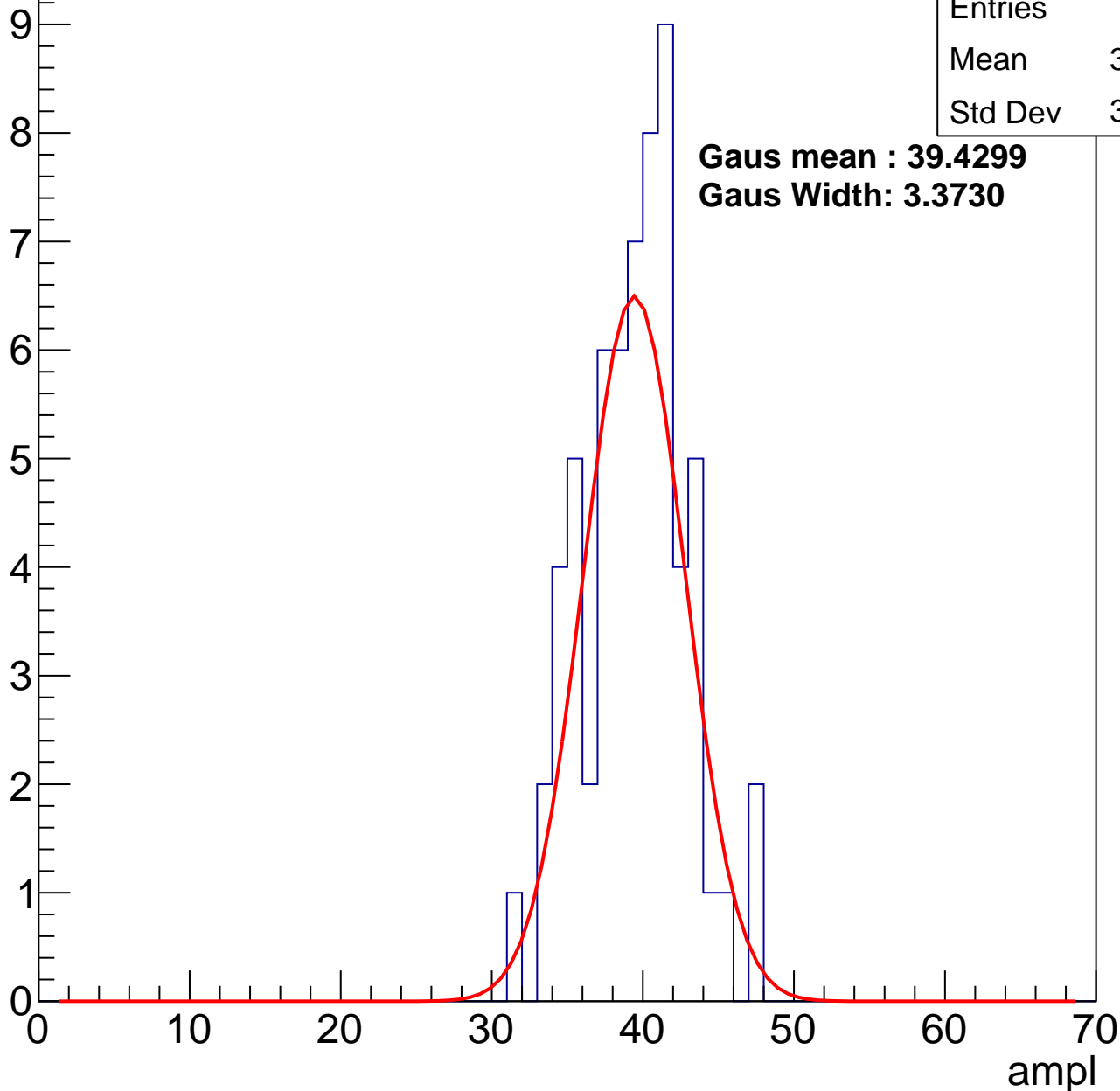
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	39.02
Std Dev	3.364

Gaus mean : 39.4299

Gaus Width: 3.3730



B0L001S, U18-ch51, adc2

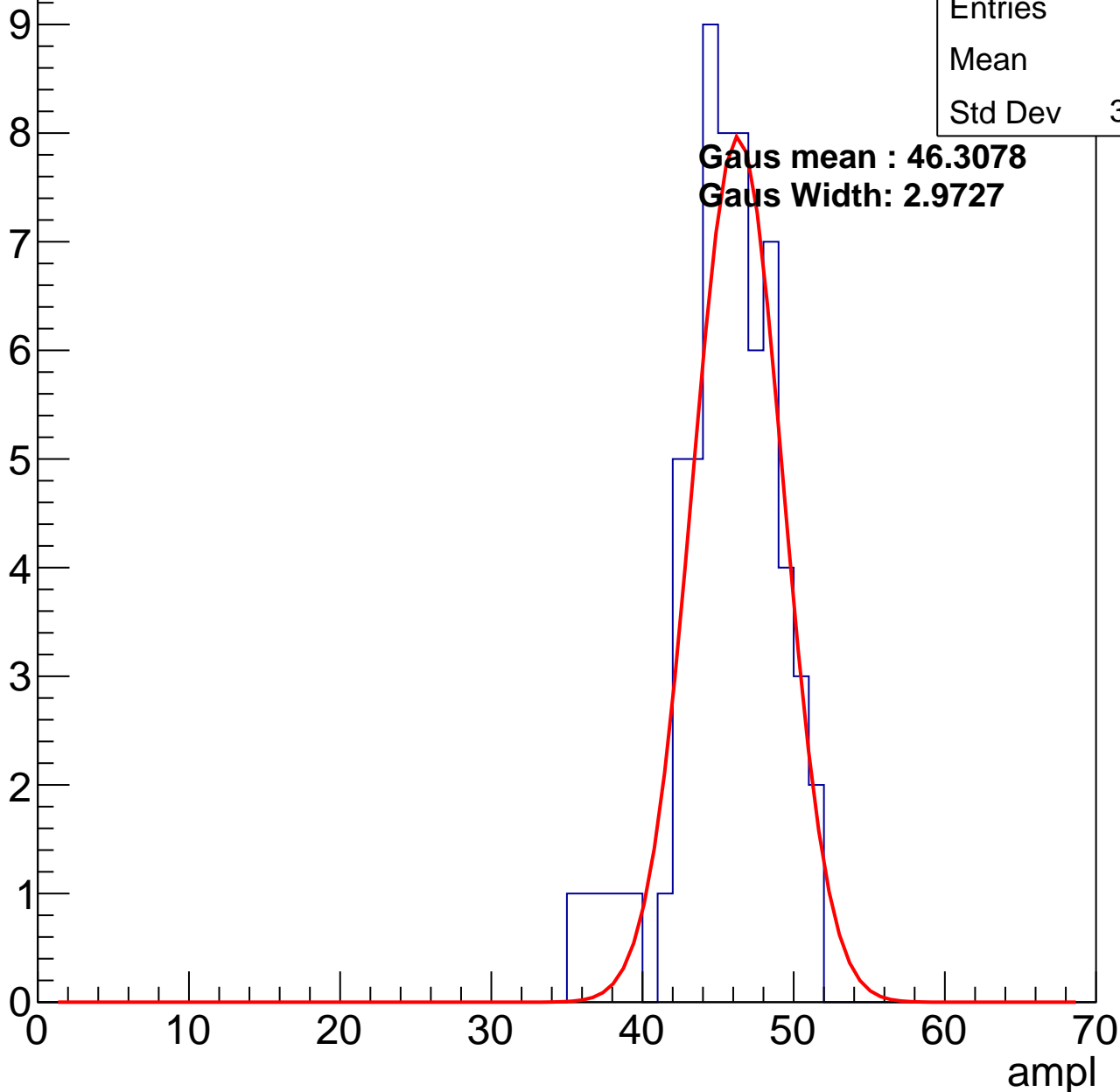
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	45.1
Std Dev	3.393

Gaus mean : 46.3078

Gaus Width: 2.9727

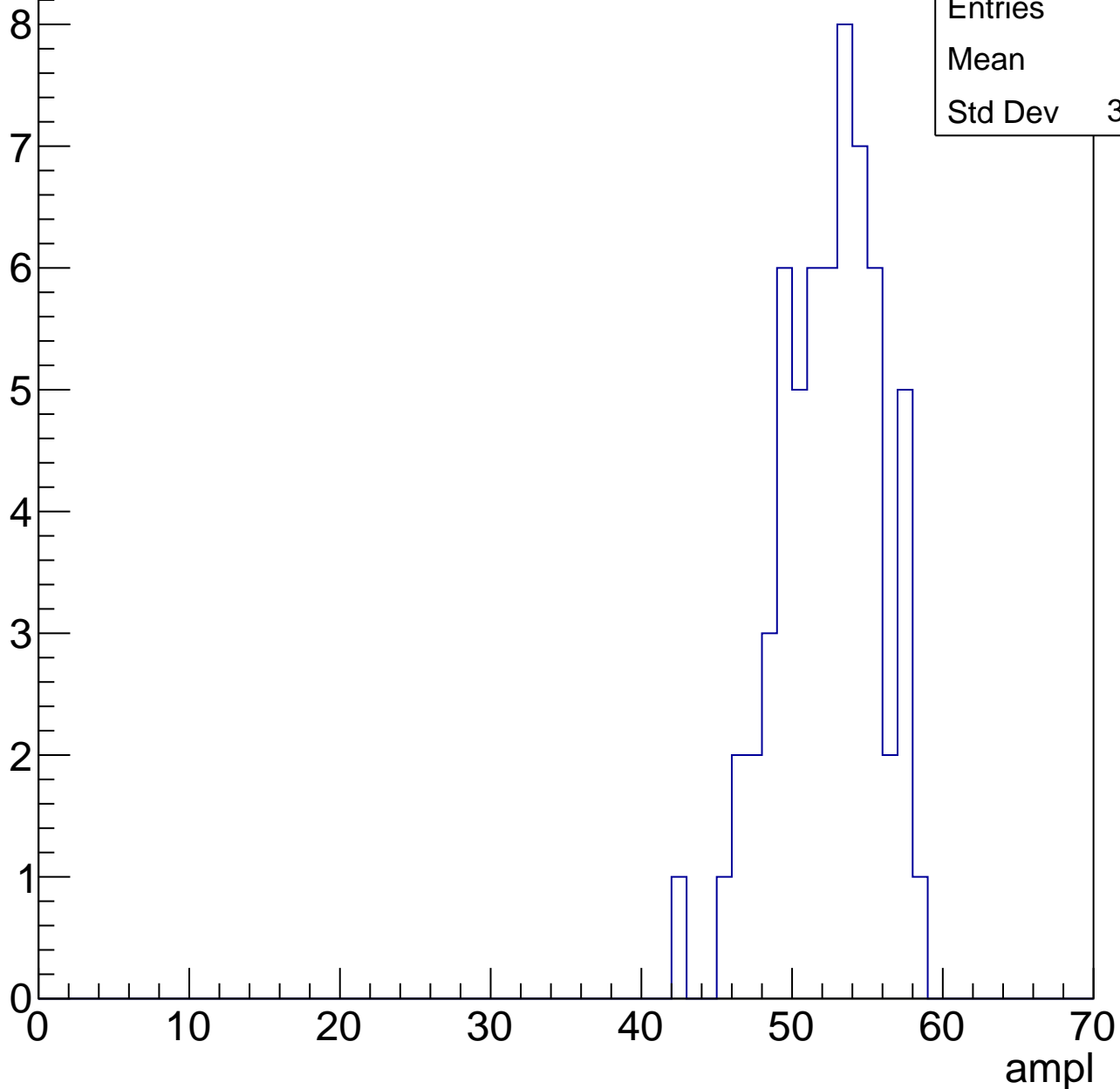


B0L001S, U18-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	51.9
Std Dev	3.342

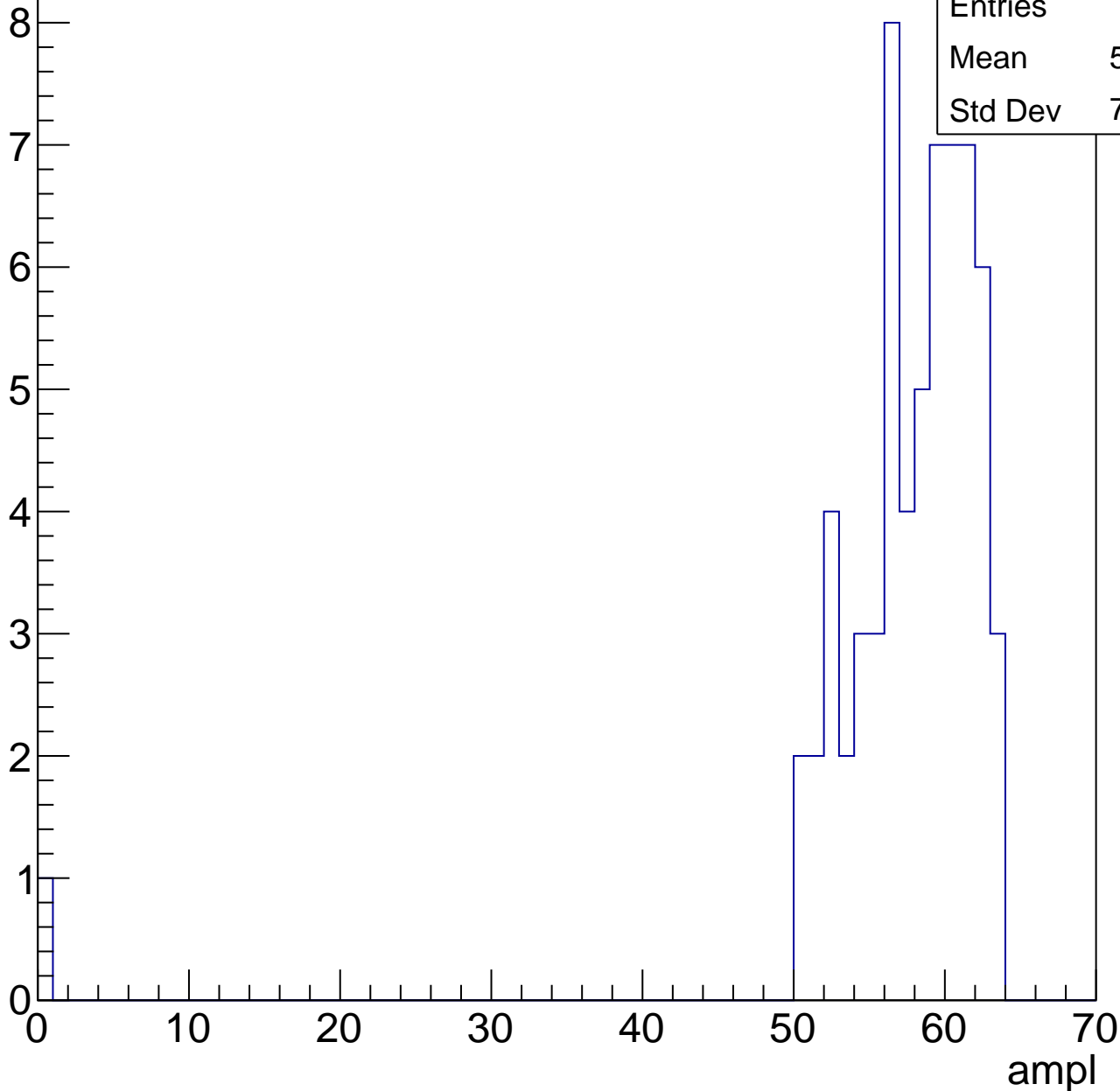


B0L001S, U18-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.72
Std Dev	7.964

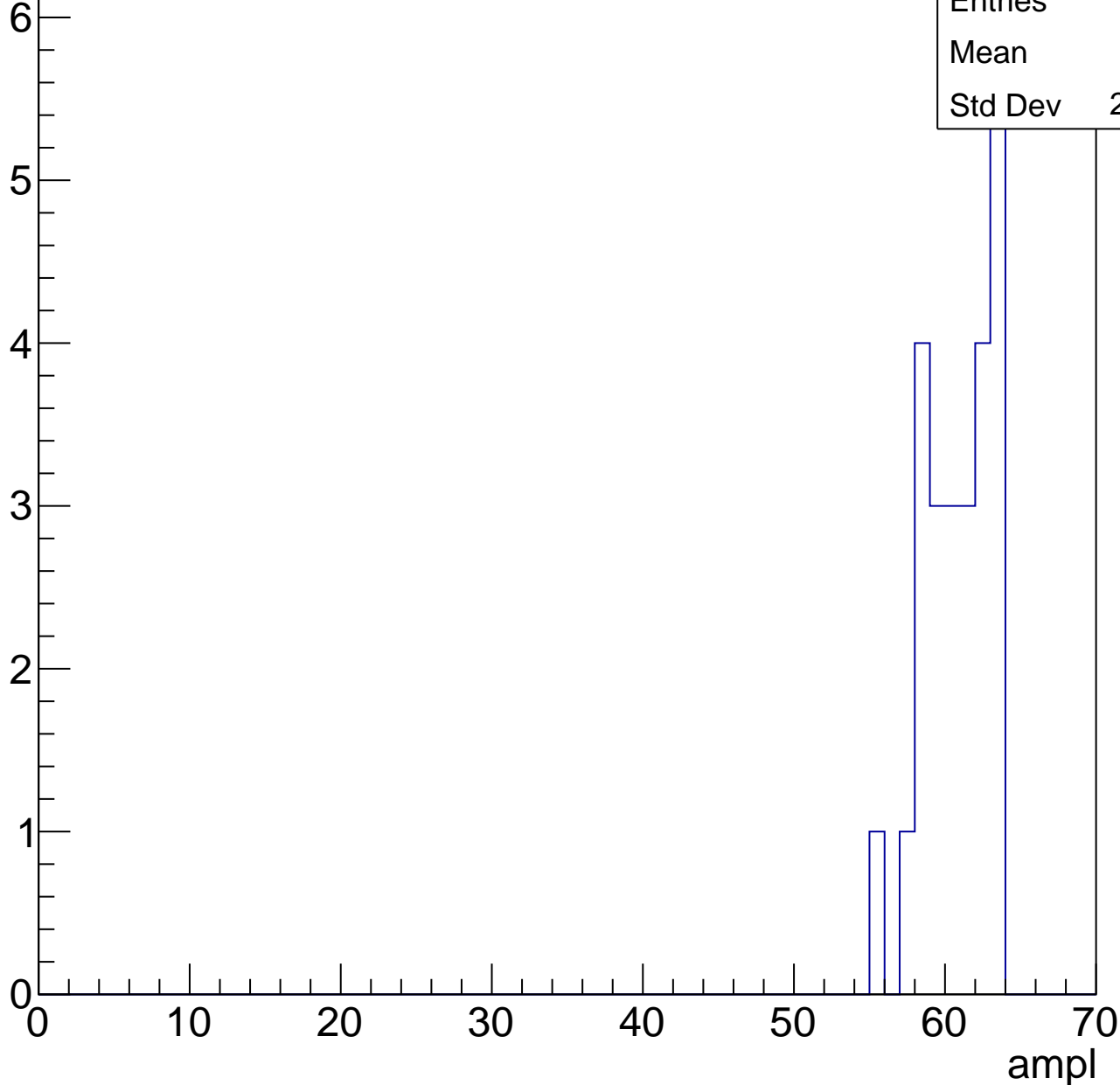


B0L001S, U18-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	60.4
Std Dev	2.209



B0L001S, U18-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U18-ch52, adc0

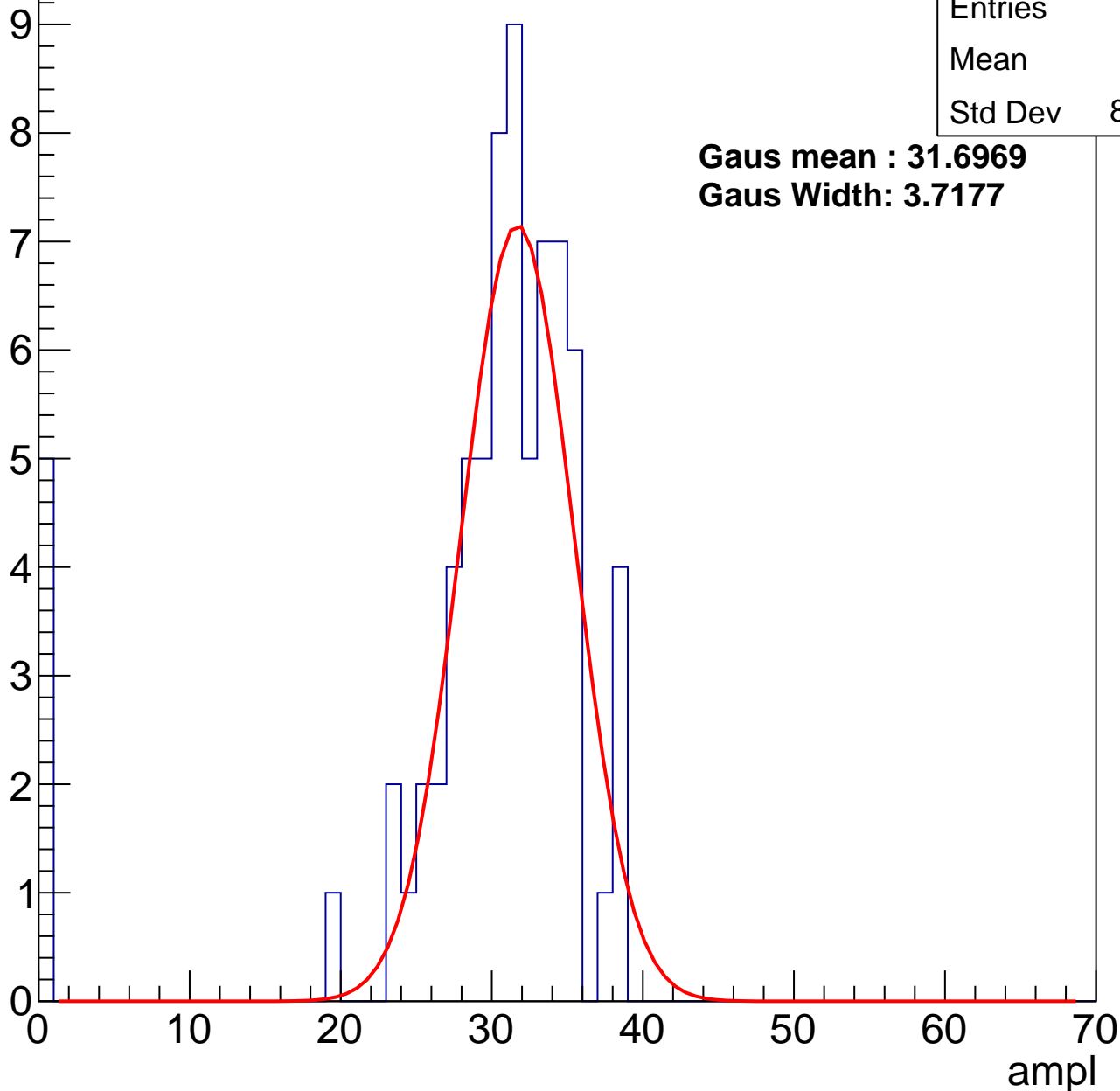
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	28.8
Std Dev	8.582

Gaus mean : 31.6969

Gaus Width: 3.7177



B0L001S, U18-ch52, adc1

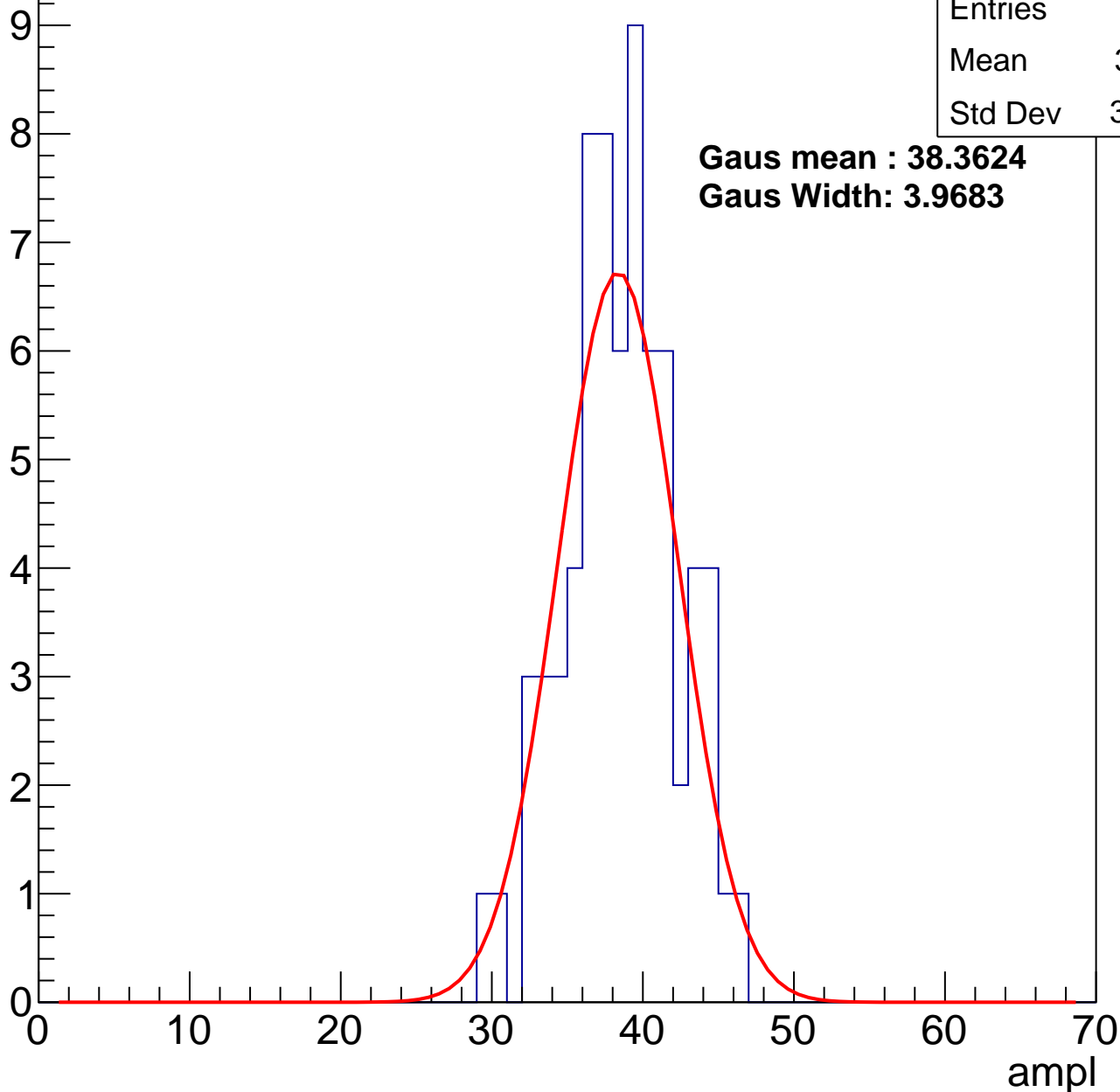
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.11
Std Dev	3.639

Gaus mean : 38.3624

Gaus Width: 3.9683



B0L001S, U18-ch52, adc2

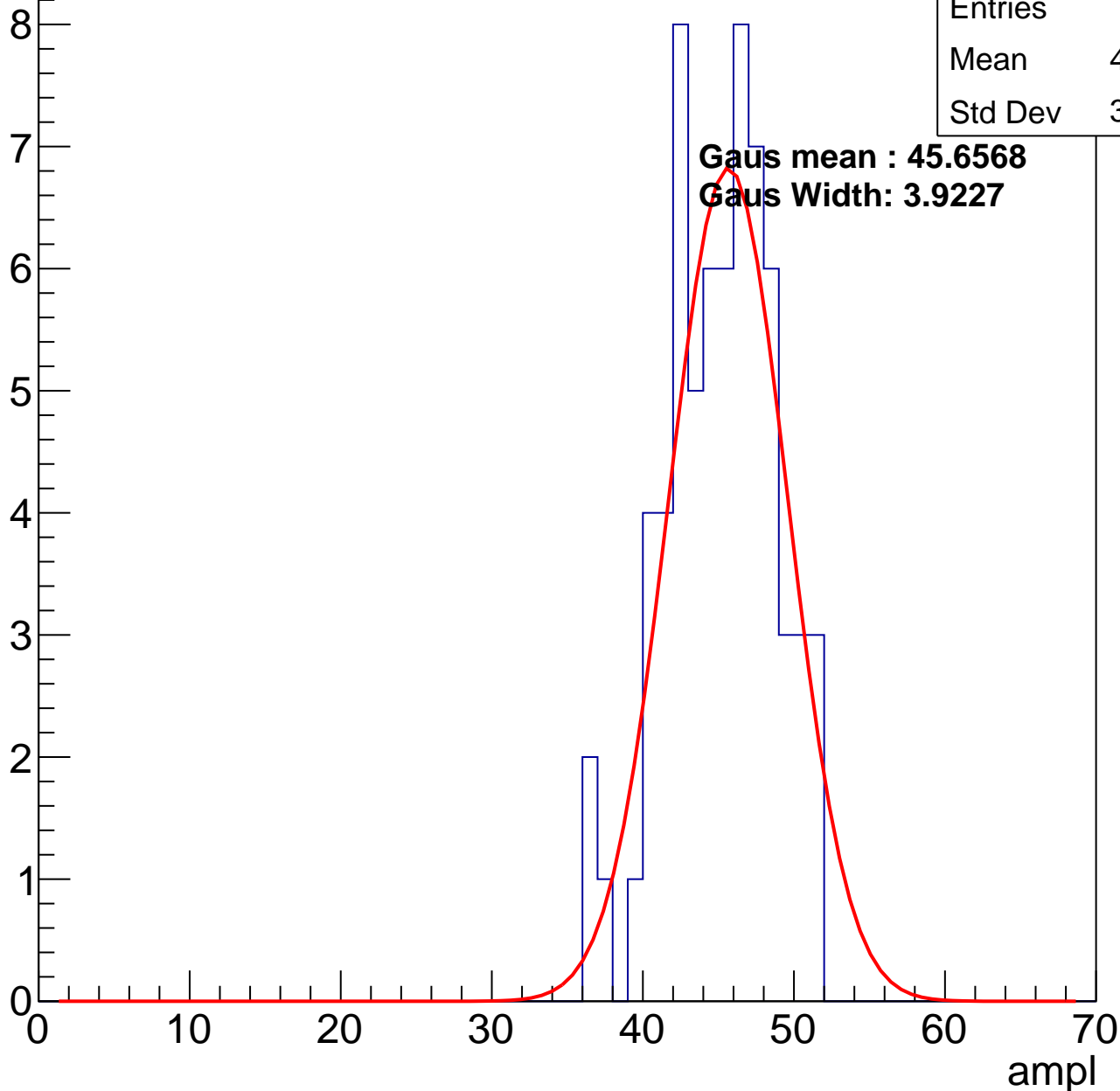
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.66
Std Dev	3.535

Gaus mean : 45.6568

Gaus Width: 3.9227



B0L001S, U18-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	52.3
Std Dev	3.593

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

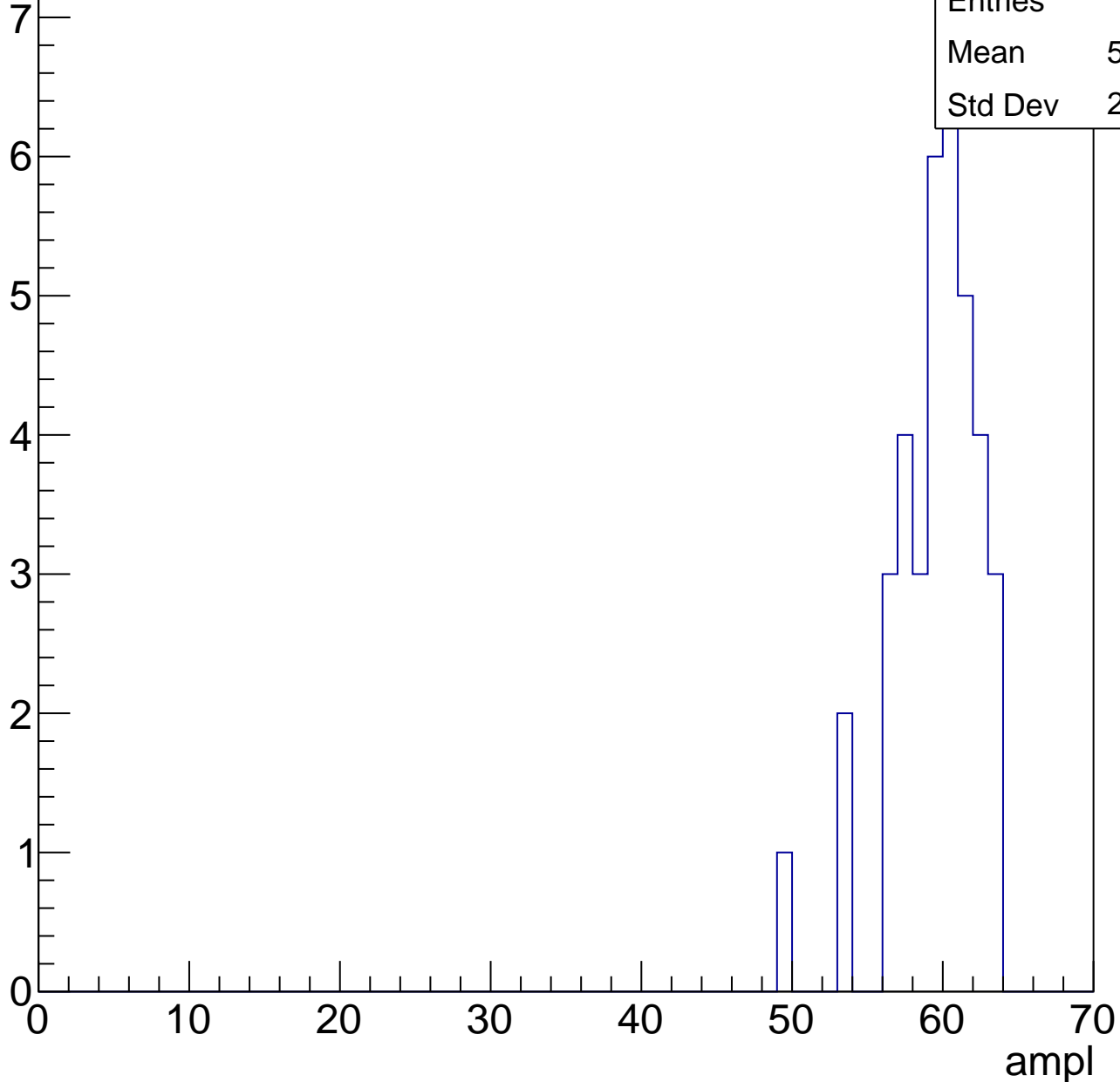
ampl

70

B0L001S, U18-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

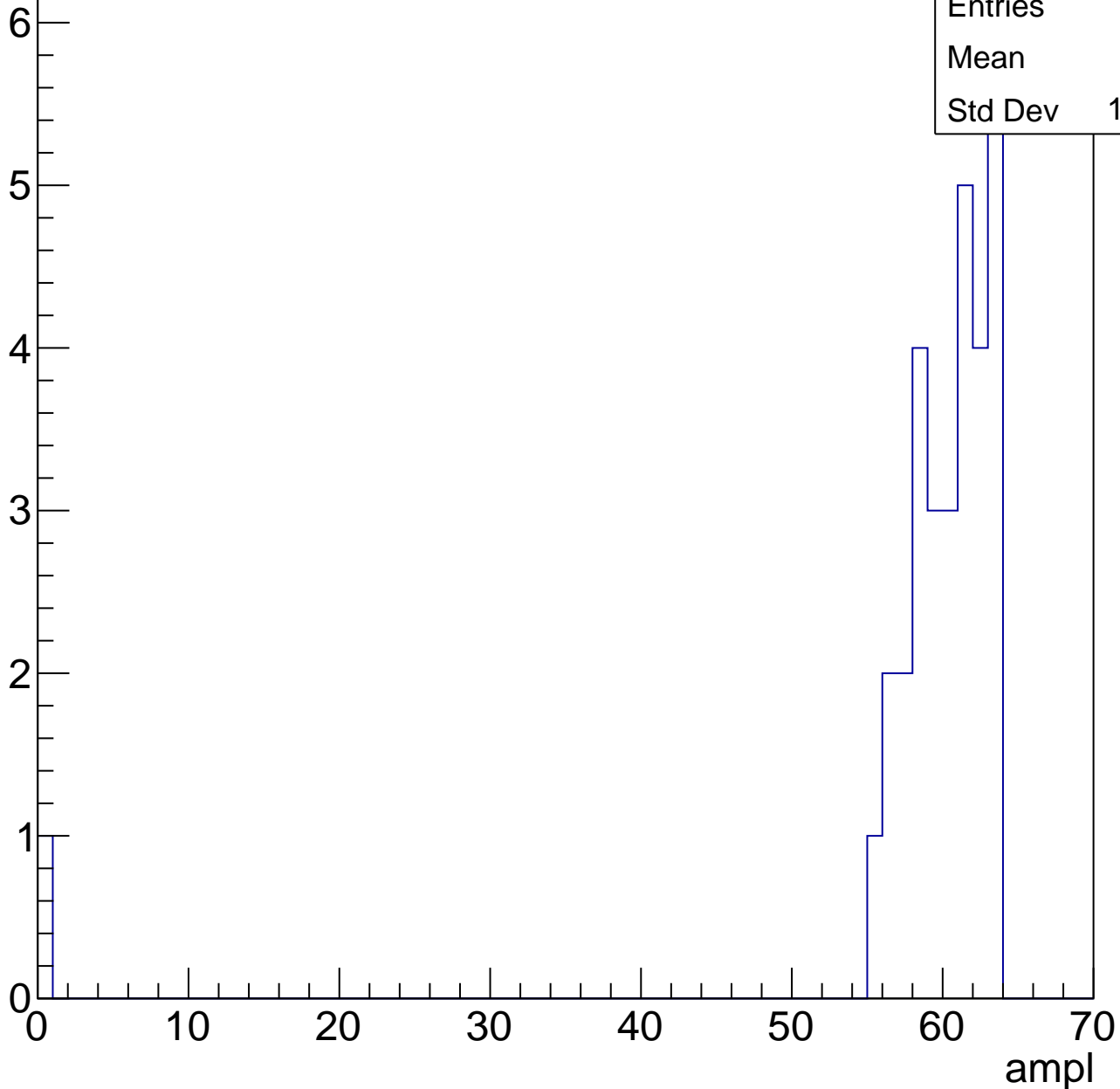


B0L001S, U18-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.1
Std Dev	10.86



B0L001S, U18-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch53, adc0

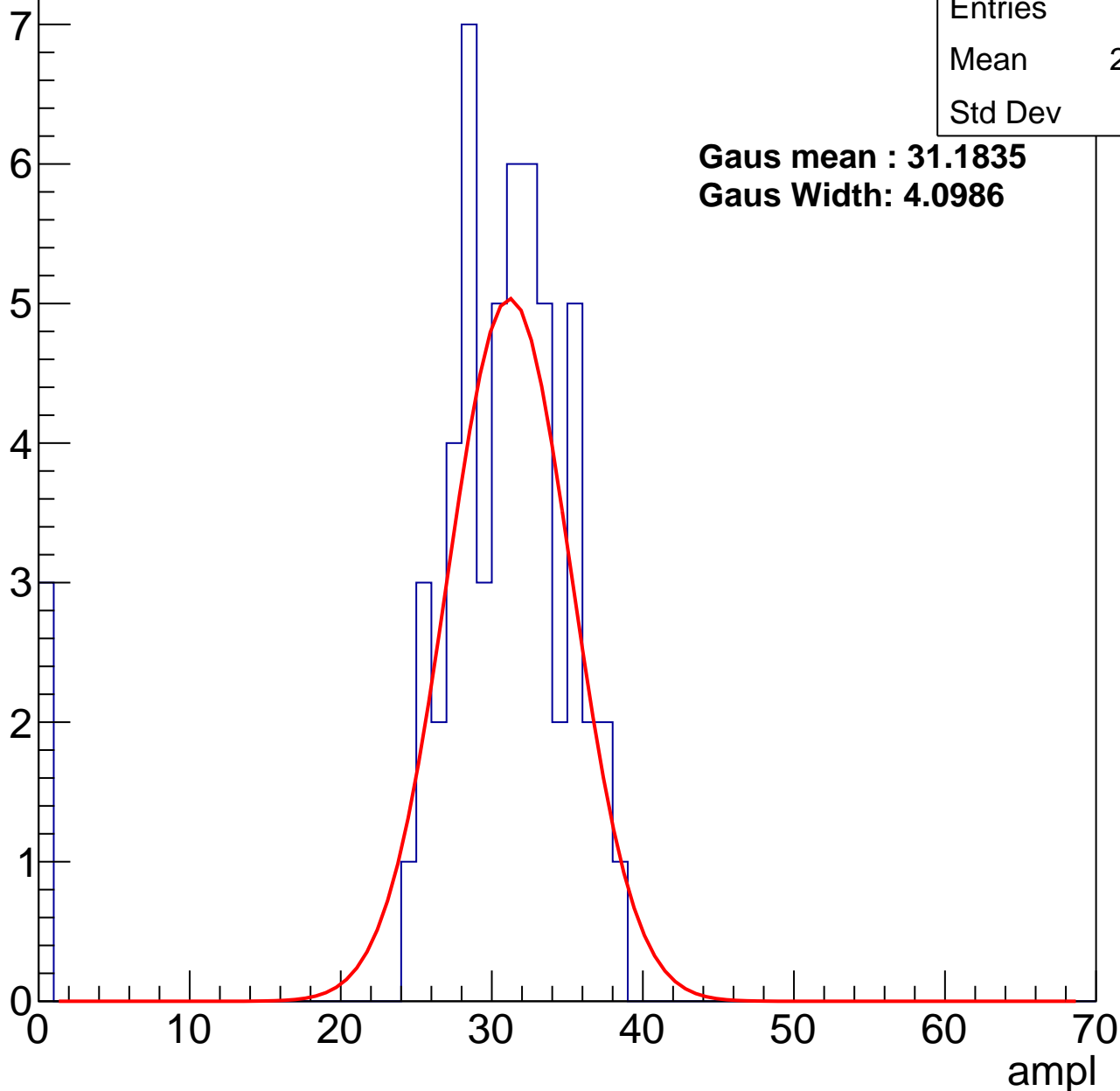
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.16
Std Dev	7.65

Gaus mean : 31.1835

Gaus Width: 4.0986



B0L001S, U18-ch53, adc1

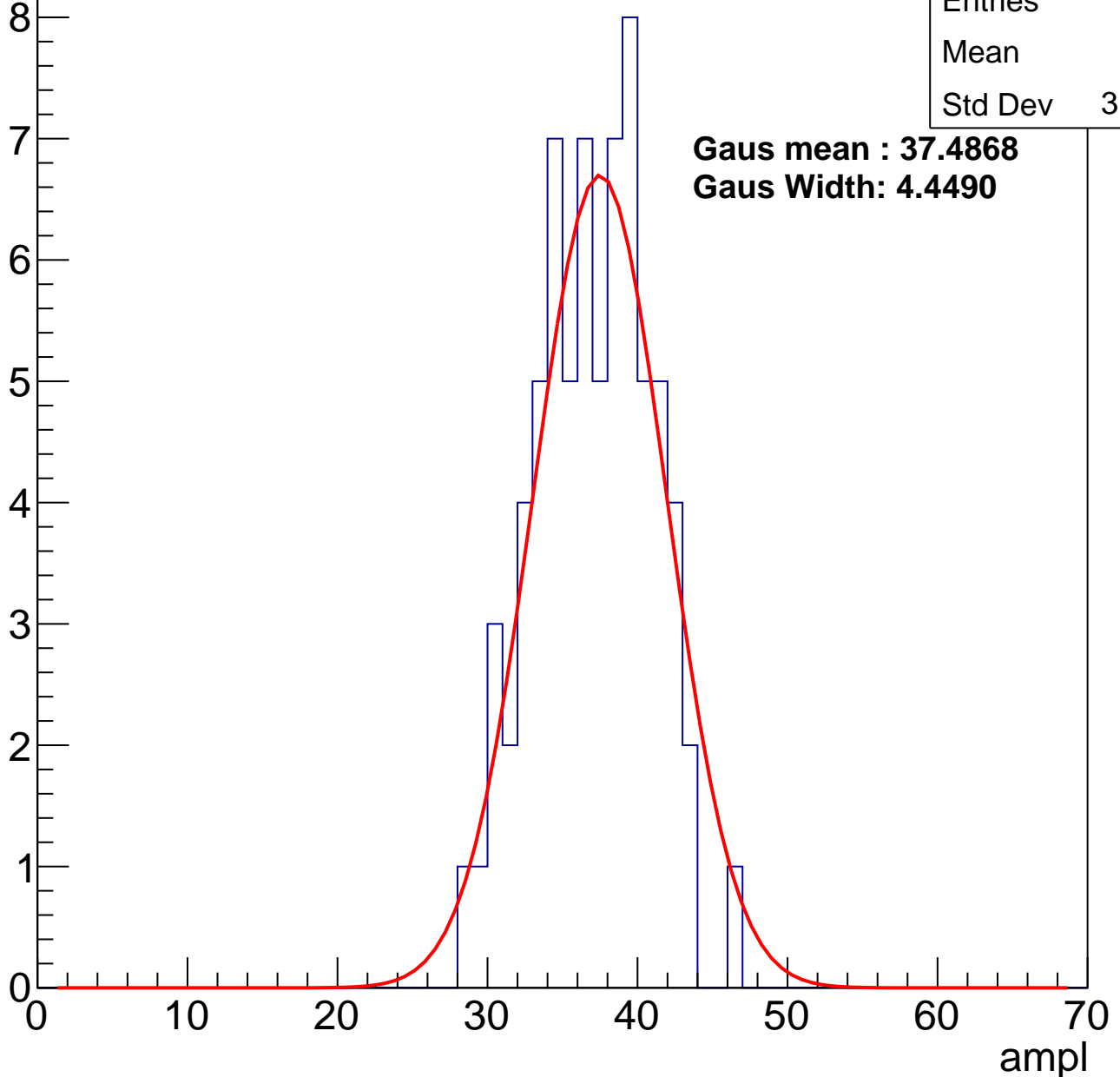
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.6
Std Dev	3.799

Gaus mean : 37.4868

Gaus Width: 4.4490



B0L001S, U18-ch53, adc2

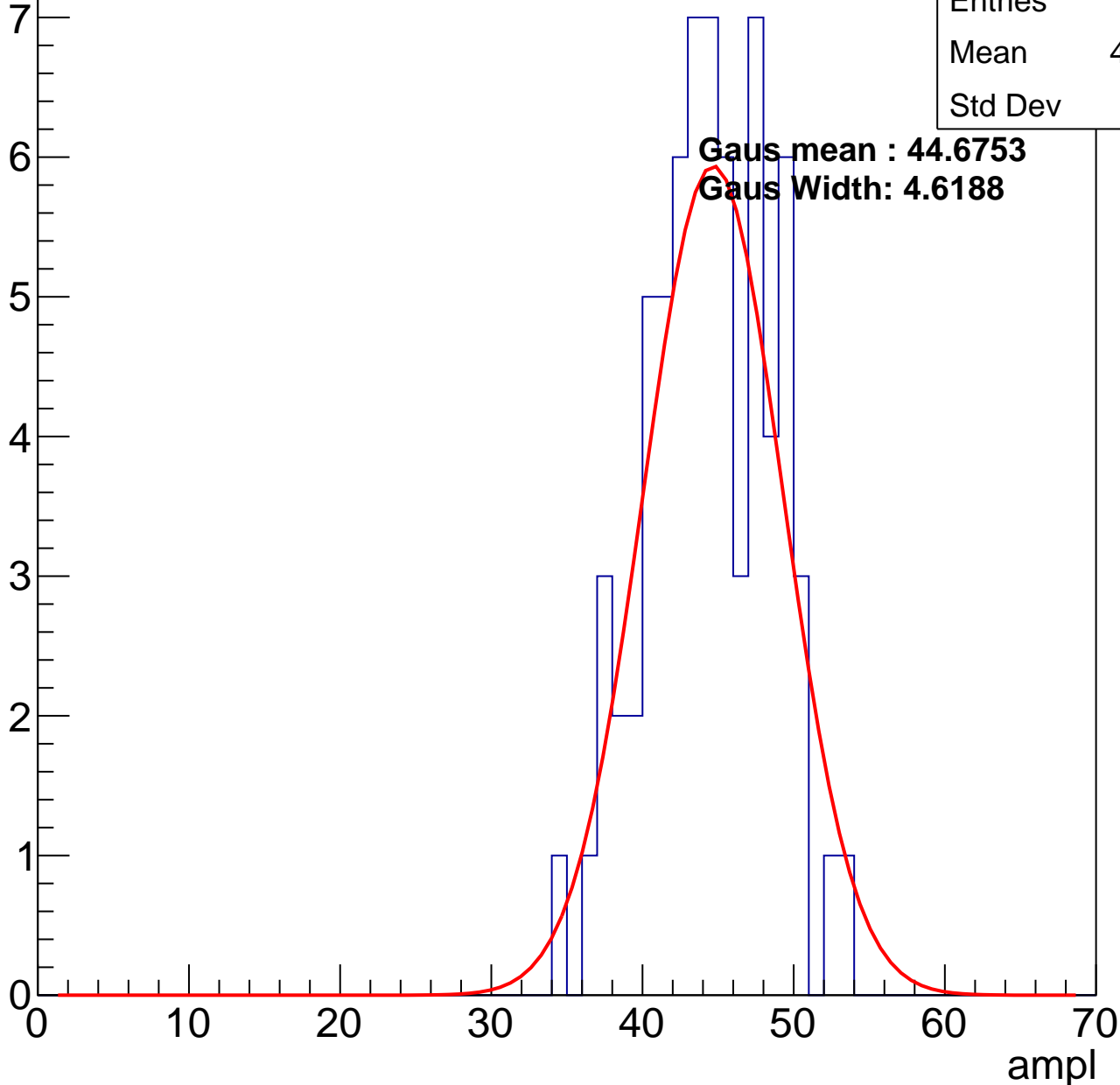
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.99
Std Dev	4.03

Gaus mean : 44.6753

Gaus Width: 4.6188

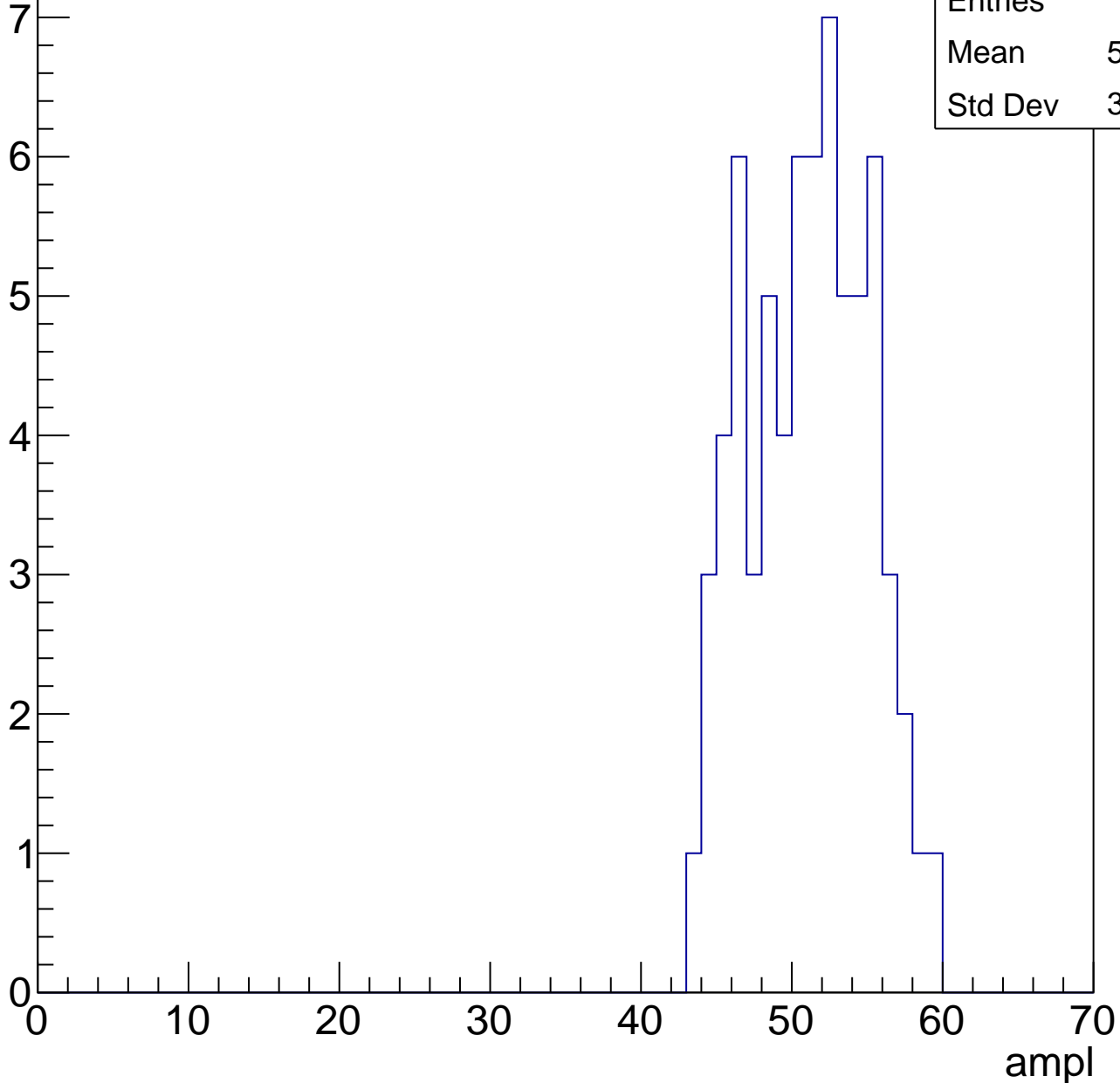


B0L001S, U18-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.62
Std Dev	3.907

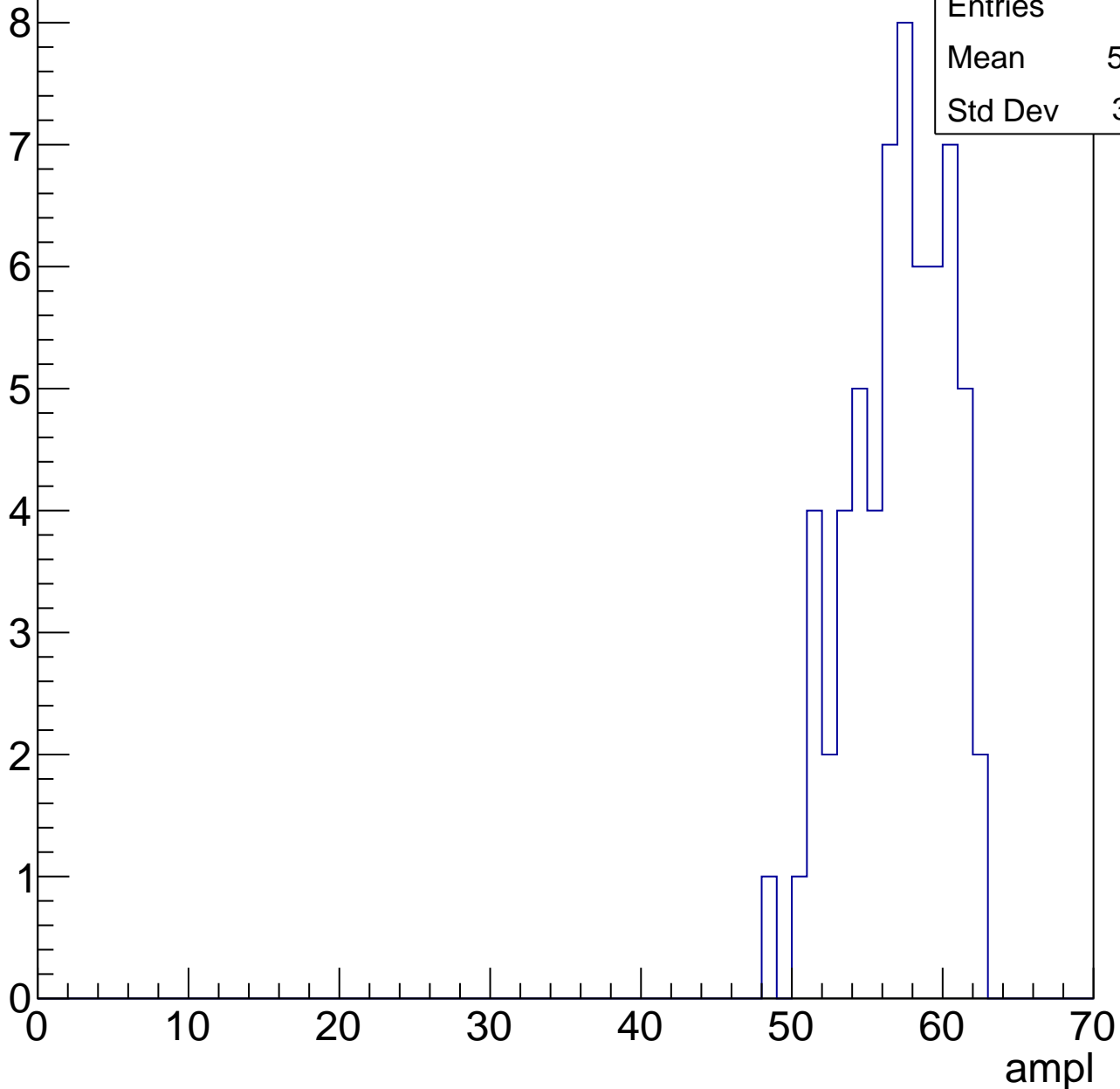


B0L001S, U18-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.56
Std Dev	3.281

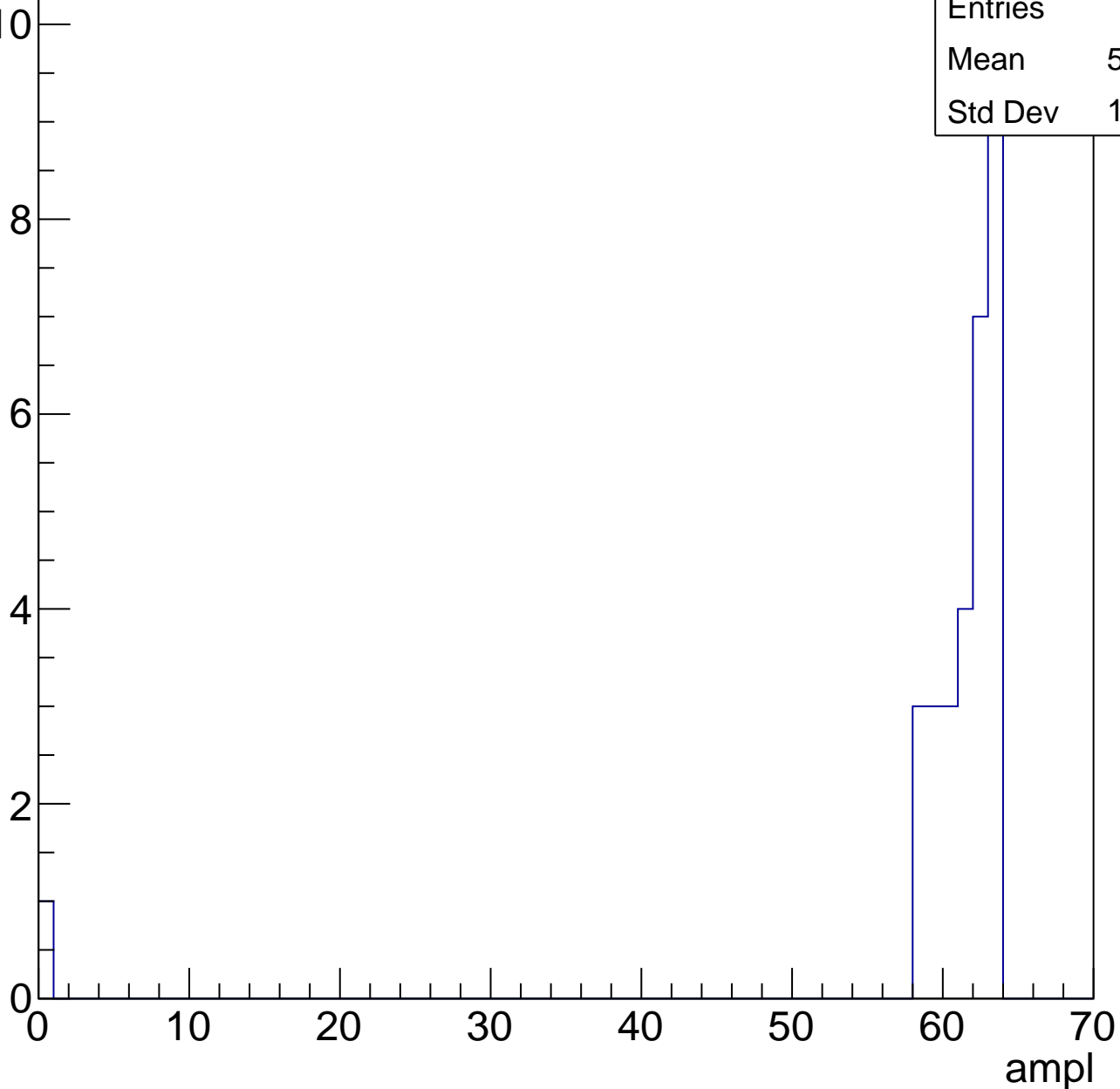


B0L001S, U18-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.32
Std Dev	10.96



B0L001S, U18-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch54, adc0

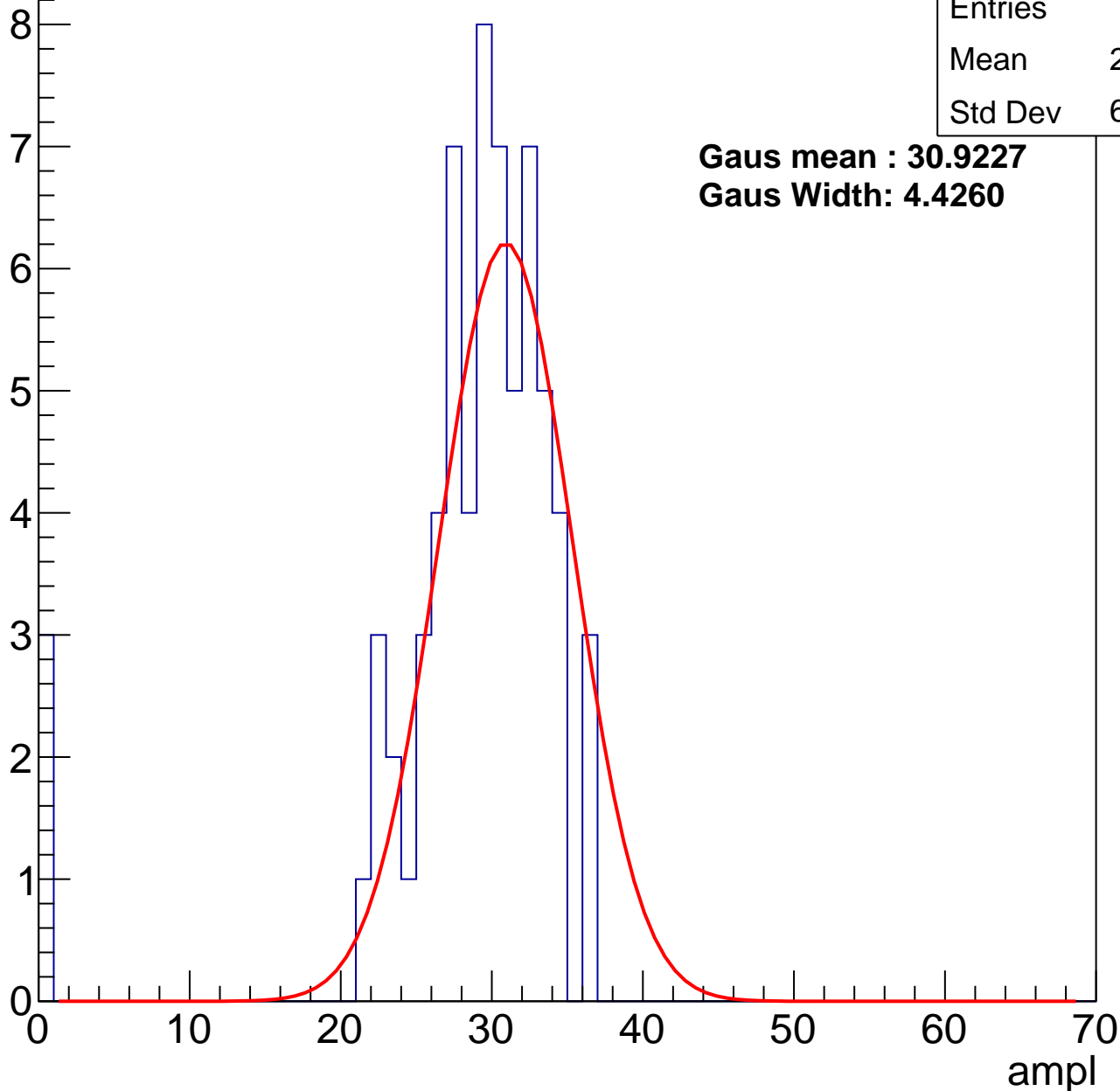
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	27.87
Std Dev	6.994

Gaus mean : 30.9227

Gaus Width: 4.4260



B0L001S, U18-ch54, adc1

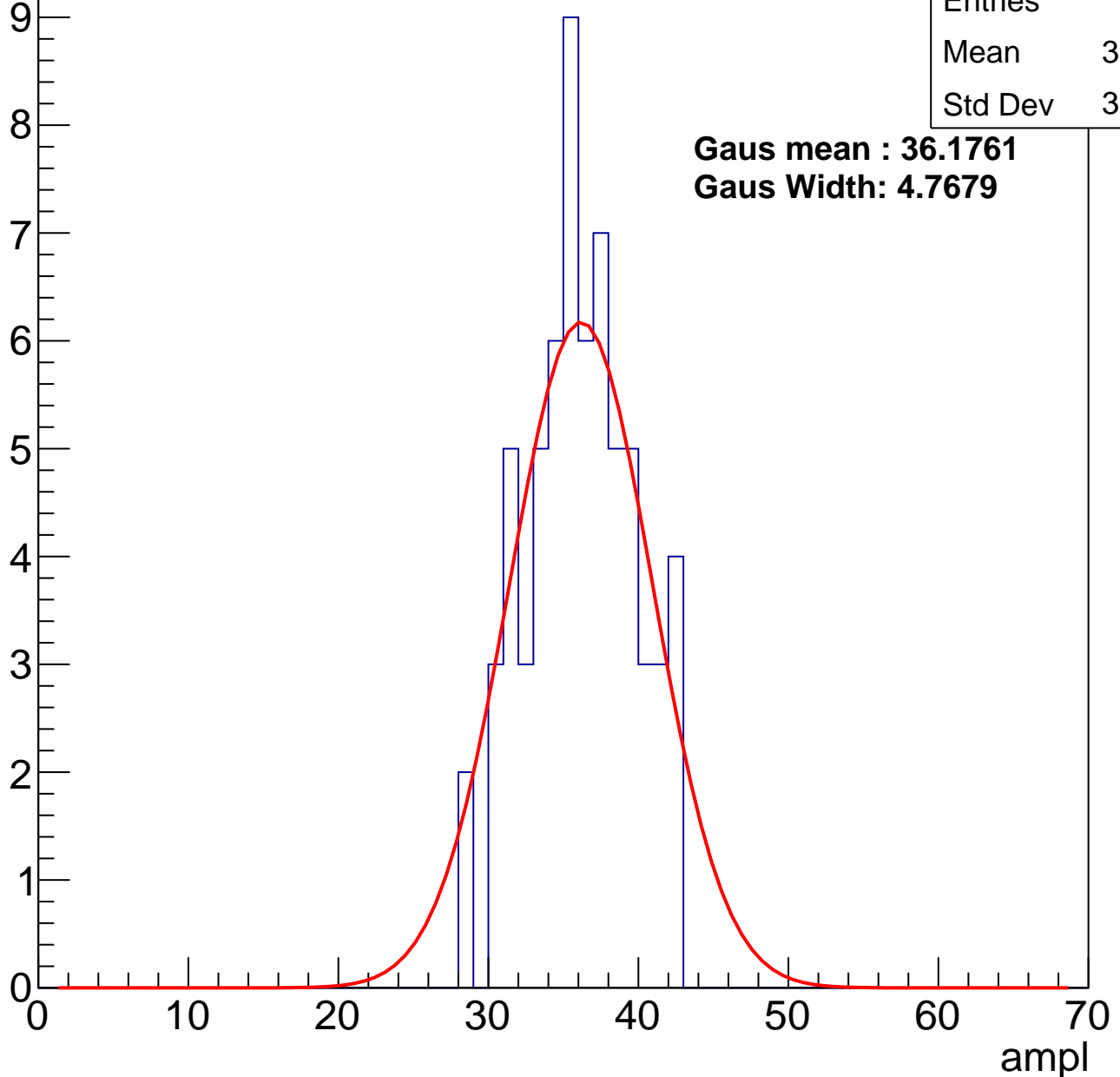
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	35.64
Std Dev	3.519

Gaus mean : 36.1761

Gaus Width: 4.7679



B0L001S, U18-ch54, adc2

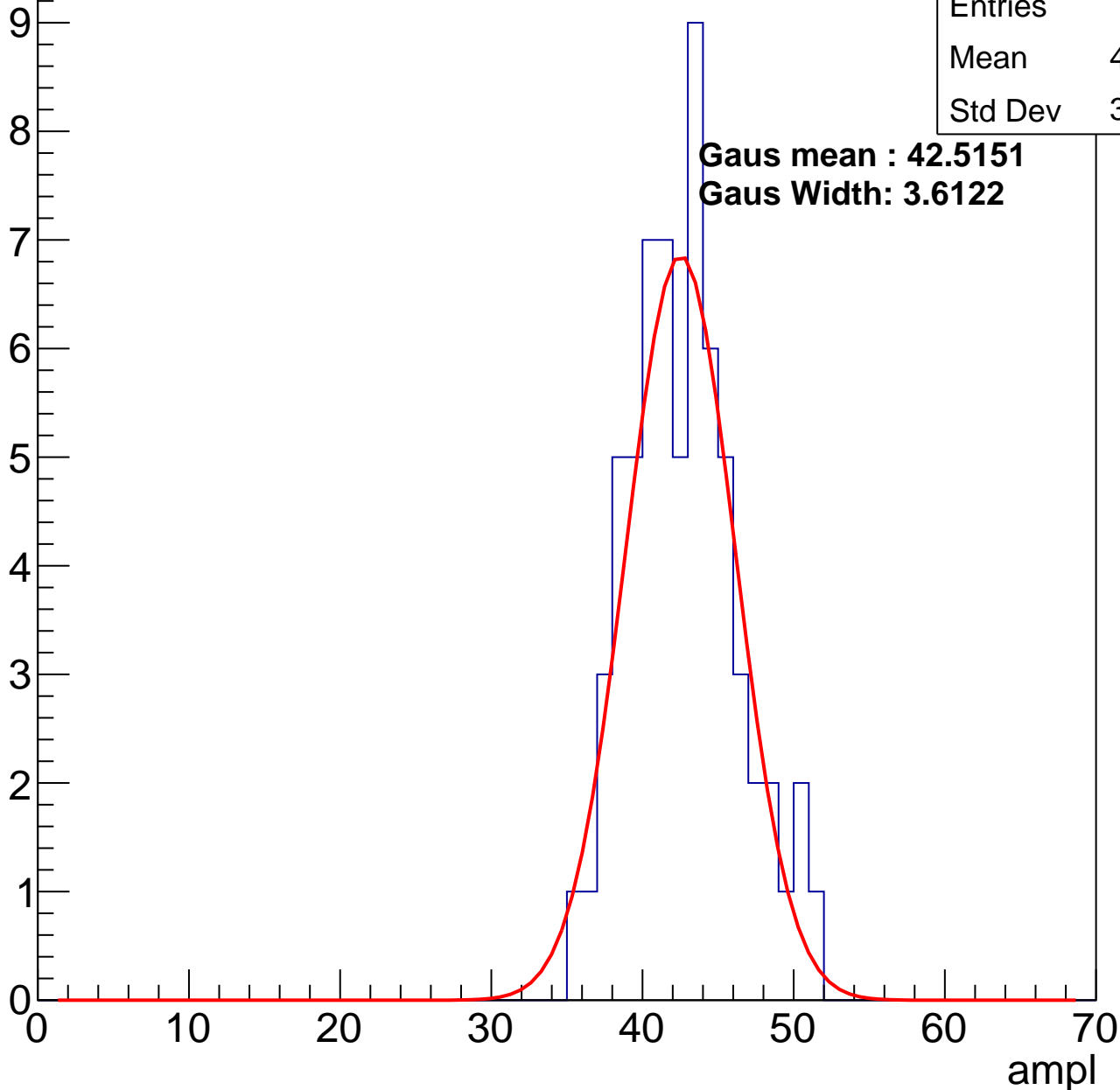
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	42.28
Std Dev	3.554

Gaus mean : 42.5151

Gaus Width: 3.6122

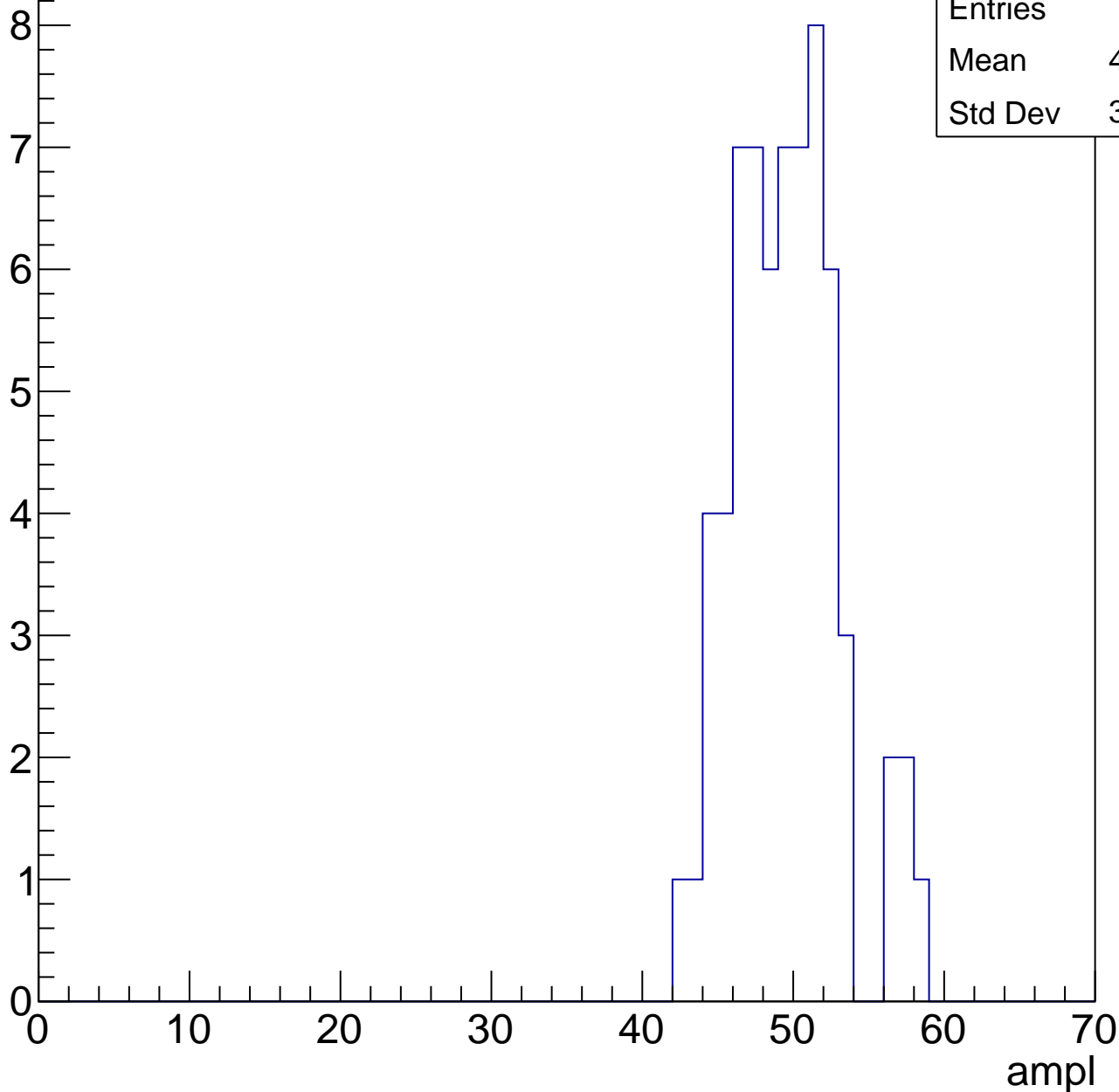


B0L001S, U18-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.03
Std Dev	3.468



B0L001S, U18-ch54, adc4

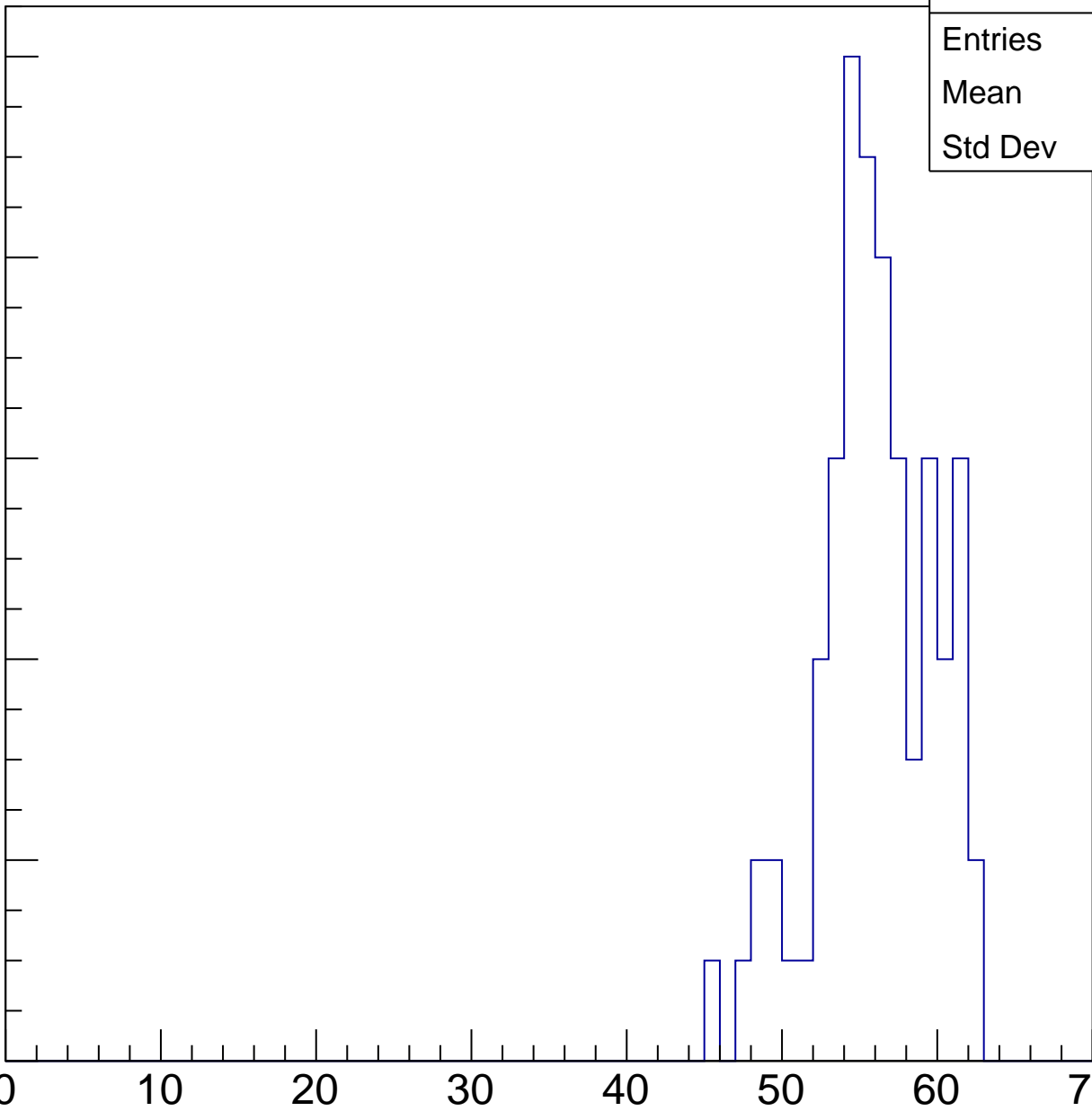
calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	55.5
Std Dev	3.719

Entry

10
8
6
4
2
0

ampl

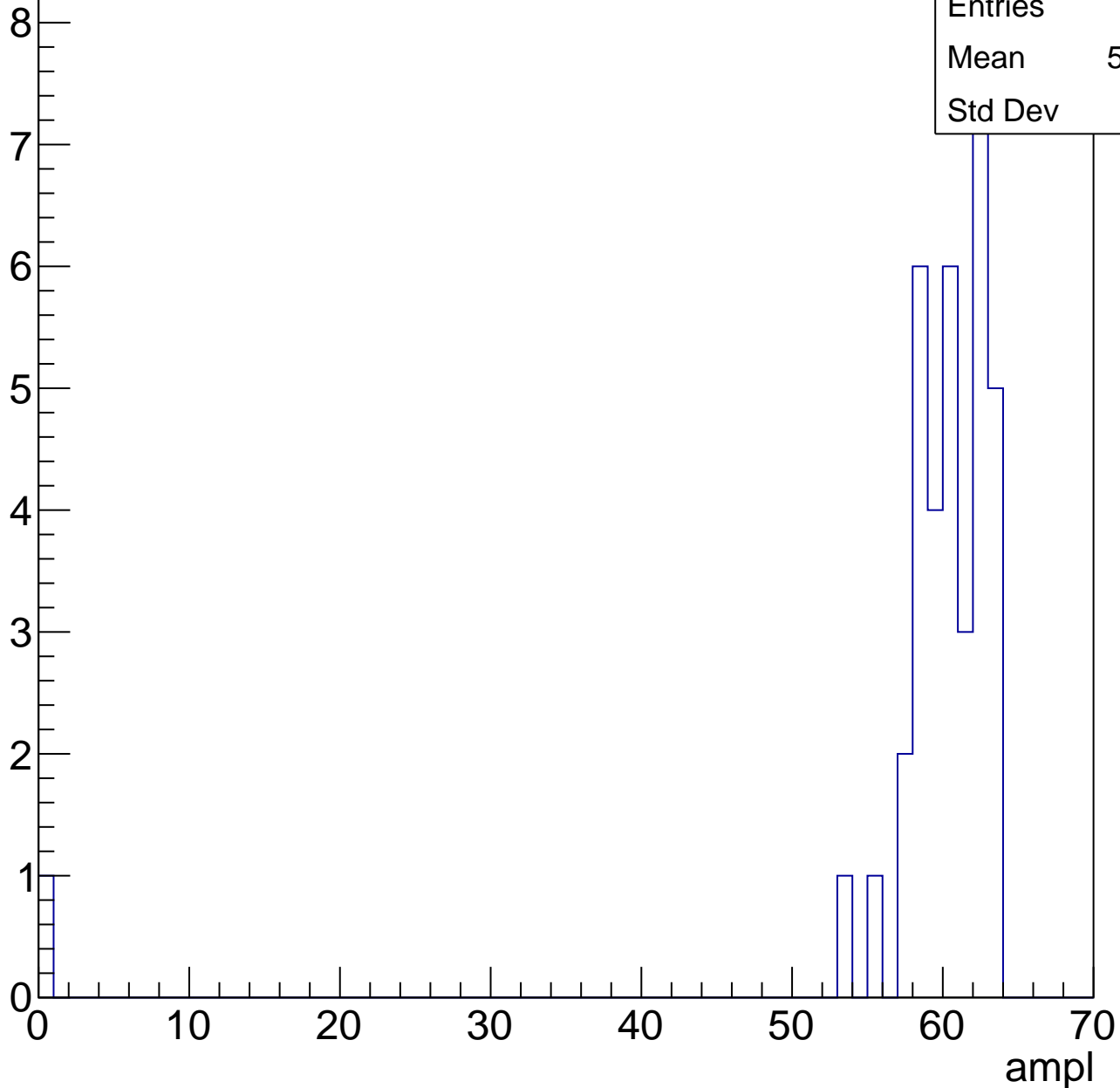


B0L001S, U18-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.38
Std Dev	10

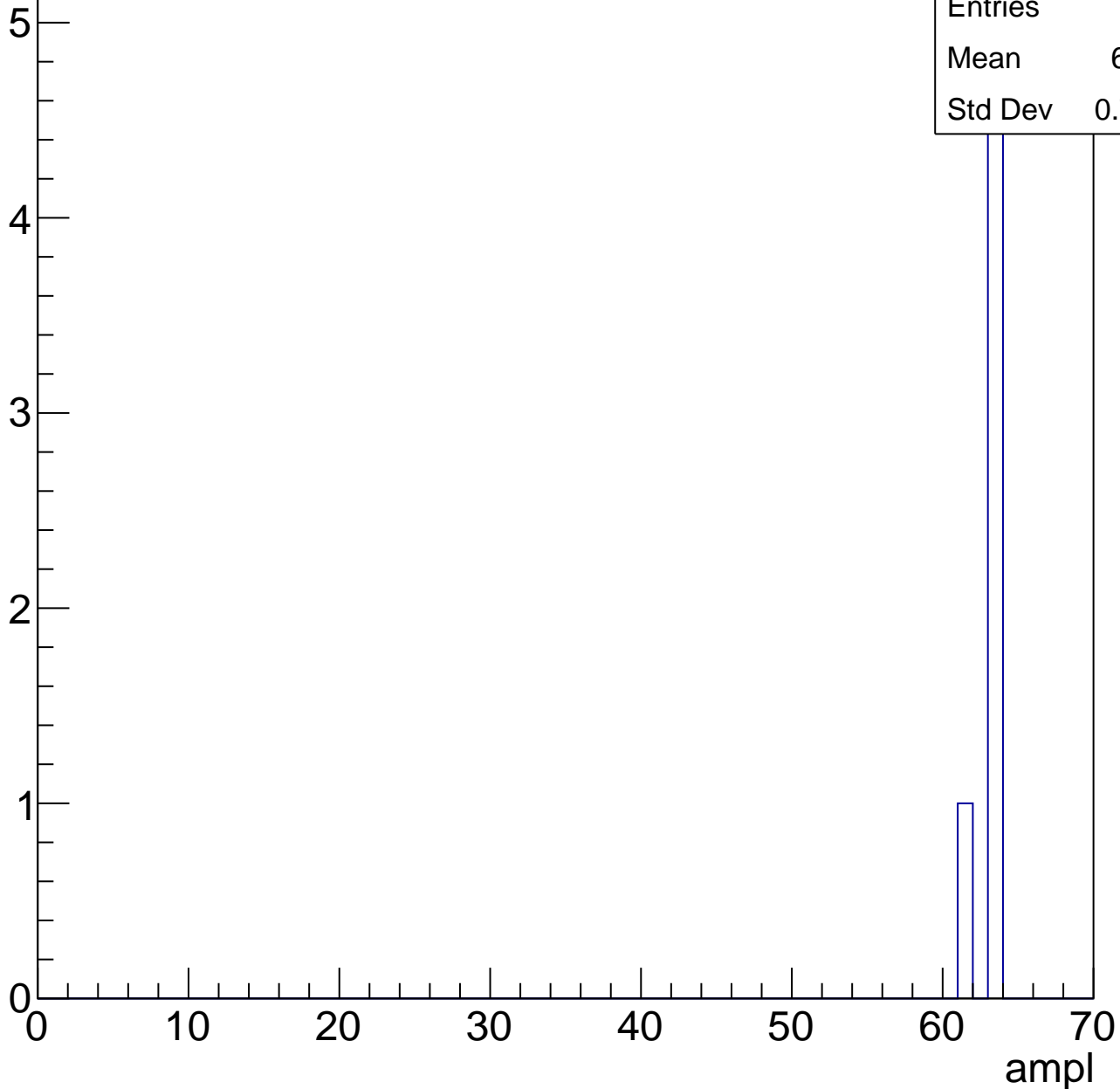


B0L001S, U18-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B0L001S, U18-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch55, adc0

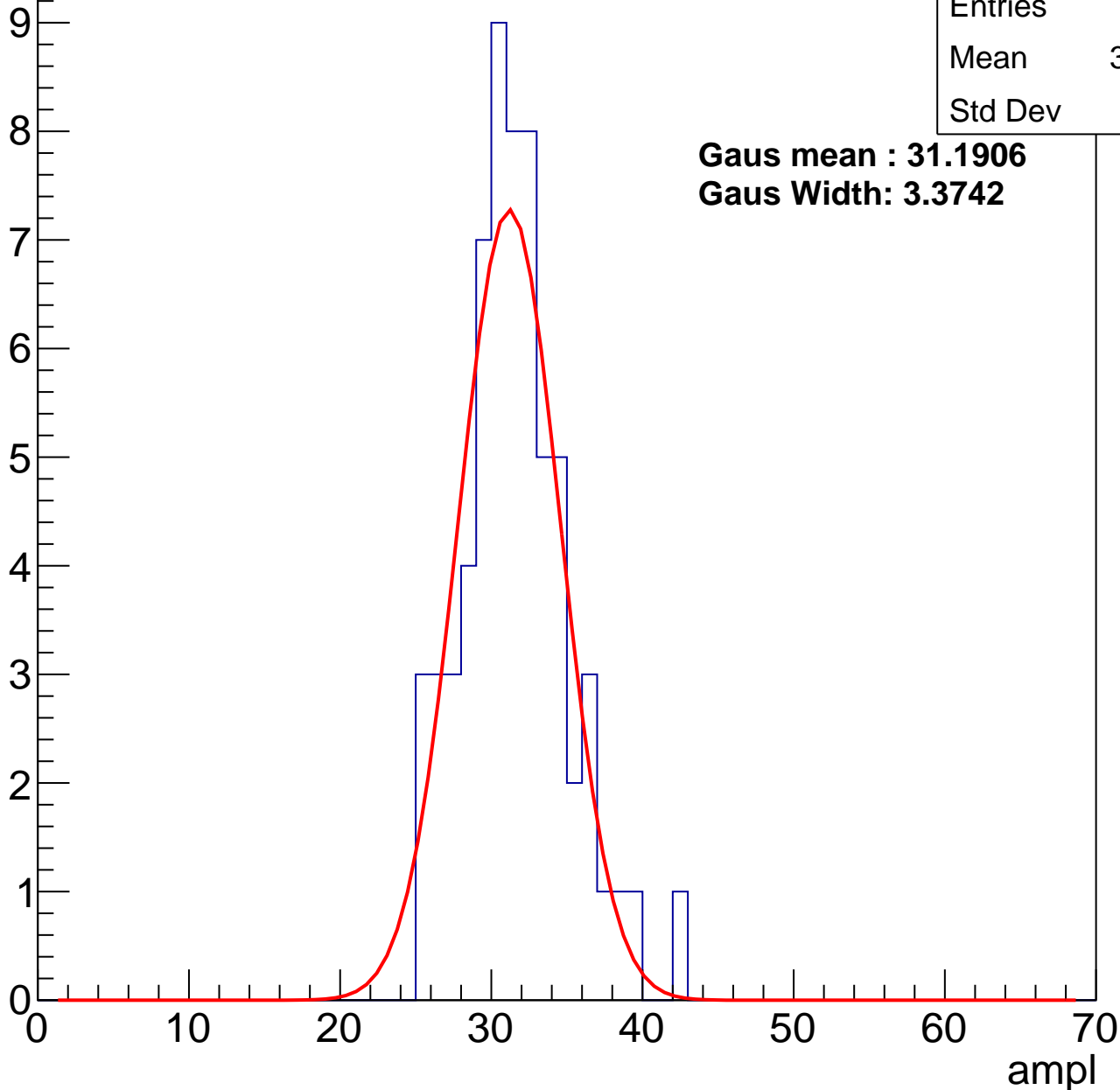
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.12
Std Dev	3.43

Gaus mean : 31.1906

Gaus Width: 3.3742



B0L001S, U18-ch55, adc1

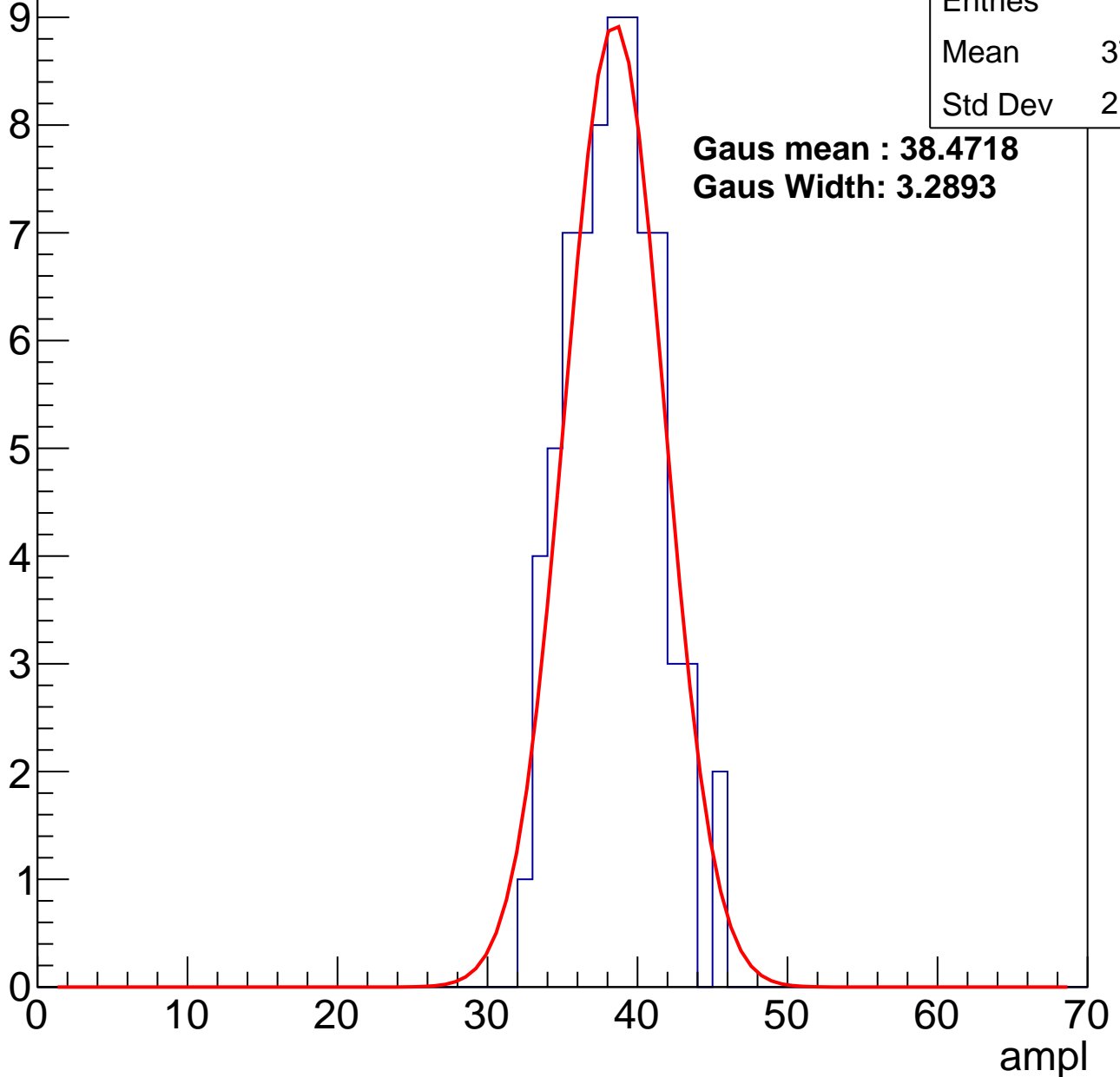
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.94
Std Dev	2.972

Gaus mean : 38.4718

Gaus Width: 3.2893



B0L001S, U18-ch55, adc2

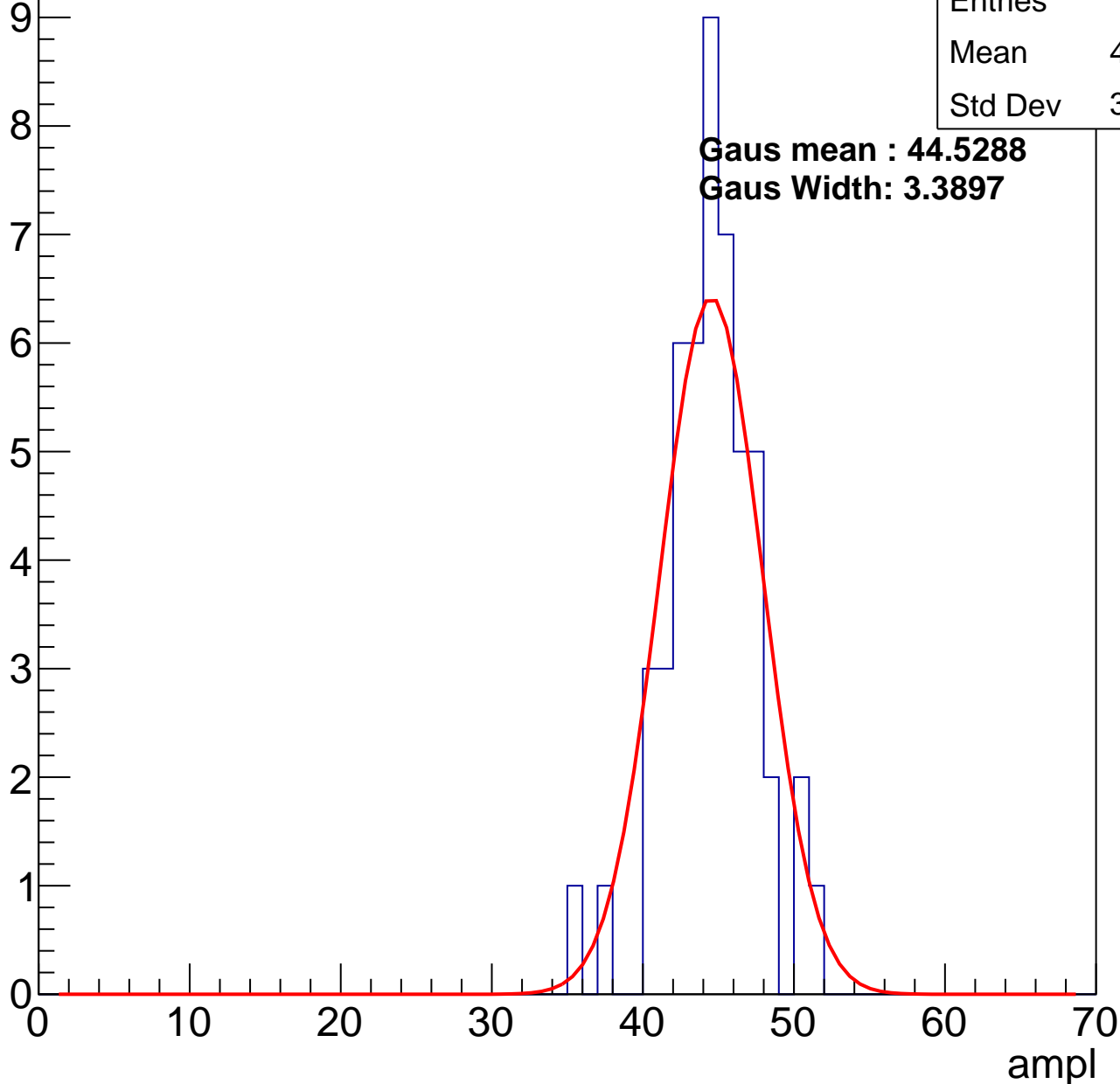
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.08
Std Dev	3.002

Gaus mean : 44.5288

Gaus Width: 3.3897

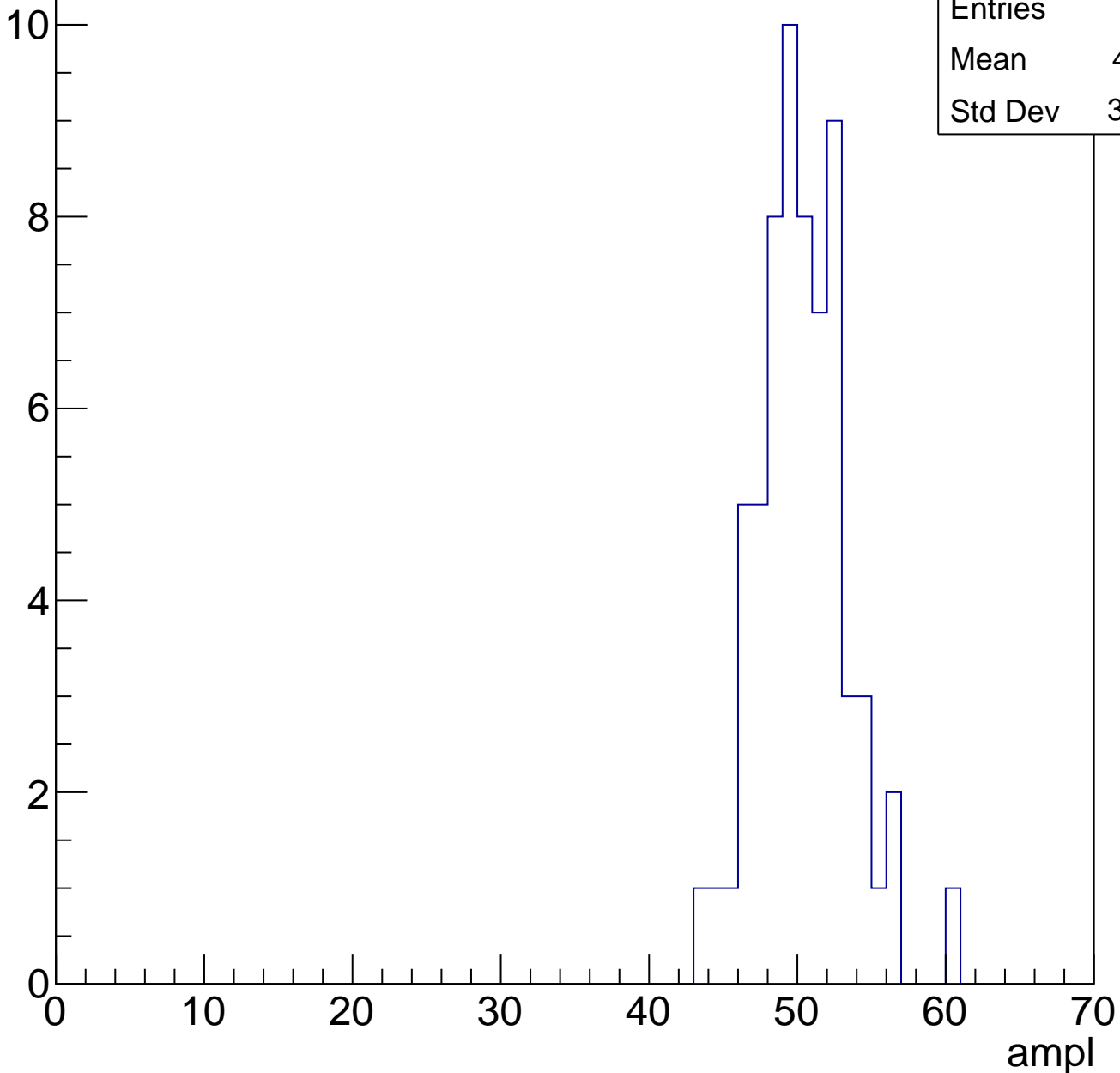


B0L001S, U18-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.91
Std Dev	3.016

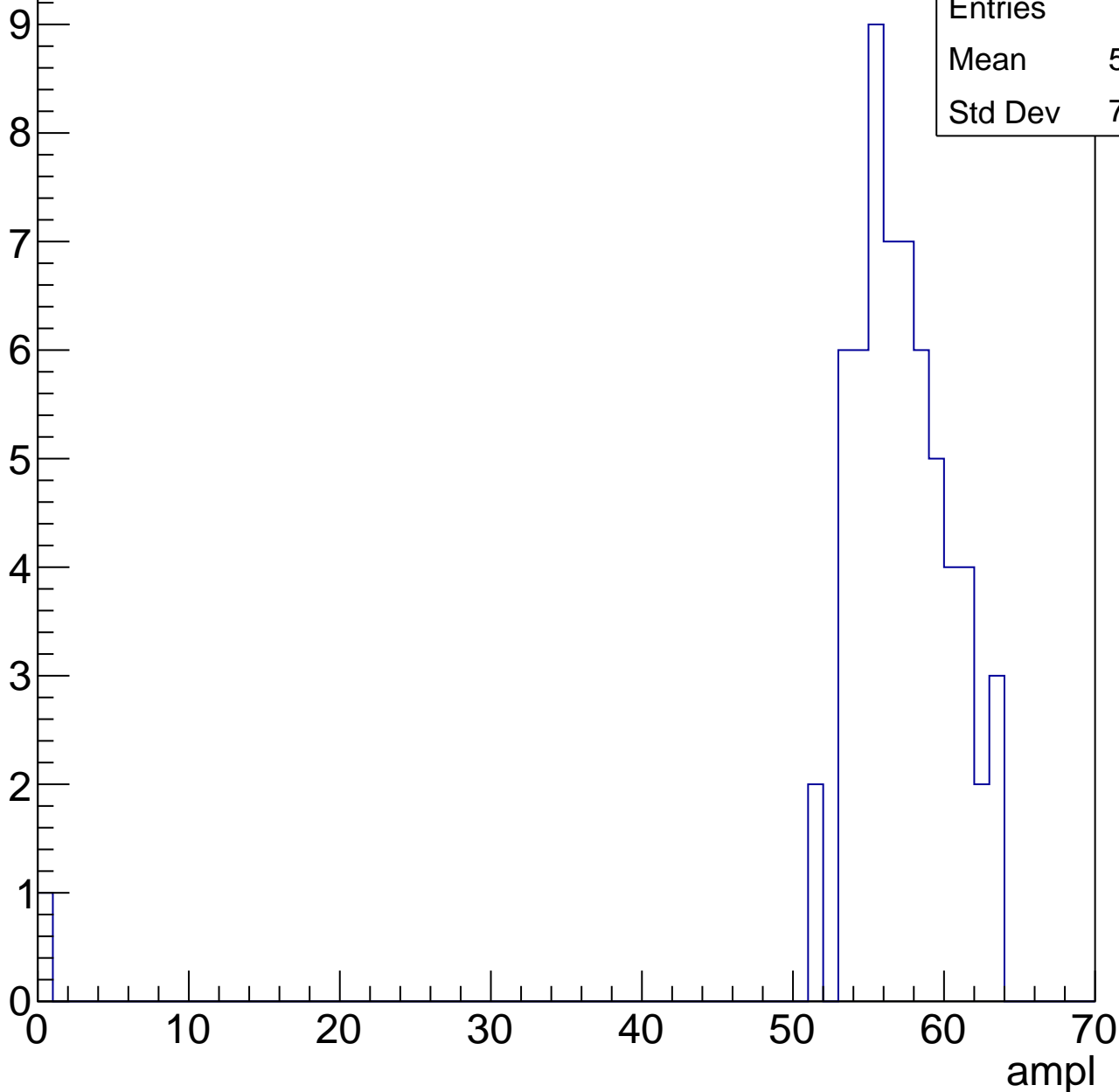


B0L001S, U18-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	55.97
Std Dev	7.758

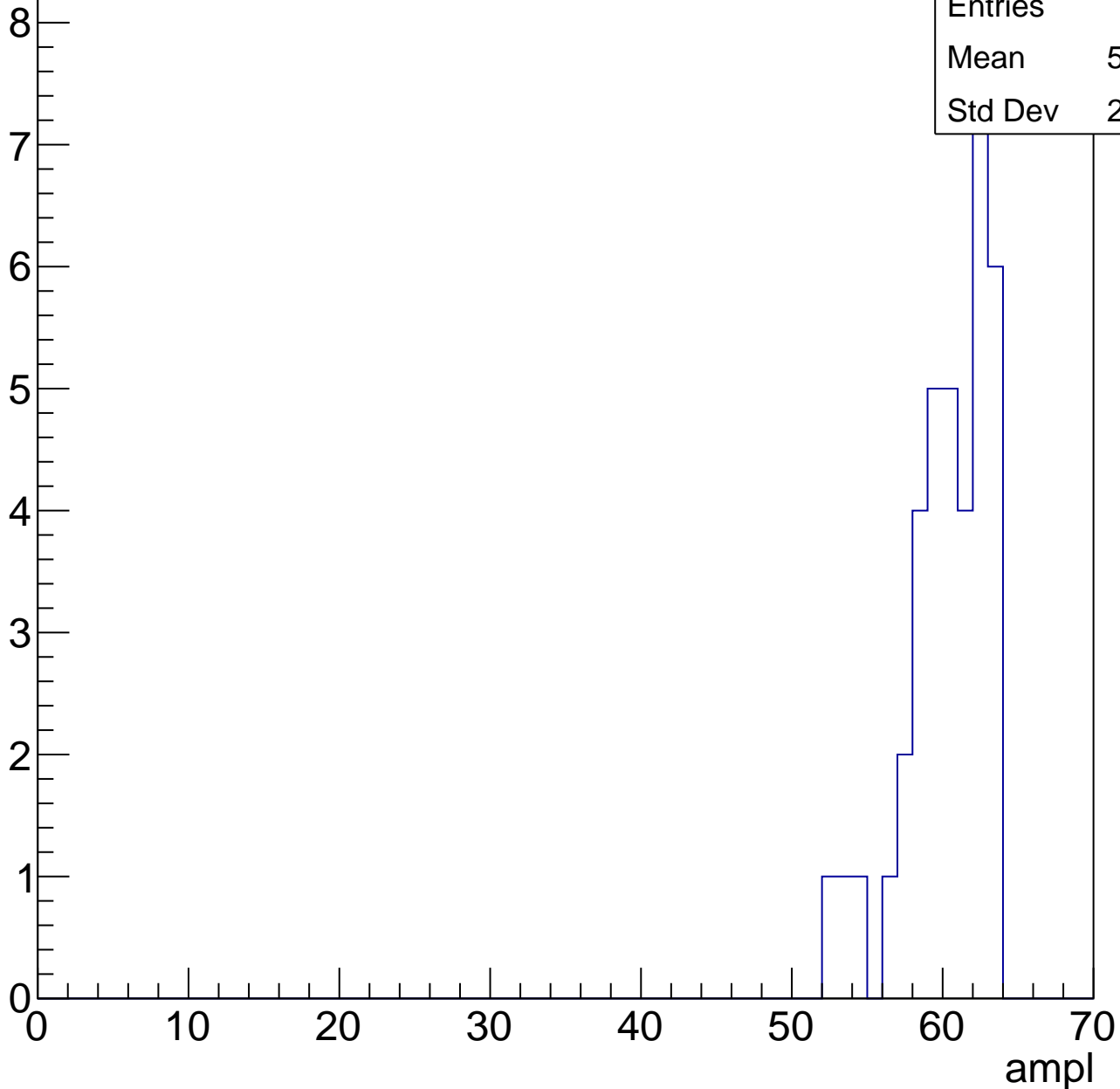


B0L001S, U18-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

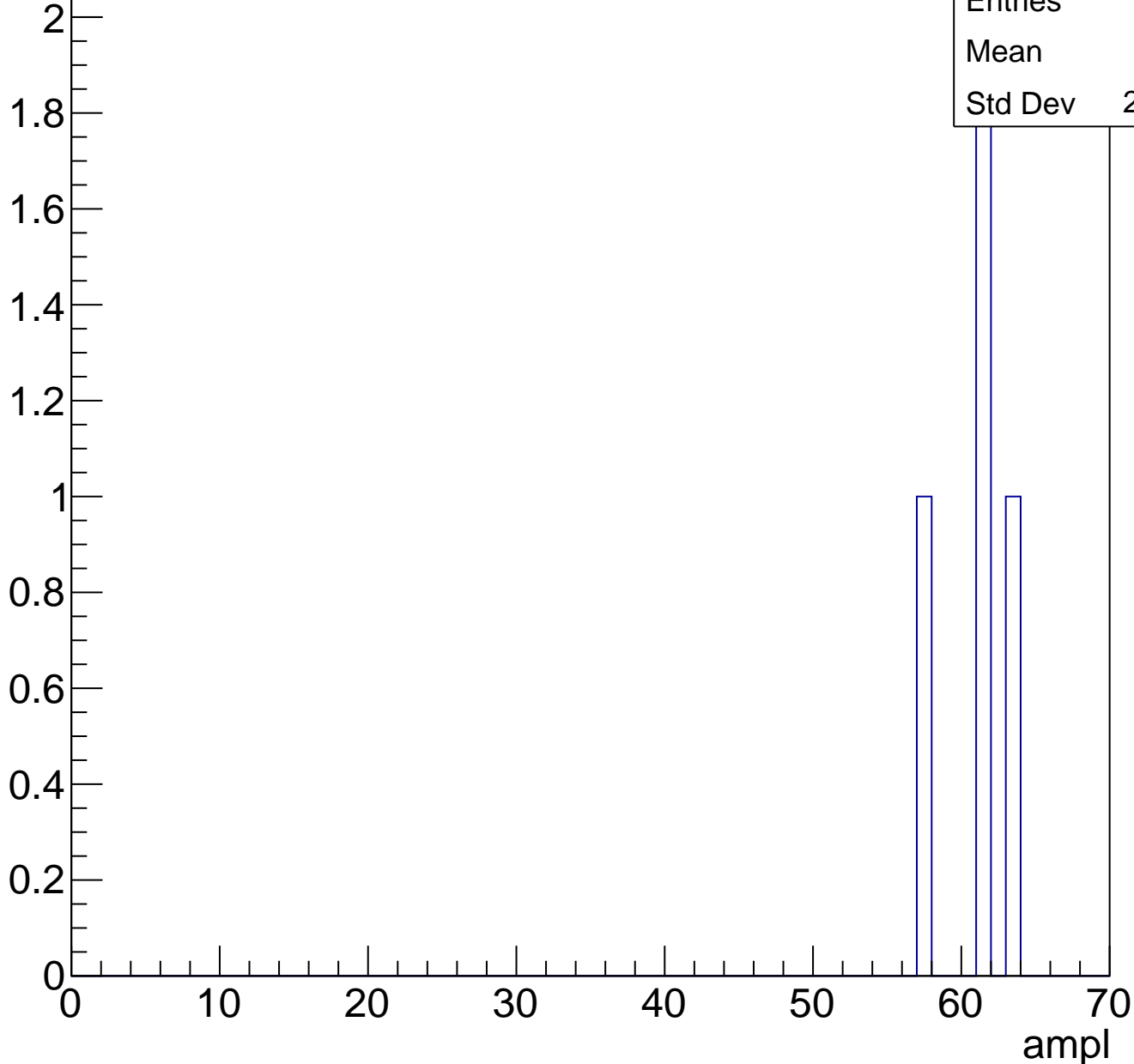
Entries	38
Mean	59.84
Std Dev	2.777



B0L001S, U18-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	22
Std Dev	0

B0L001S, U18-ch56, adc0

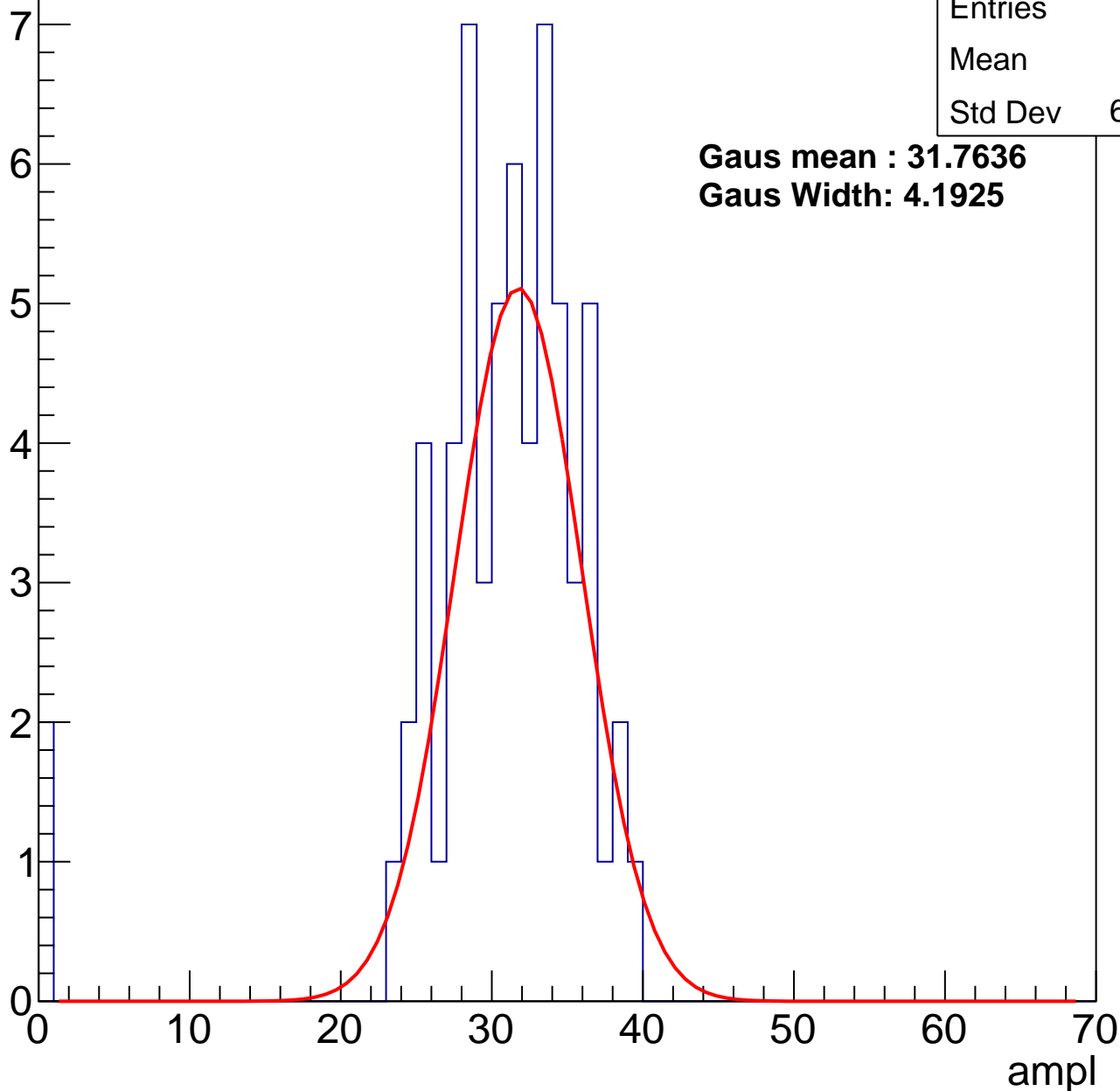
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30
Std Dev	6.645

Gaus mean : 31.7636

Gaus Width: 4.1925



B0L001S, U18-ch56, adc1

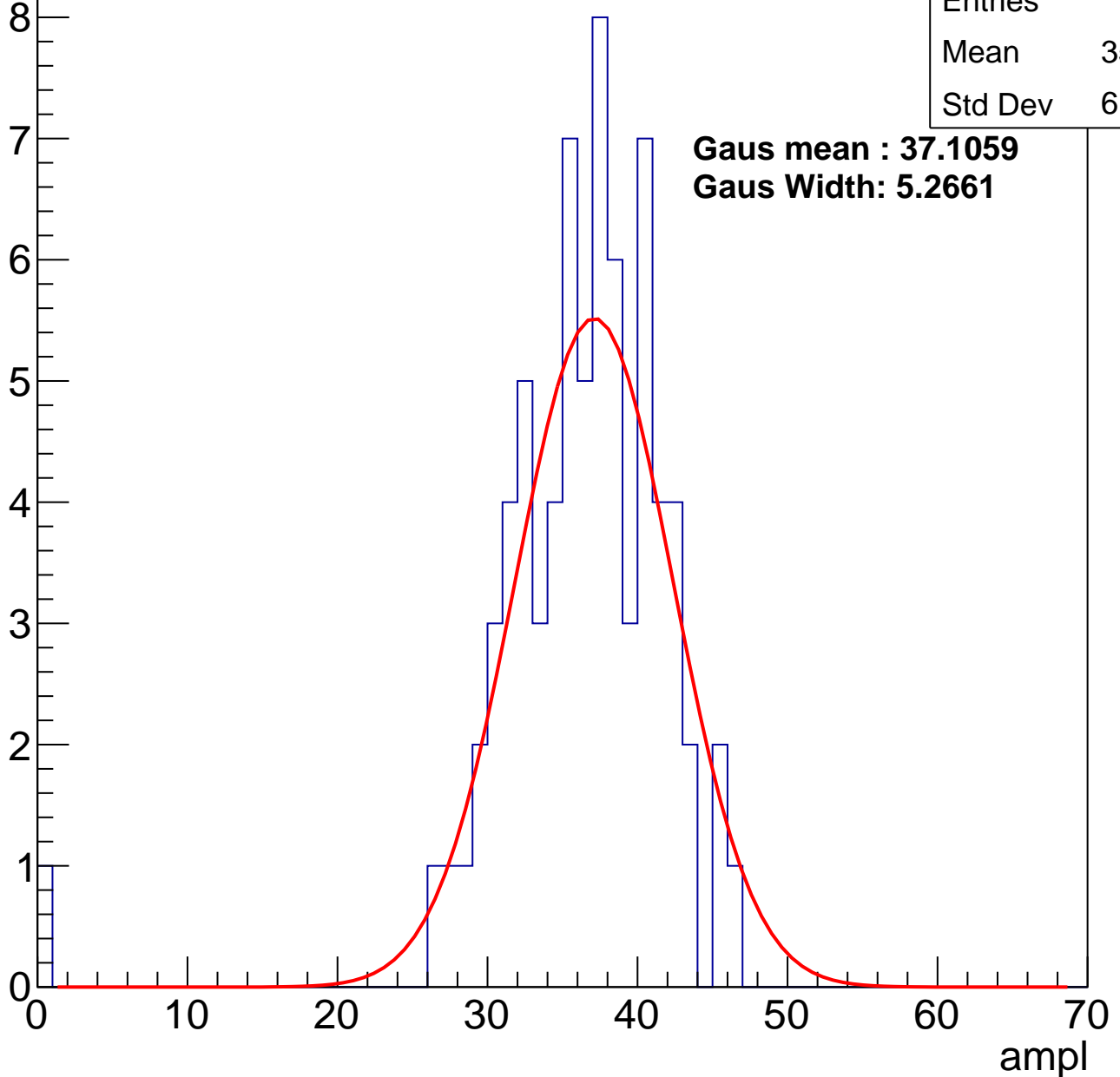
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	35.78
Std Dev	6.094

Gaus mean : 37.1059

Gaus Width: 5.2661



B0L001S, U18-ch56, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	43.85
Std Dev	3.59

Gaus mean : 44.4608

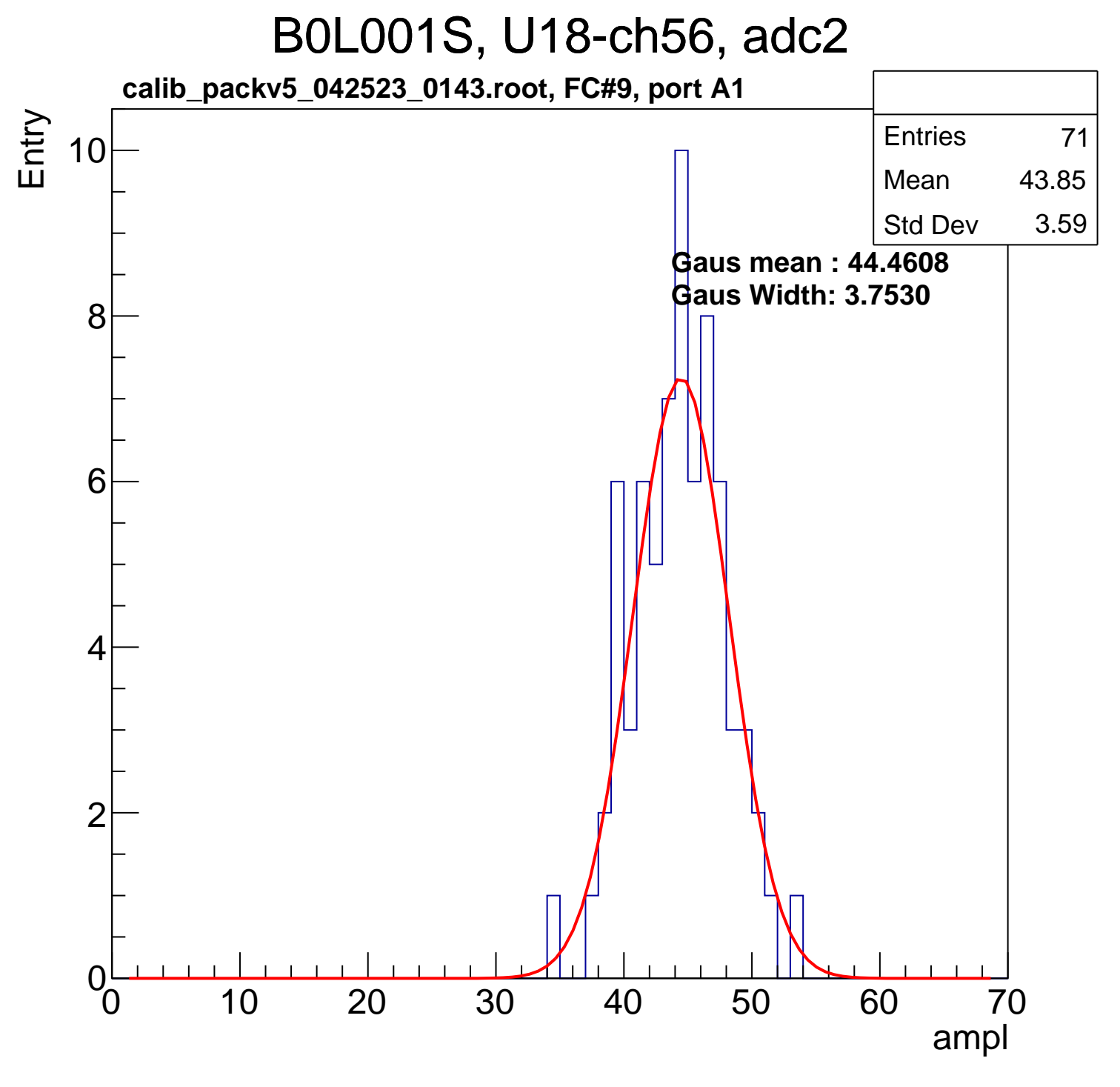
Gaus Width: 3.7530

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

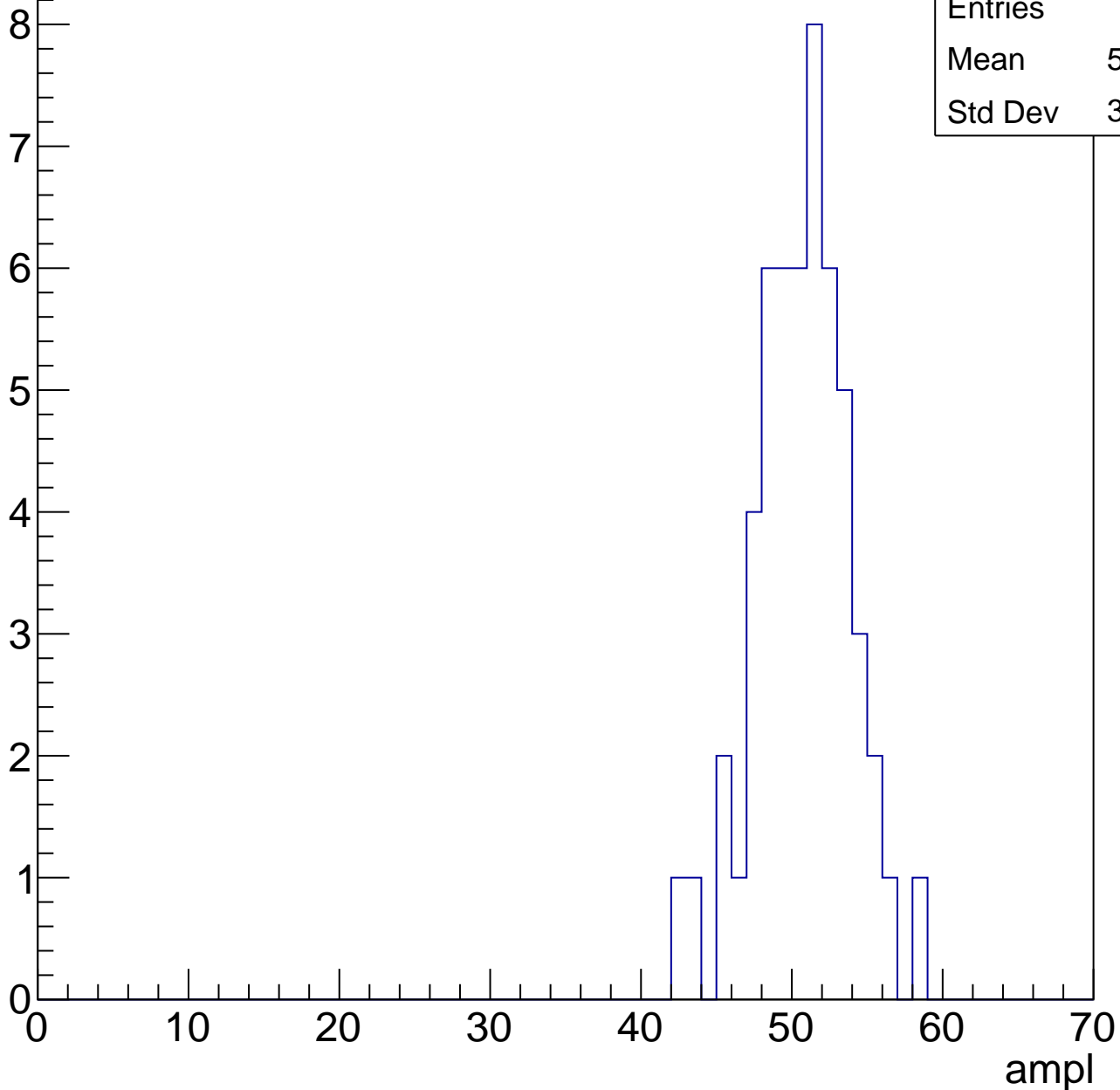


B0L001S, U18-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

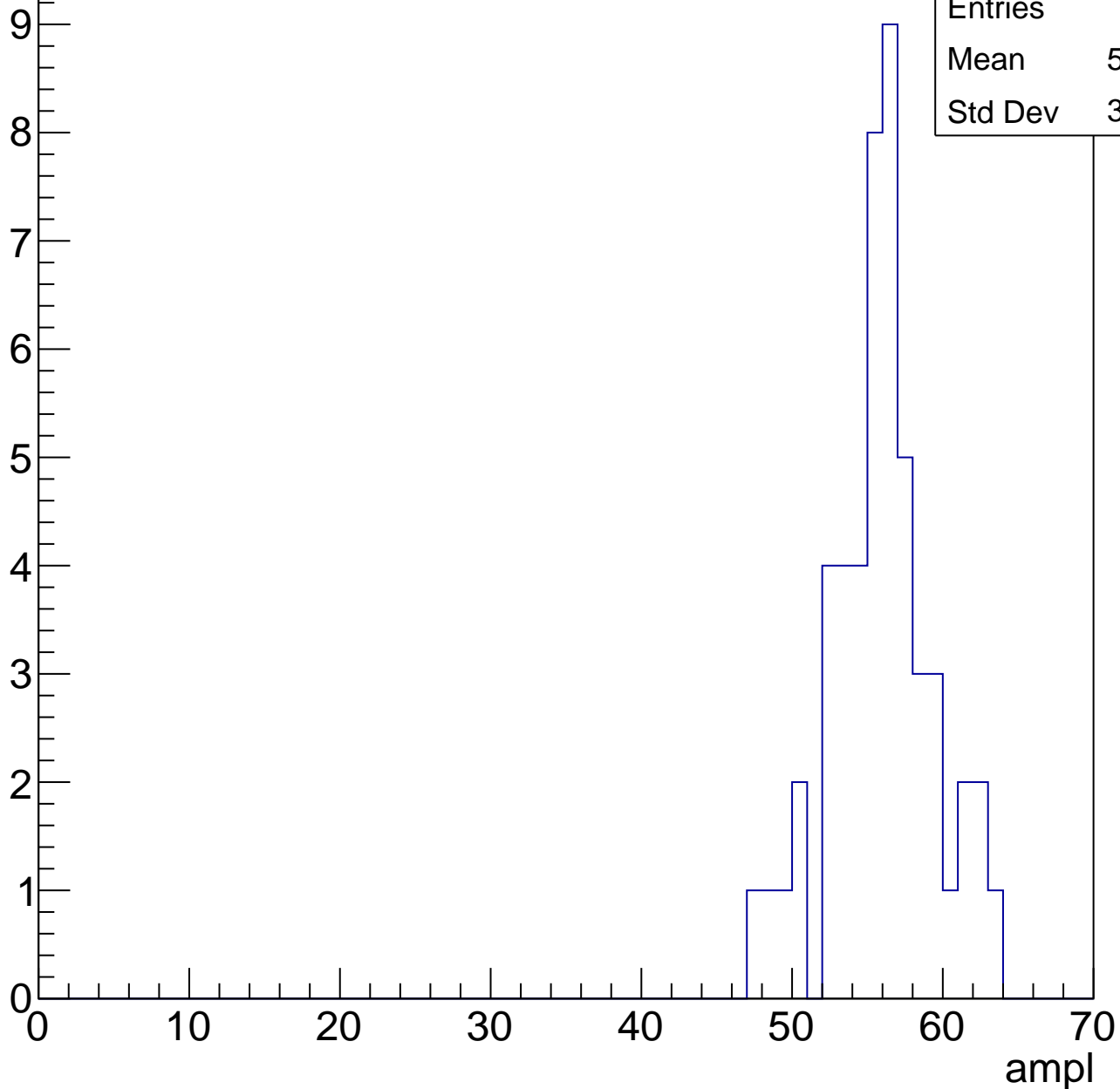
Entries	53
Mean	50.23
Std Dev	3.124



B0L001S, U18-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



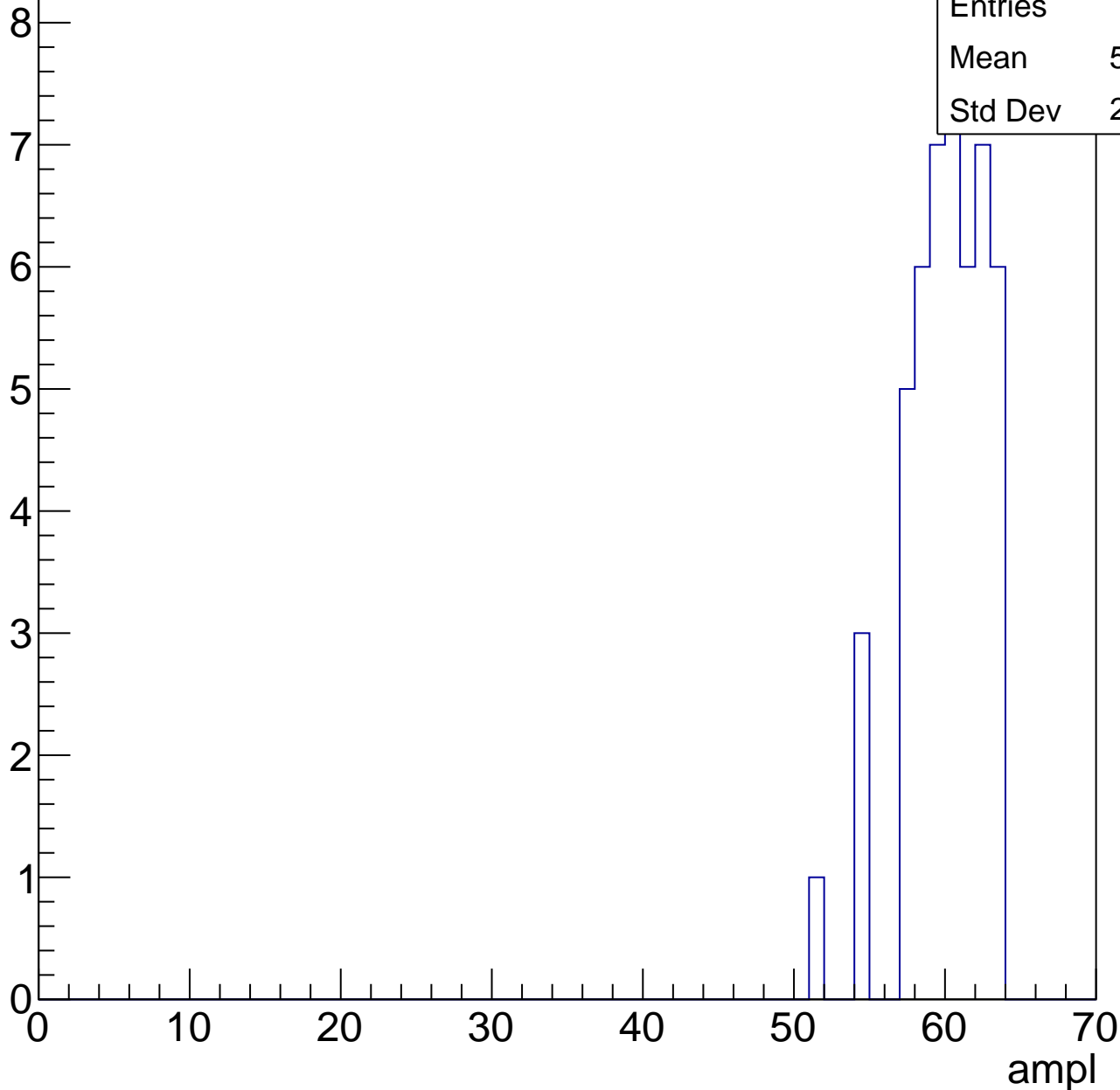
Entries	51
Mean	55.47
Std Dev	3.449

B0L001S, U18-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

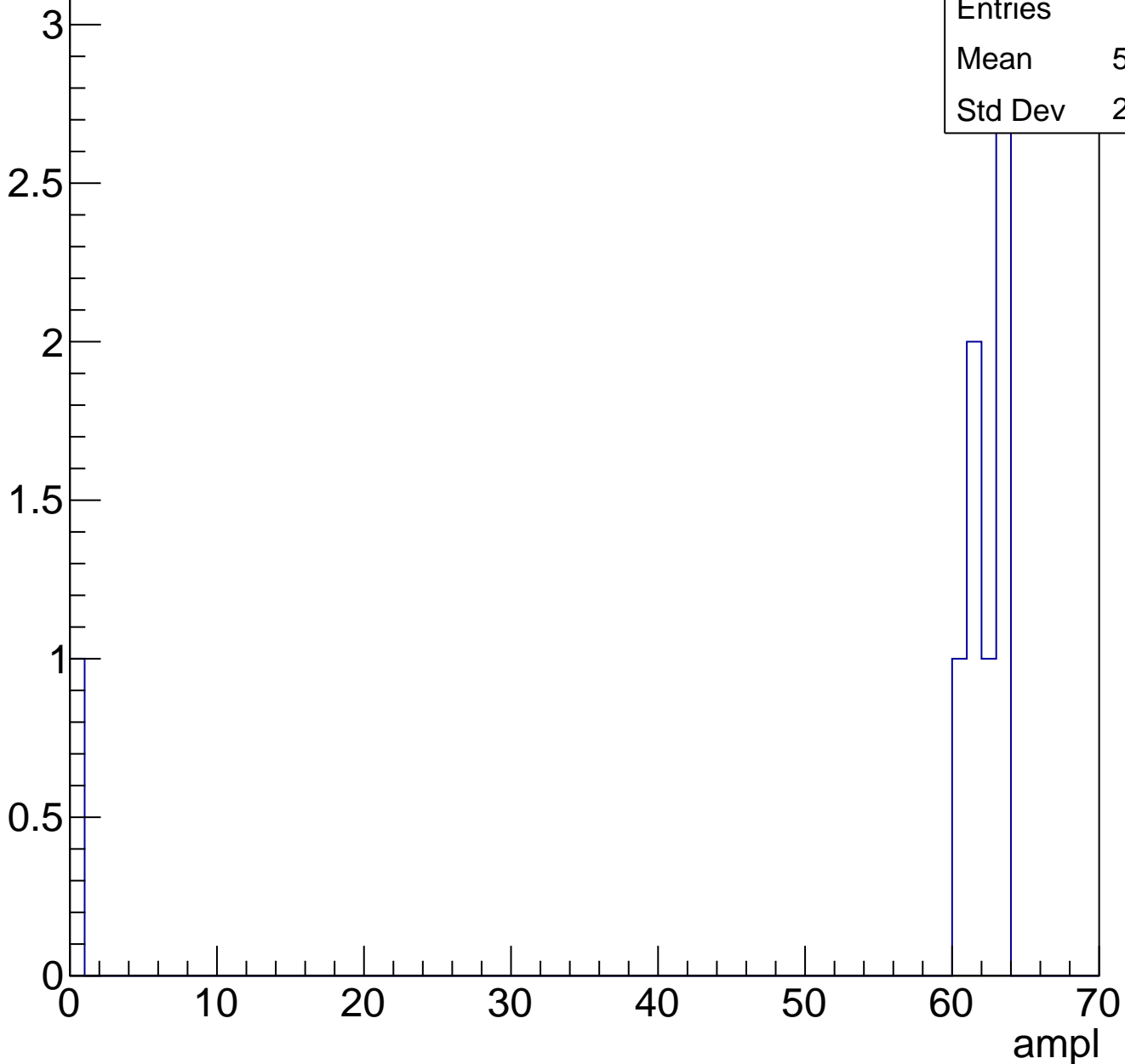
Entries	49
Mean	59.53
Std Dev	2.643



B0L001S, U18-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch57, adc0

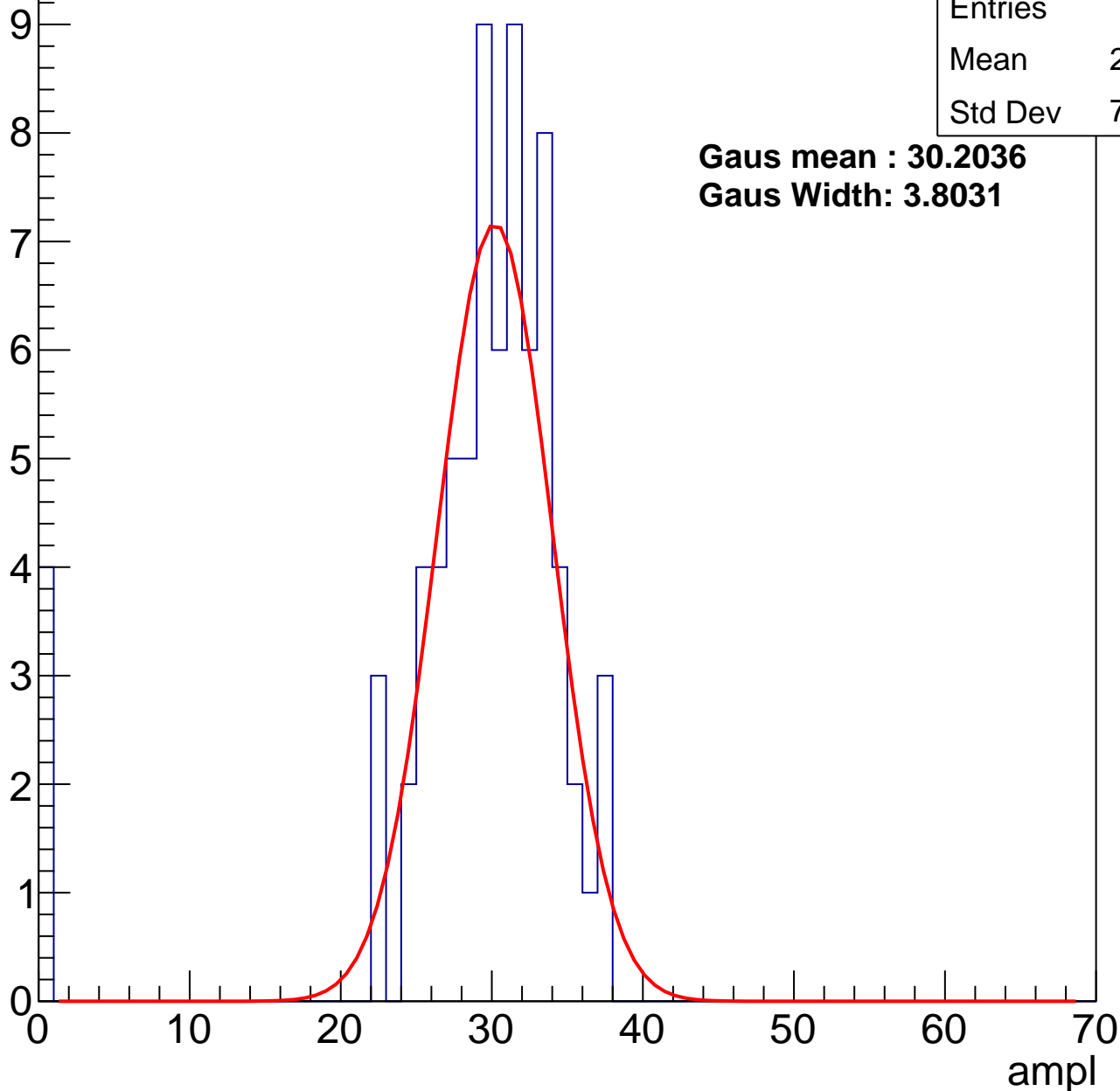
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	28.29
Std Dev	7.559

Gaus mean : 30.2036

Gaus Width: 3.8031



B0L001S, U18-ch57, adc1

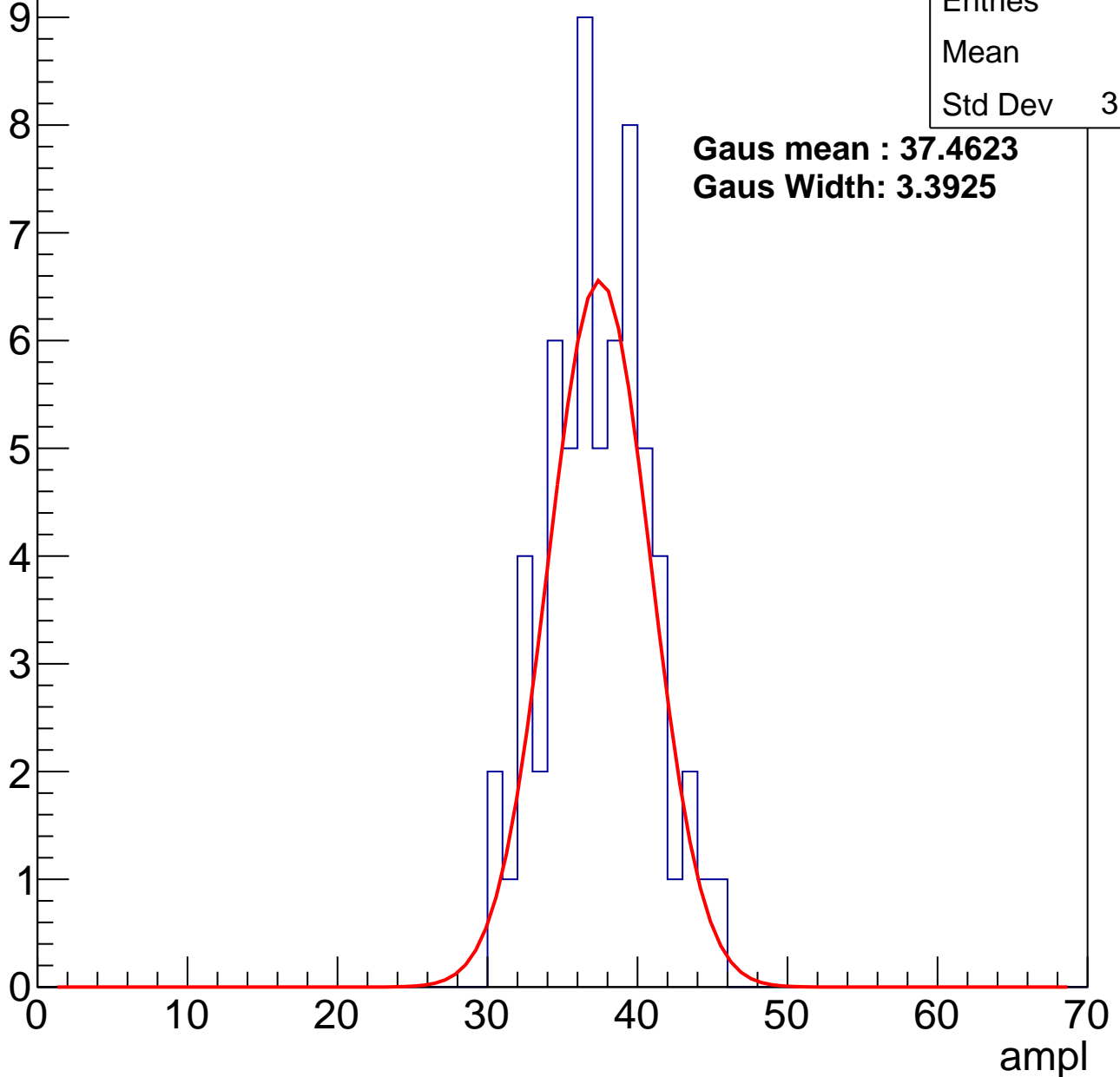
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37
Std Dev	3.374

Gaus mean : 37.4623

Gaus Width: 3.3925



B0L001S, U18-ch57, adc2

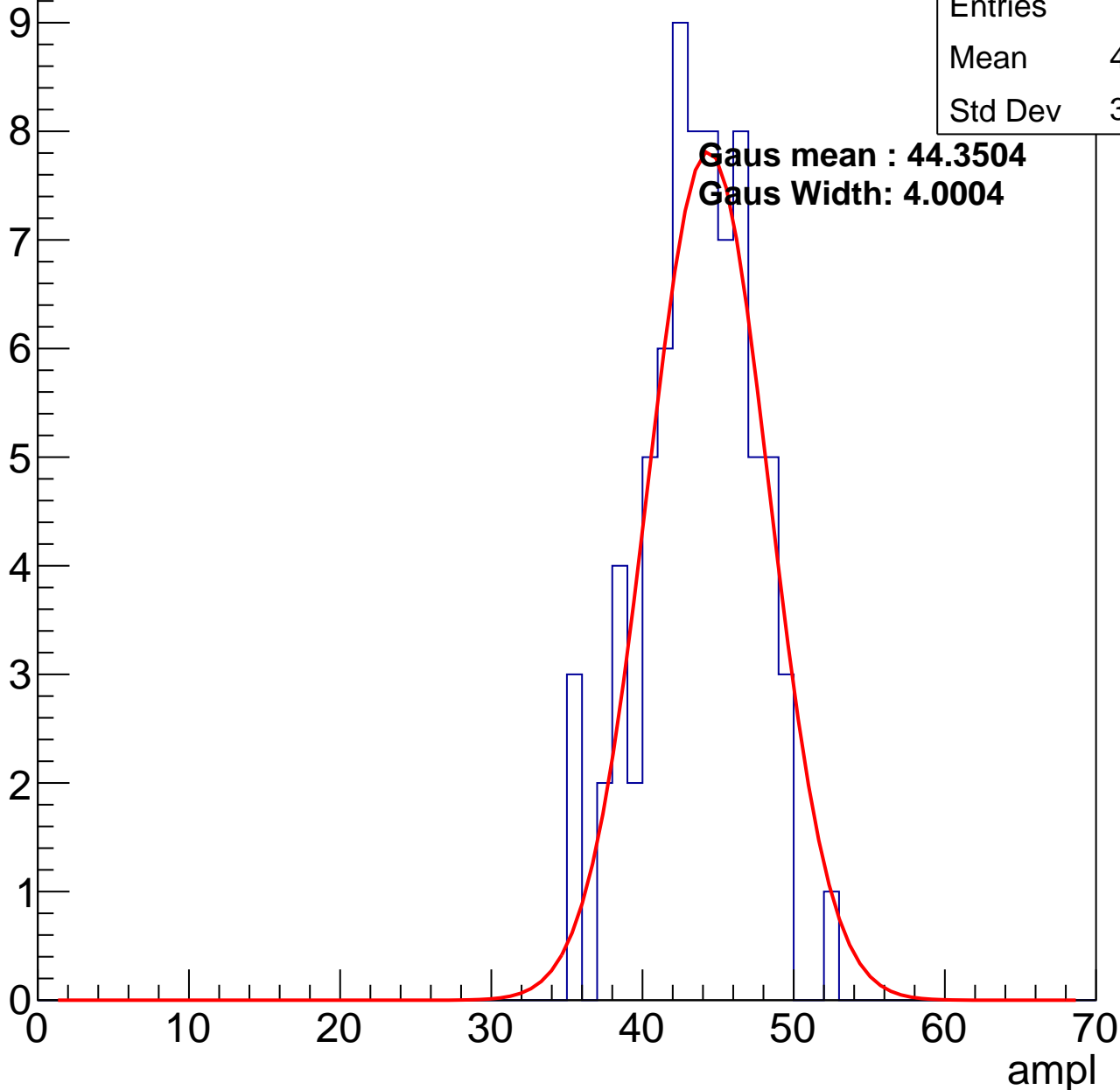
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	43.24
Std Dev	3.576

Gaus mean : 44.3504

Gaus Width: 4.0004

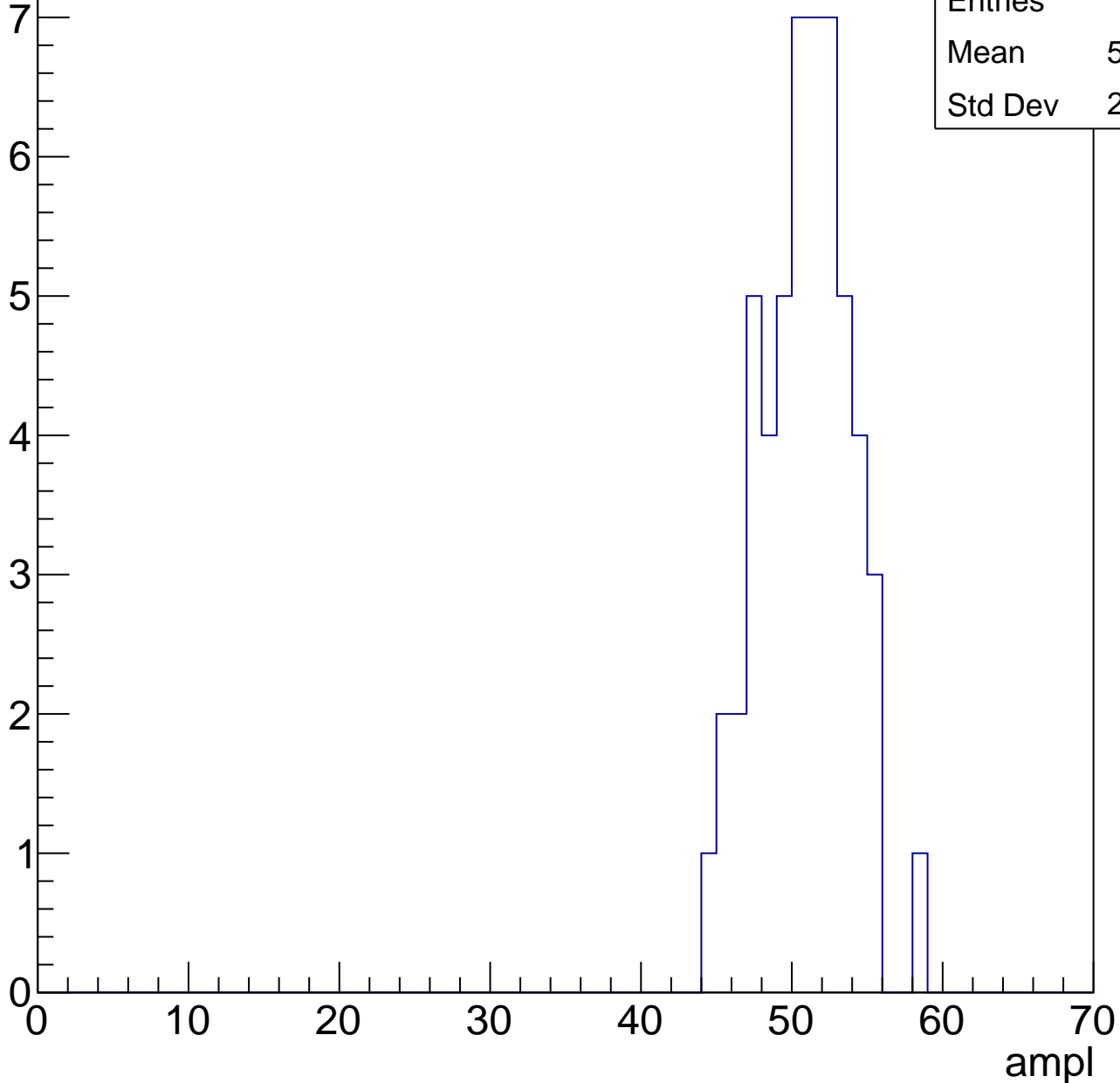


B0L001S, U18-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.43
Std Dev	2.936

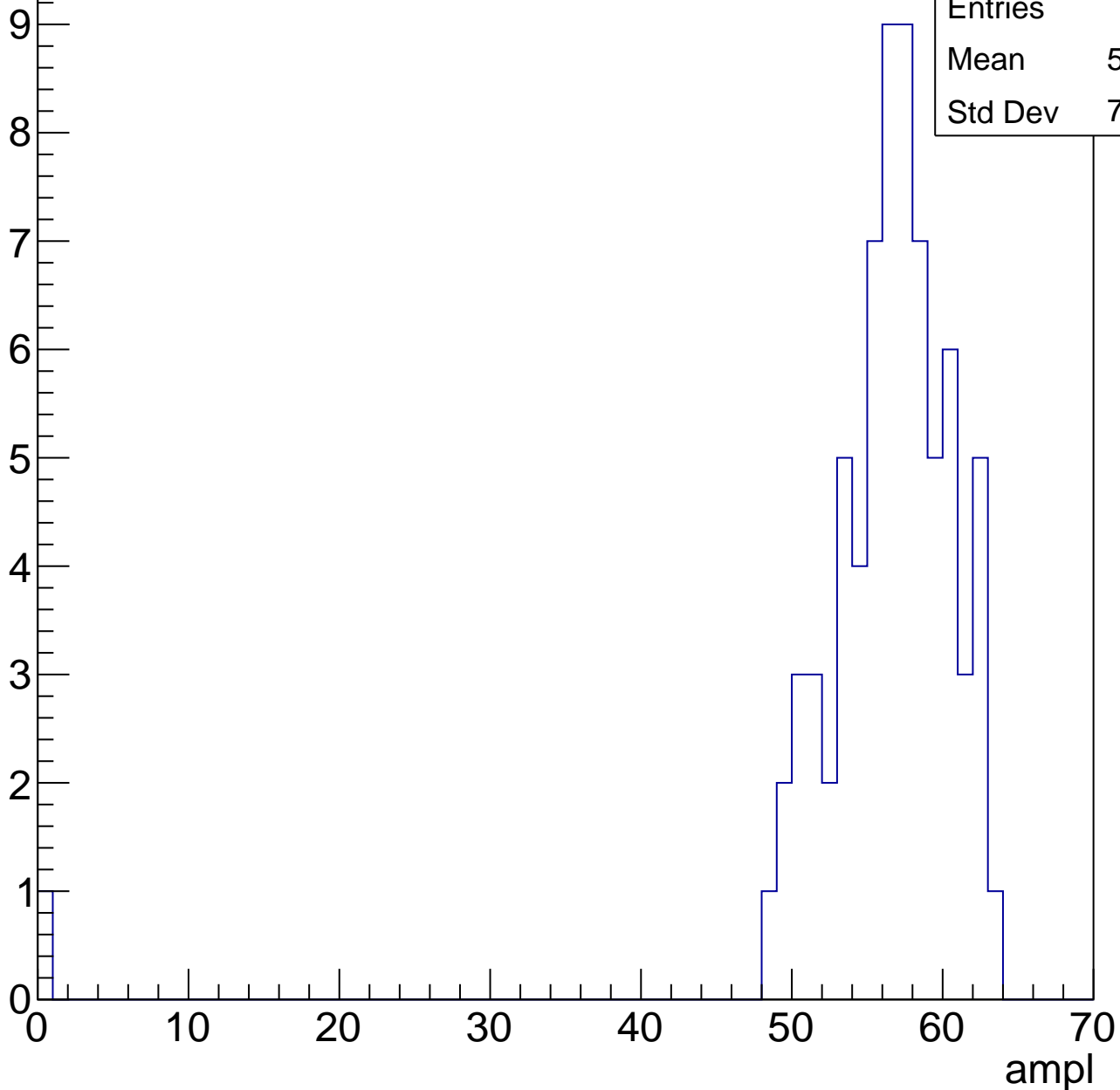


B0L001S, U18-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	55.52
Std Dev	7.445

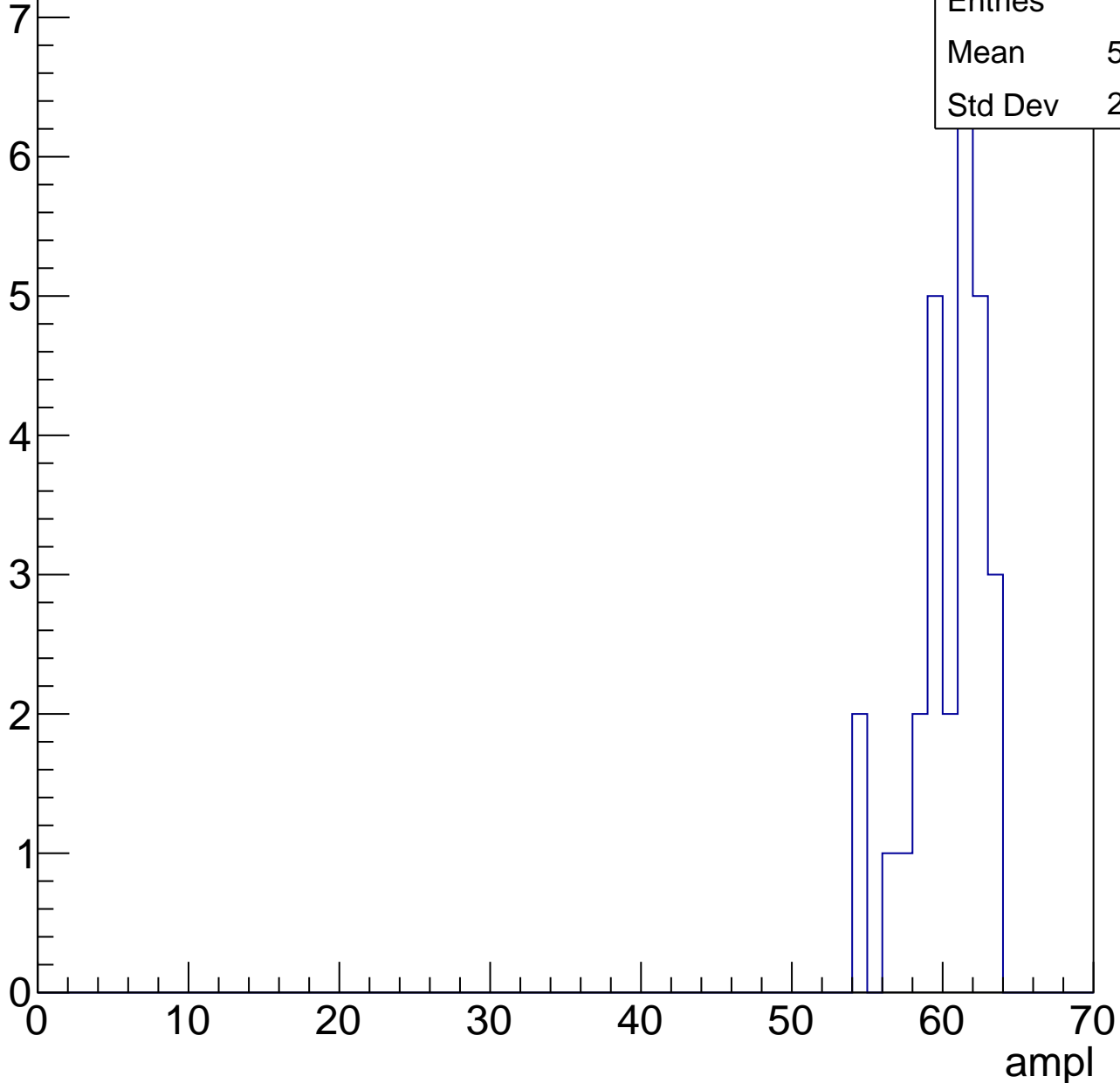


B0L001S, U18-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	59.93
Std Dev	2.419

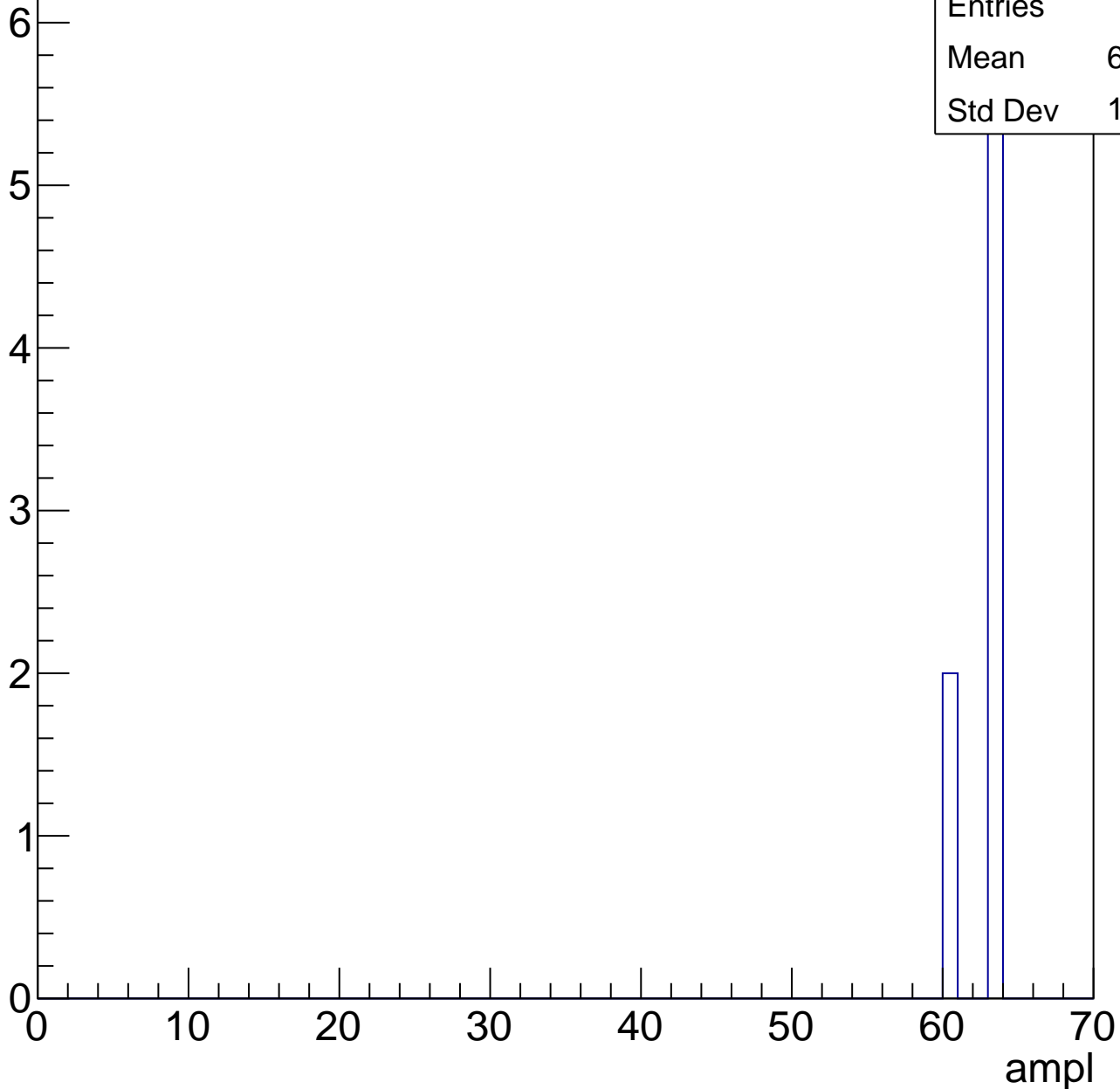


B0L001S, U18-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.25
Std Dev	1.299



B0L001S, U18-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch58, adc0

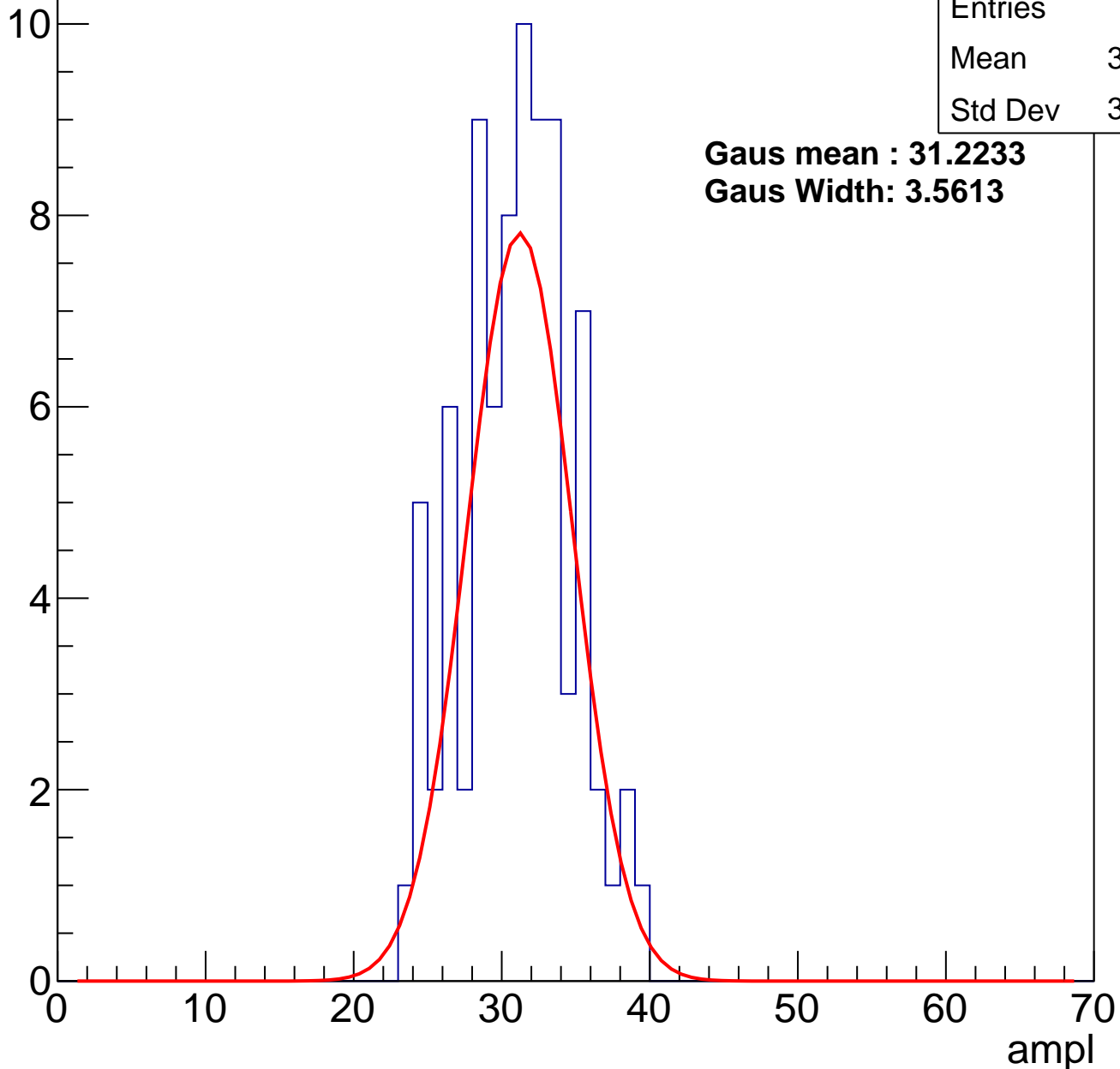
calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	30.54
Std Dev	3.625

Gaus mean : 31.2233

Gaus Width: 3.5613

Entry



B0L001S, U18-ch58, adc1

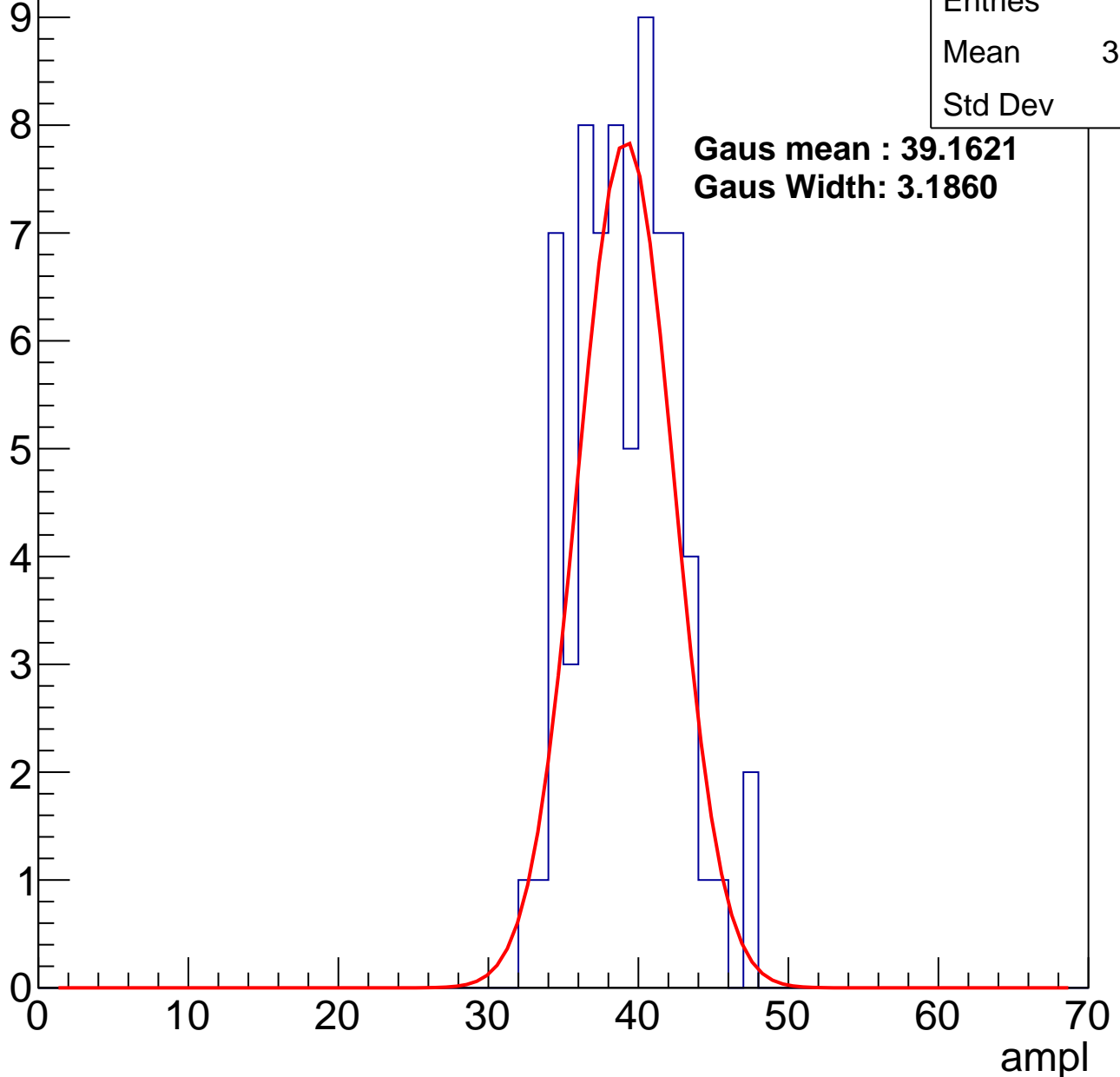
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.73
Std Dev	3.28

Gaus mean : 39.1621

Gaus Width: 3.1860



B0L001S, U18-ch58, adc2

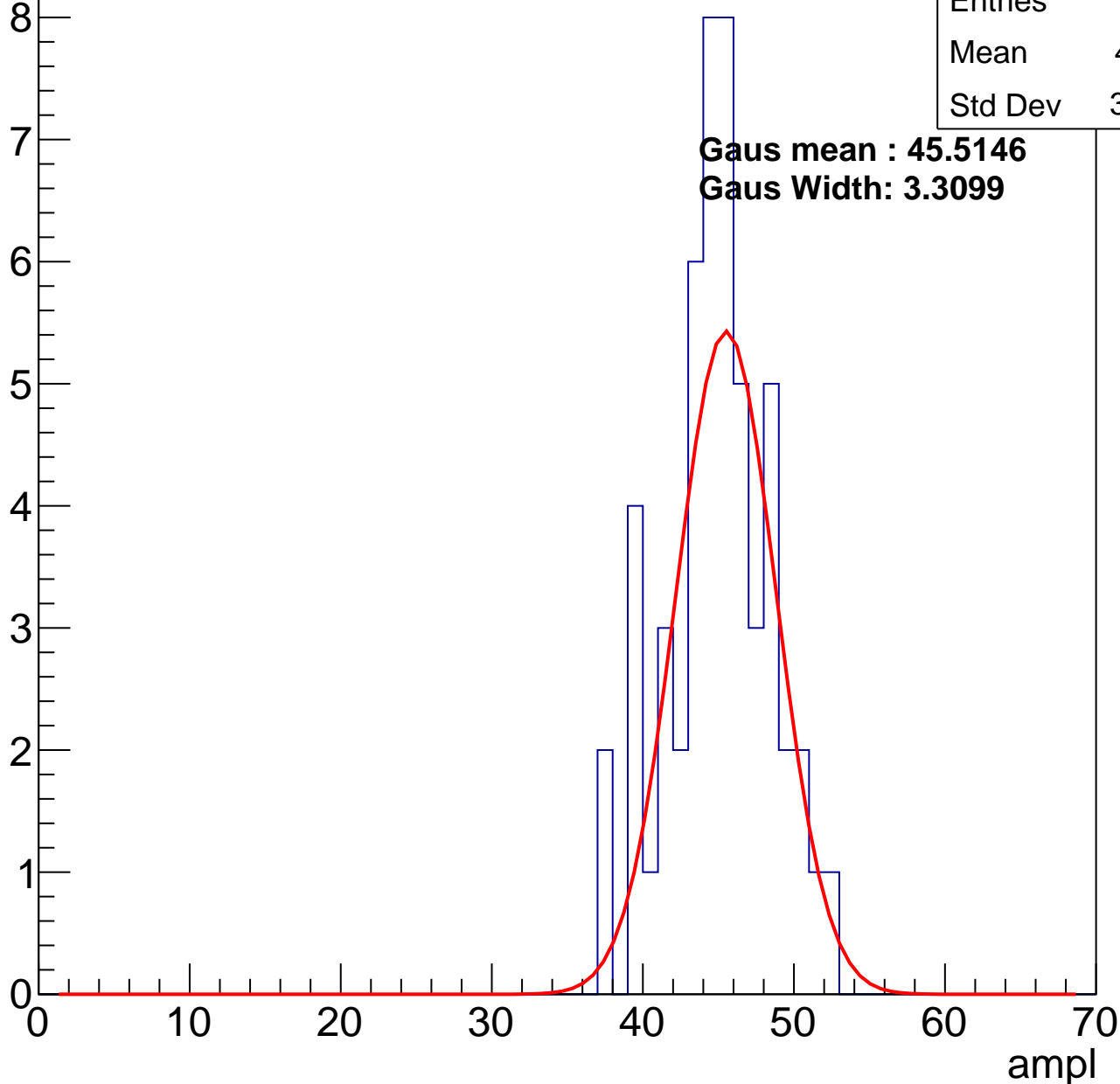
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.51
Std Dev	3.396

Gaus mean : 45.5146

Gaus Width: 3.3099



B0L001S, U18-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	51.66
Std Dev	3.648

Entry

10

8

6

4

2

0

0

10

20

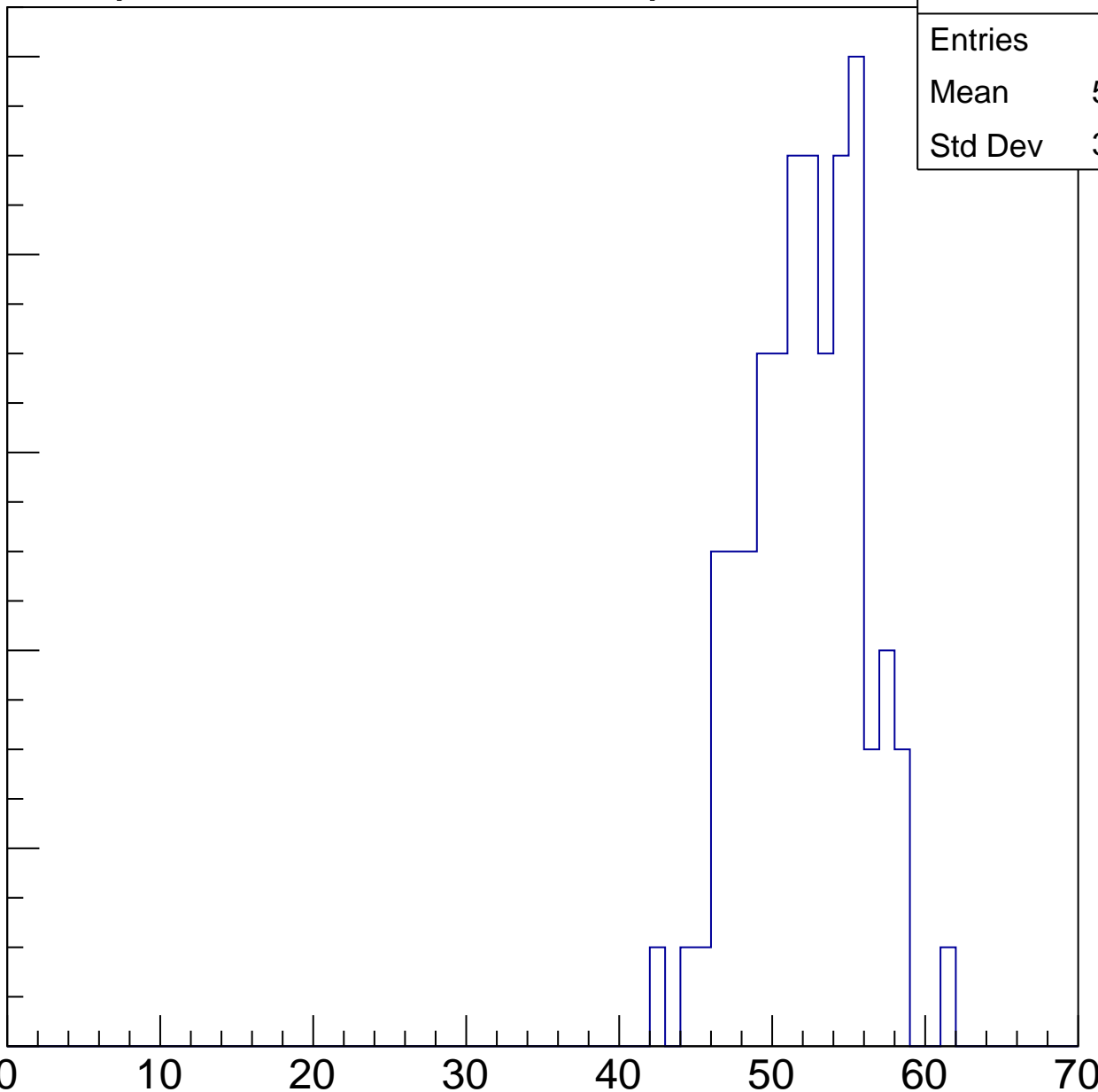
30

40

50

60

ampl

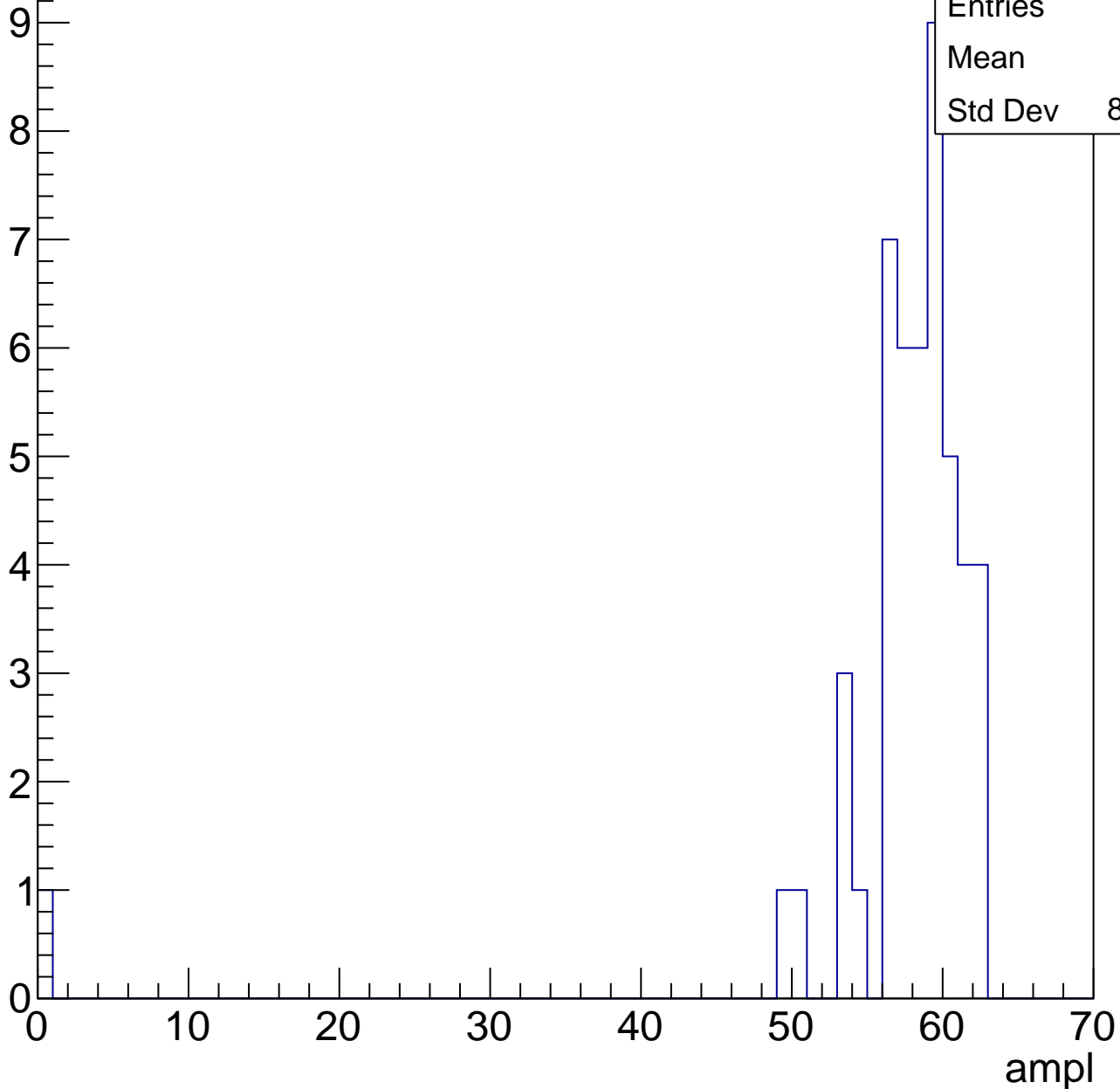


B0L001S, U18-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.6
Std Dev	8.743

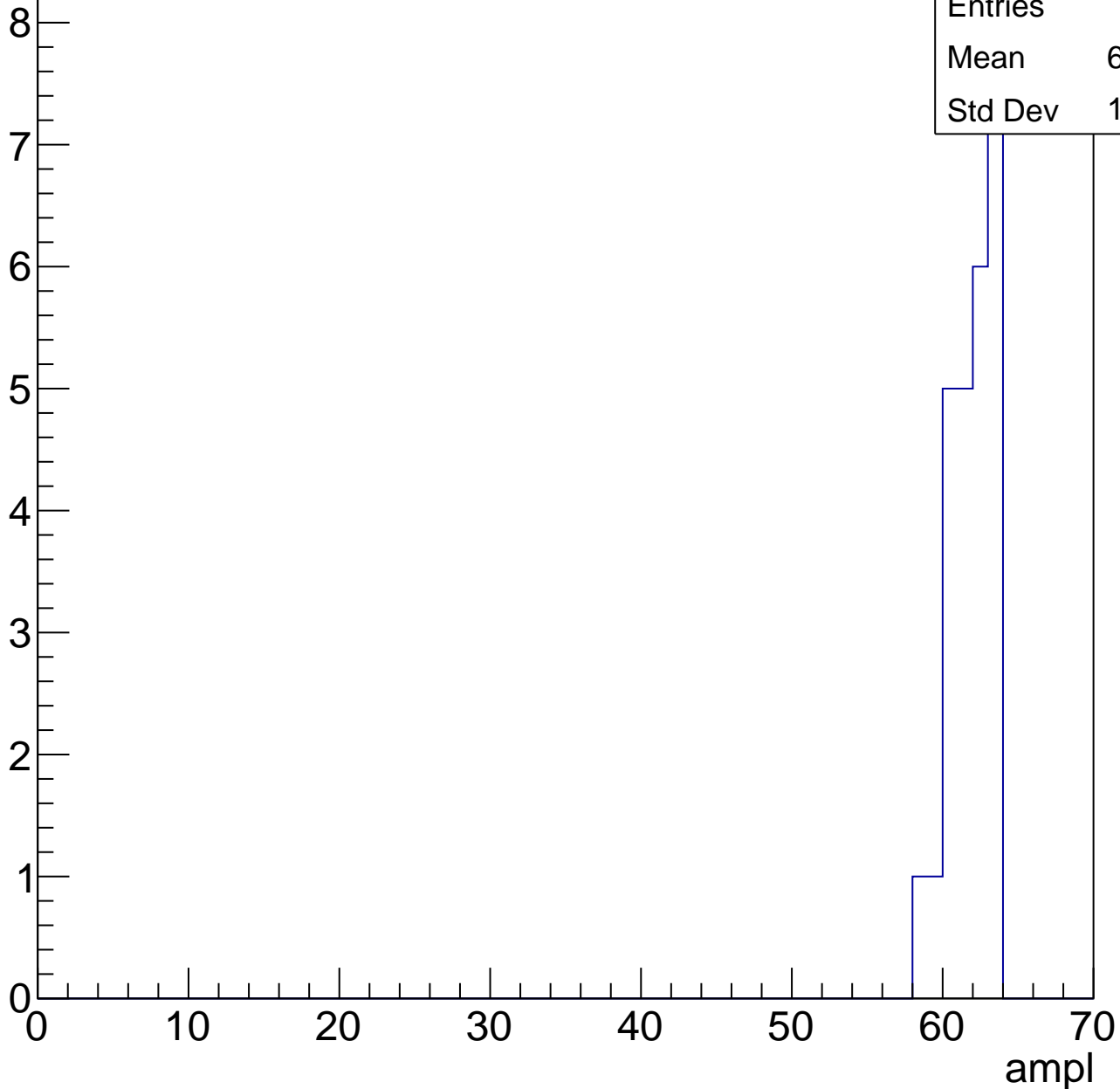


B0L001S, U18-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.46
Std Dev	1.393



B0L001S, U18-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U18-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch59, adc0

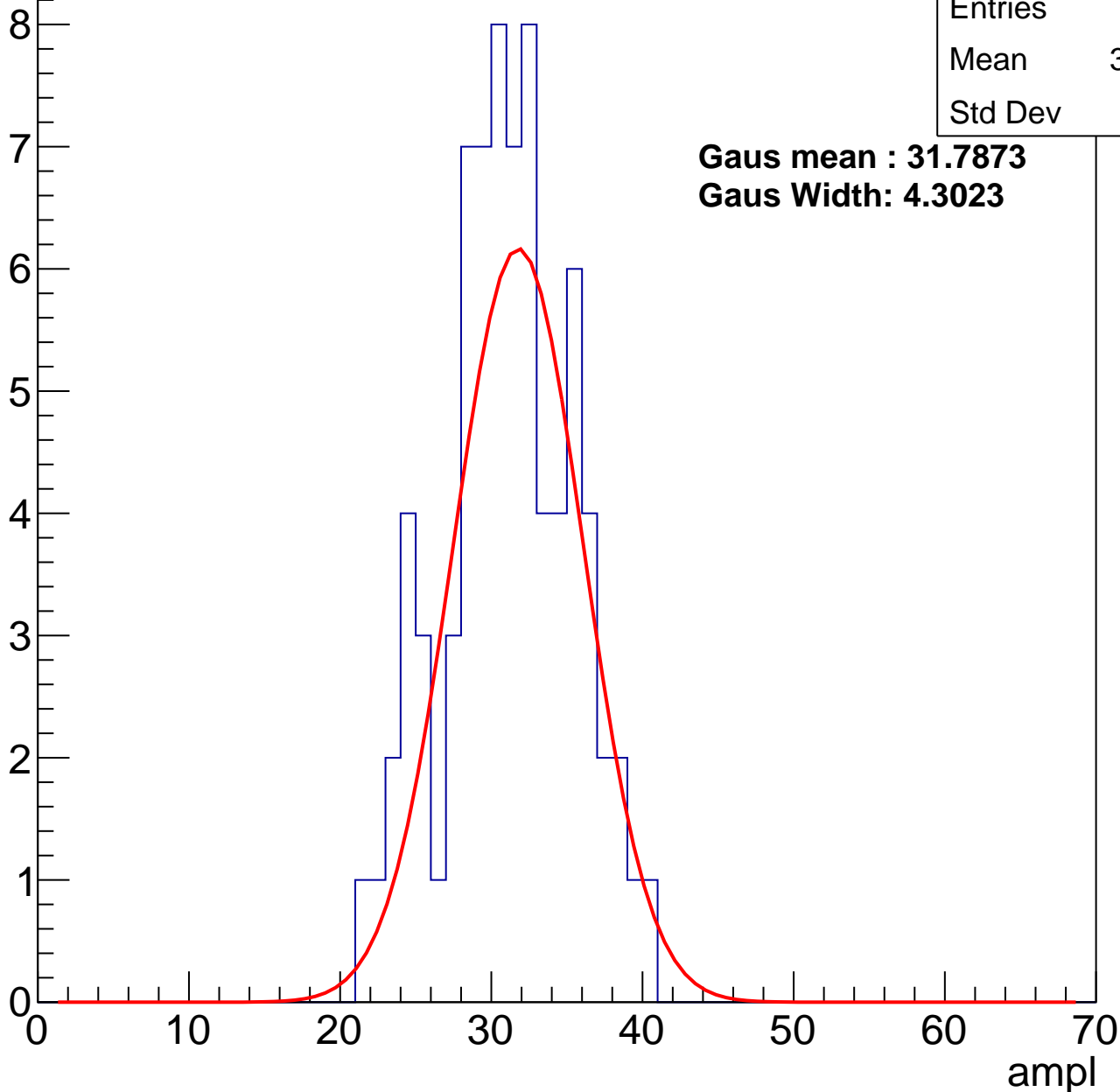
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.66
Std Dev	4.21

Gaus mean : 31.7873

Gaus Width: 4.3023



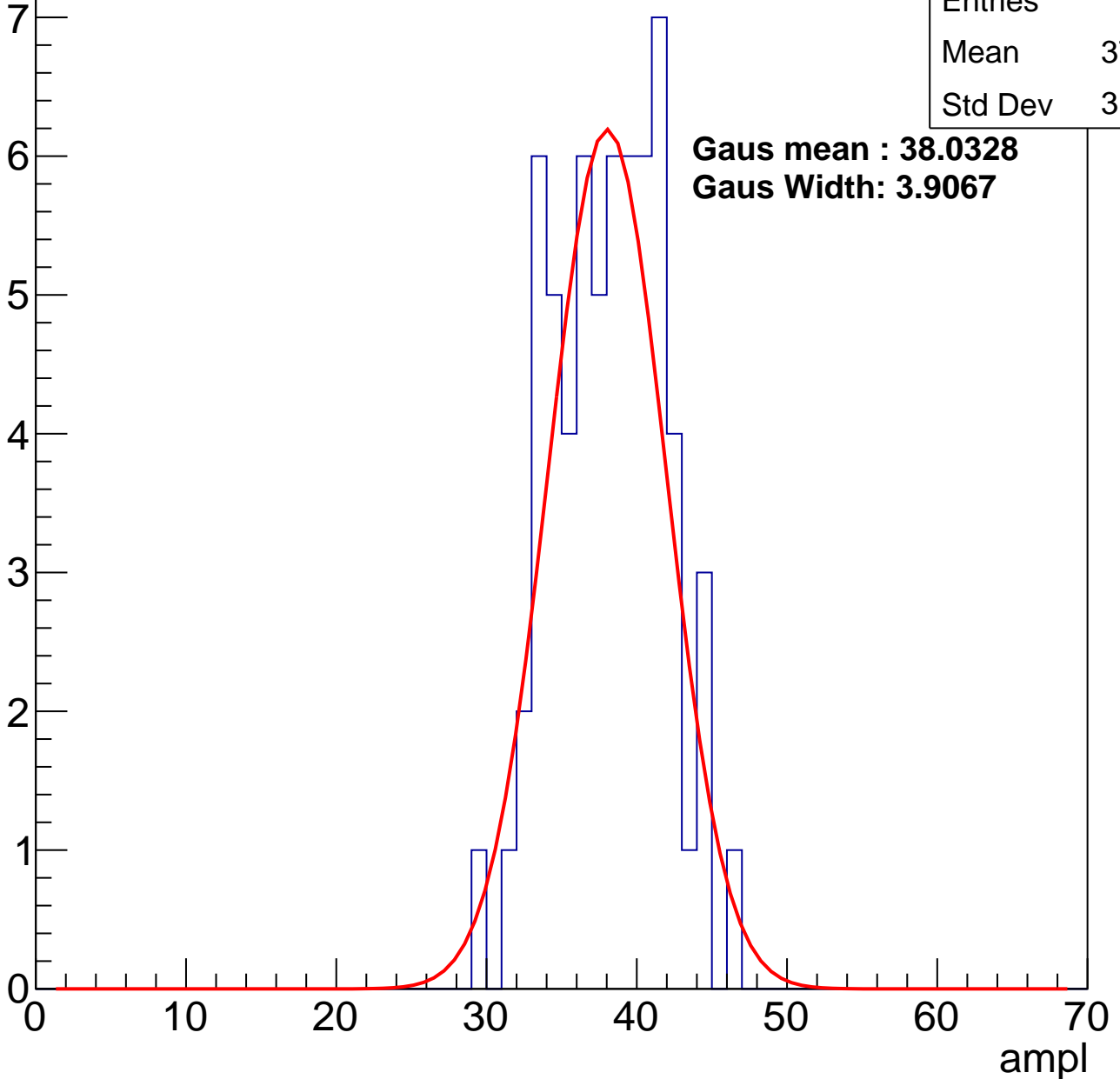
B0L001S, U18-ch59, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.67
Std Dev	3.632

Gaus mean : 38.0328
Gaus Width: 3.9067



B0L001S, U18-ch59, adc2

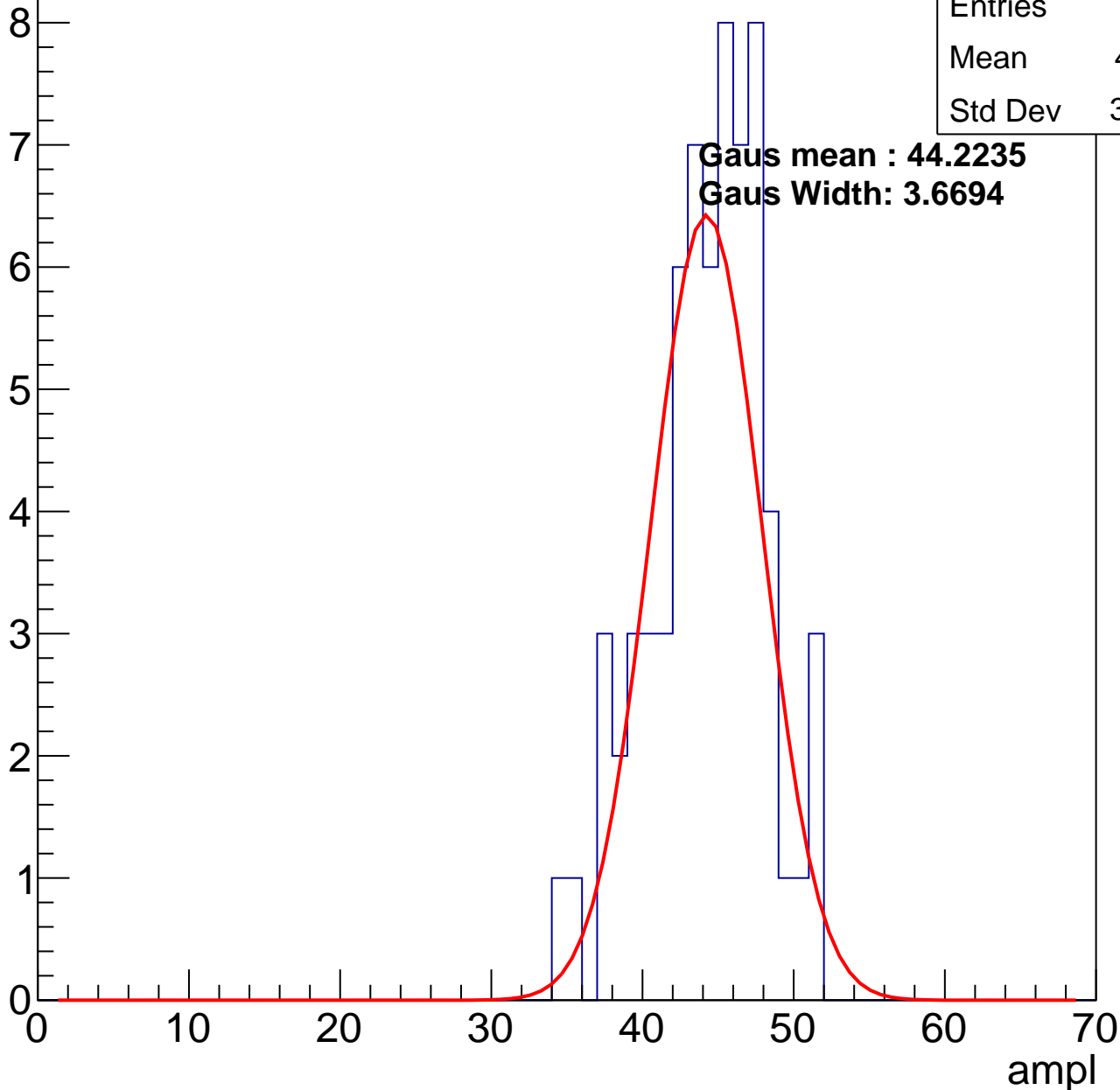
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.81
Std Dev	3.782

Gaus mean : 44.2235

Gaus Width: 3.6694

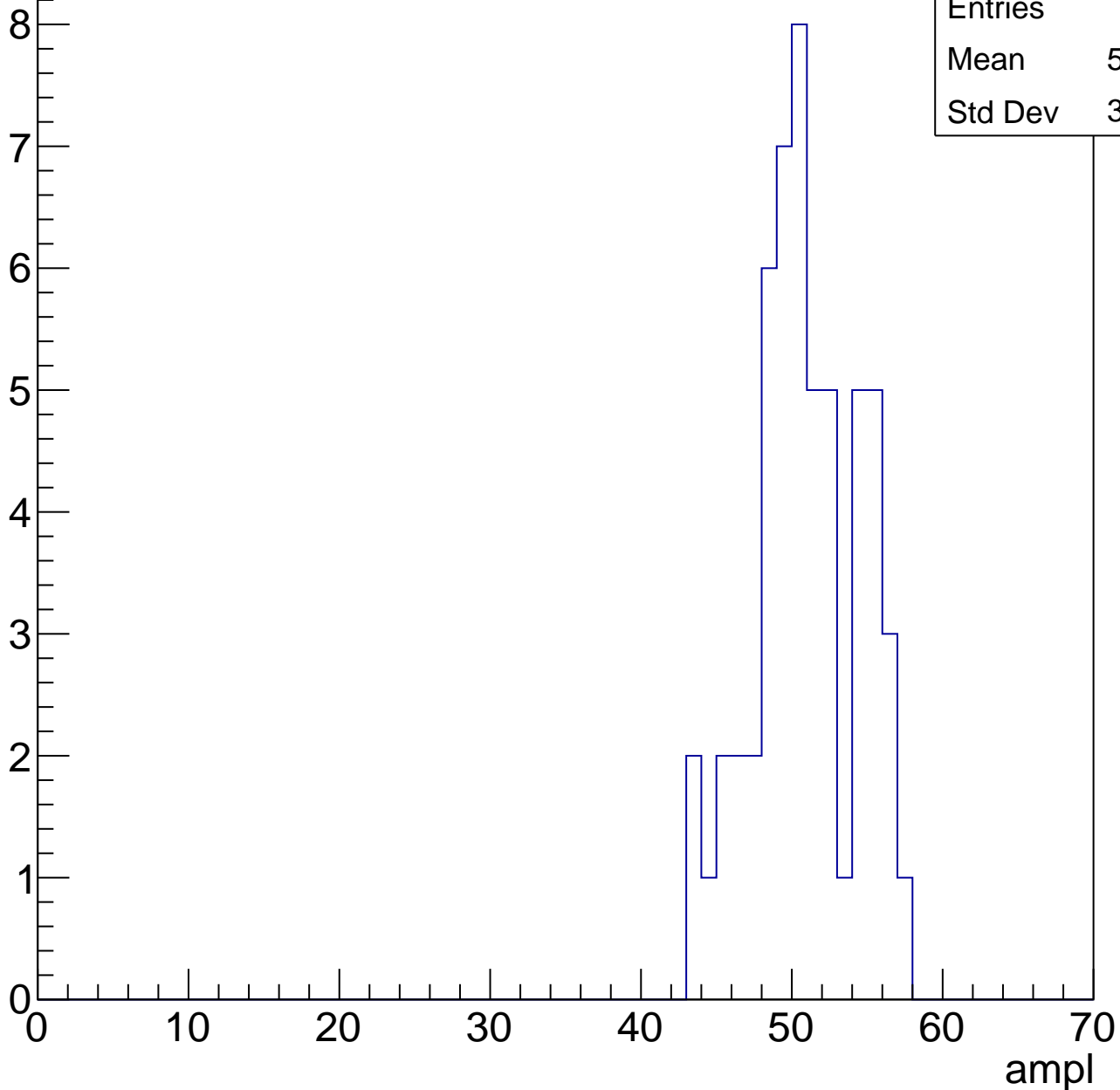


B0L001S, U18-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.45
Std Dev	3.437

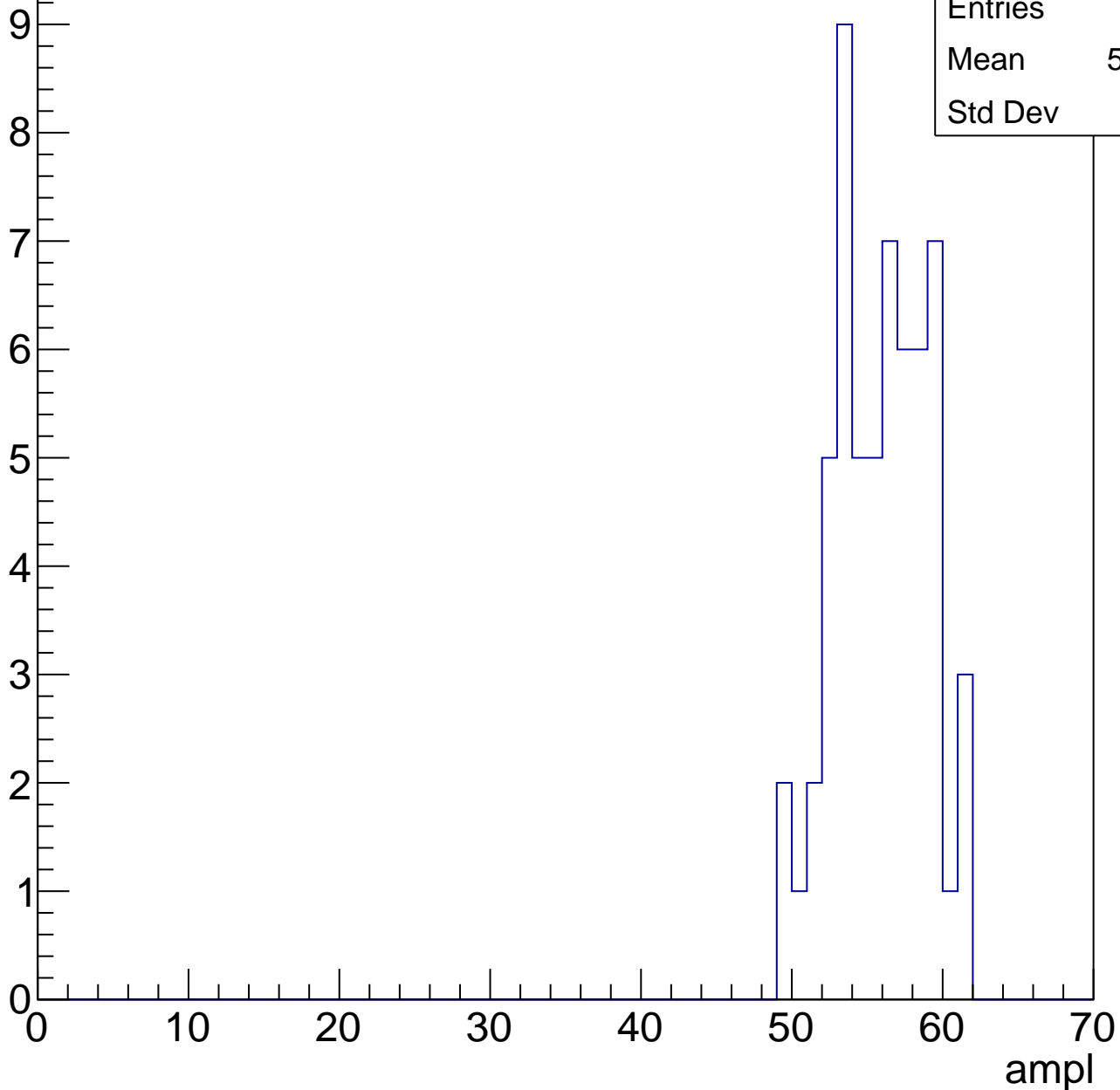


B0L001S, U18-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.42
Std Dev	3.01

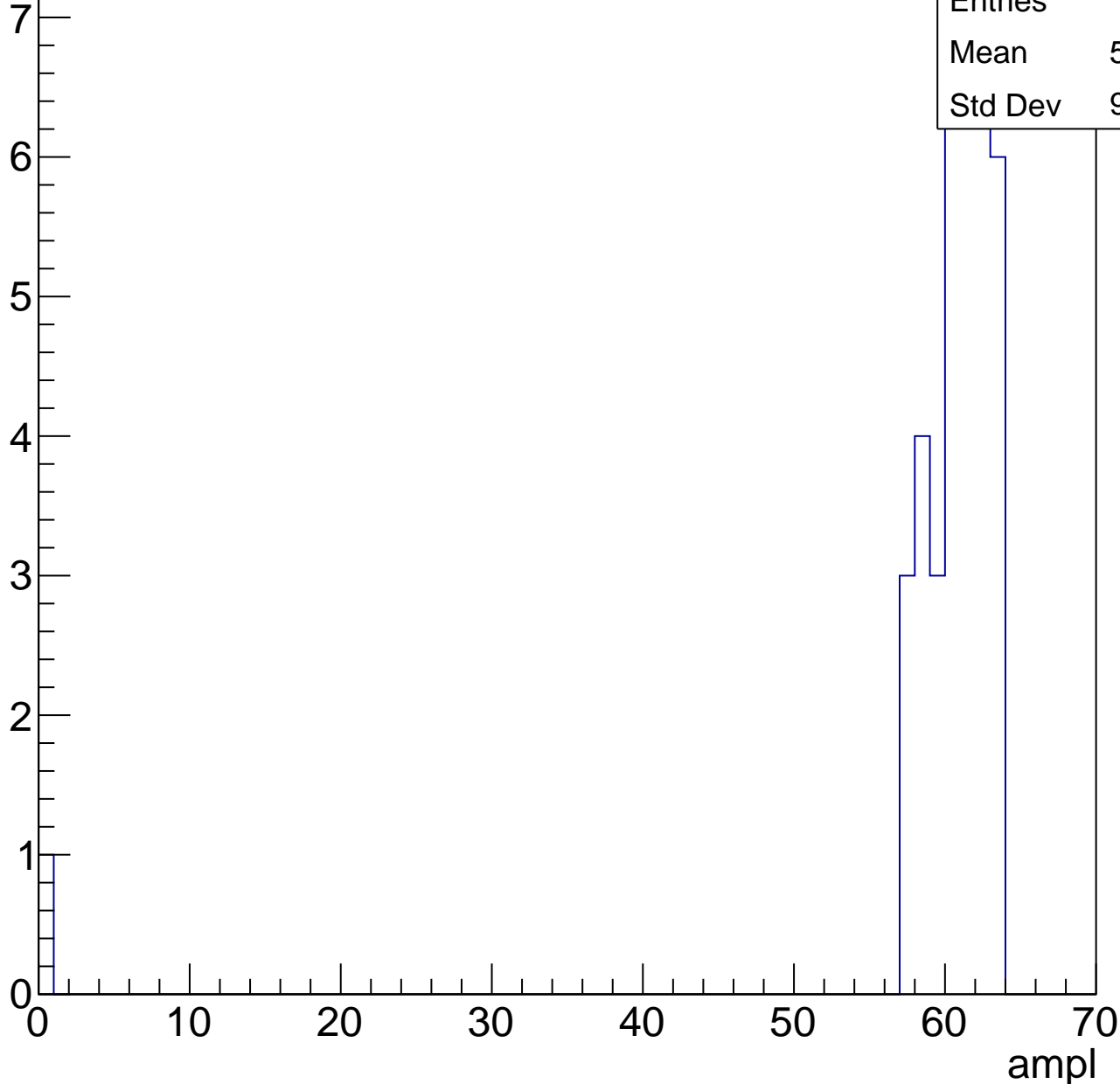


B0L001S, U18-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

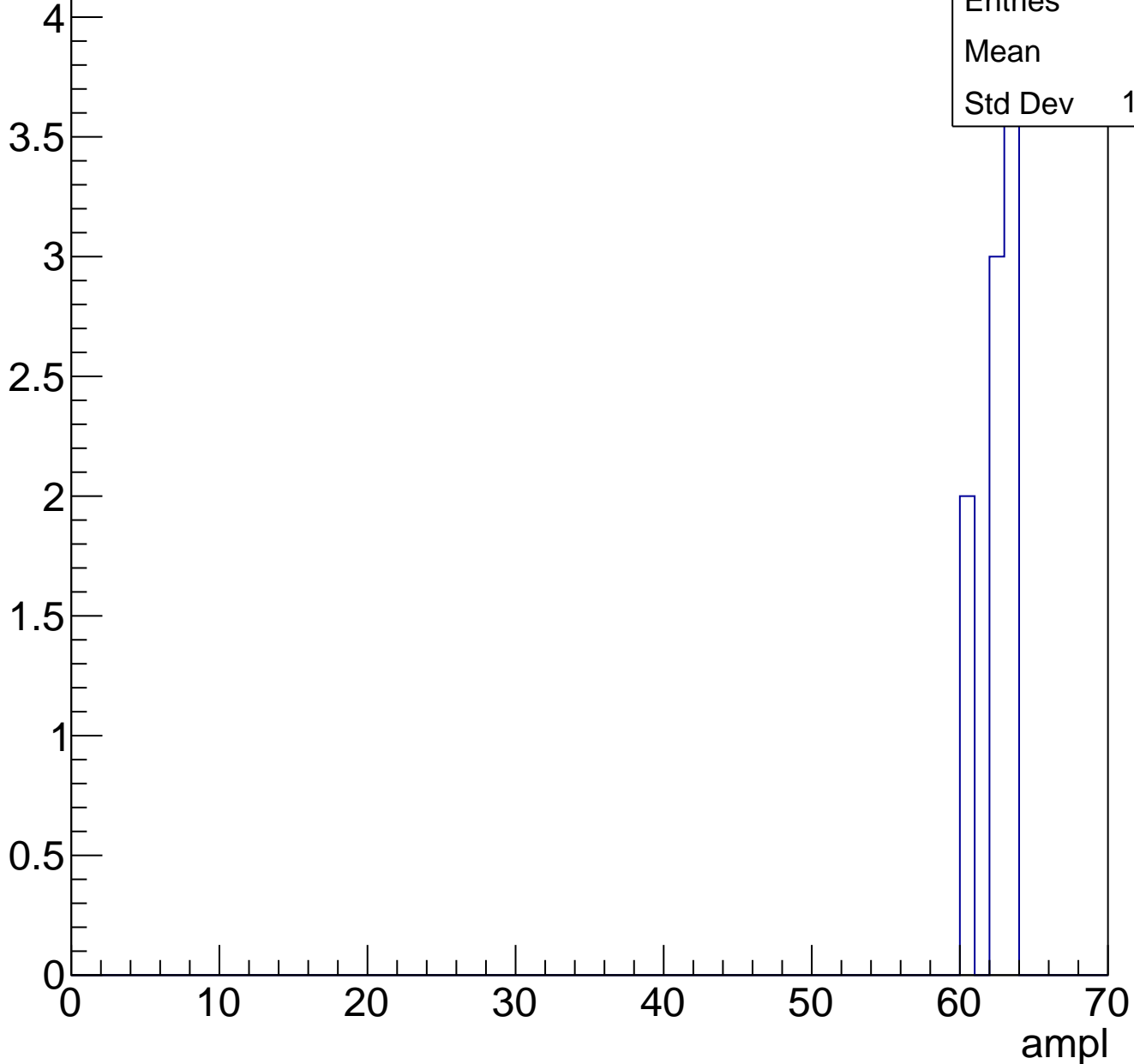
Entries	38
Mean	58.92
Std Dev	9.855



B0L001S, U18-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch60, adc0

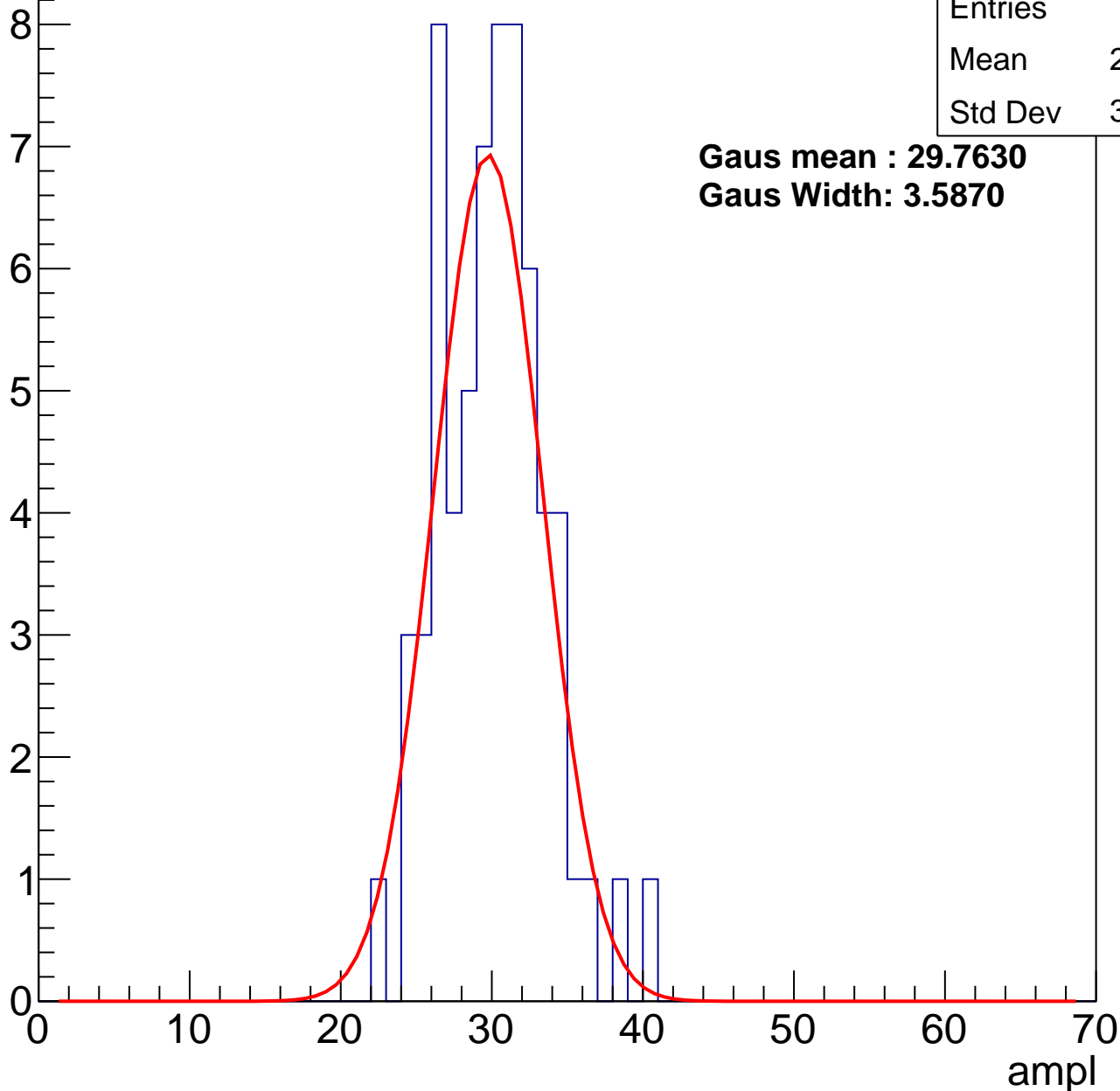
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.62
Std Dev	3.476

Gaus mean : 29.7630

Gaus Width: 3.5870



B0L001S, U18-ch60, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	36.94
Std Dev	3.696

Gaus mean : 36.9682

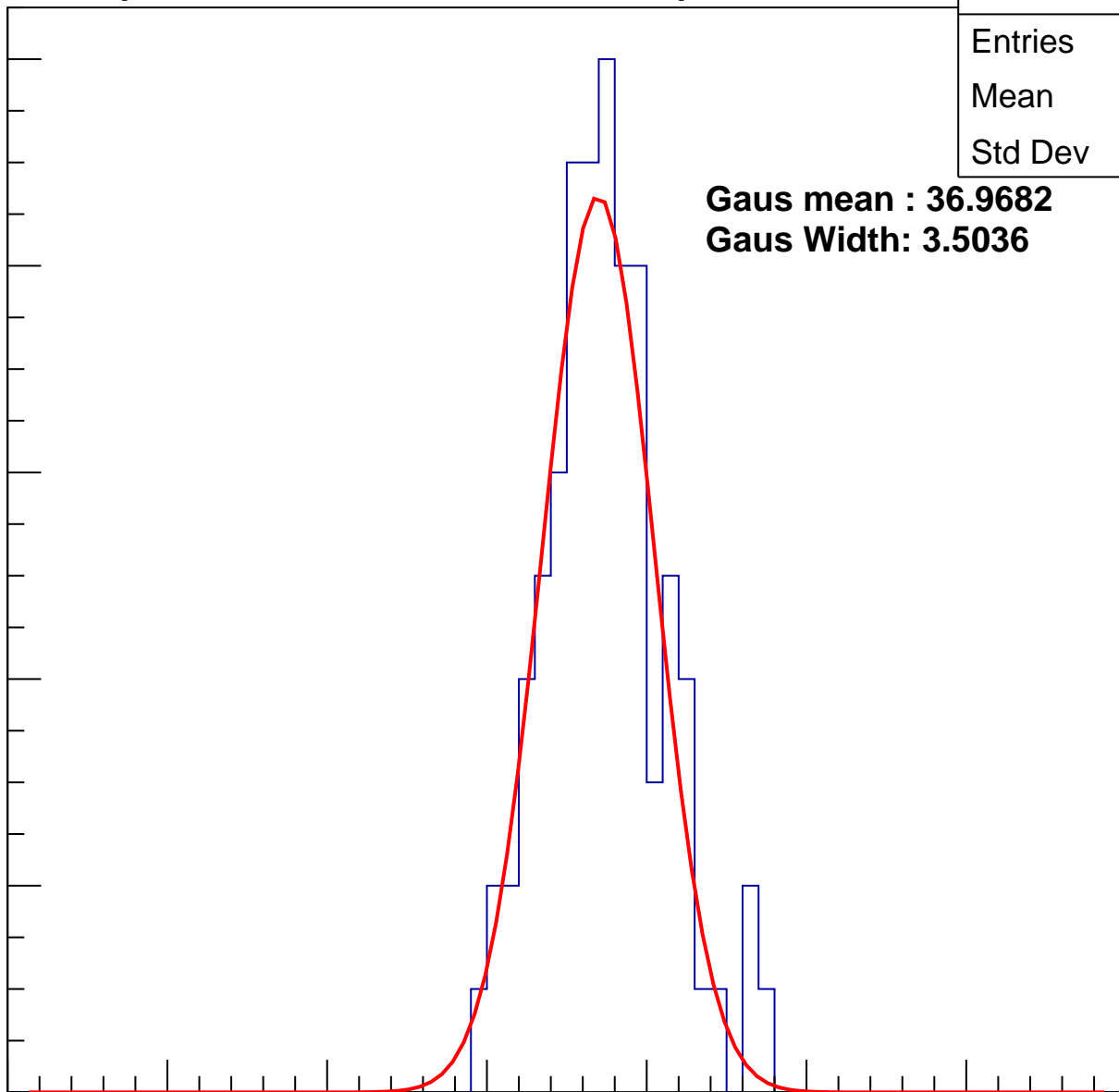
Gaus Width: 3.5036

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

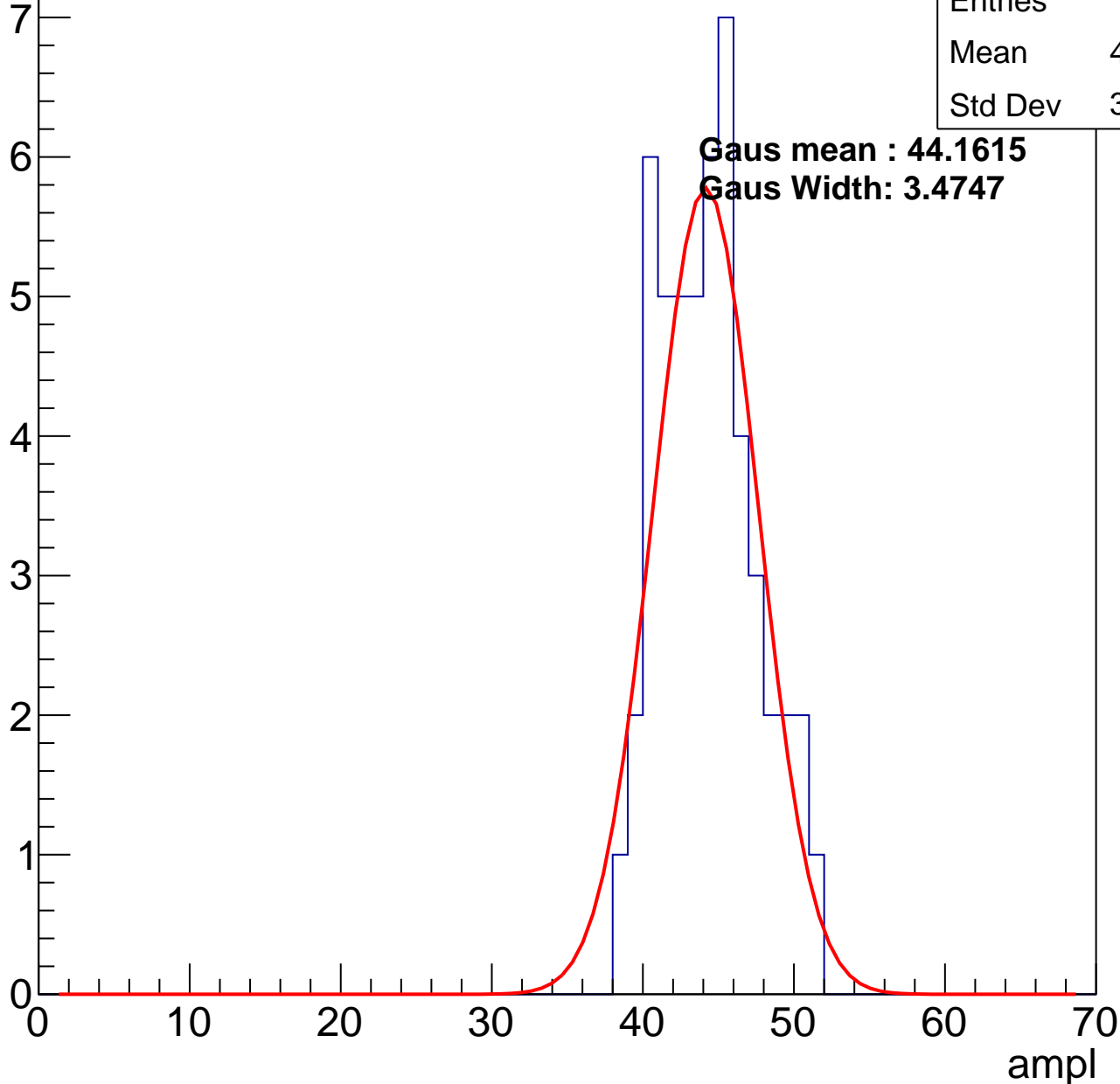


B0L001S, U18-ch60, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	43.82
Std Dev	3.142

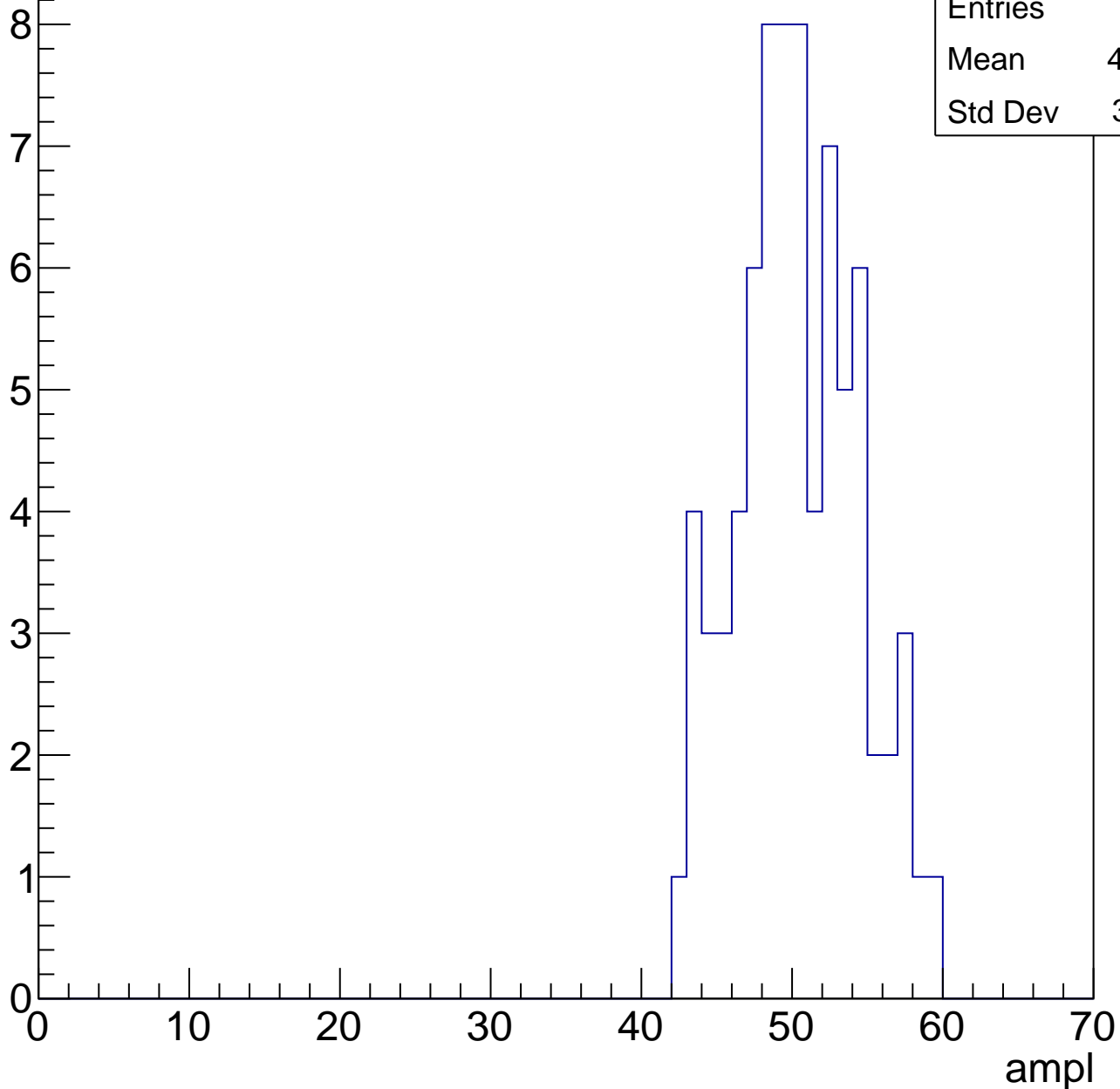


B0L001S, U18-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

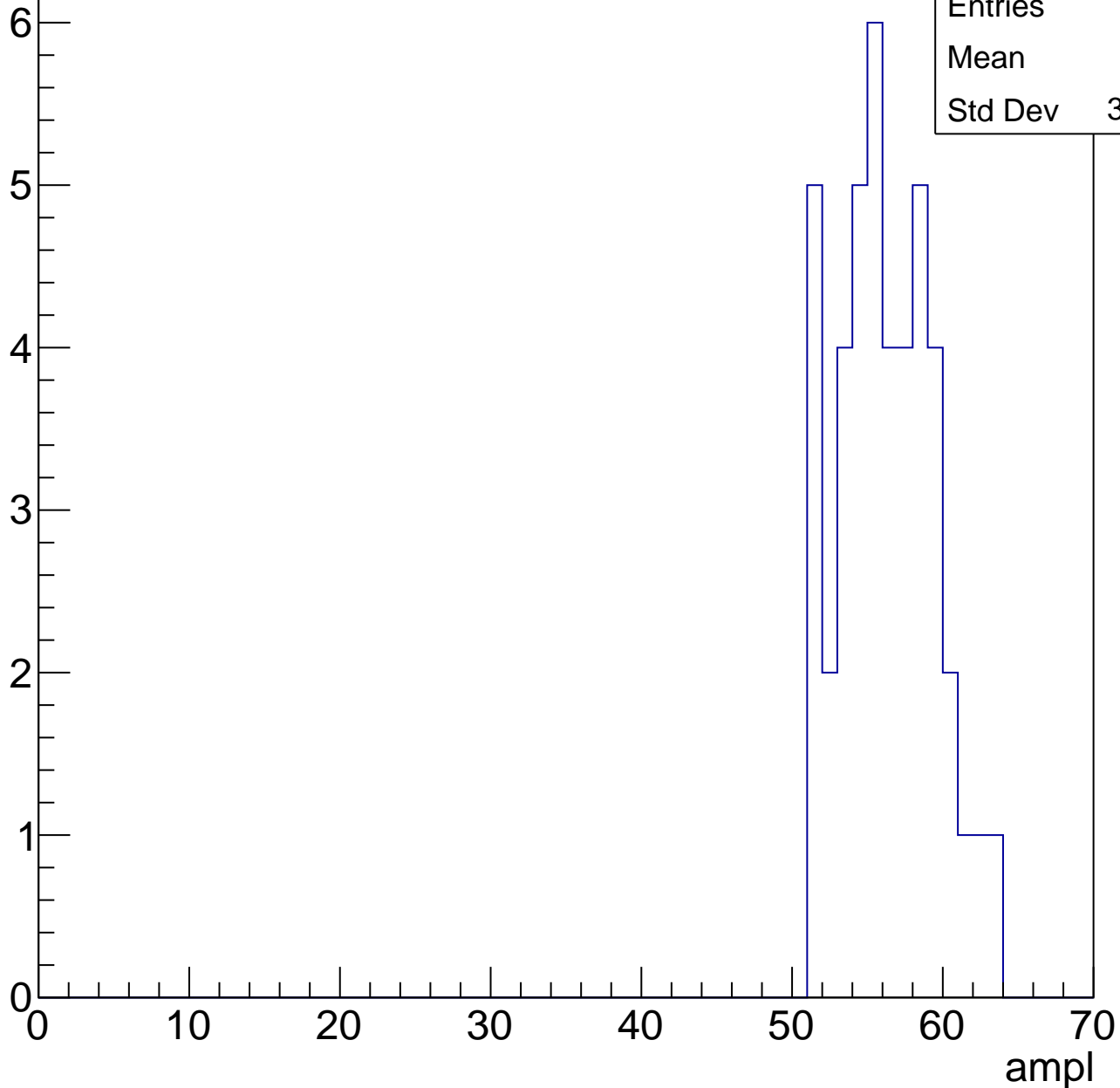
Entries	76
Mean	49.87
Std Dev	3.951



B0L001S, U18-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



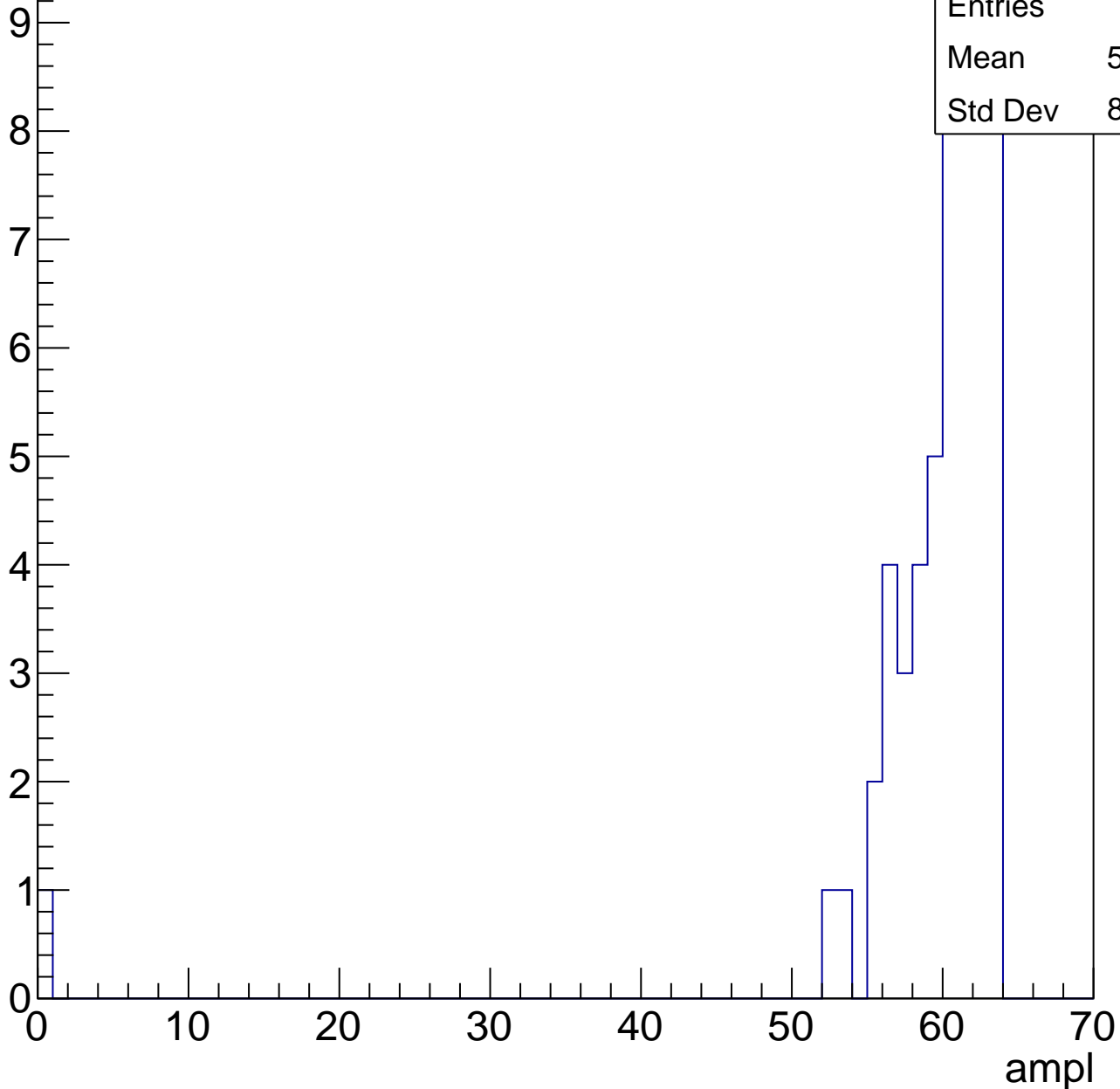
Entries	44
Mean	55.8
Std Dev	3.086

B0L001S, U18-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	58.67
Std Dev	8.498



B0L001S, U18-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch61, adc0

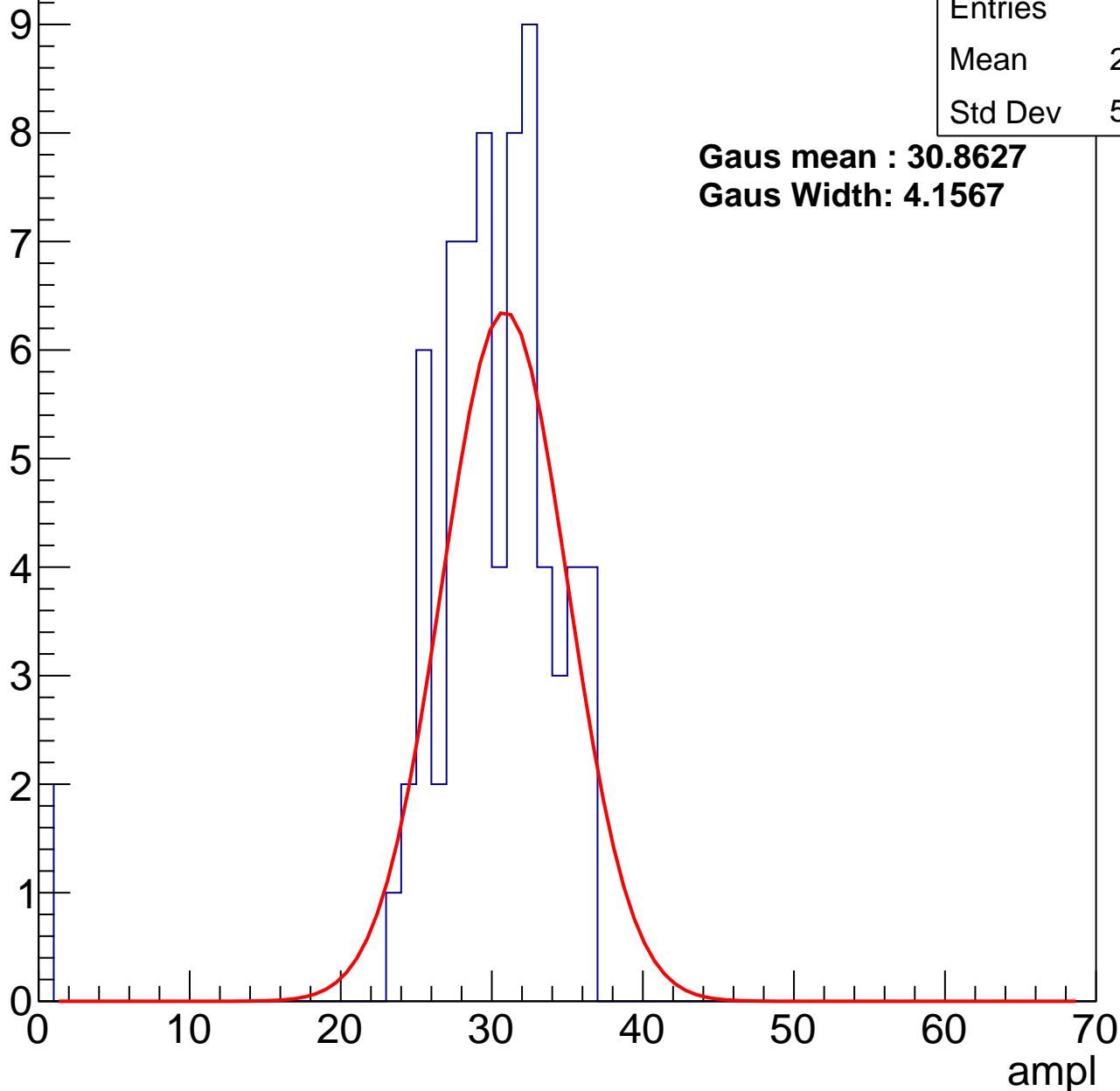
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.07
Std Dev	5.949

Gaus mean : 30.8627

Gaus Width: 4.1567



B0L001S, U18-ch61, adc1

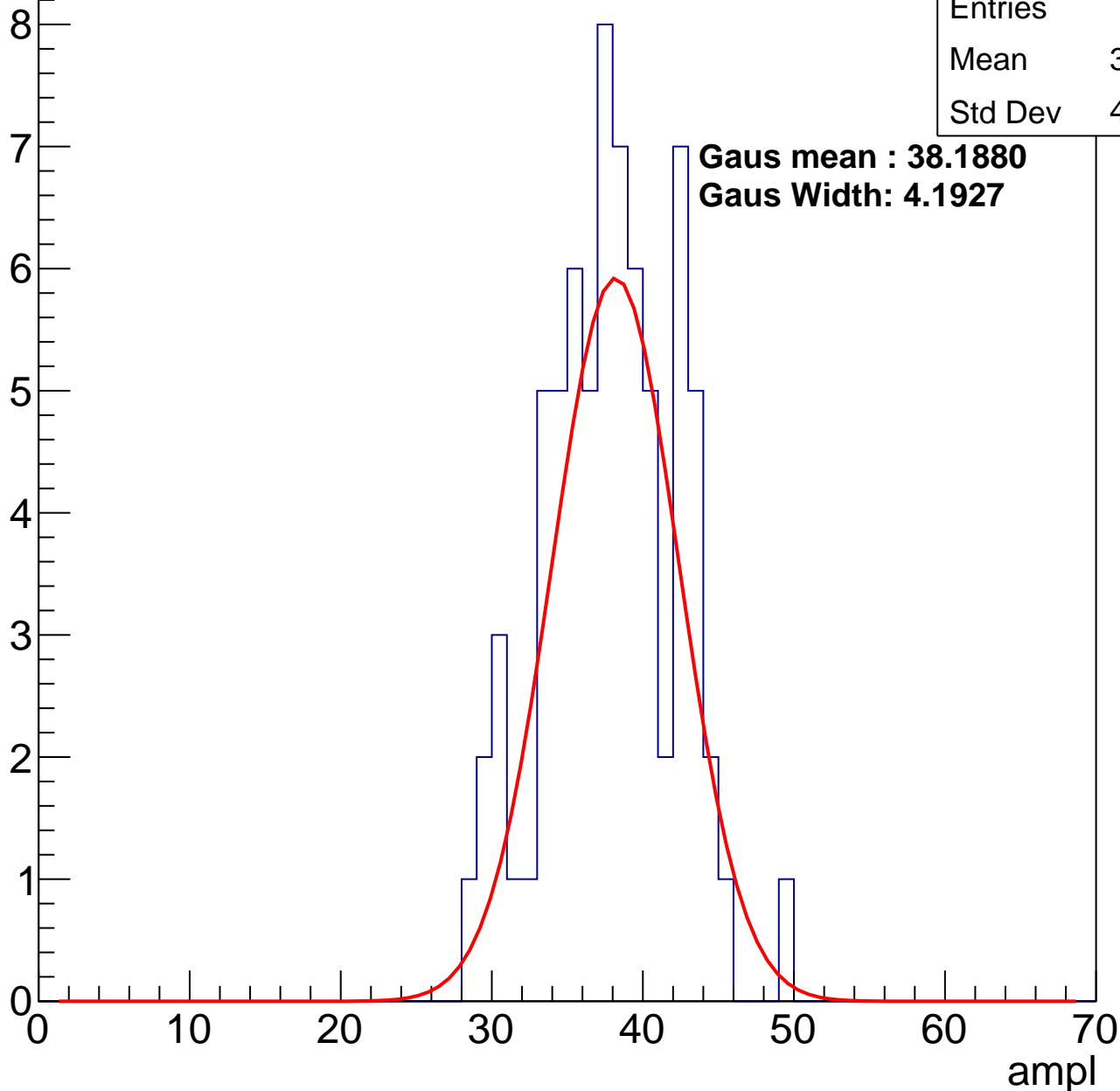
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.44
Std Dev	4.262

Gaus mean : 38.1880

Gaus Width: 4.1927



B0L001S, U18-ch61, adc2

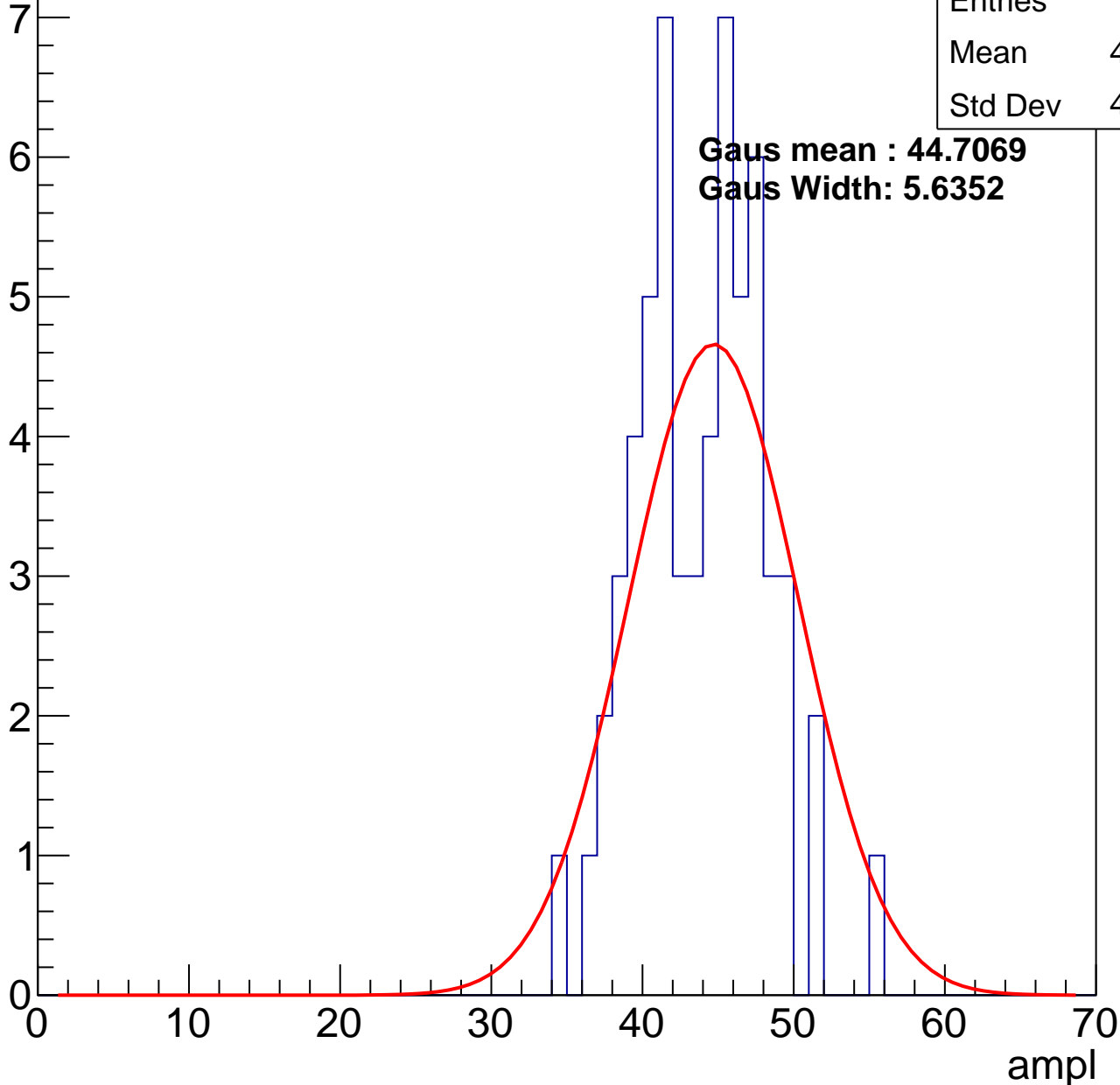
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.45
Std Dev	4.149

Gaus mean : 44.7069

Gaus Width: 5.6352

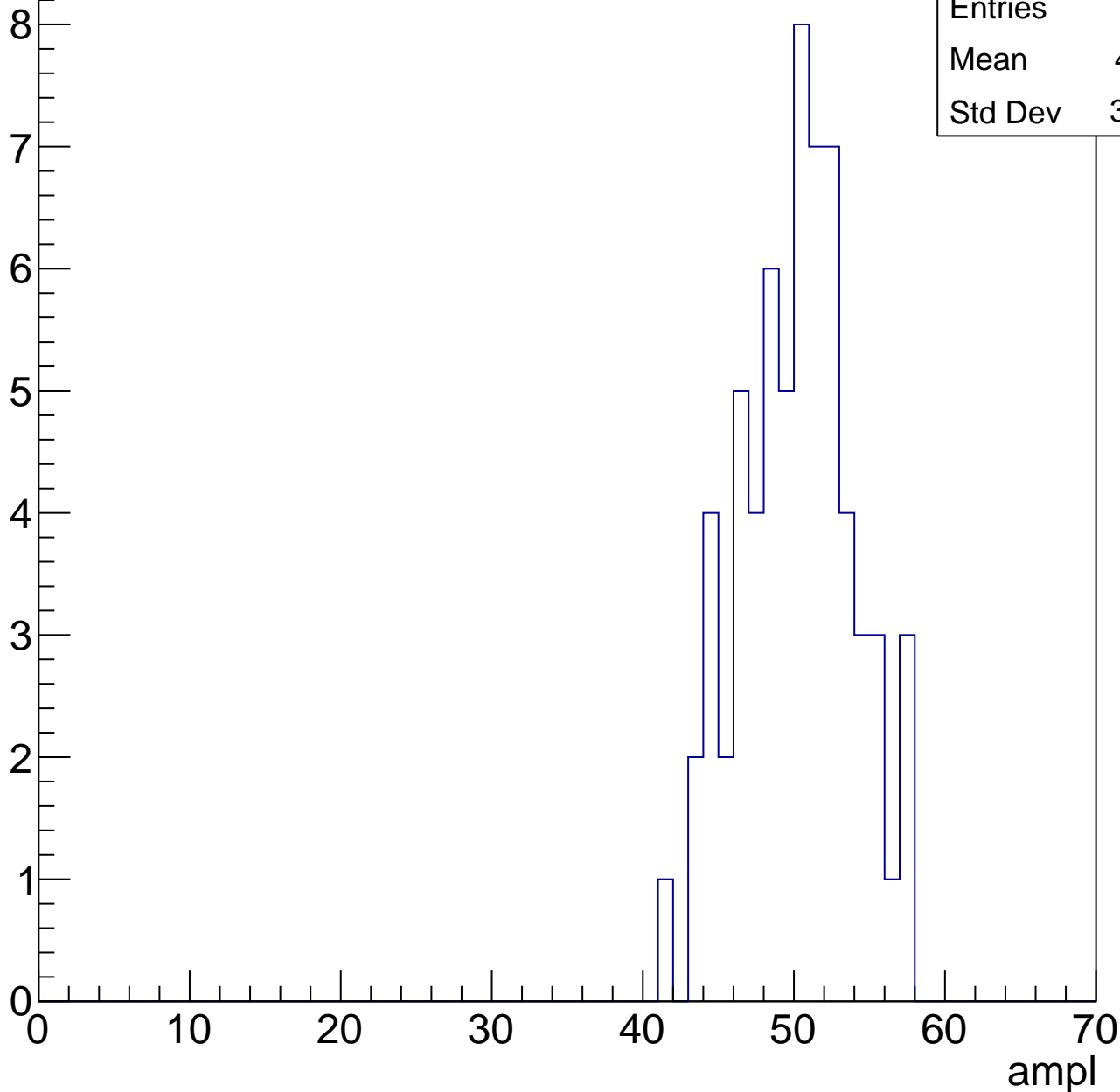


B0L001S, U18-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.71
Std Dev	3.699

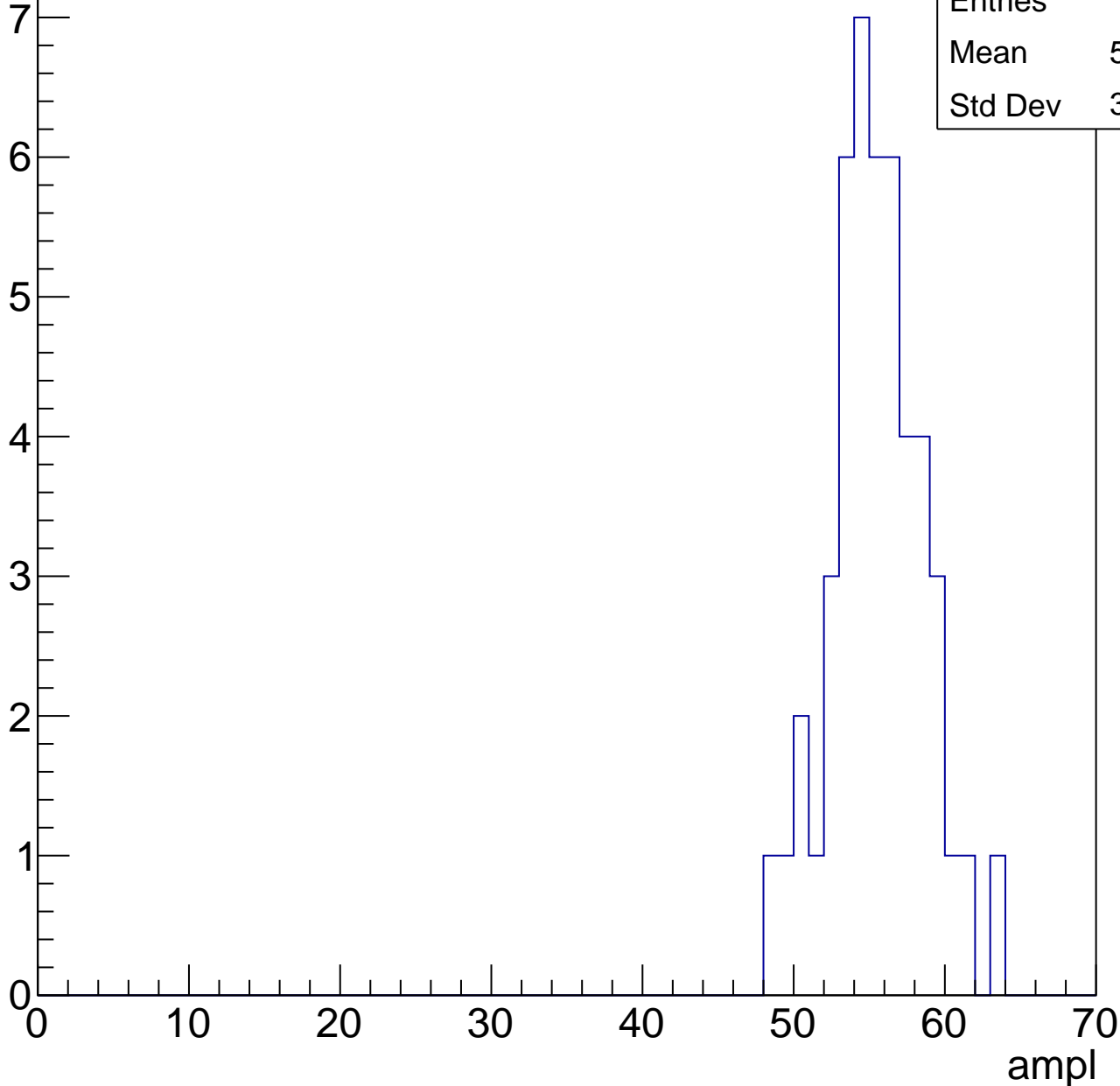


B0L001S, U18-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	55.04
Std Dev	3.059

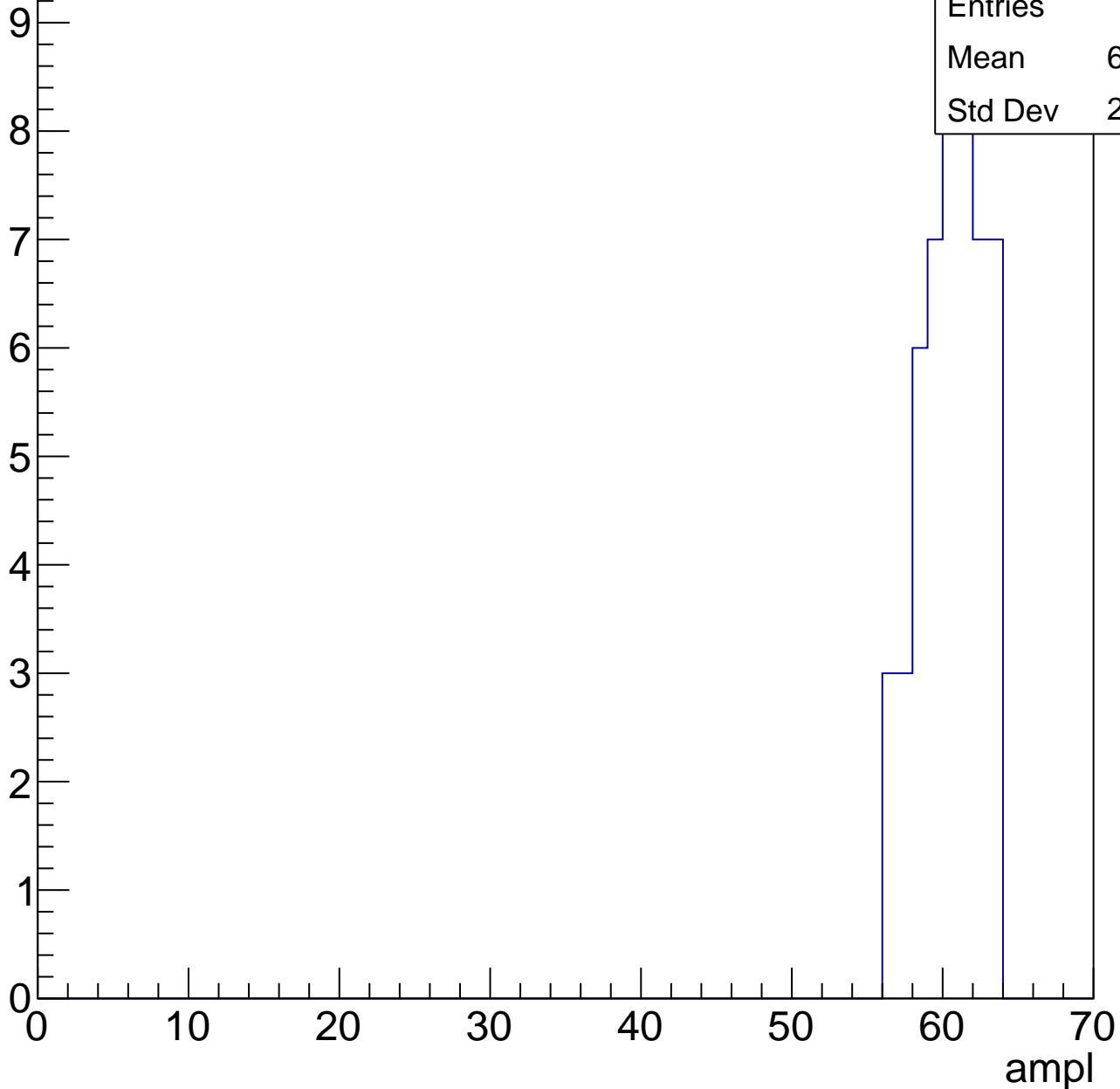


B0L001S, U18-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

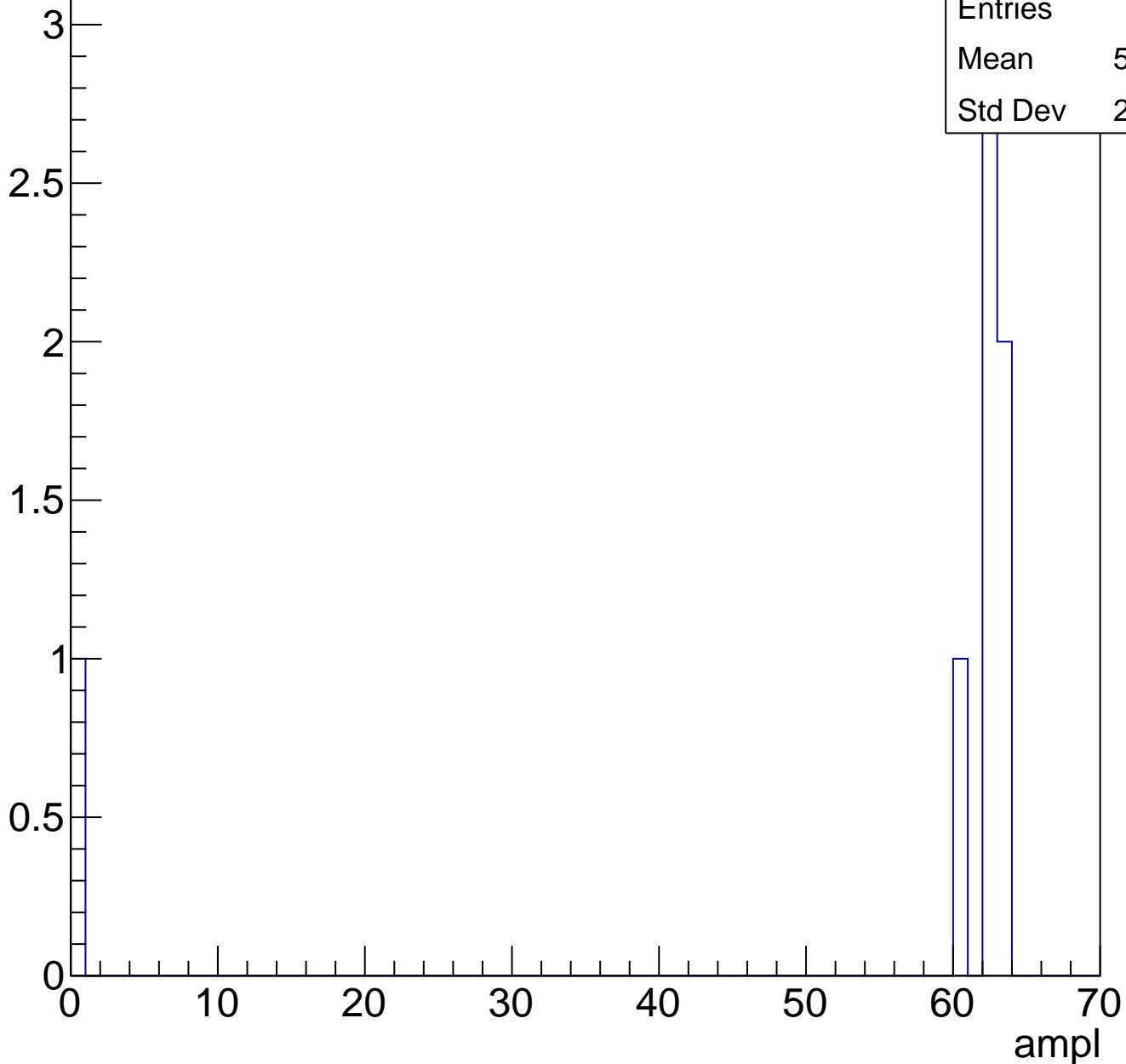
Entries	50
Mean	60.08
Std Dev	2.028



B0L001S, U18-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch62, adc0

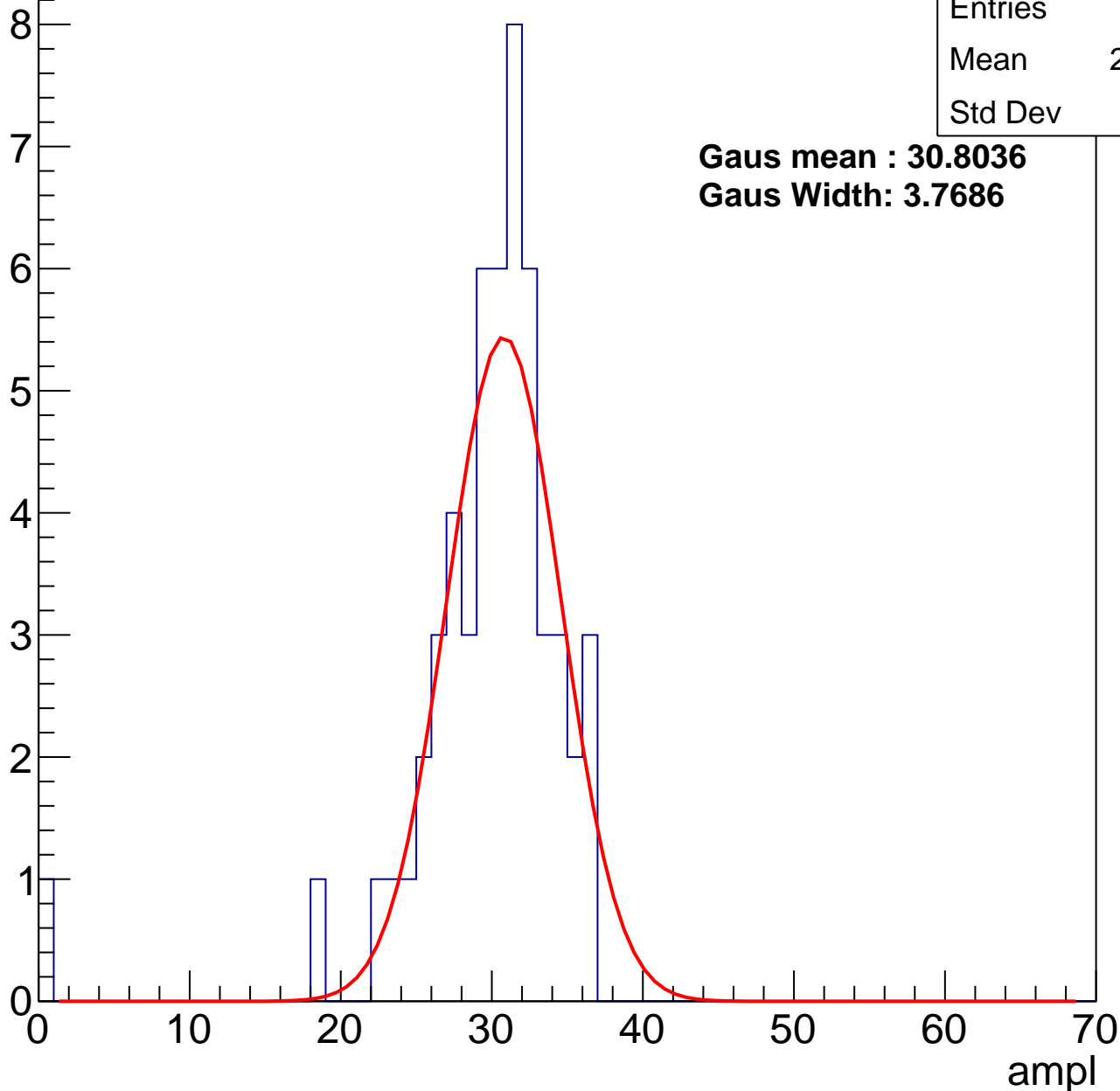
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.26
Std Dev	5.41

Gaus mean : 30.8036

Gaus Width: 3.7686



B0L001S, U18-ch62, adc1

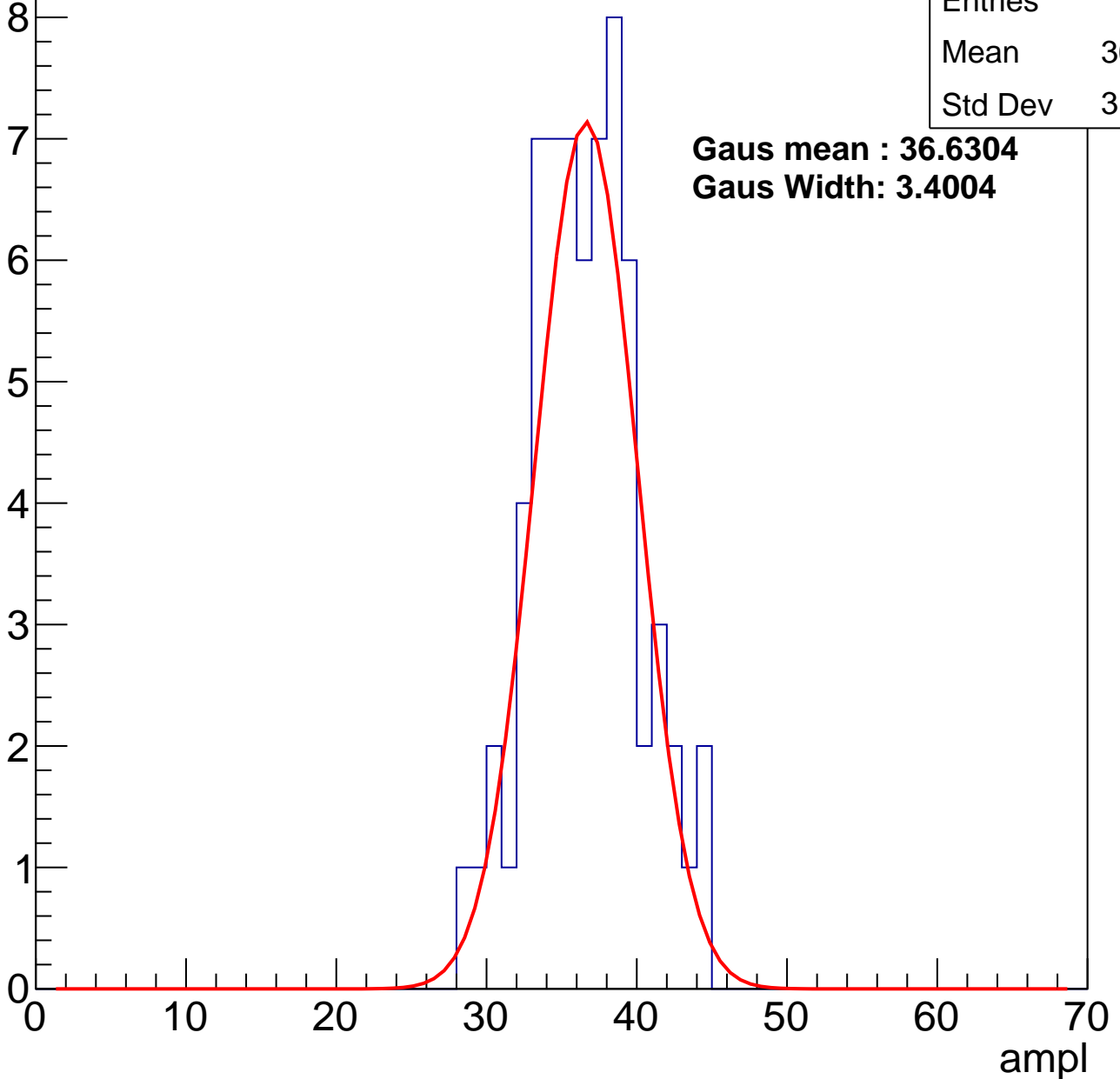
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.13
Std Dev	3.498

Gaus mean : 36.6304

Gaus Width: 3.4004



B0L001S, U18-ch62, adc2

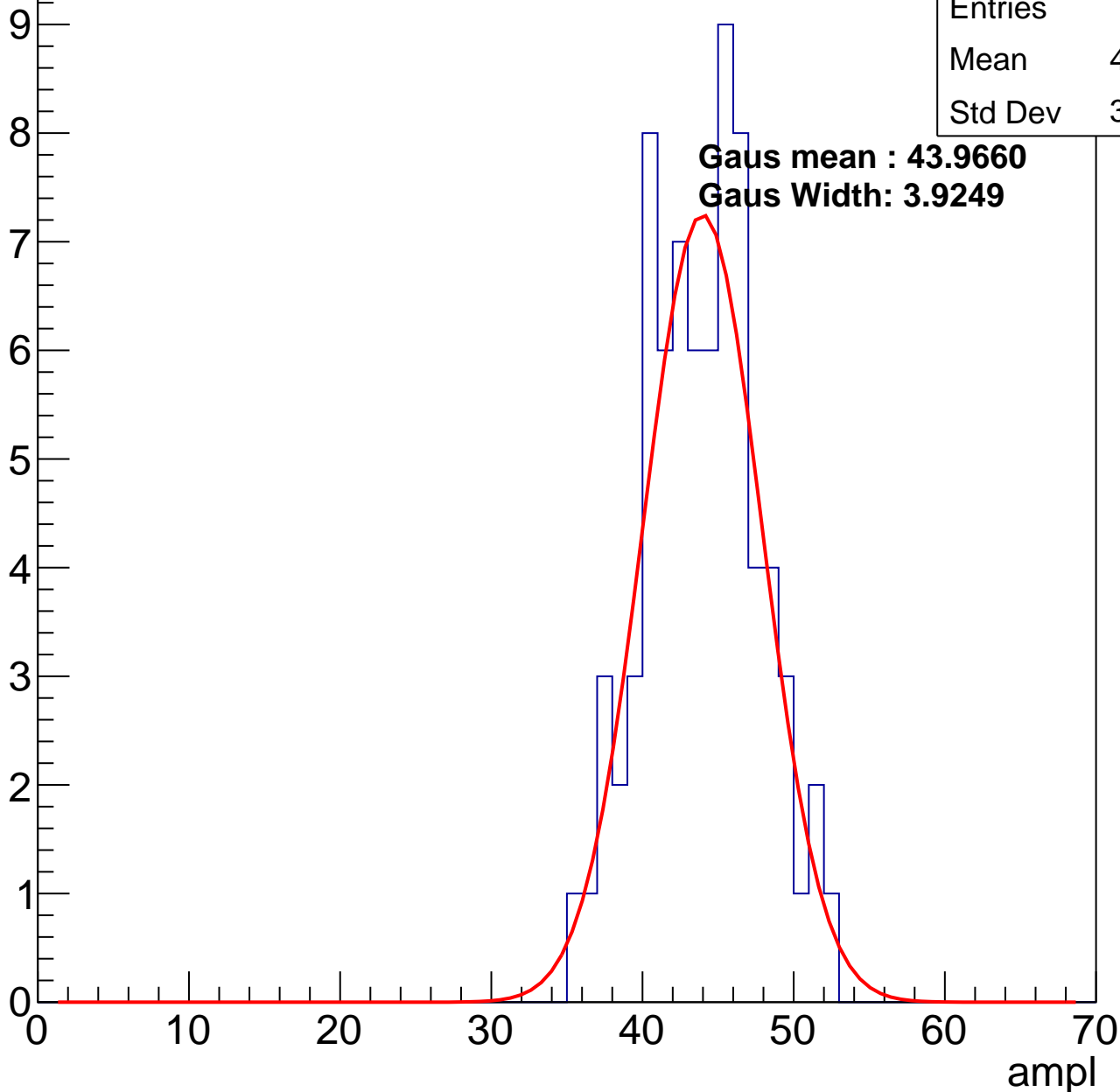
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.48
Std Dev	3.739

Gaus mean : 43.9660

Gaus Width: 3.9249

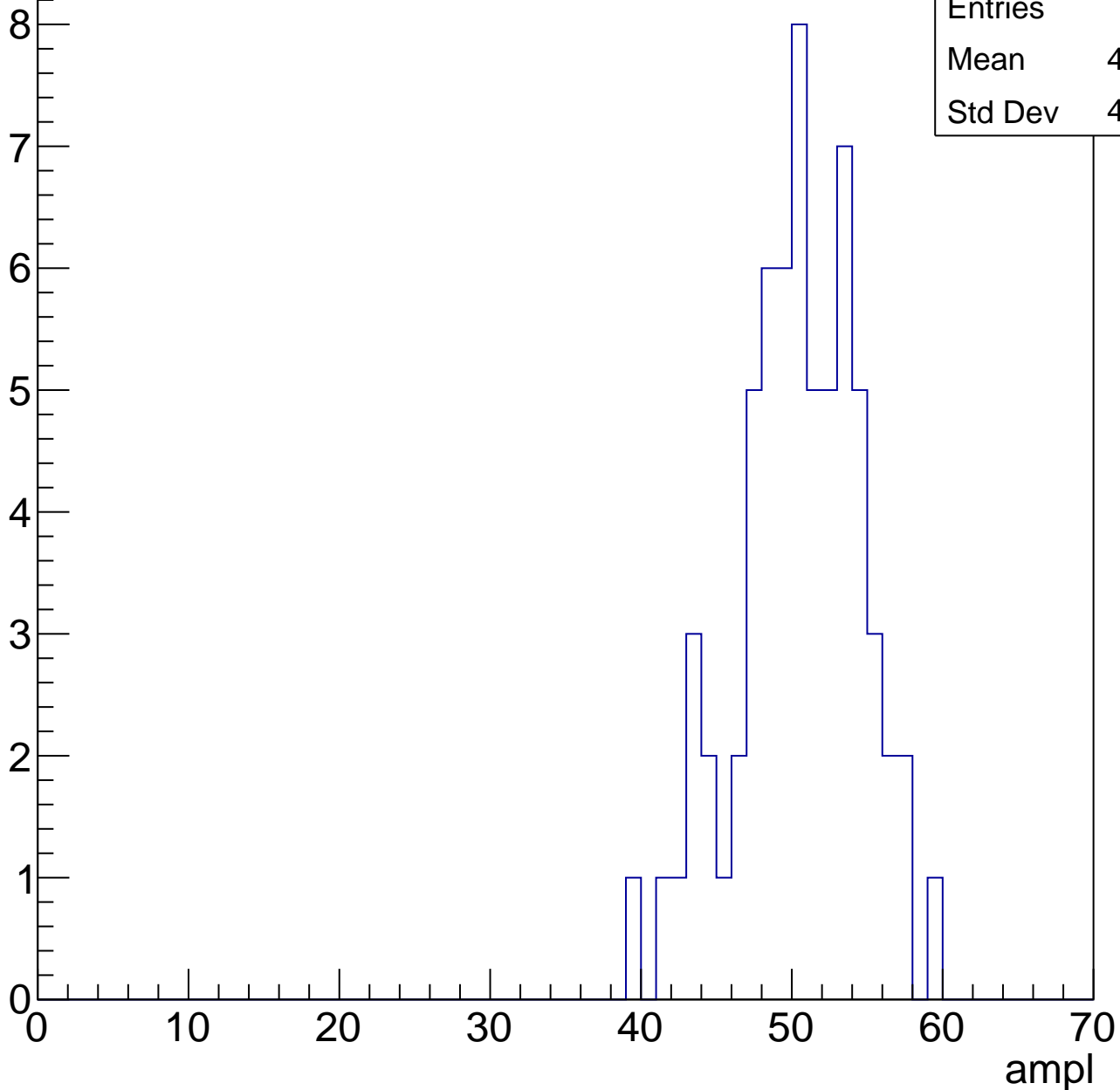


B0L001S, U18-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

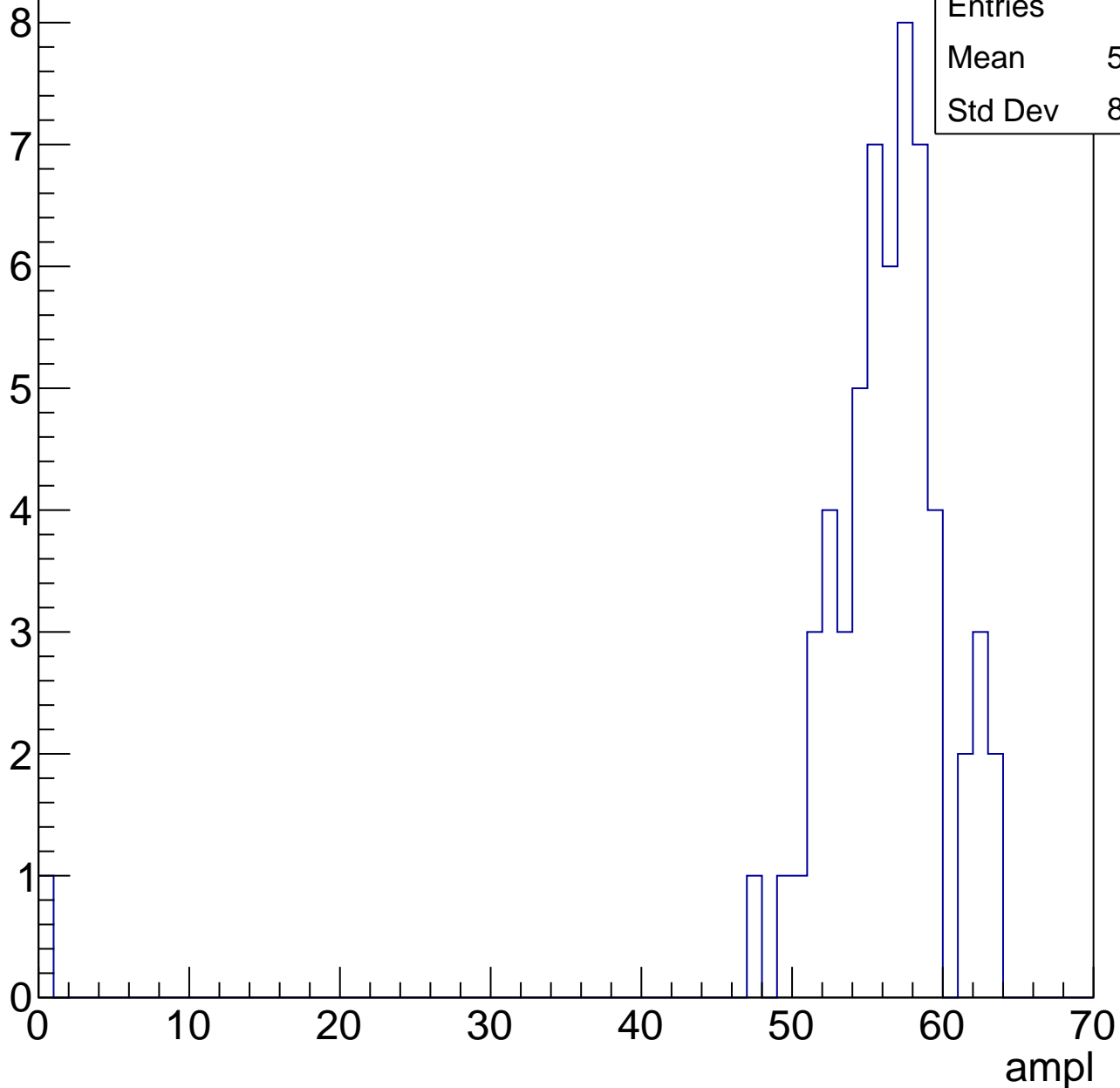
Entries	66
Mean	49.98
Std Dev	4.103



B0L001S, U18-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch62, adc5

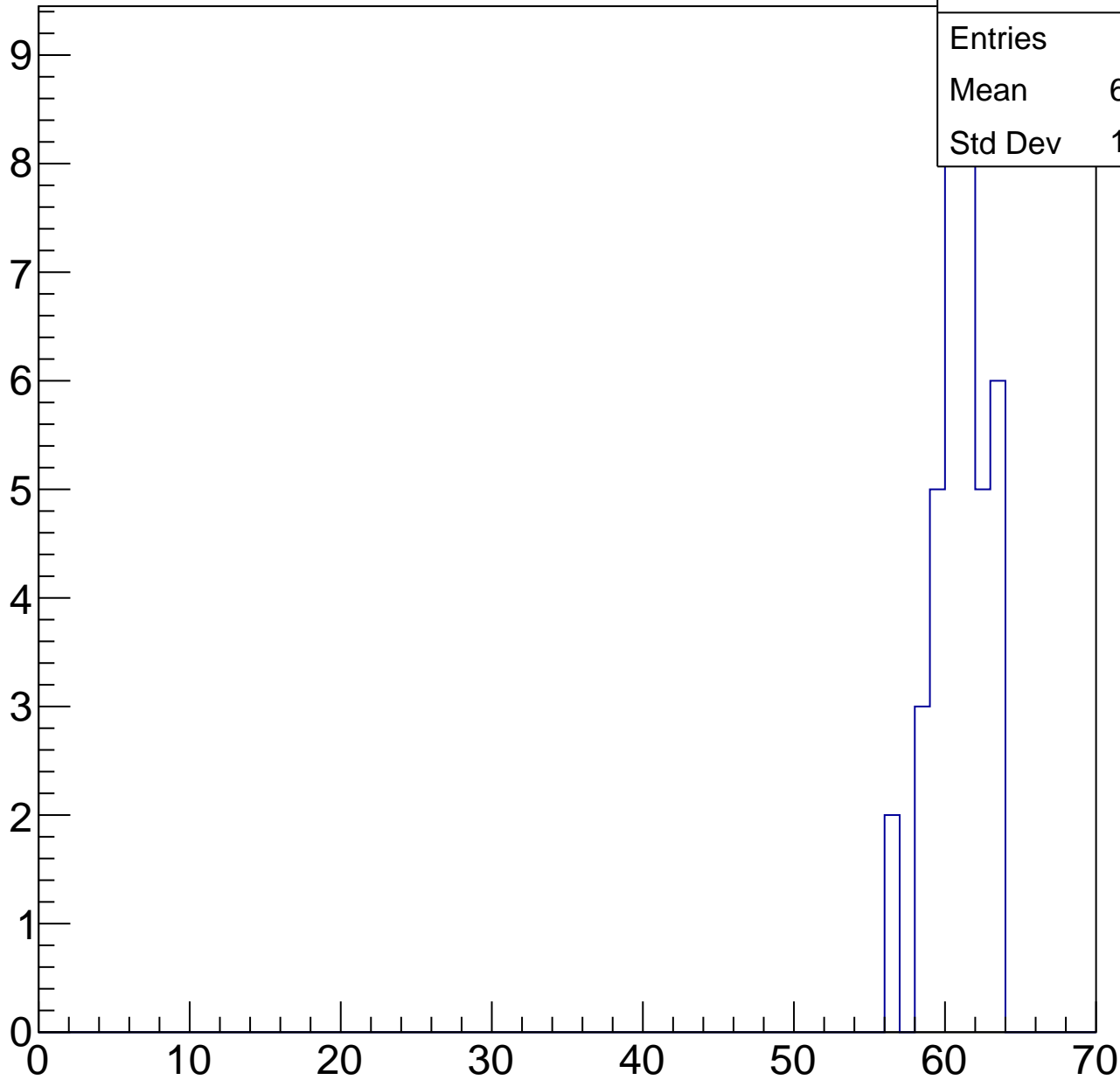
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.45
Std Dev	1.802

ampl



B0L001S, U18-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch63, adc0

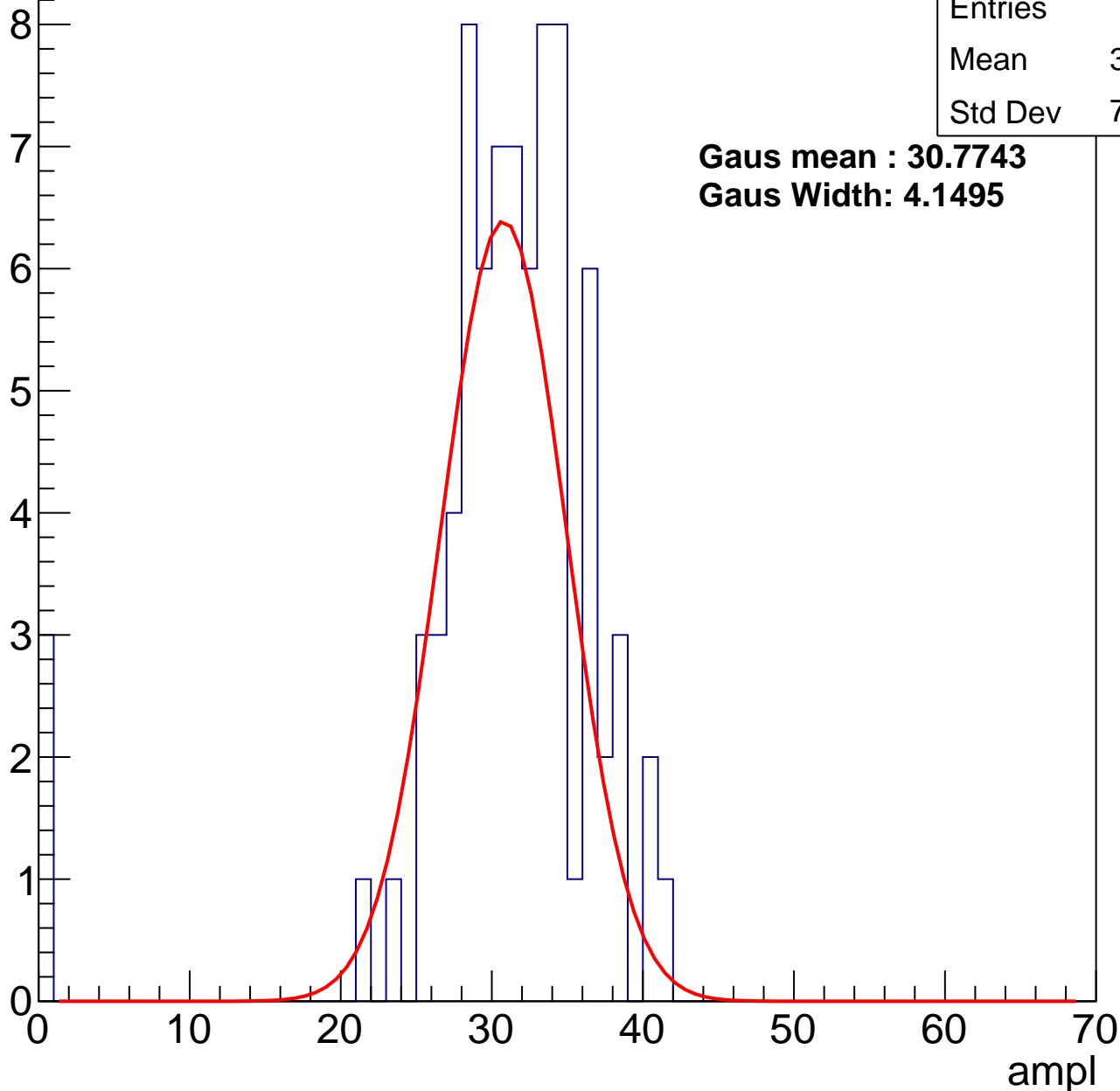
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.23
Std Dev	7.164

Gaus mean : 30.7743

Gaus Width: 4.1495



B0L001S, U18-ch63, adc1

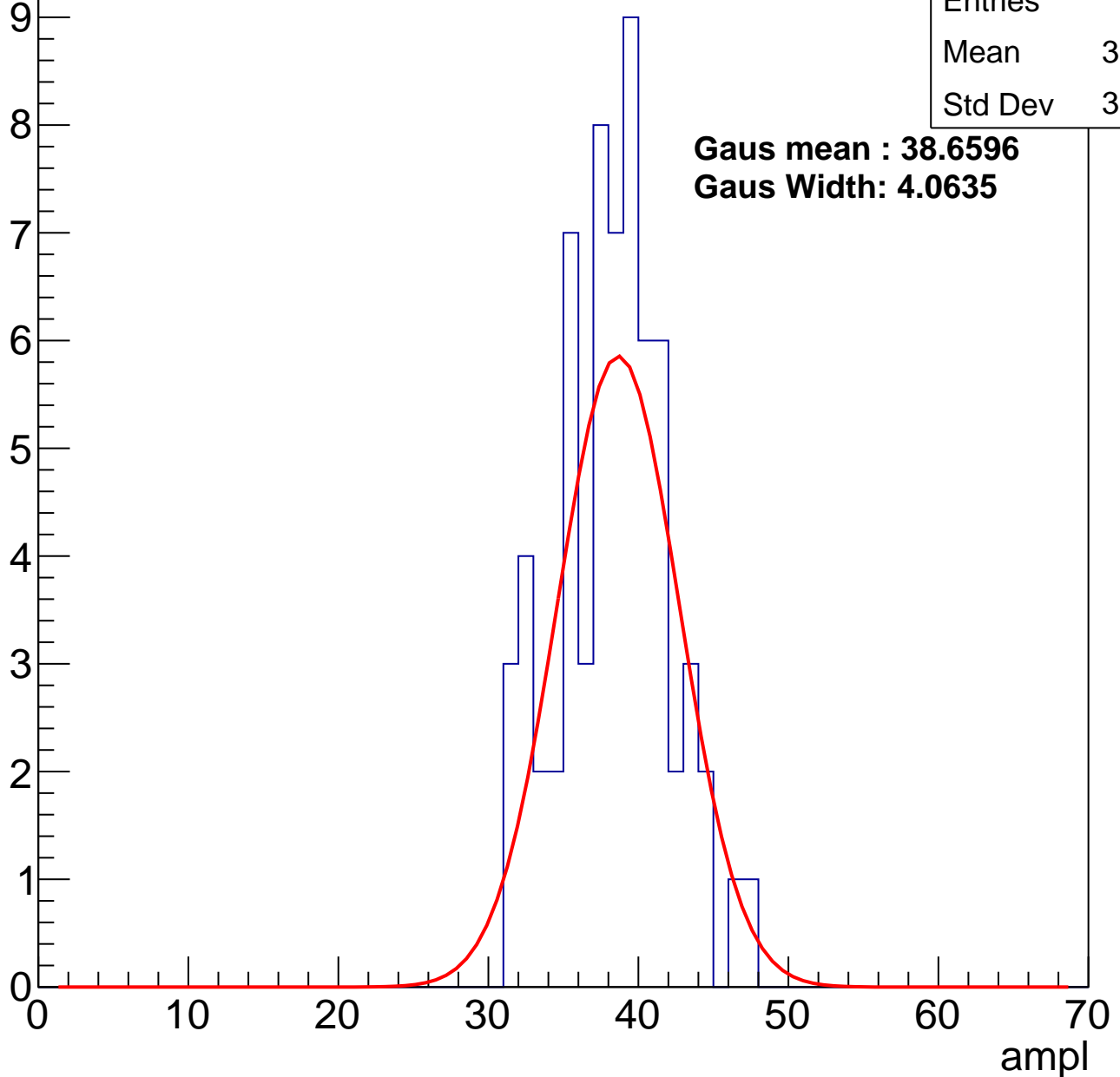
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.89
Std Dev	3.623

Gaus mean : 38.6596

Gaus Width: 4.0635



B0L001S, U18-ch63, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	45.27
Std Dev	3.659

Gaus mean : 45.8300

Gaus Width: 3.3162

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

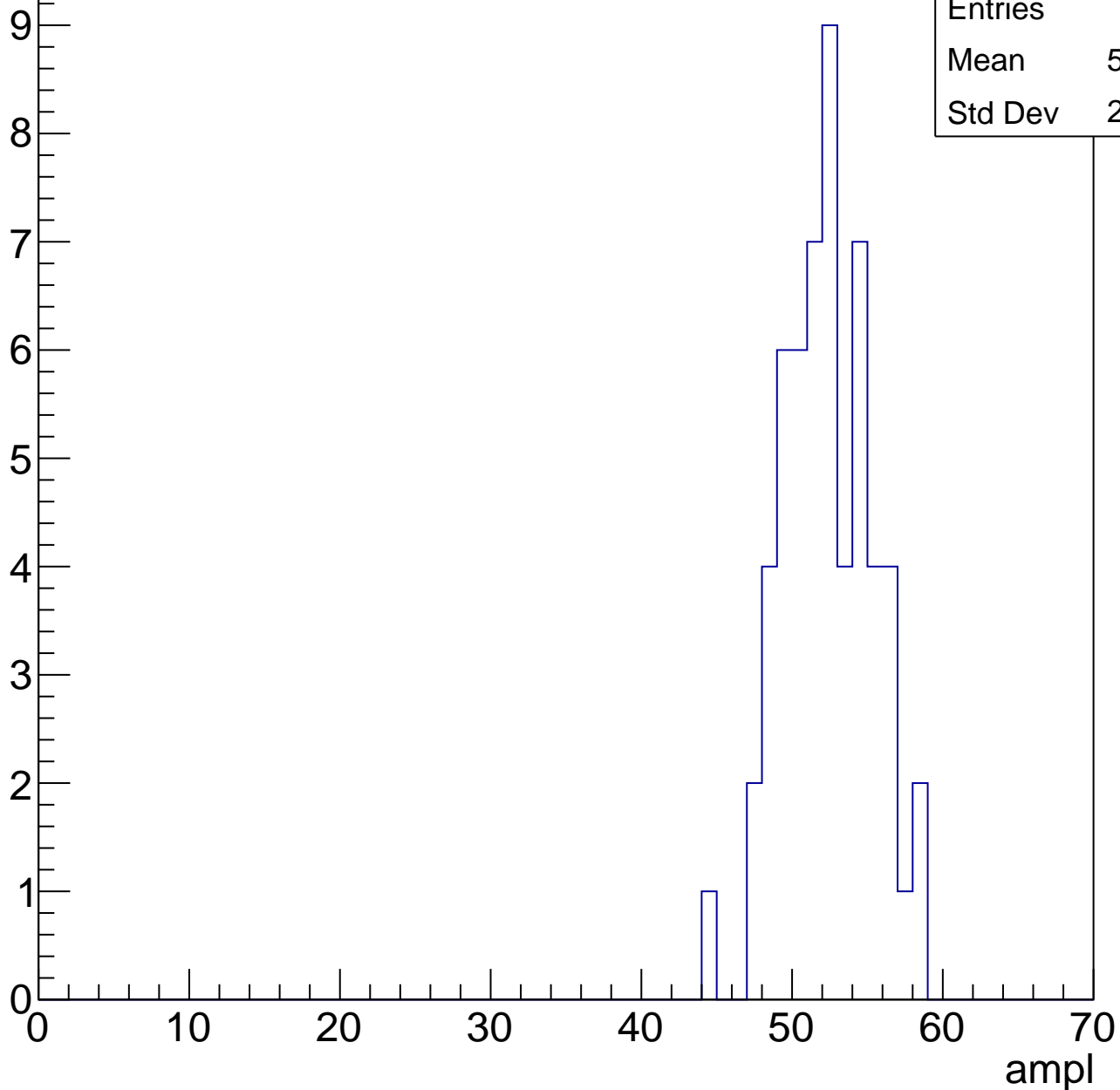
10

B0L001S, U18-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	51.86
Std Dev	2.935

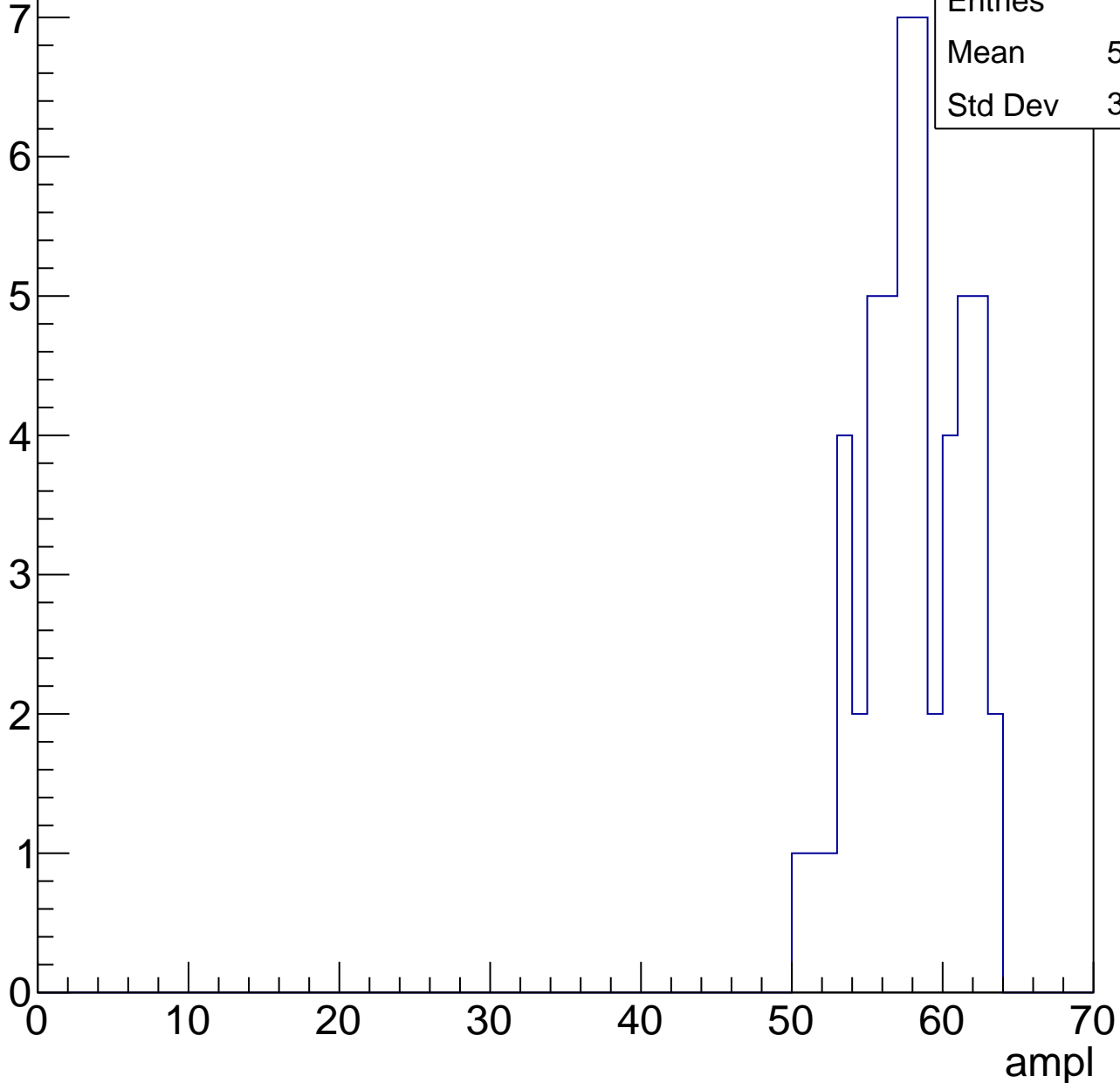


B0L001S, U18-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

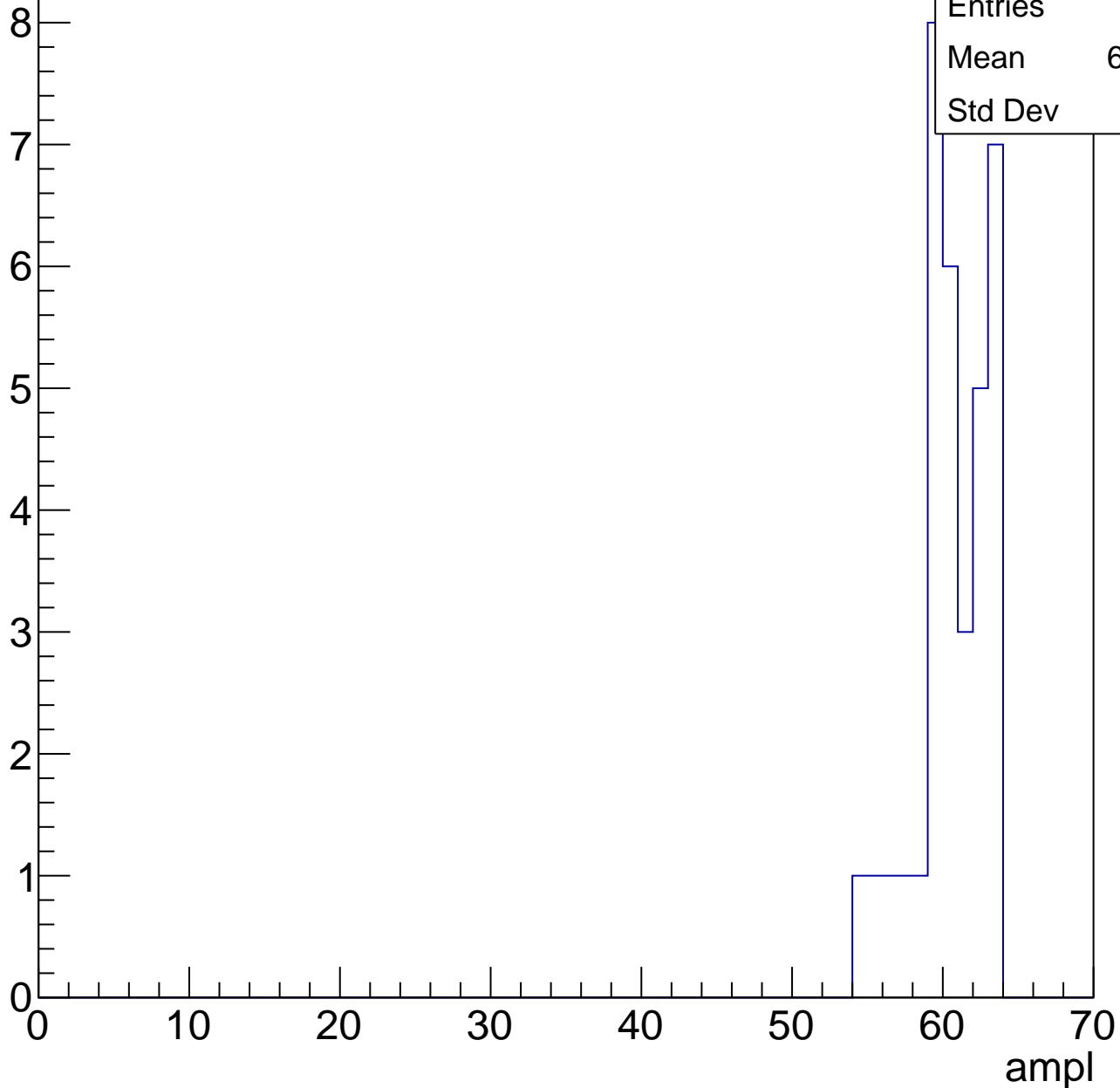
Entries	51
Mean	57.49
Std Dev	3.232



B0L001S, U18-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

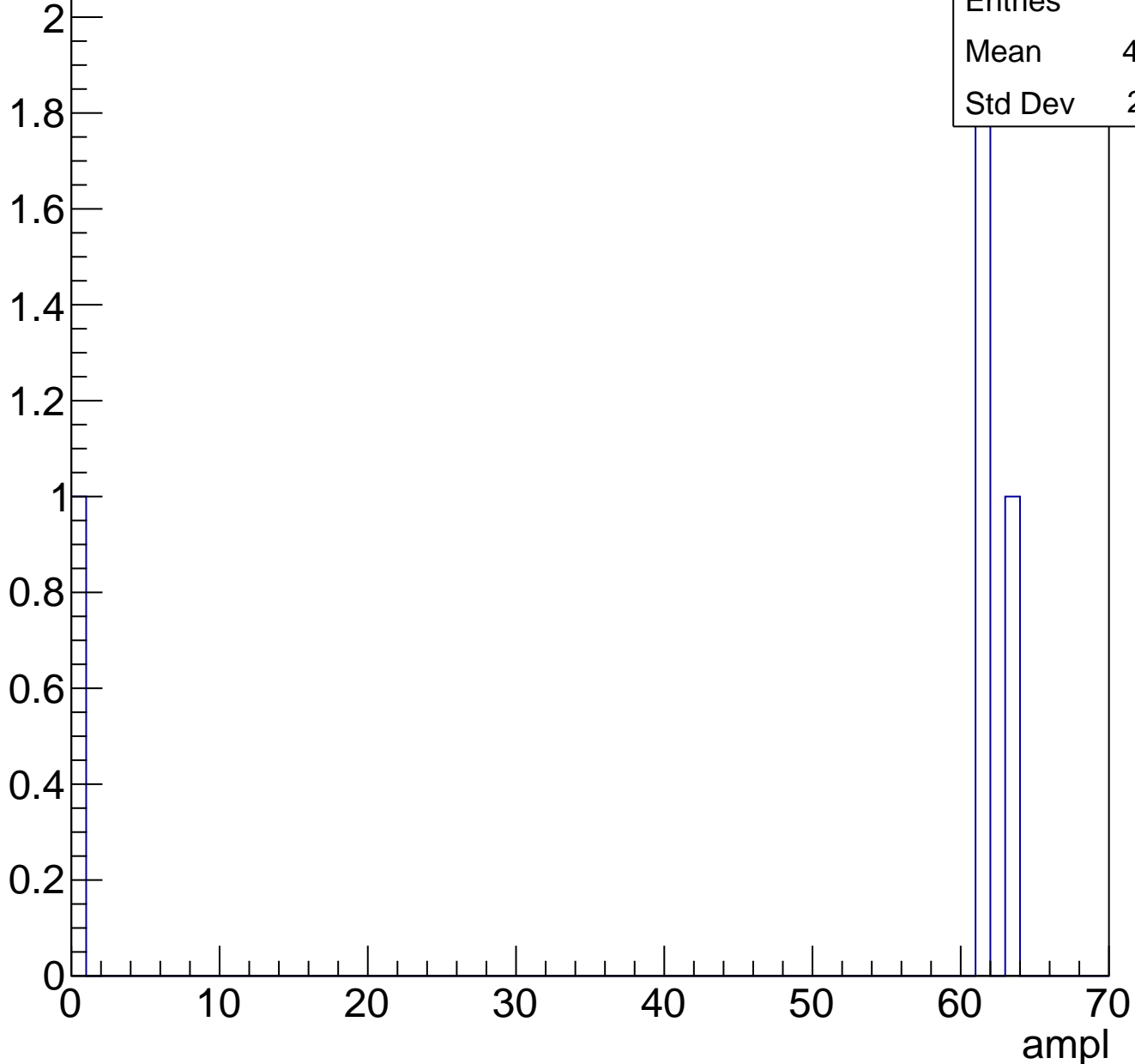
Entry



B0L001S, U18-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	46.25
Std Dev	26.71

B0L001S, U18-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch64, adc0

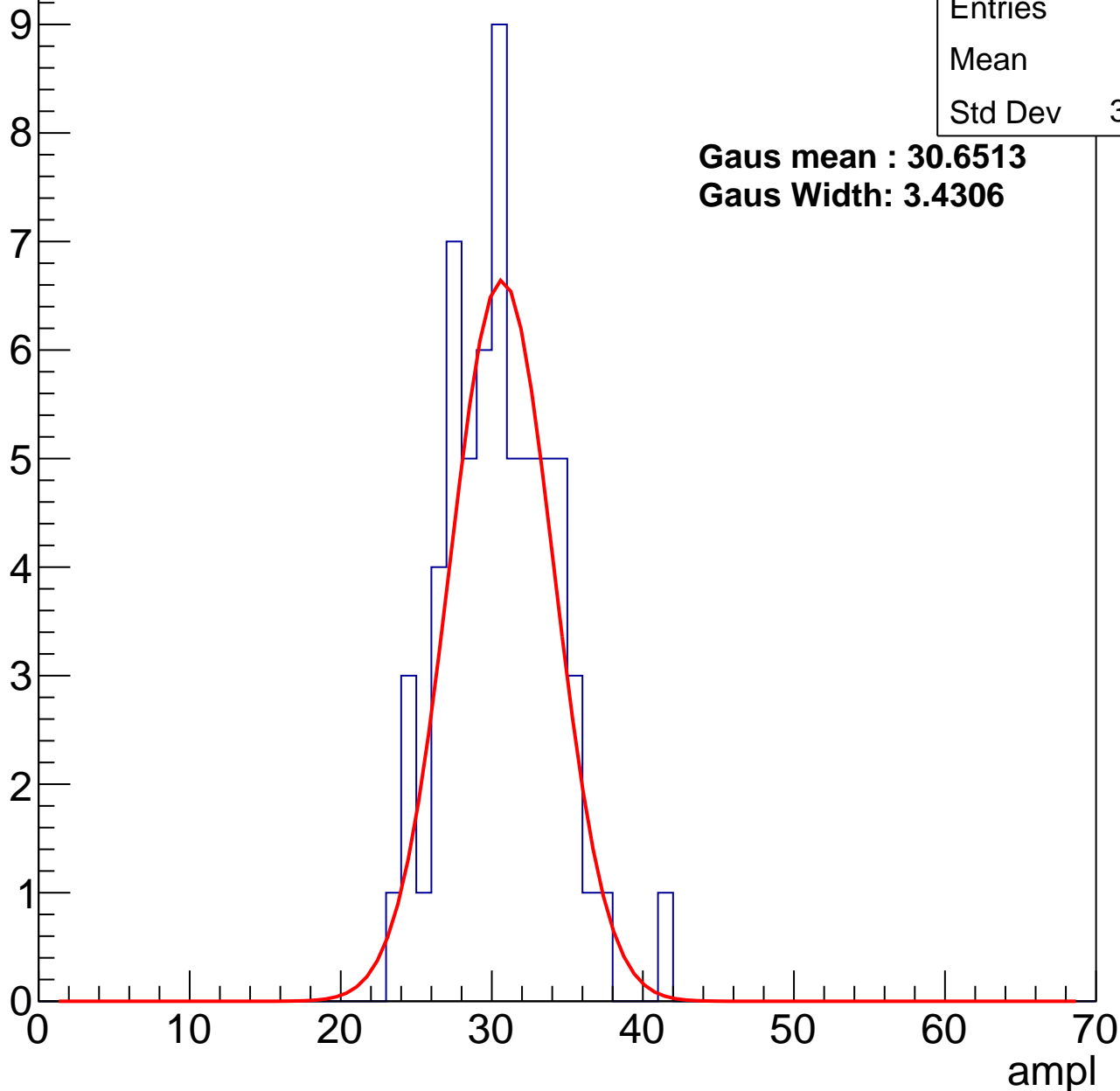
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.1
Std Dev	3.514

Gaus mean : 30.6513

Gaus Width: 3.4306



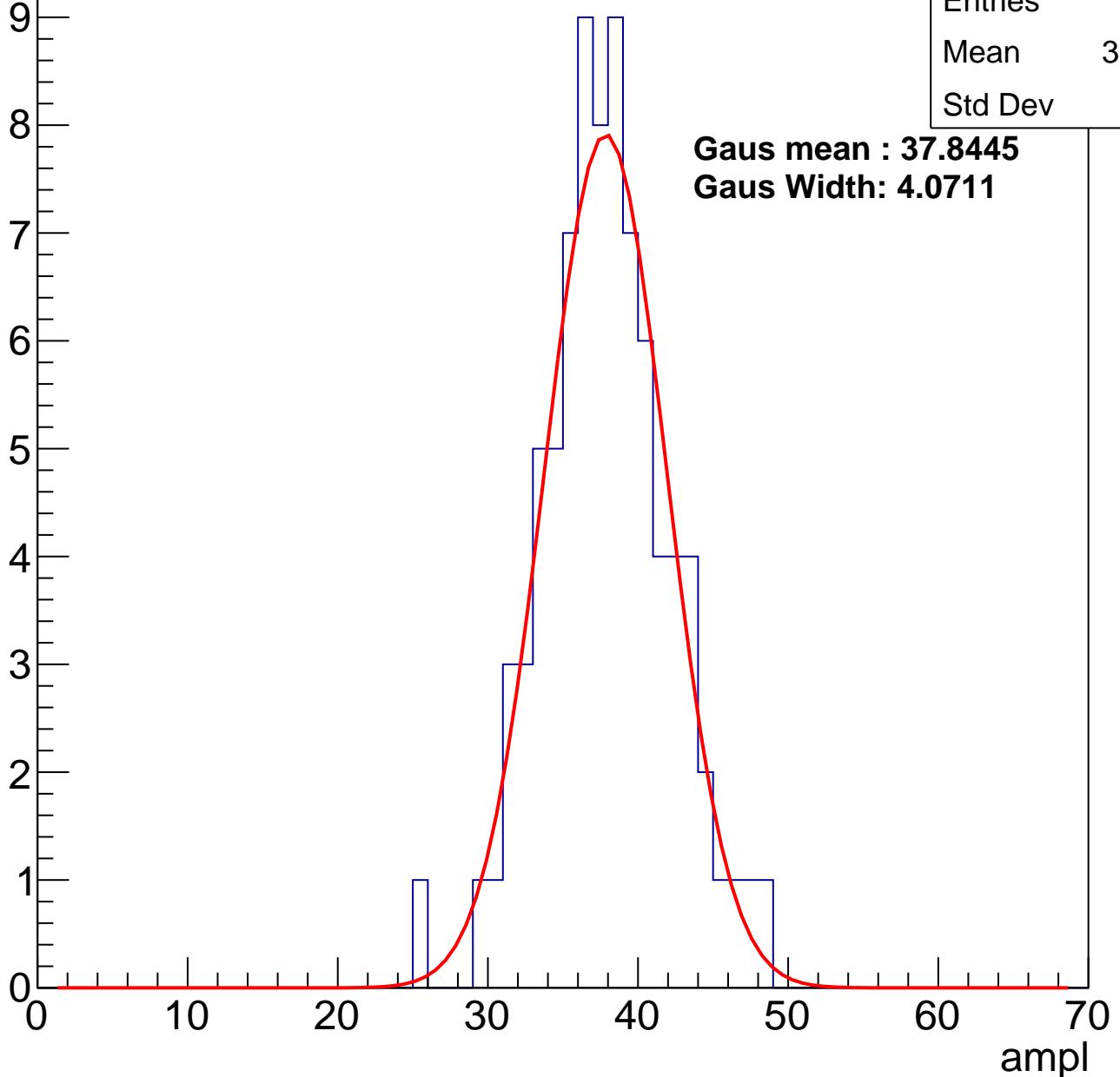
B0L001S, U18-ch64, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	37.42
Std Dev	4.18

Gaus mean : 37.8445
Gaus Width: 4.0711



B0L001S, U18-ch64, adc2

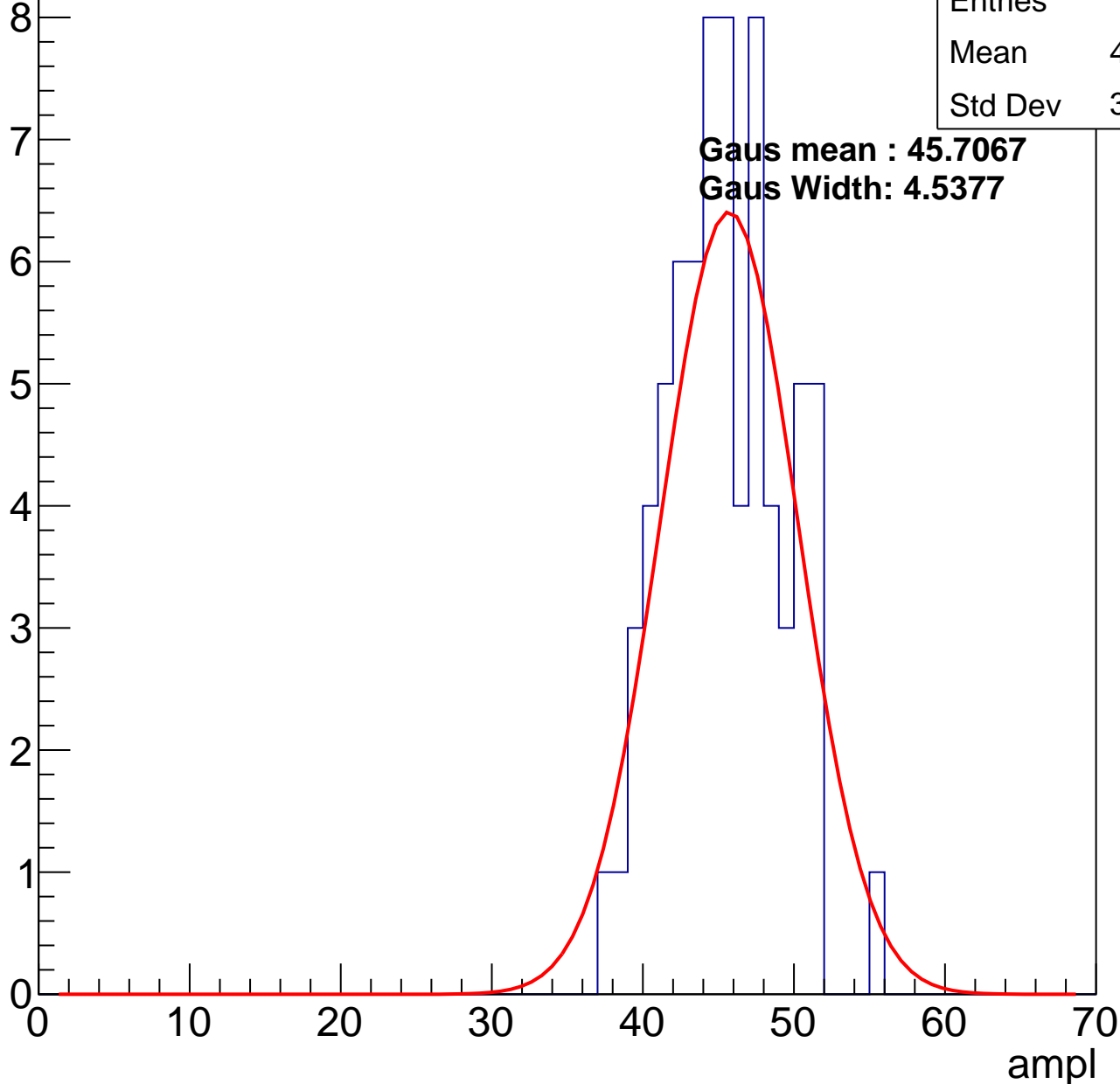
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	44.97
Std Dev	3.749

Gaus mean : 45.7067

Gaus Width: 4.5377

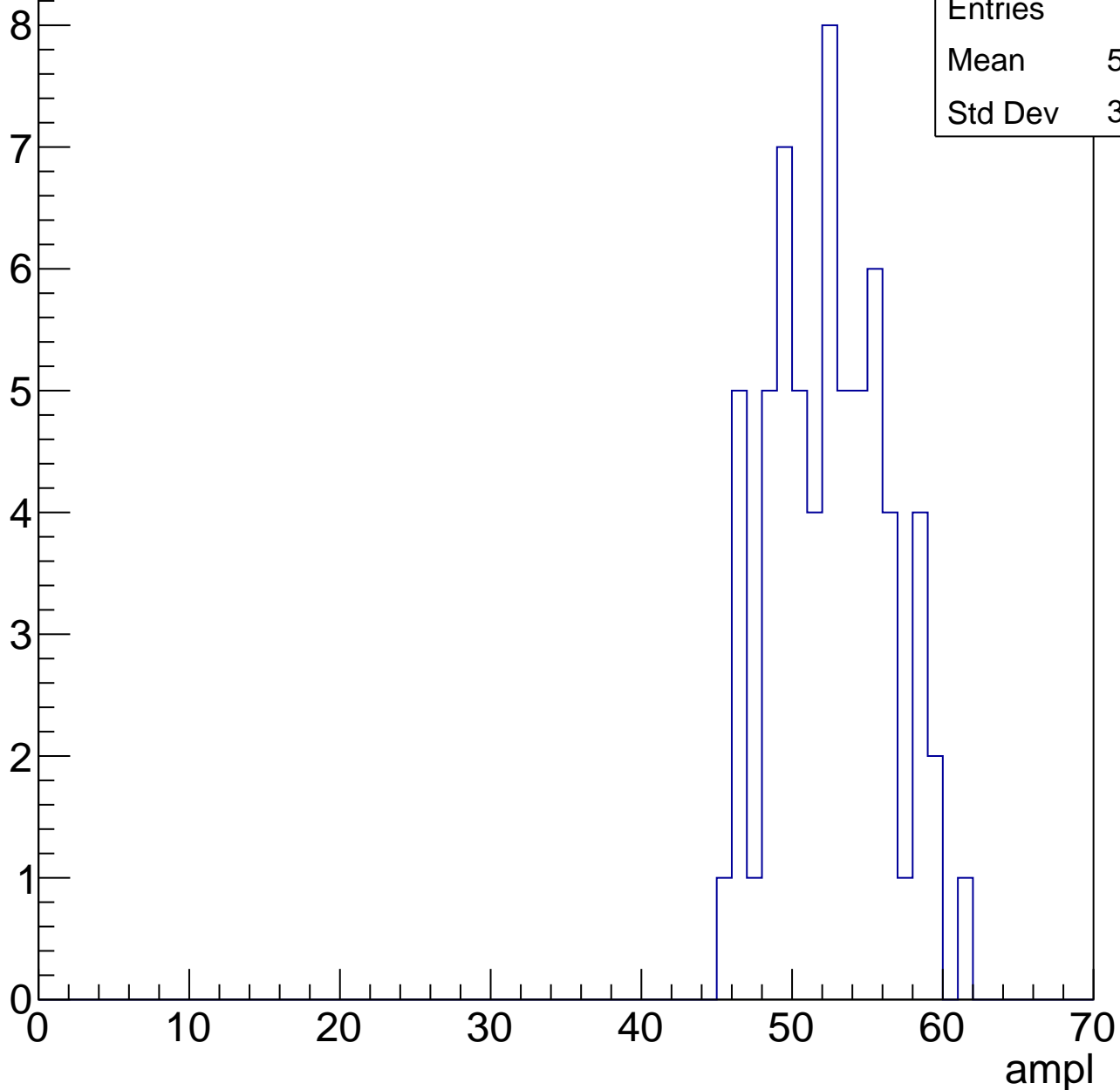


B0L001S, U18-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	52.06
Std Dev	3.774

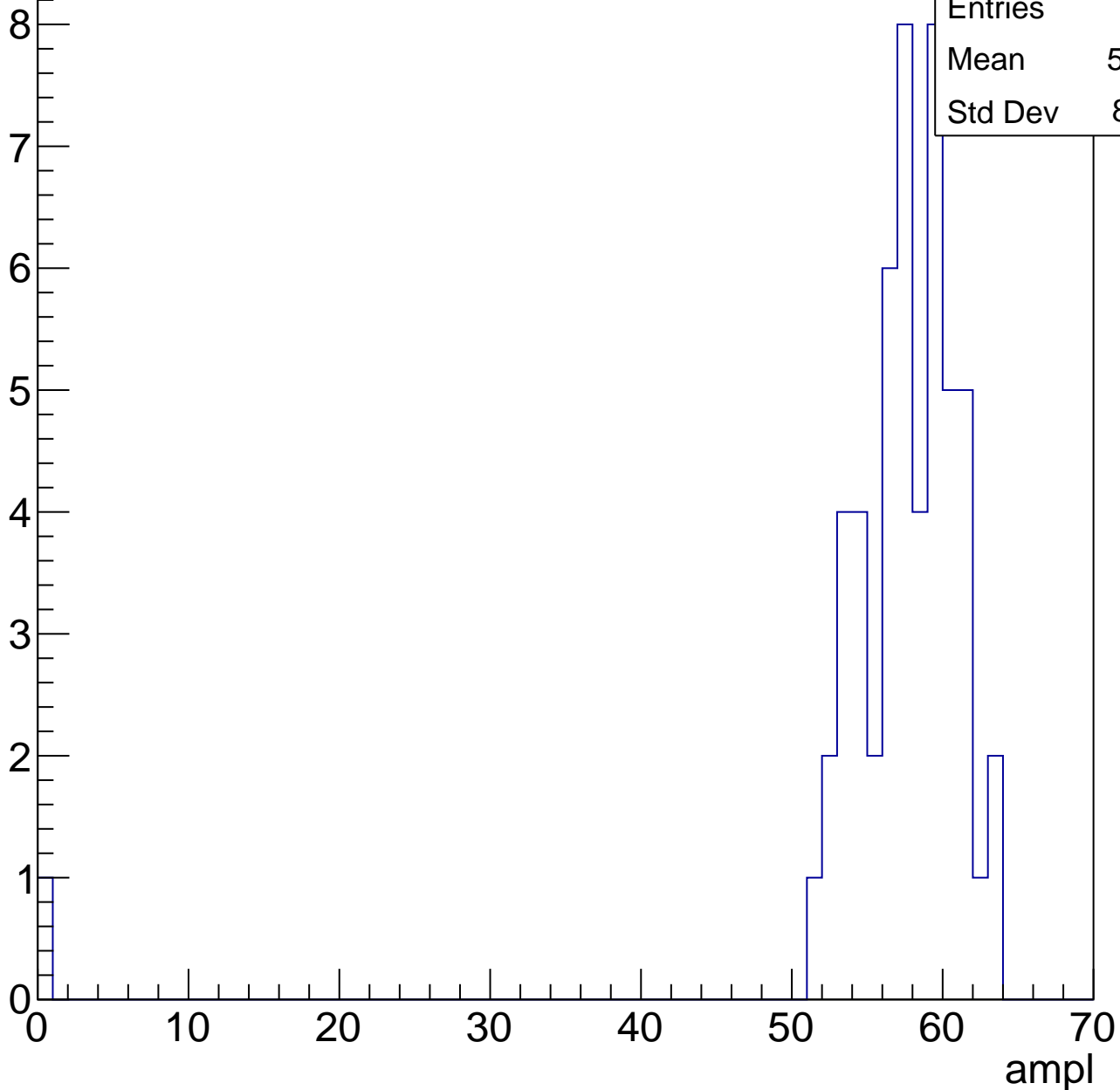


B0L001S, U18-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.26
Std Dev	8.331

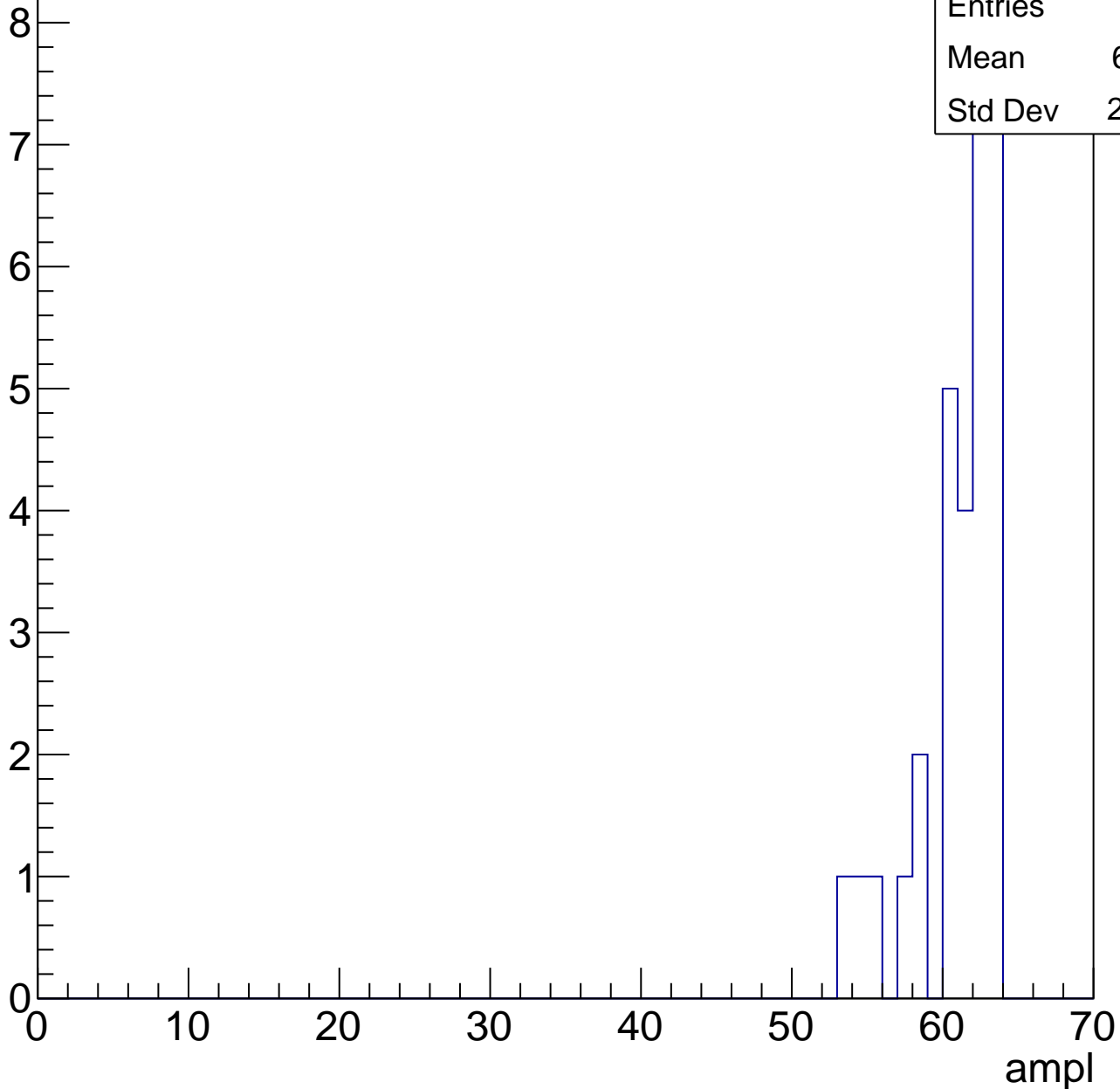


B0L001S, U18-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.61
Std Dev	2.684



B0L001S, U18-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch65, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	30.63
Std Dev	5.039

Gaus mean : 31.8667

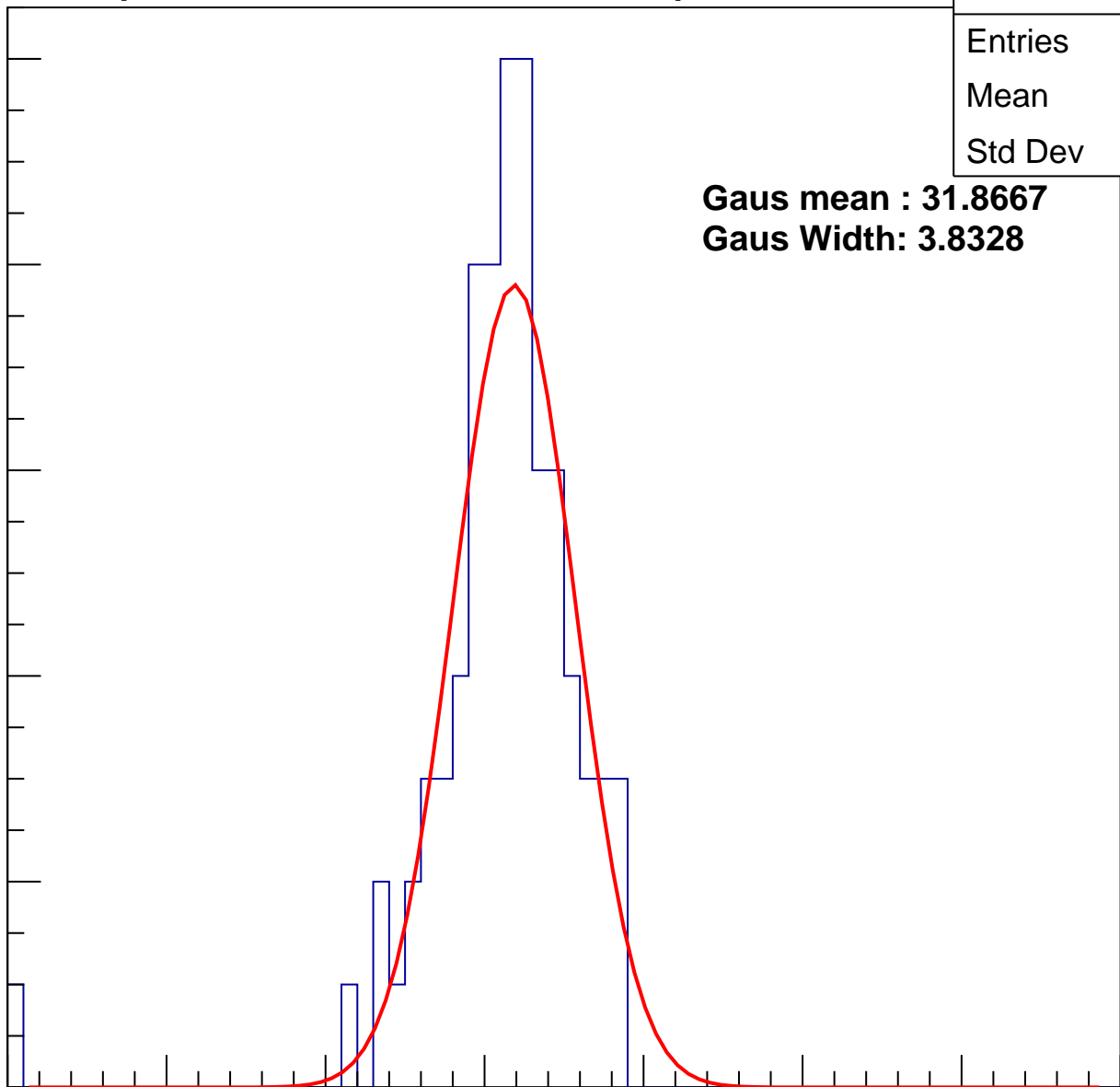
Gaus Width: 3.8328

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch65, adc1

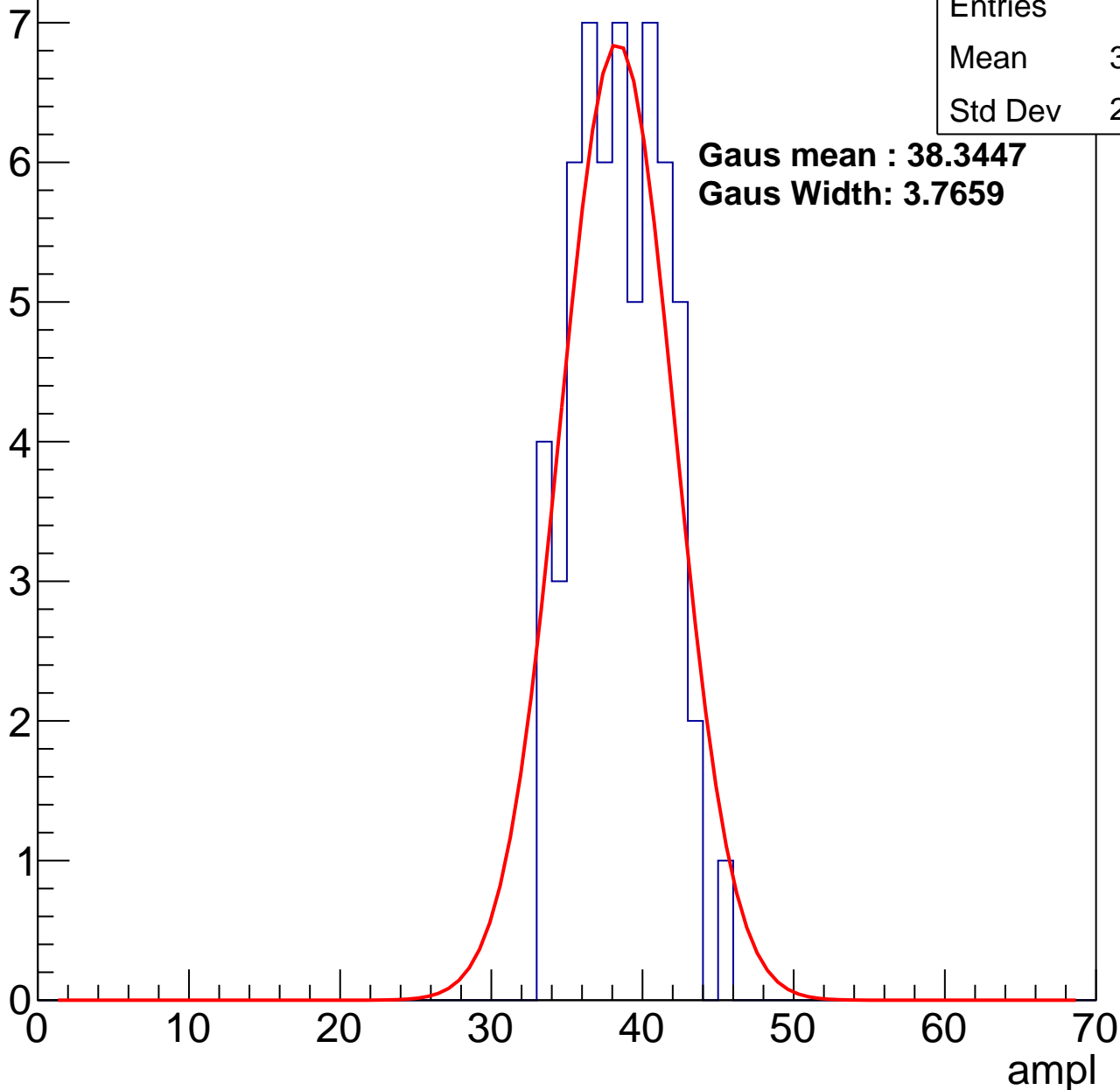
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.07
Std Dev	2.916

Gaus mean : 38.3447

Gaus Width: 3.7659



B0L001S, U18-ch65, adc2

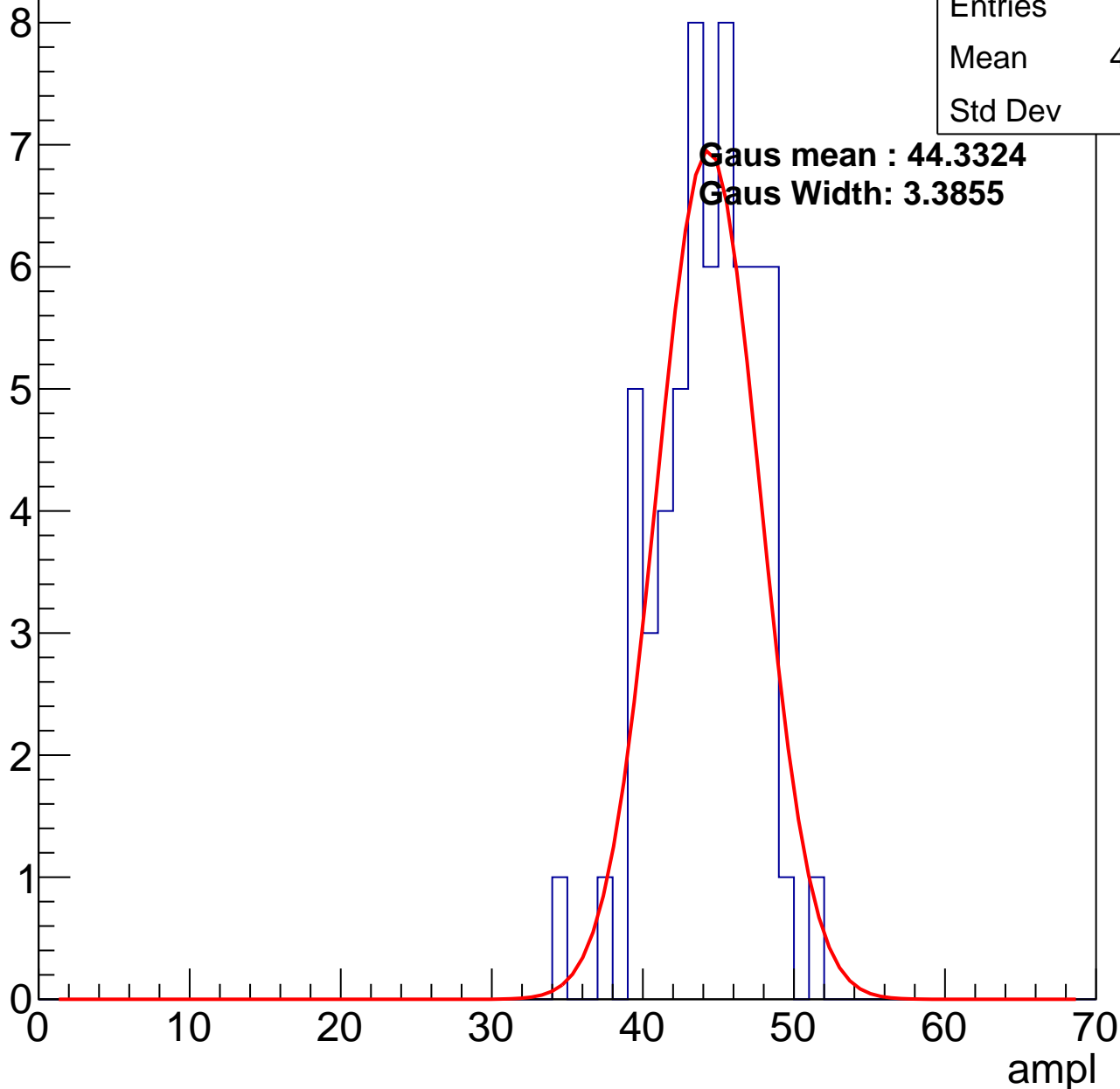
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.84
Std Dev	3.24

Gaus mean : 44.3324

Gaus Width: 3.3855

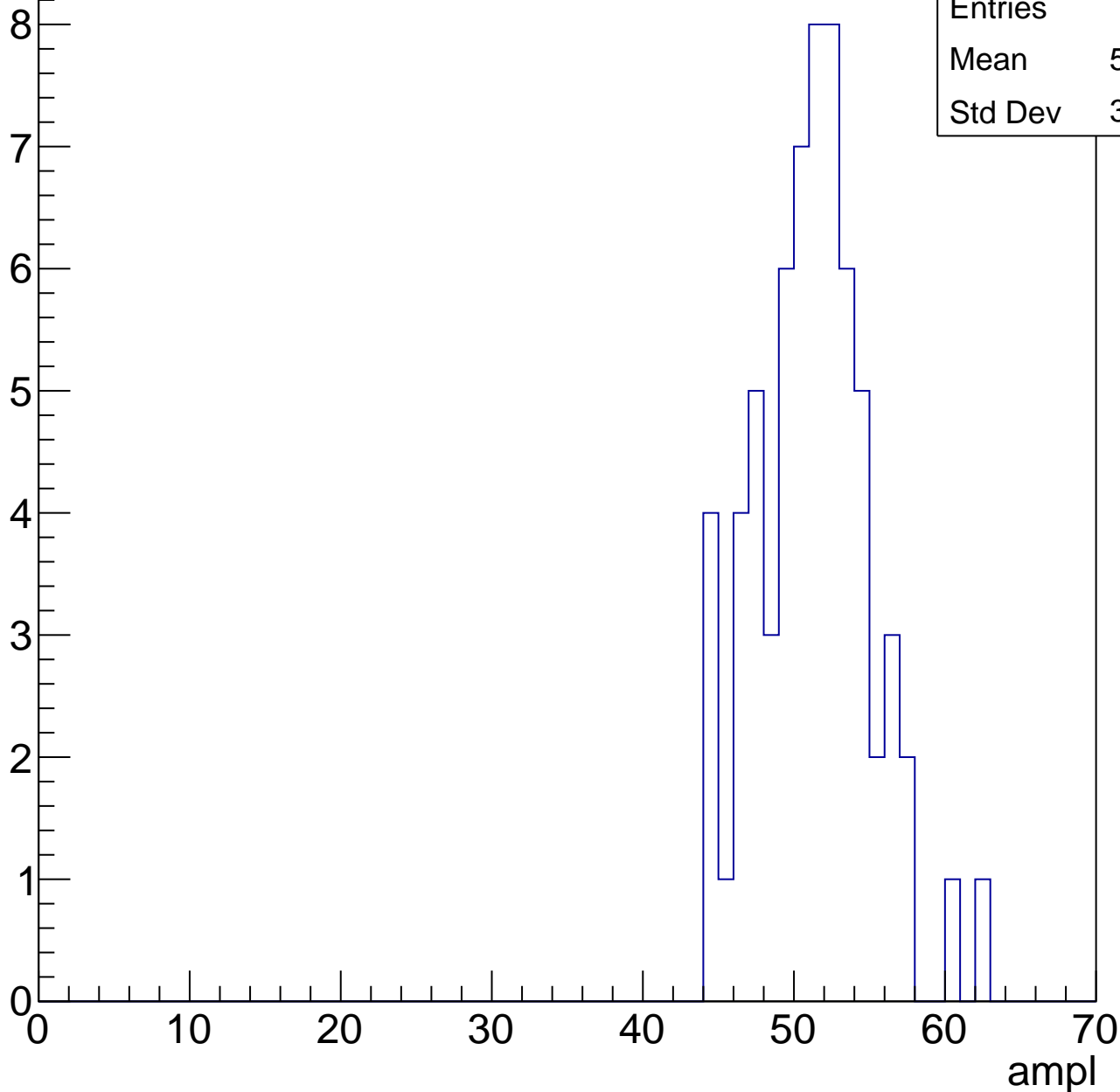


B0L001S, U18-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.82
Std Dev	3.745

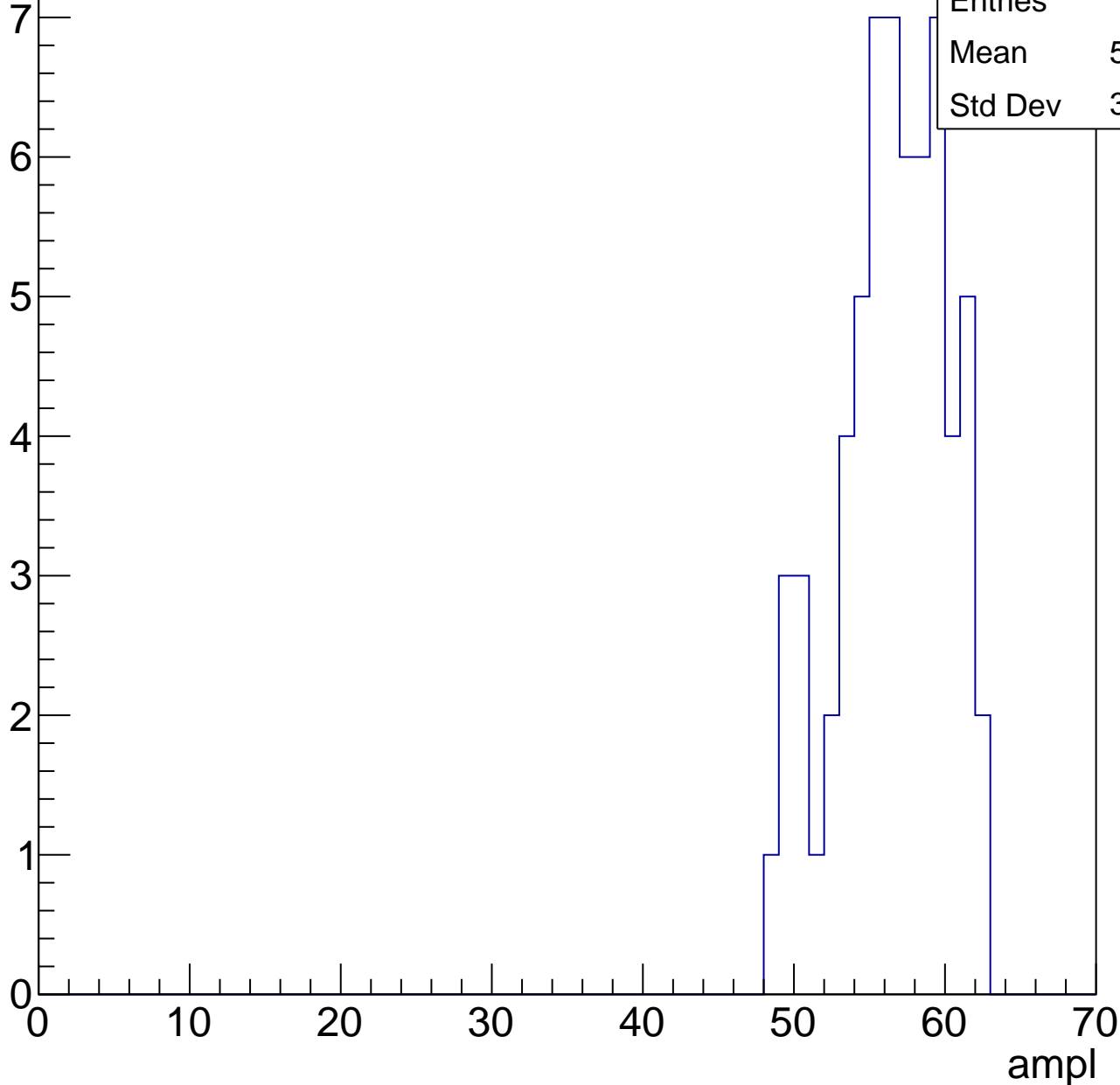


B0L001S, U18-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	56.05
Std Dev	3.548

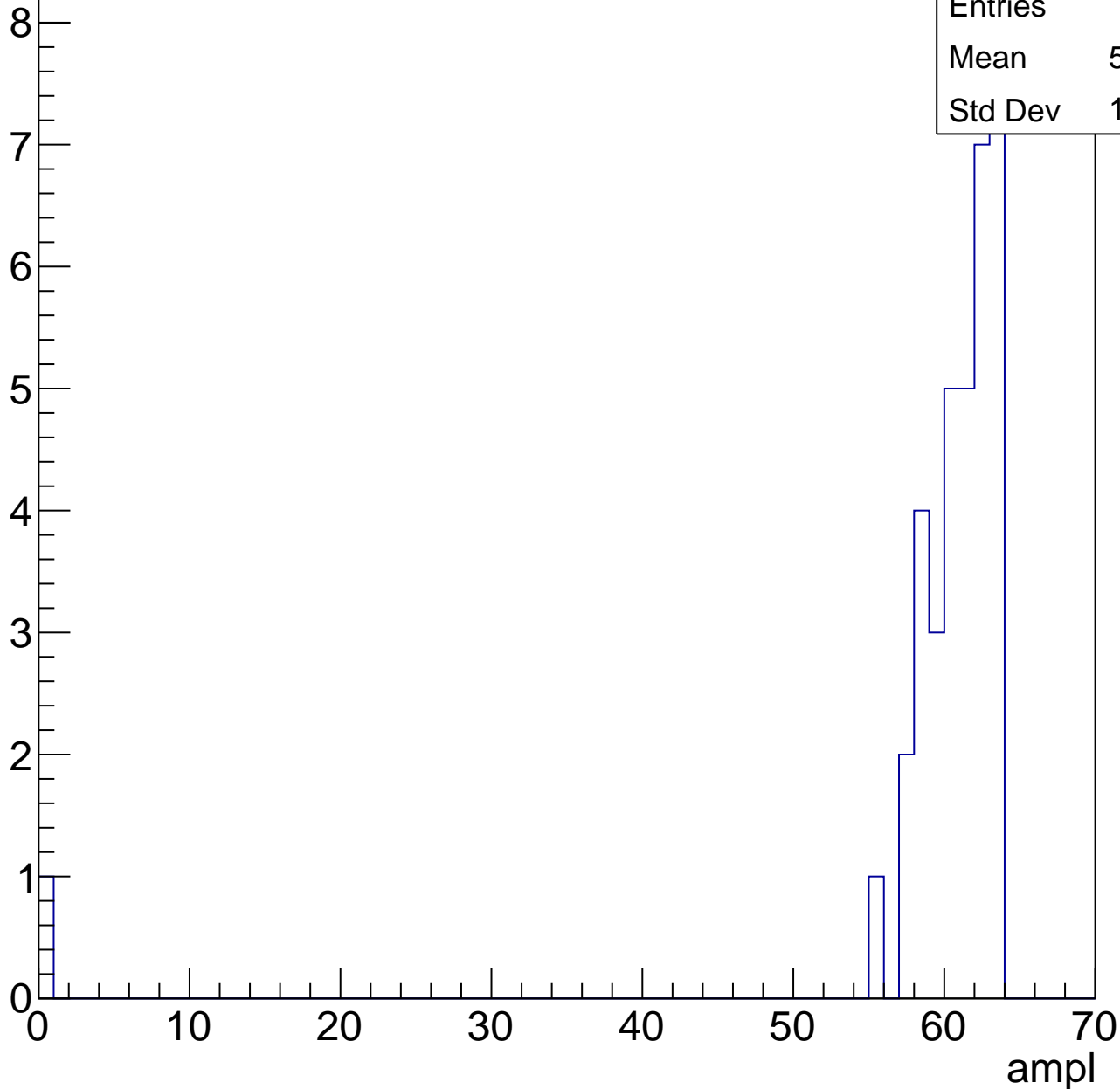


B0L001S, U18-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.92
Std Dev	10.17



B0L001S, U18-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U18-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch66, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	31.54
Std Dev	5.332

Gaus mean : 32.7195

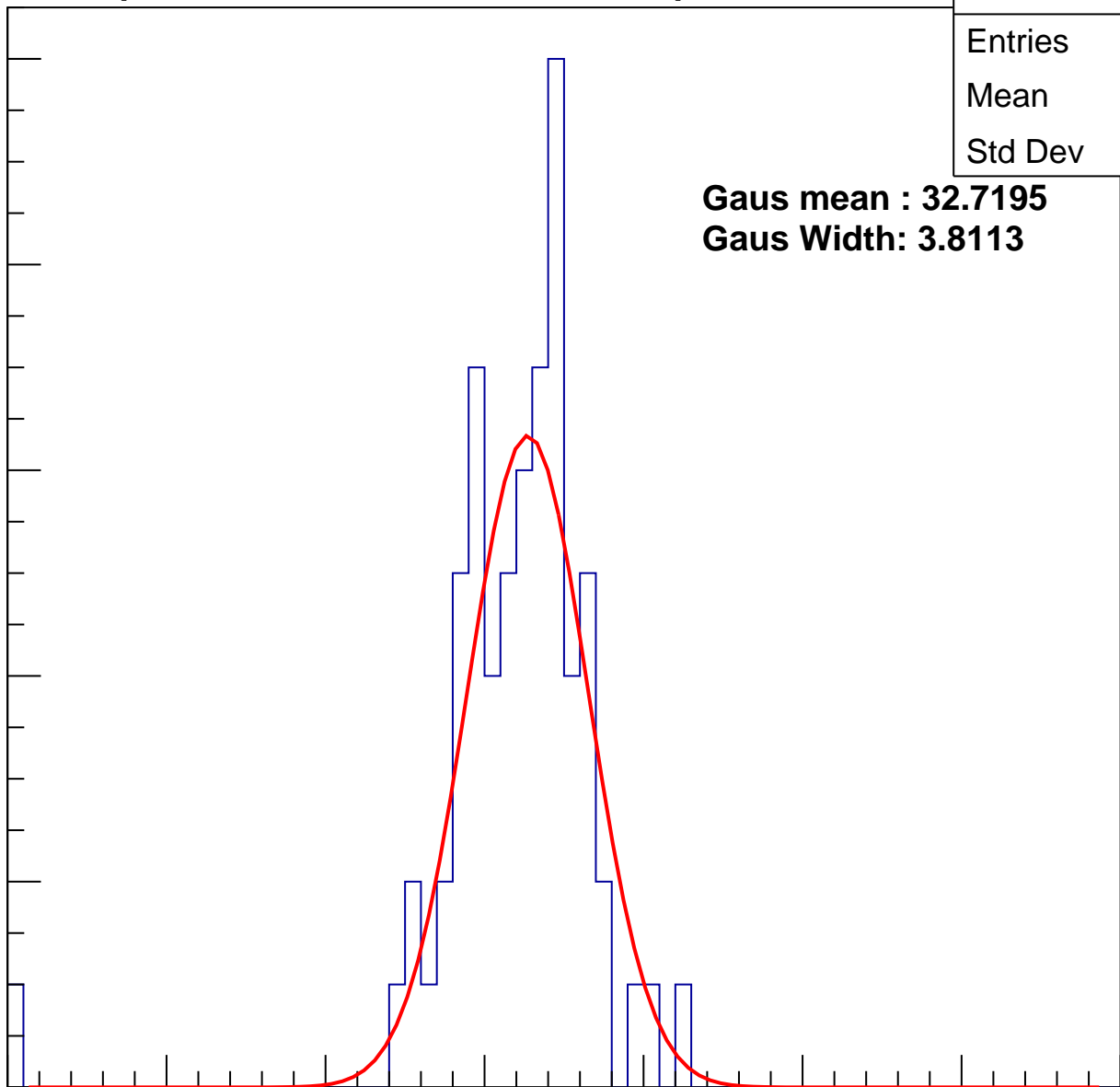
Gaus Width: 3.8113

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch66, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	38.47
Std Dev	3.287

Gaus mean : 38.7864

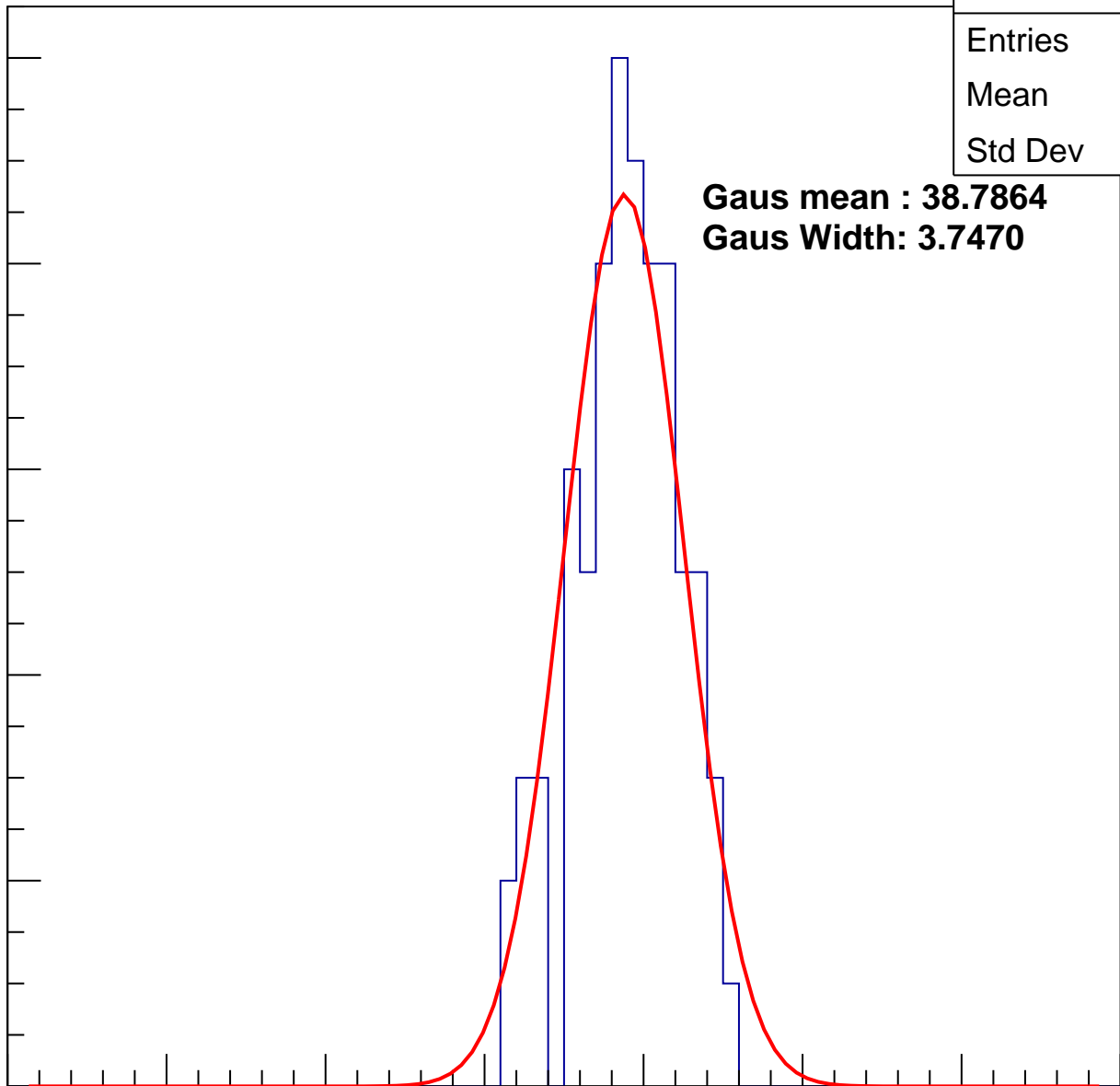
Gaus Width: 3.7470

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch66, adc2

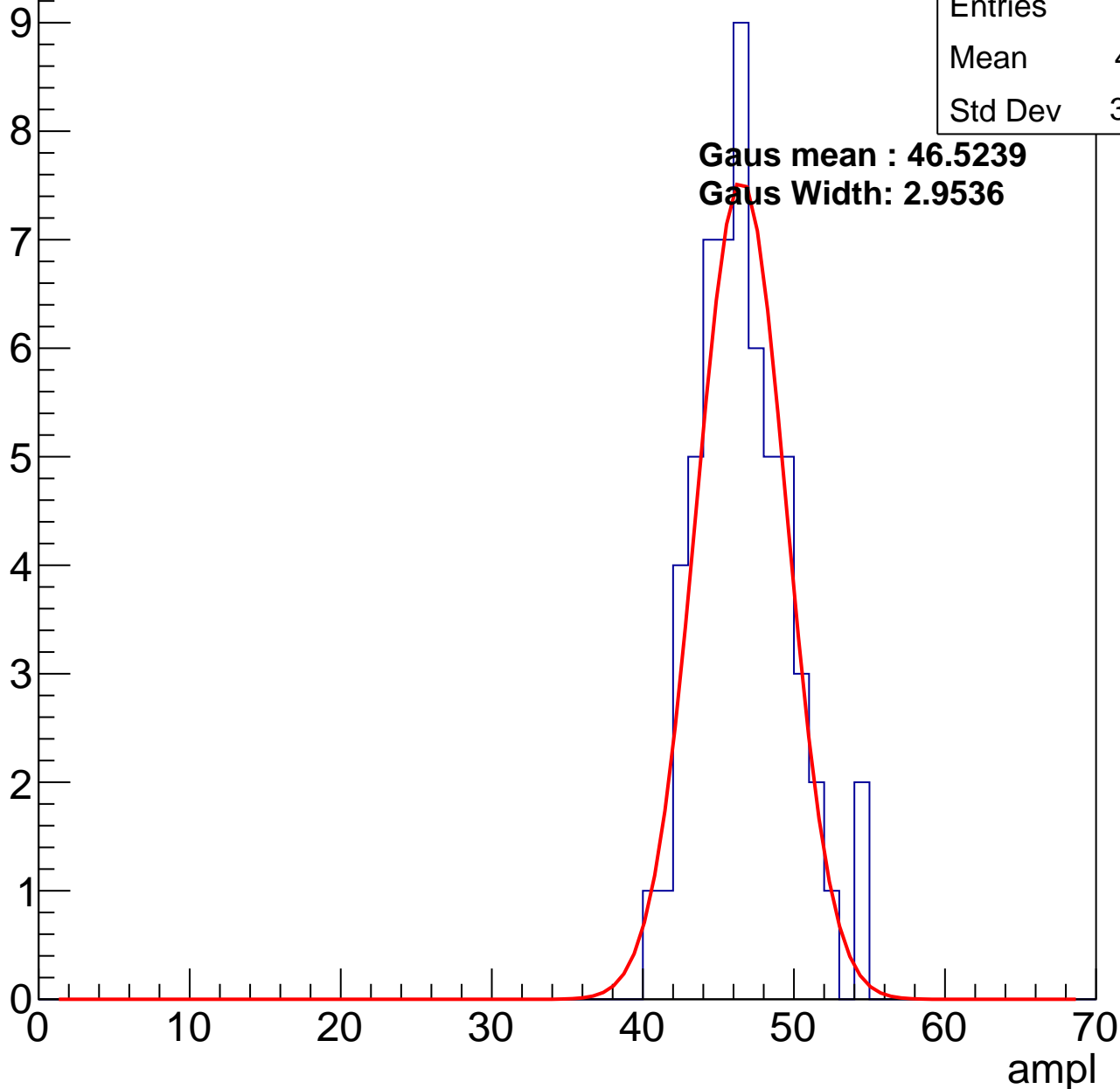
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	46.21
Std Dev	3.039

Gaus mean : 46.5239

Gaus Width: 2.9536

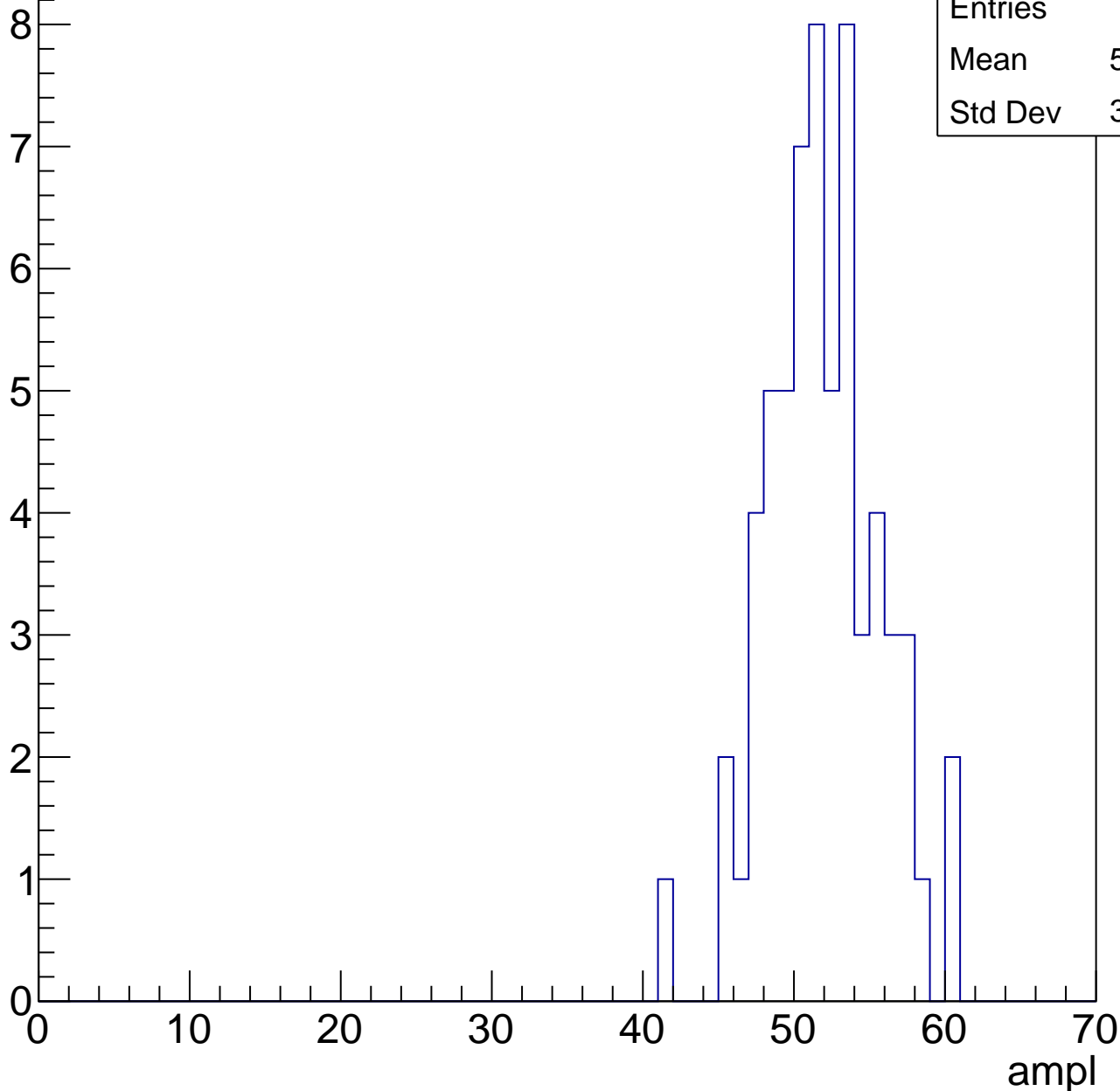


B0L001S, U18-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.47
Std Dev	3.684



B0L001S, U18-ch66, adc4

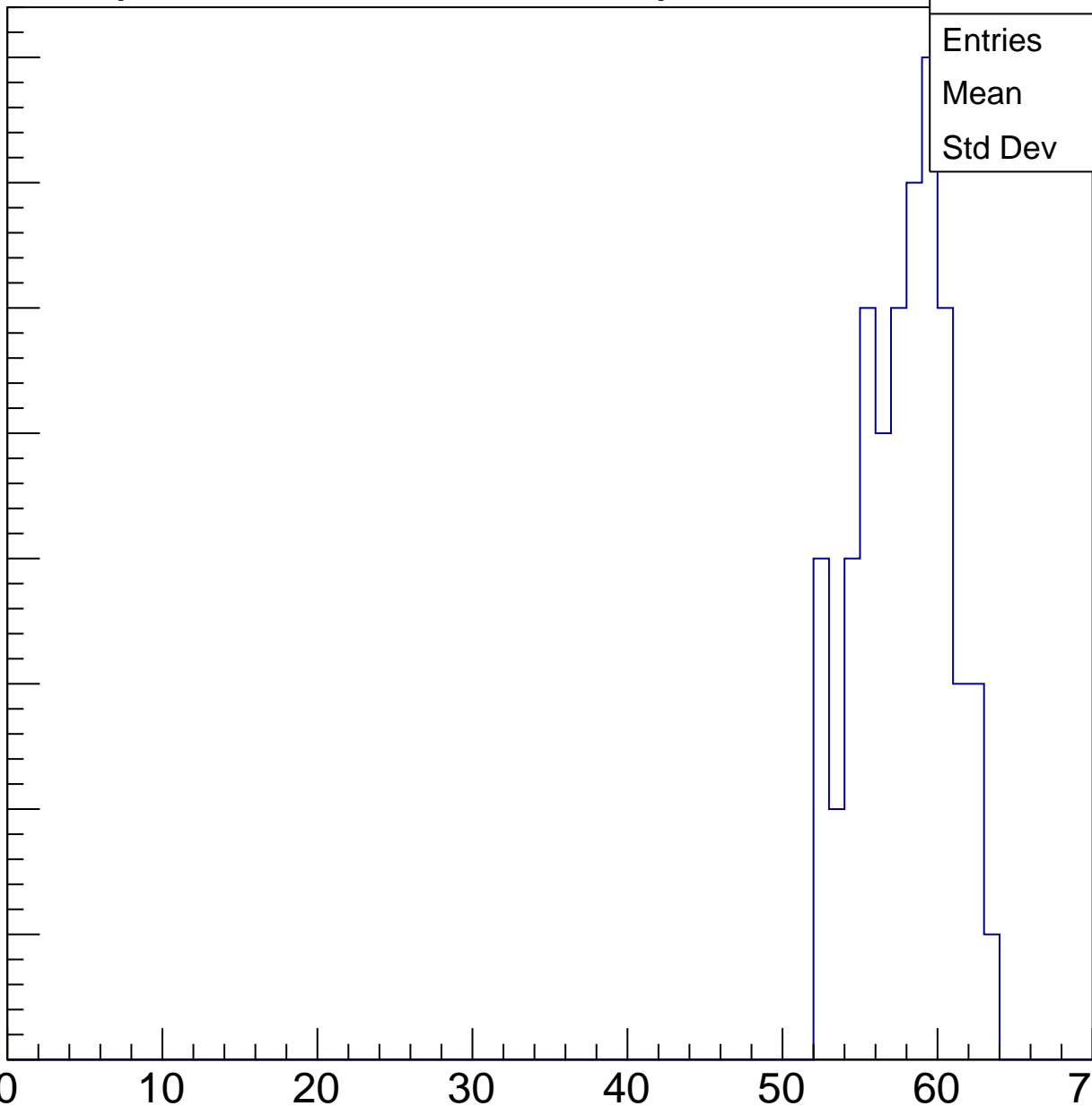
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	57.31
Std Dev	2.84

ampl

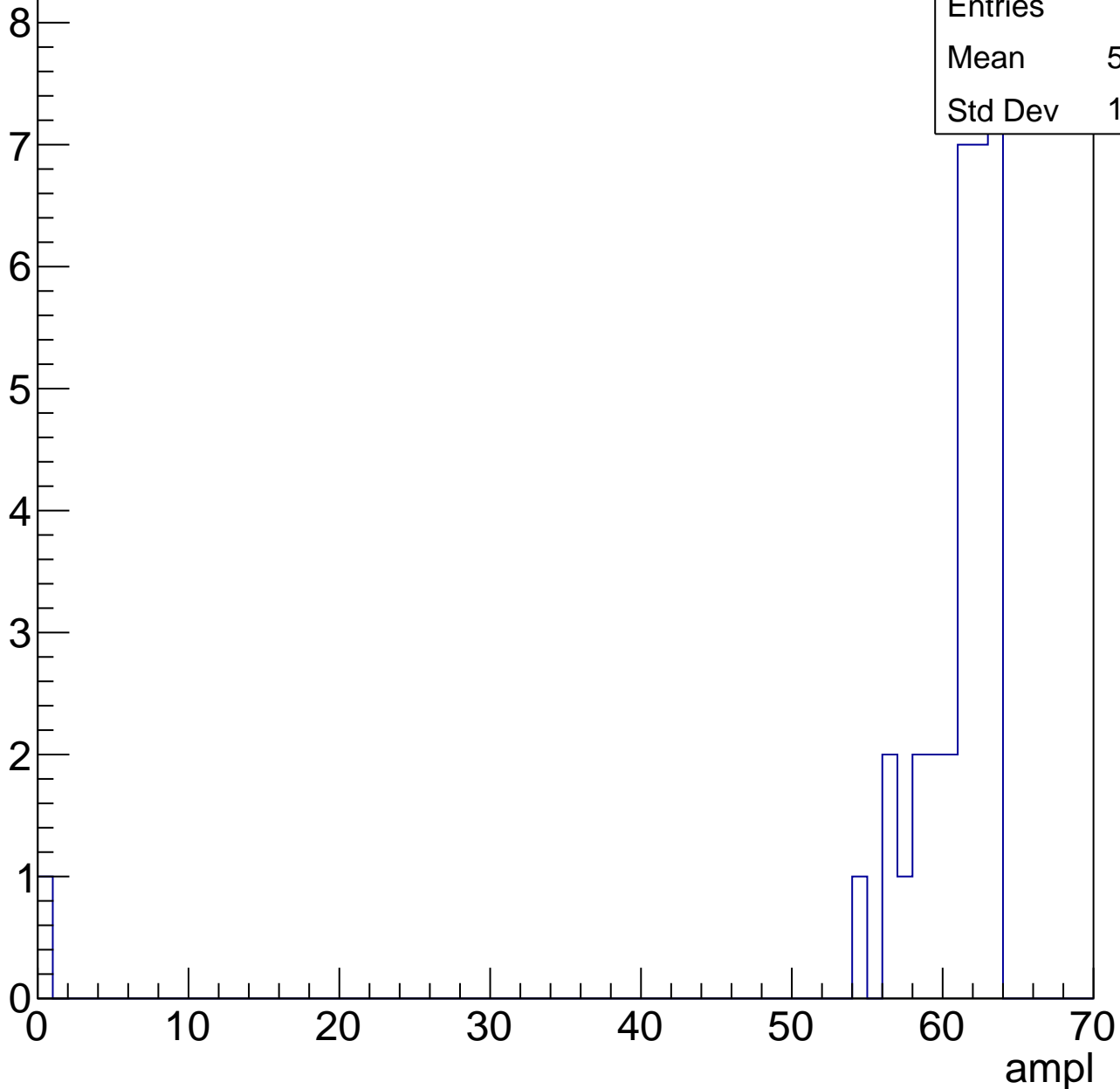


B0L001S, U18-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.85
Std Dev	10.66



B0L001S, U18-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch67, adc0

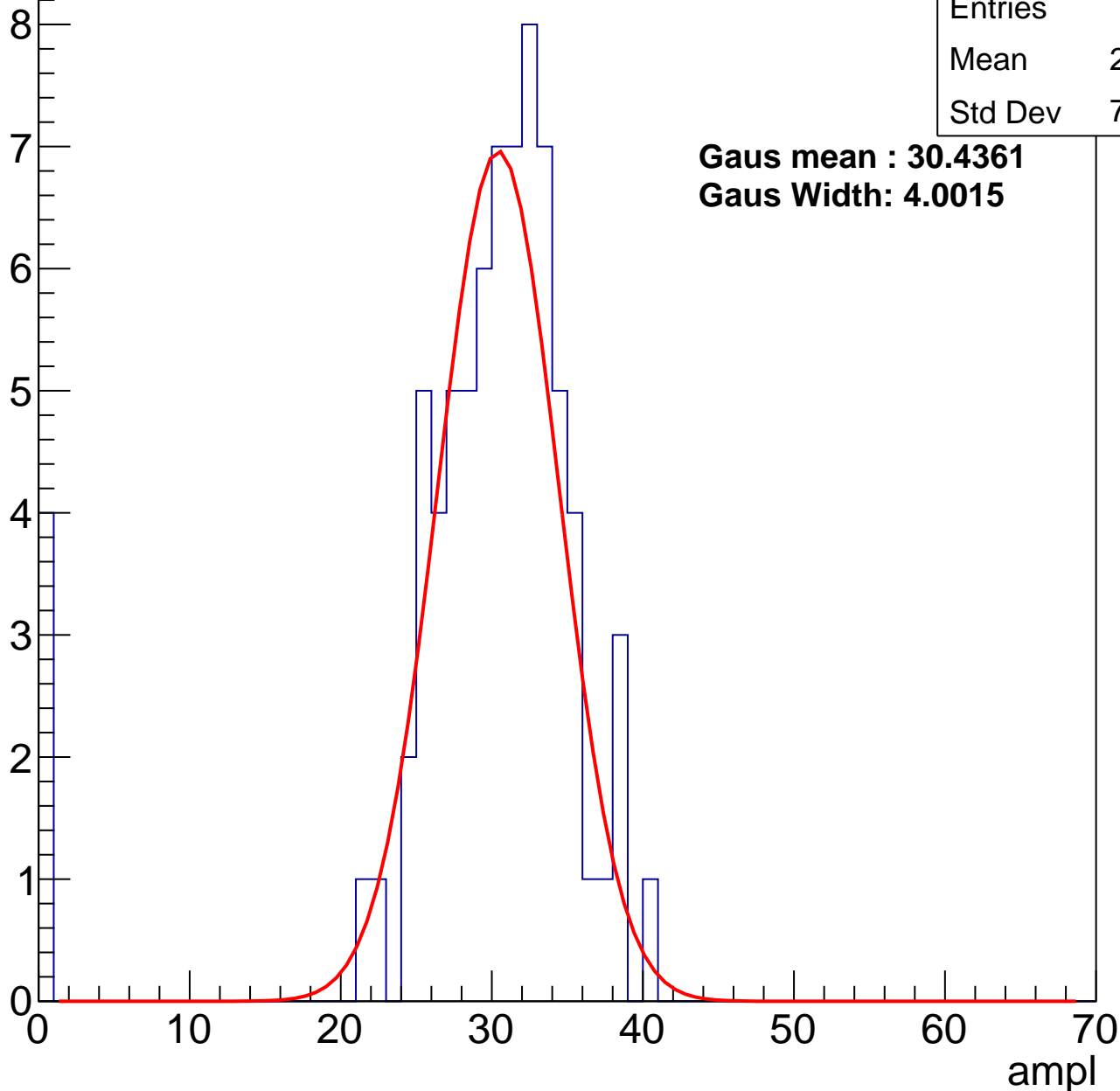
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	28.83
Std Dev	7.755

Gaus mean : 30.4361

Gaus Width: 4.0015



B0L001S, U18-ch67, adc1

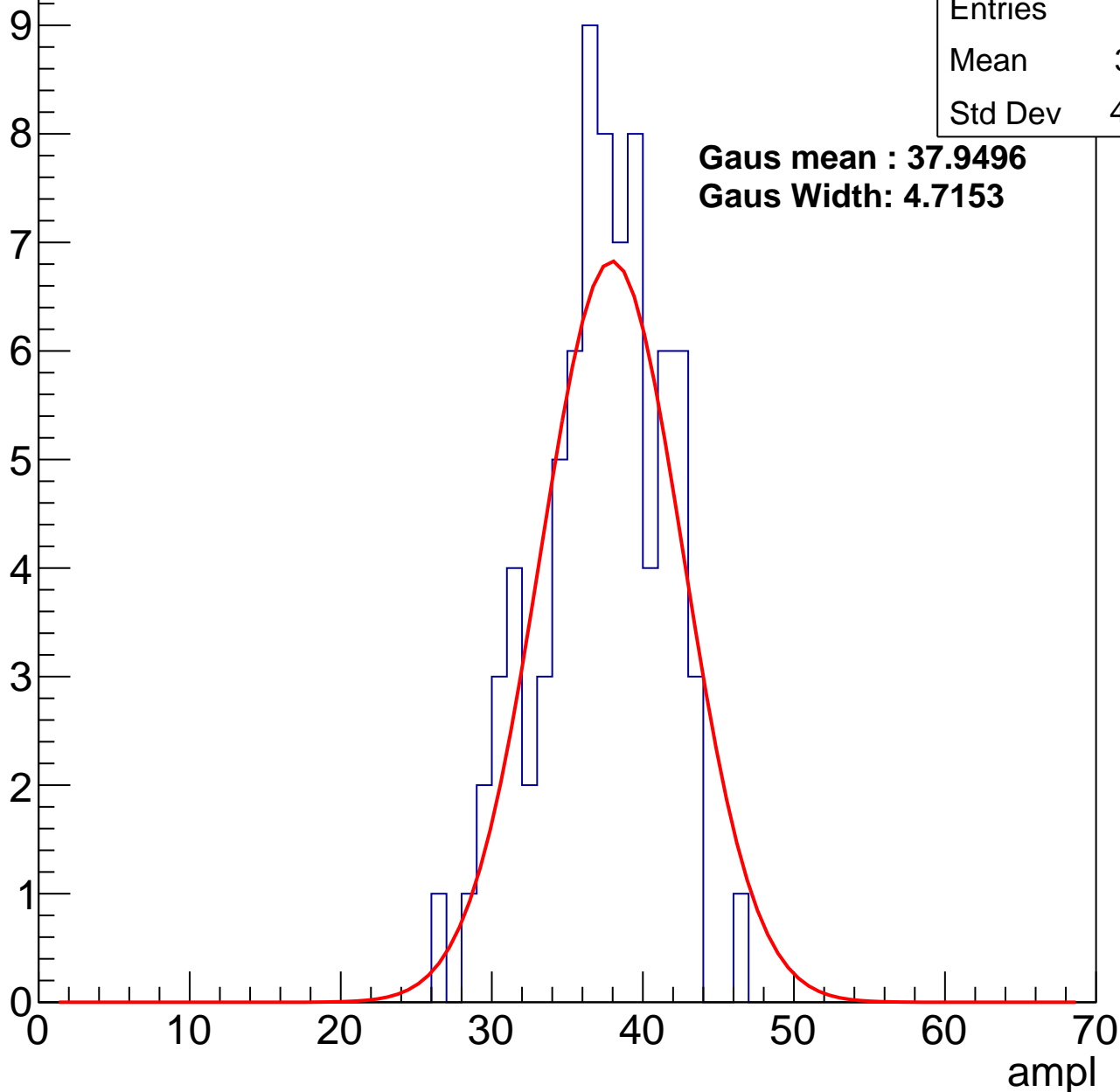
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.71
Std Dev	4.057

Gaus mean : 37.9496

Gaus Width: 4.7153

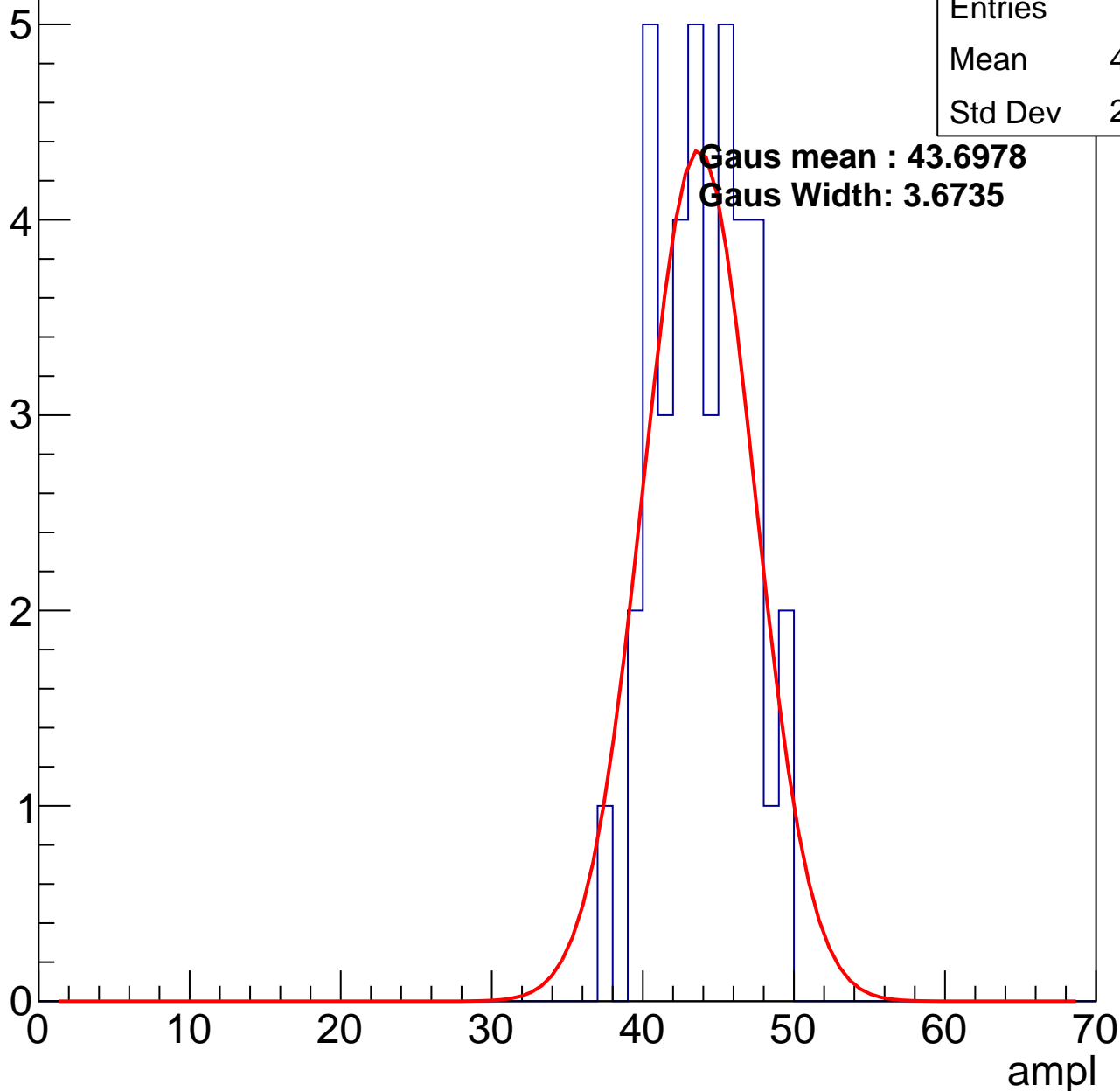


B0L001S, U18-ch67, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	43.49
Std Dev	2.952

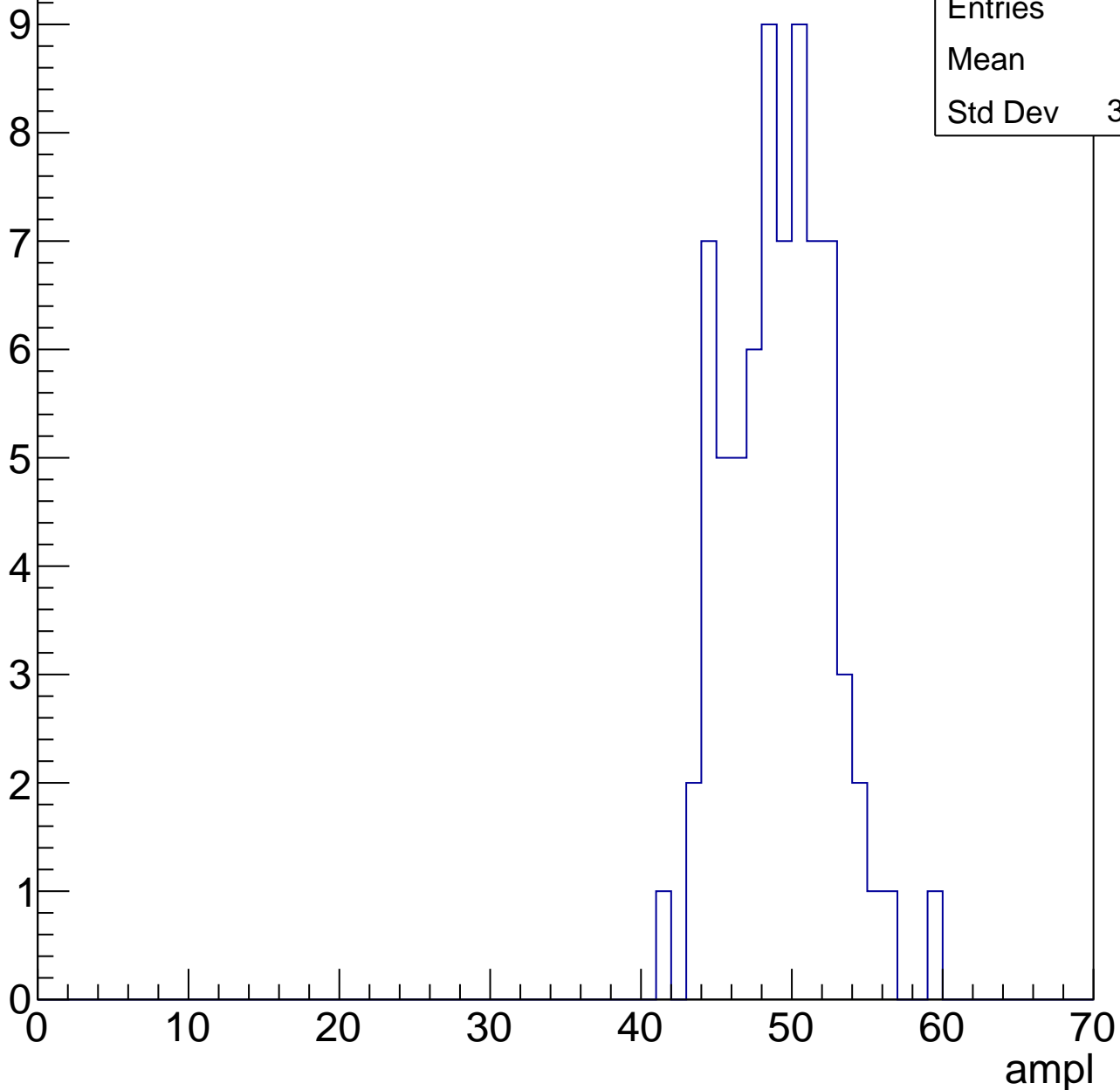


B0L001S, U18-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	48.7
Std Dev	3.403

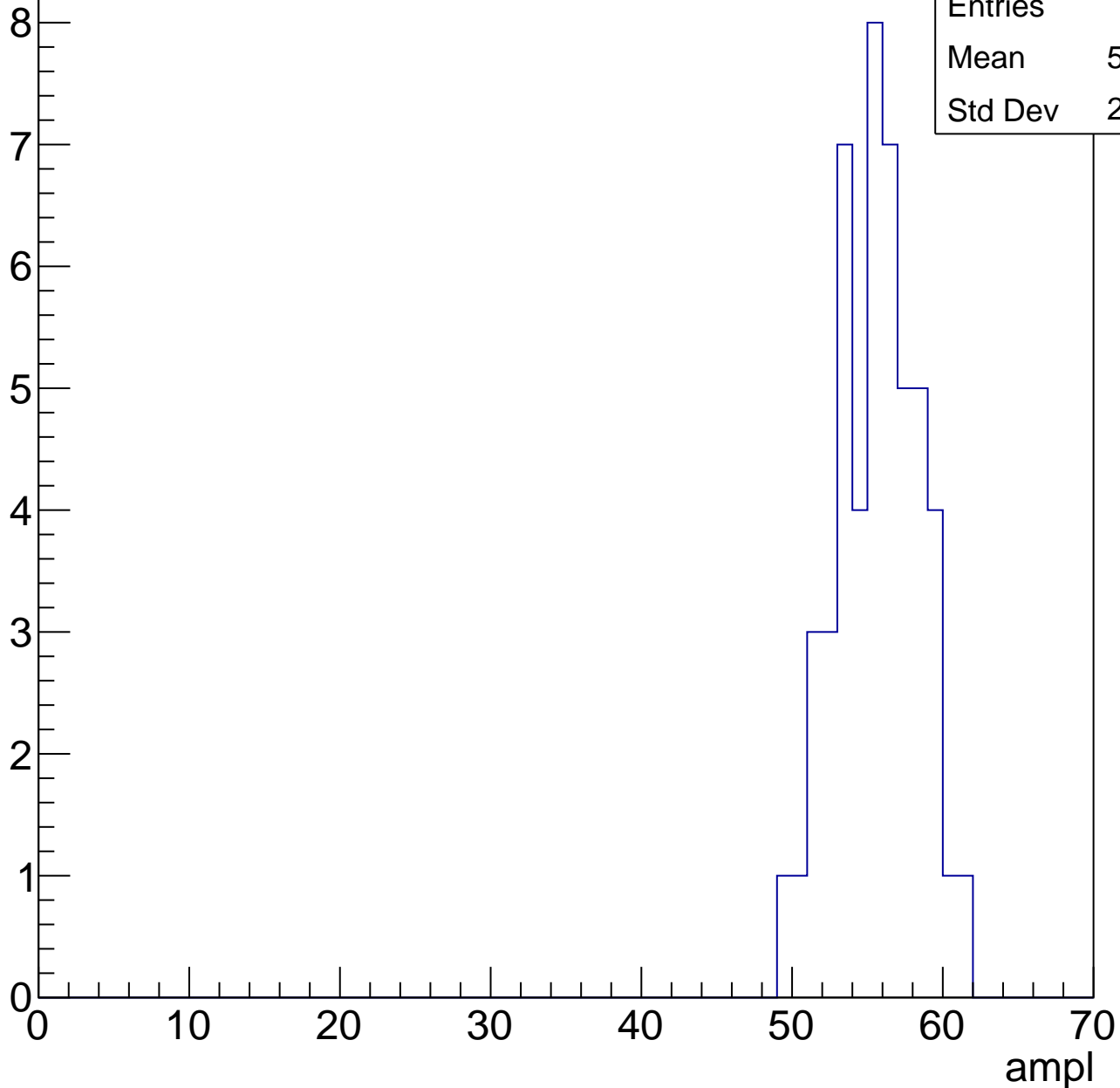


B0L001S, U18-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

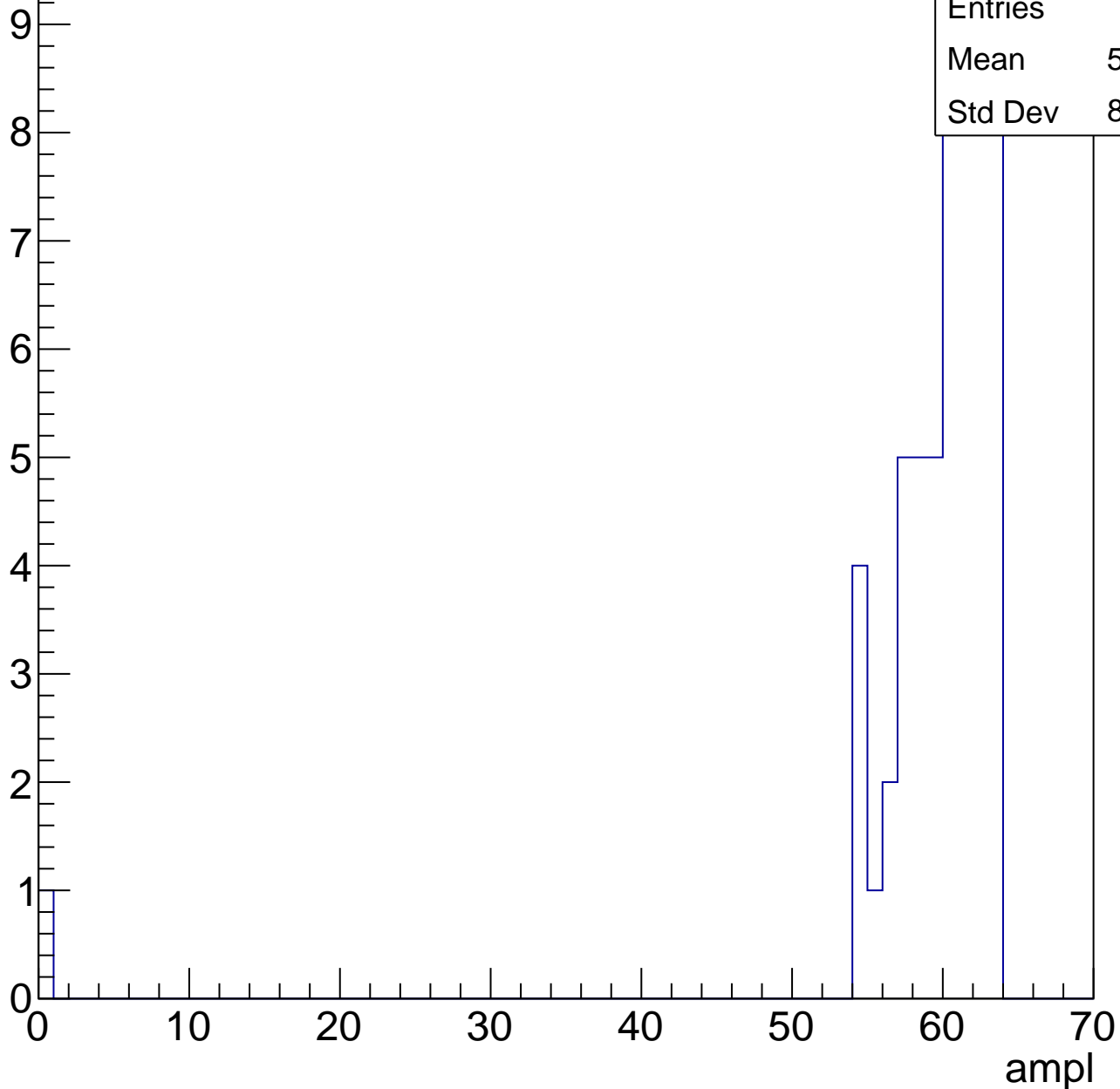
Entries	50
Mean	55.18
Std Dev	2.696



B0L001S, U18-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch68, adc0

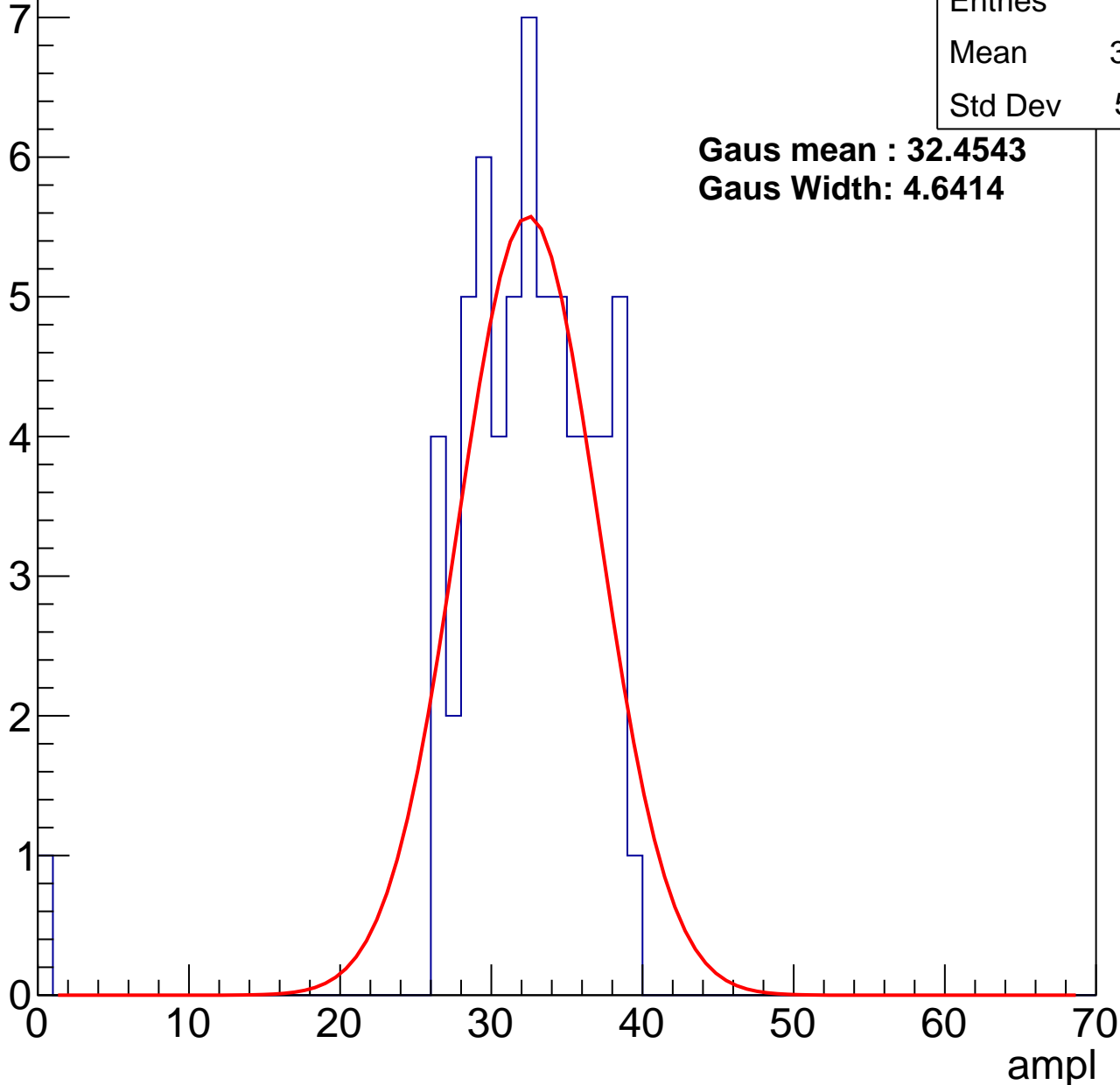
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.73
Std Dev	5.421

Gaus mean : 32.4543

Gaus Width: 4.6414



B0L001S, U18-ch68, adc1

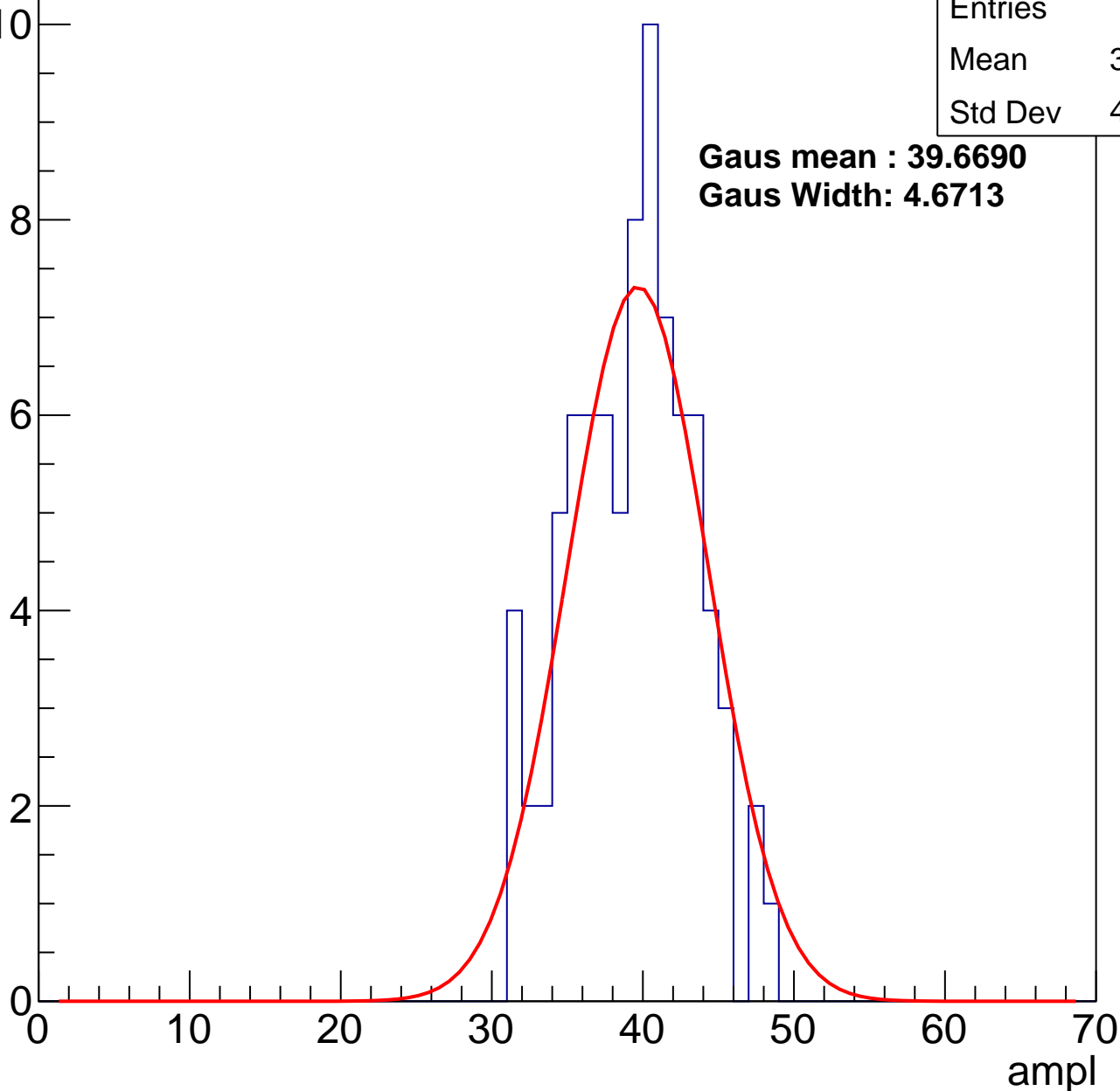
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	38.84
Std Dev	4.007

Gaus mean : 39.6690

Gaus Width: 4.6713



B0L001S, U18-ch68, adc2

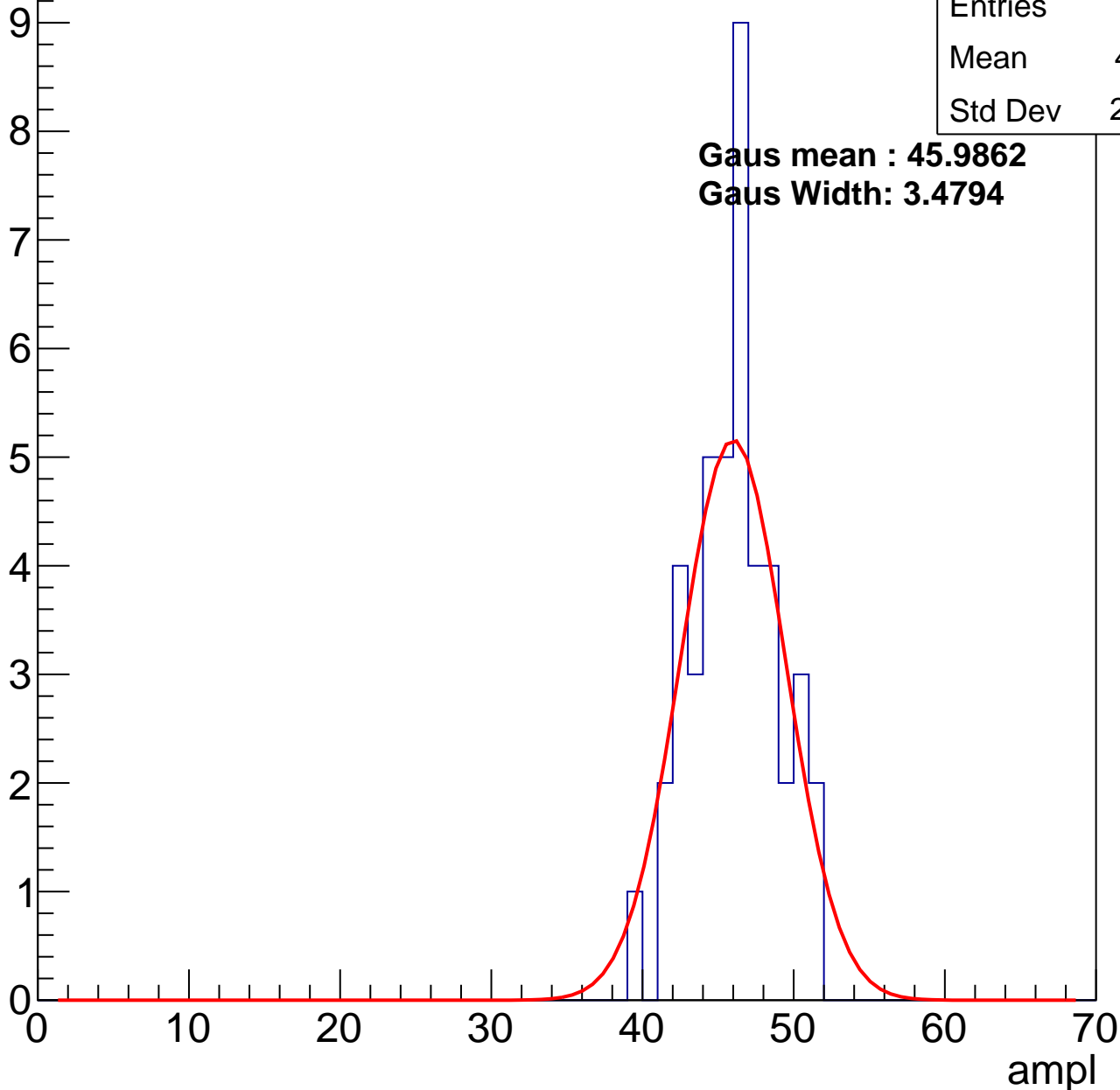
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	45.61
Std Dev	2.798

Gaus mean : 45.9862

Gaus Width: 3.4794

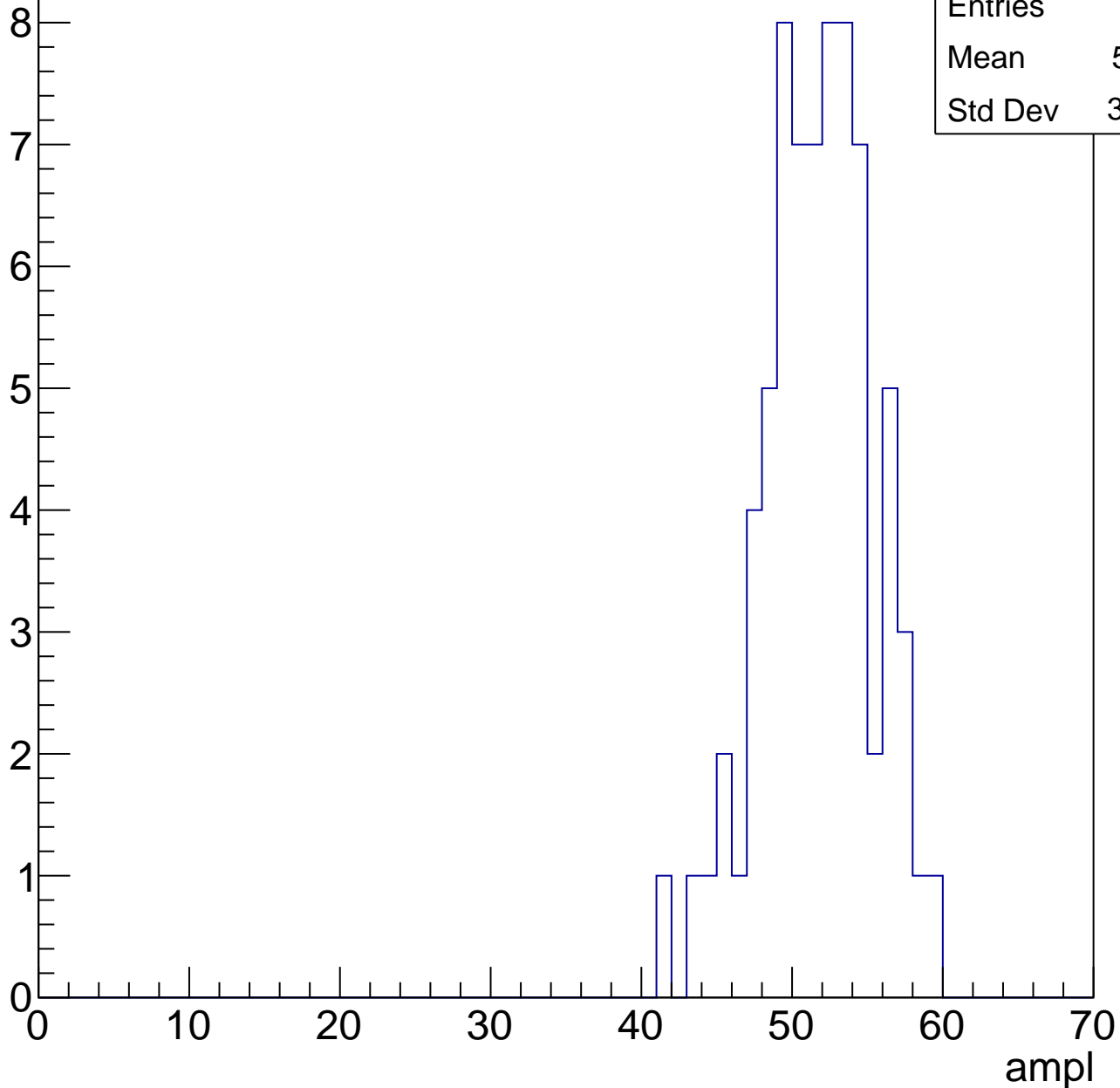


B0L001S, U18-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.21
Std Dev	3.605

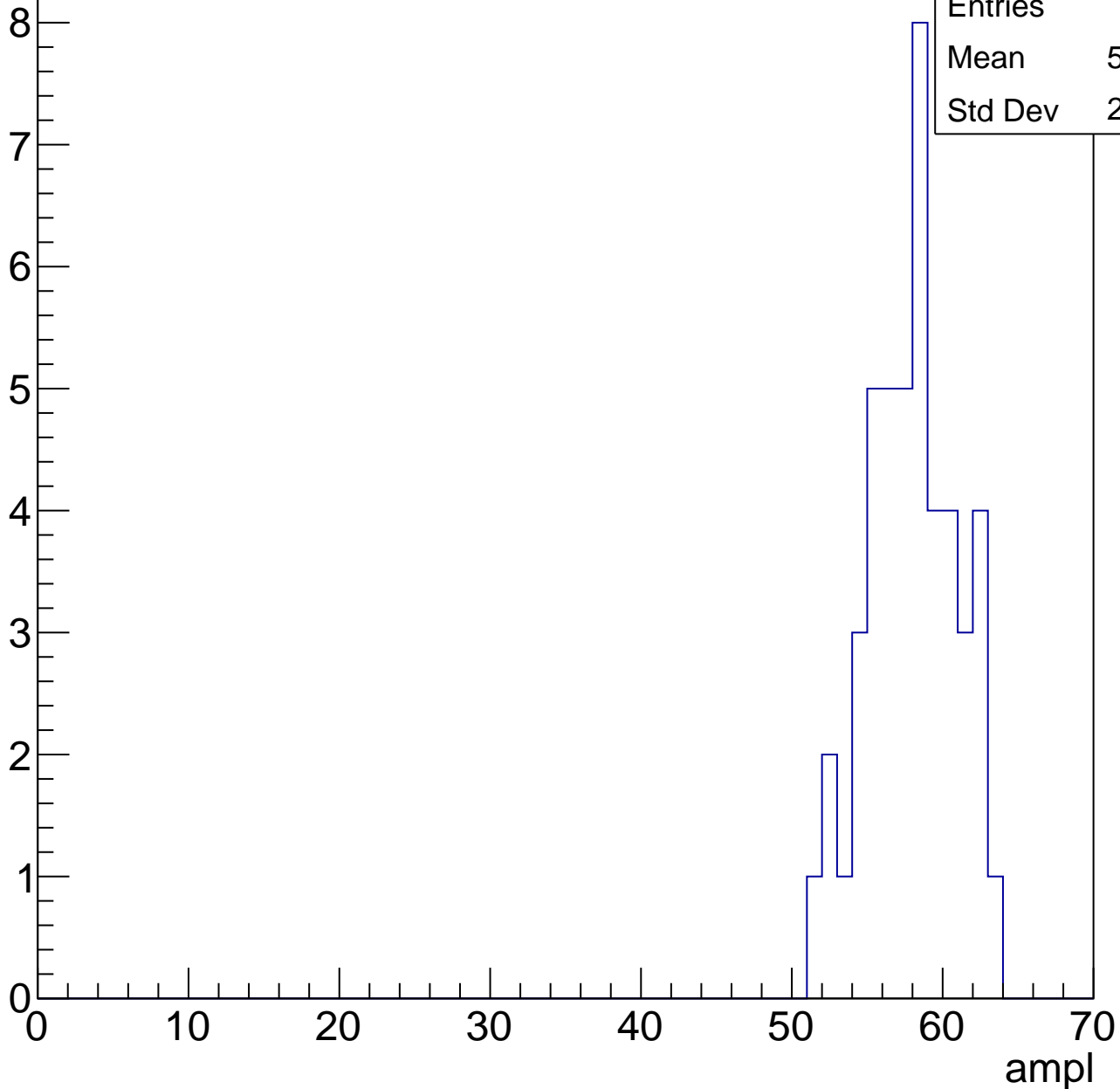


B0L001S, U18-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.48
Std Dev	2.902

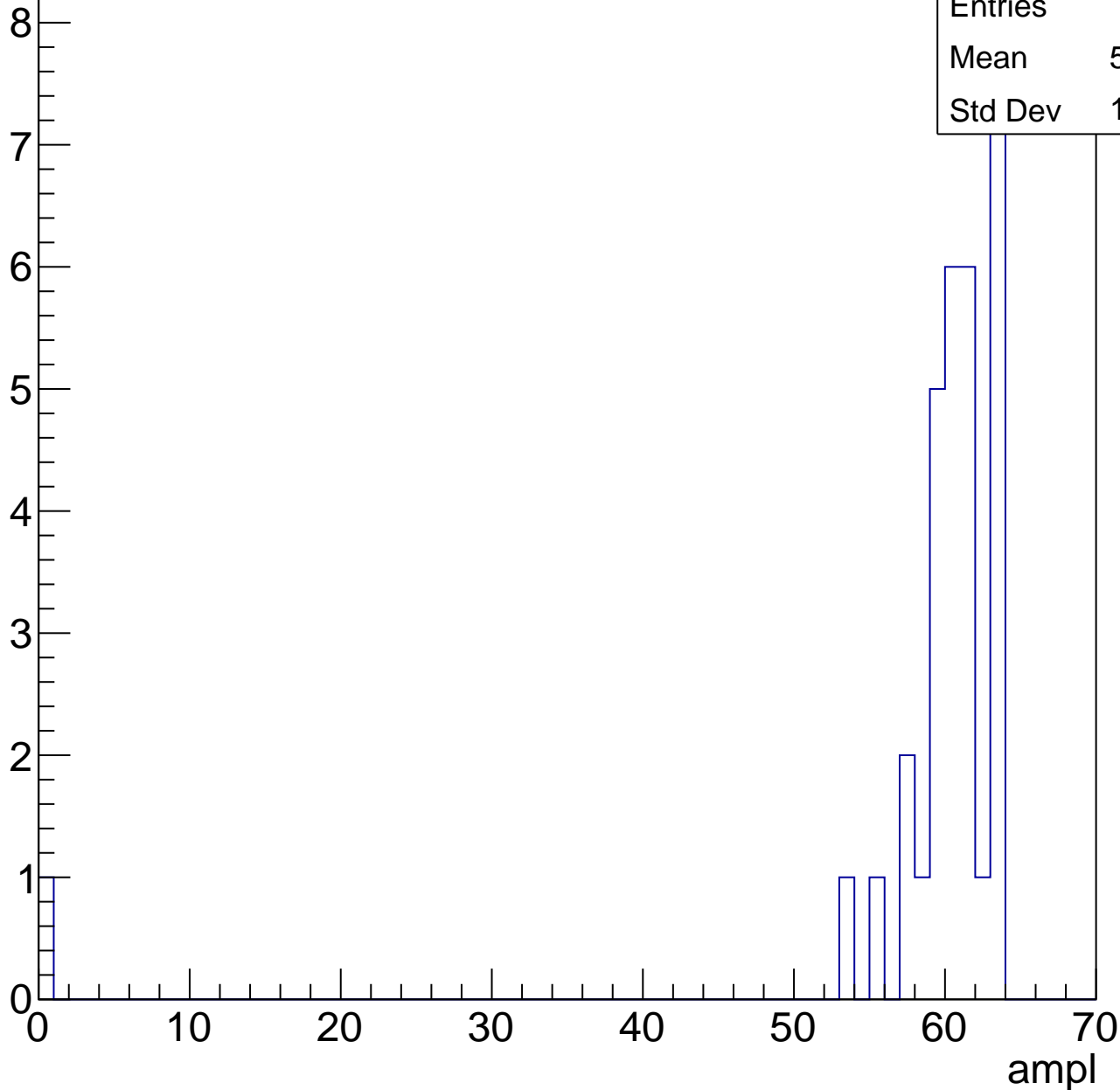


B0L001S, U18-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.34
Std Dev	10.75

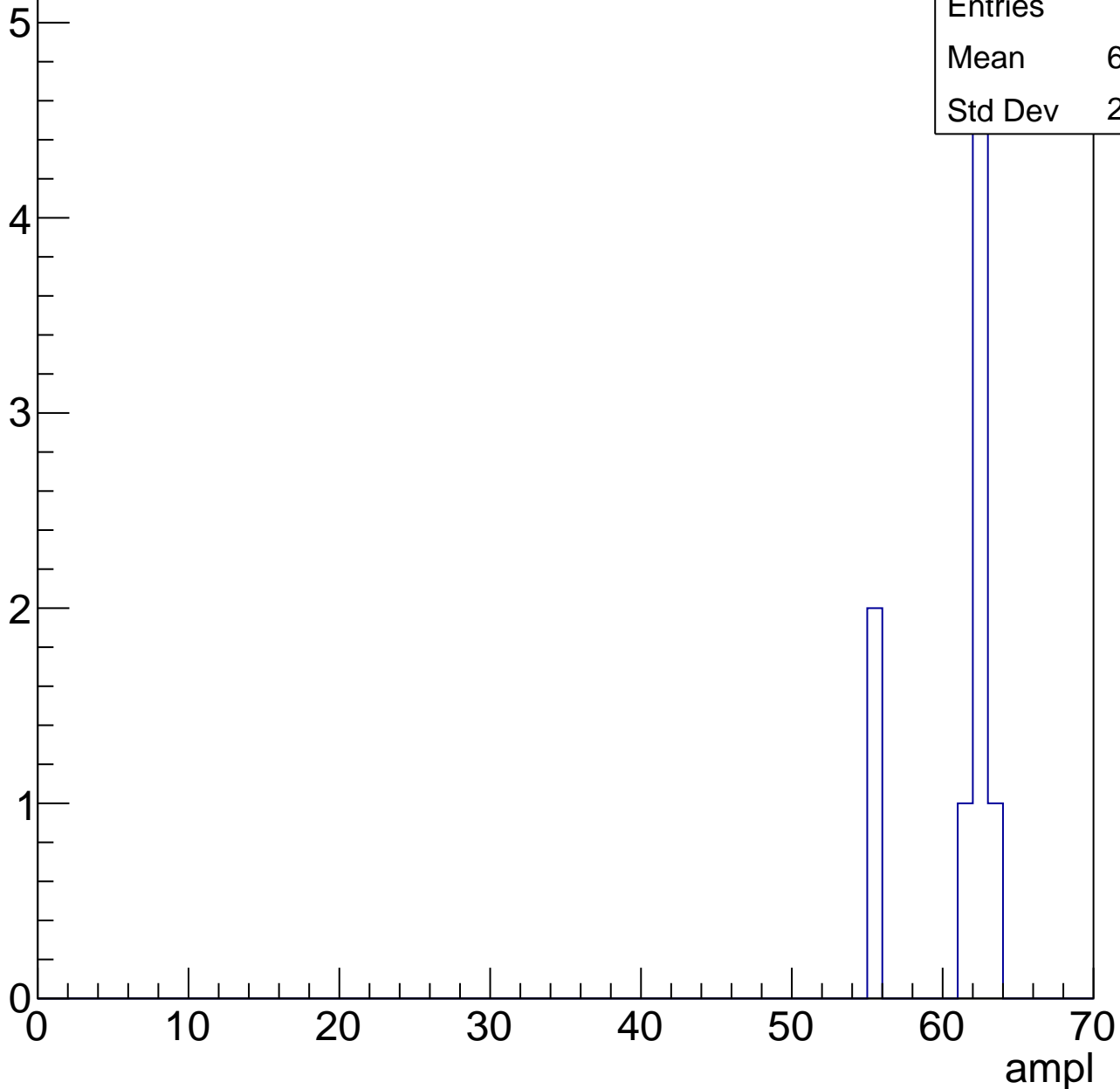


B0L001S, U18-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	60.44
Std Dev	2.948



B0L001S, U18-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch69, adc0

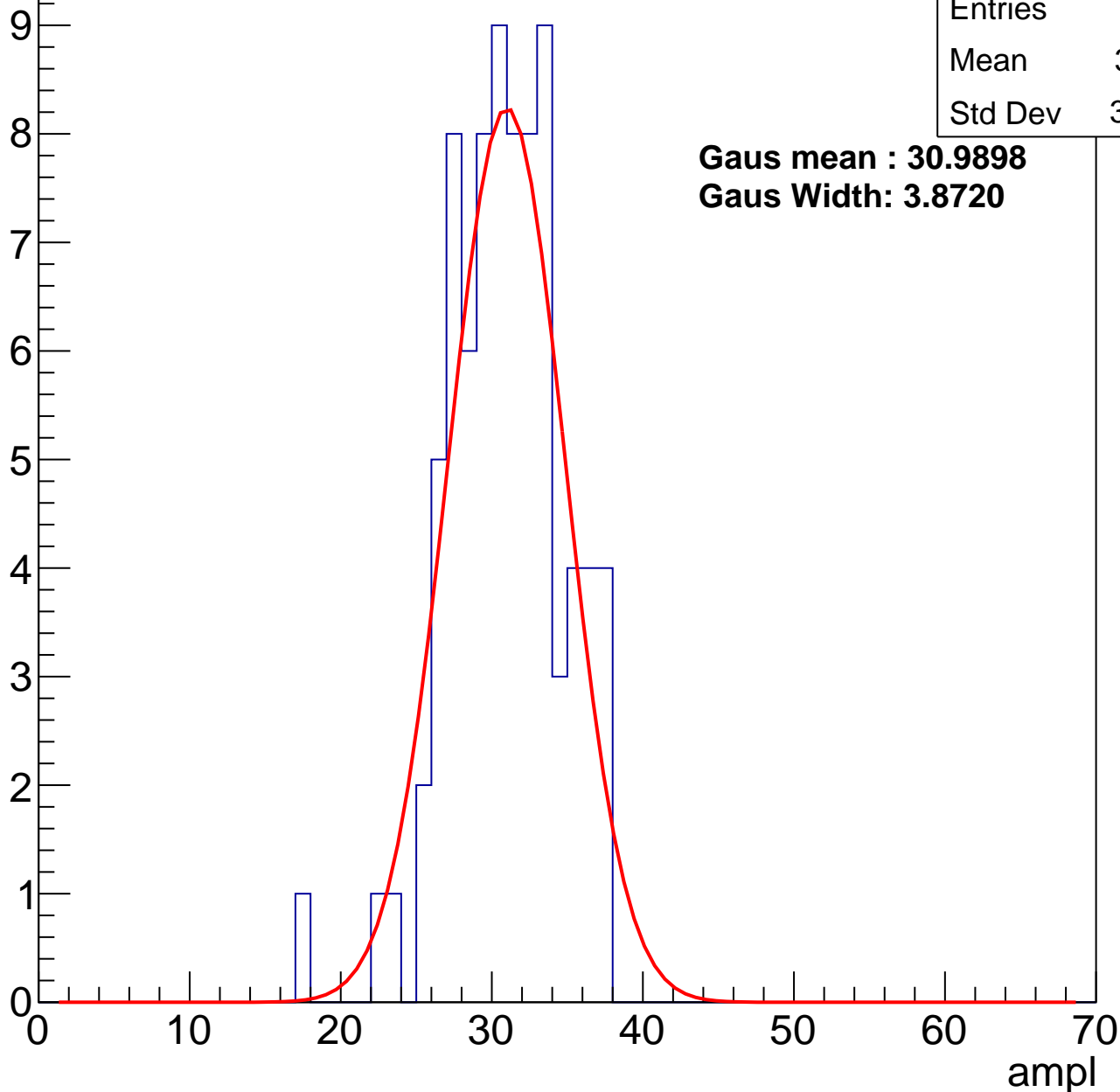
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.41
Std Dev	3.704

Gaus mean : 30.9898

Gaus Width: 3.8720



B0L001S, U18-ch69, adc1

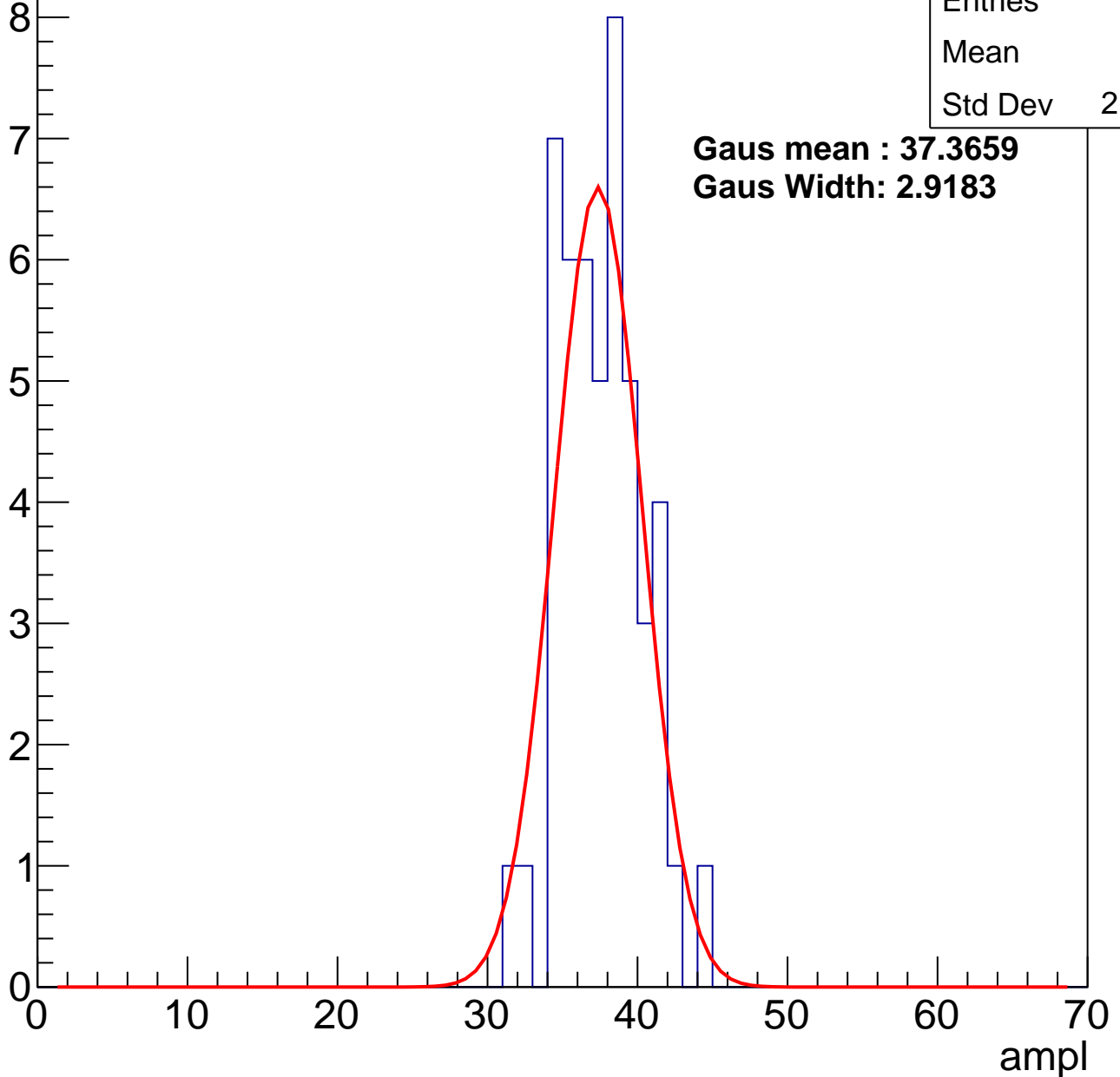
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	37.1
Std Dev	2.687

Gaus mean : 37.3659

Gaus Width: 2.9183



B0L001S, U18-ch69, adc2

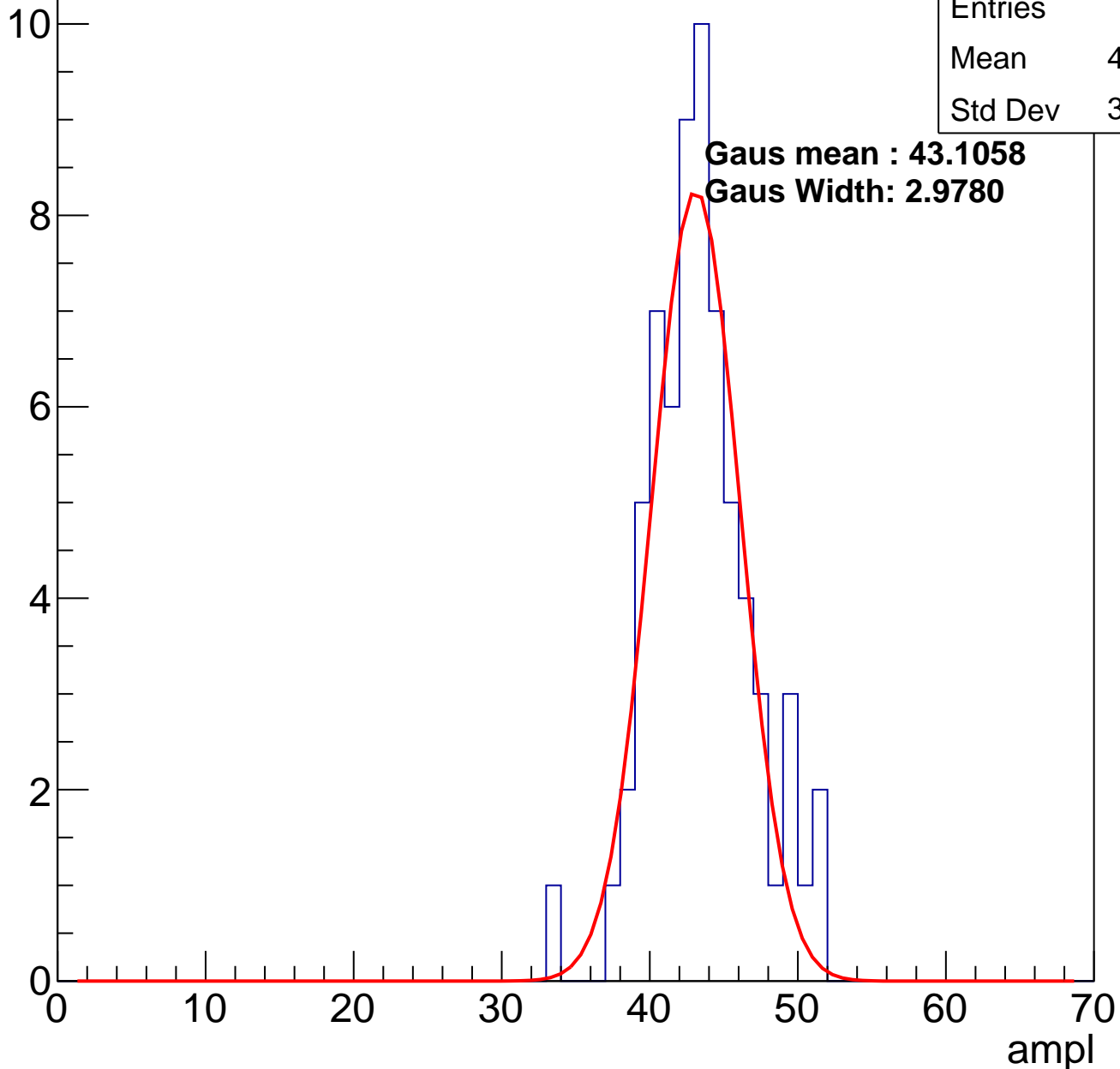
calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	42.99
Std Dev	3.419

Gaus mean : 43.1058

Gaus Width: 2.9780

Entry

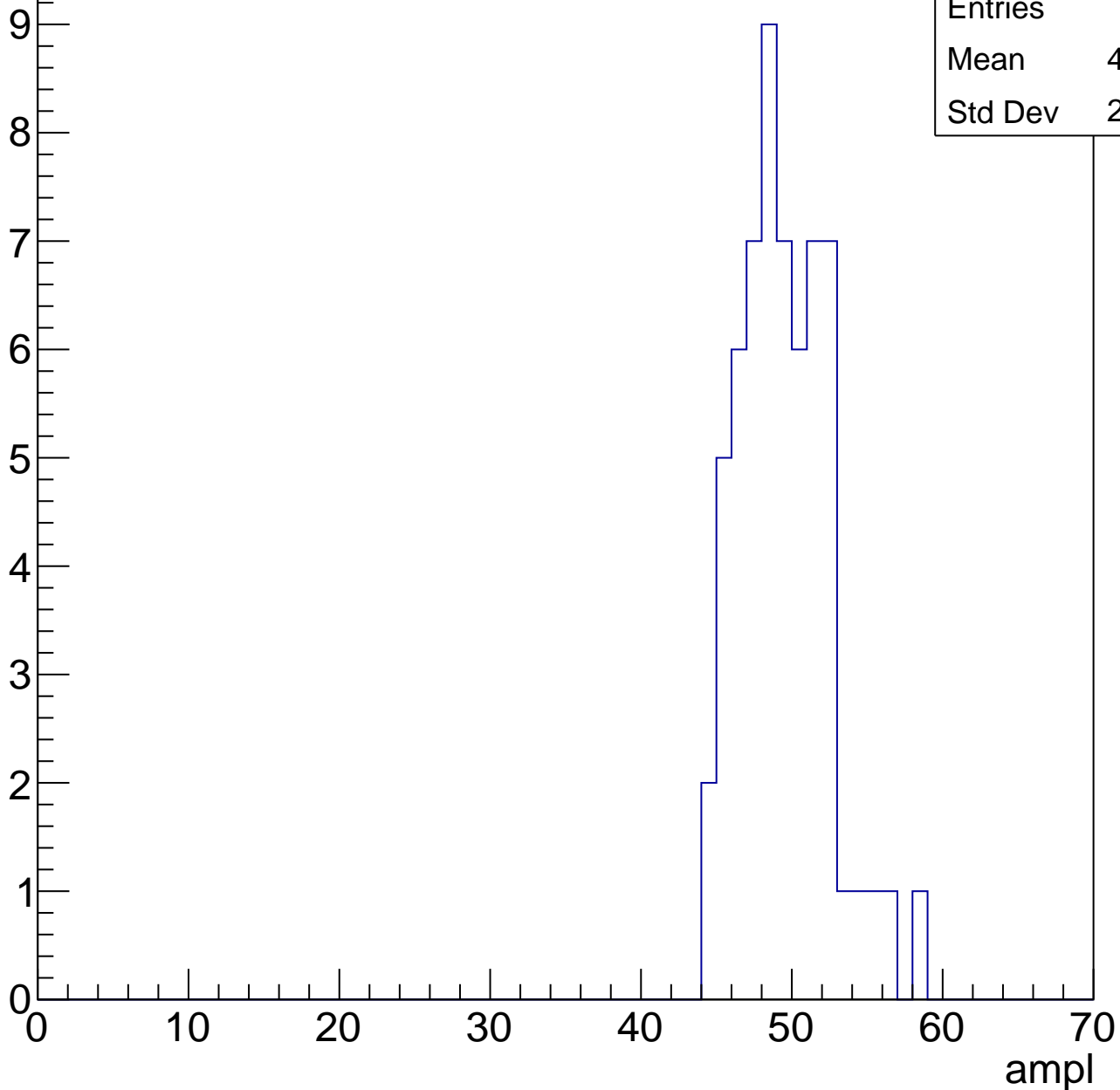


B0L001S, U18-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

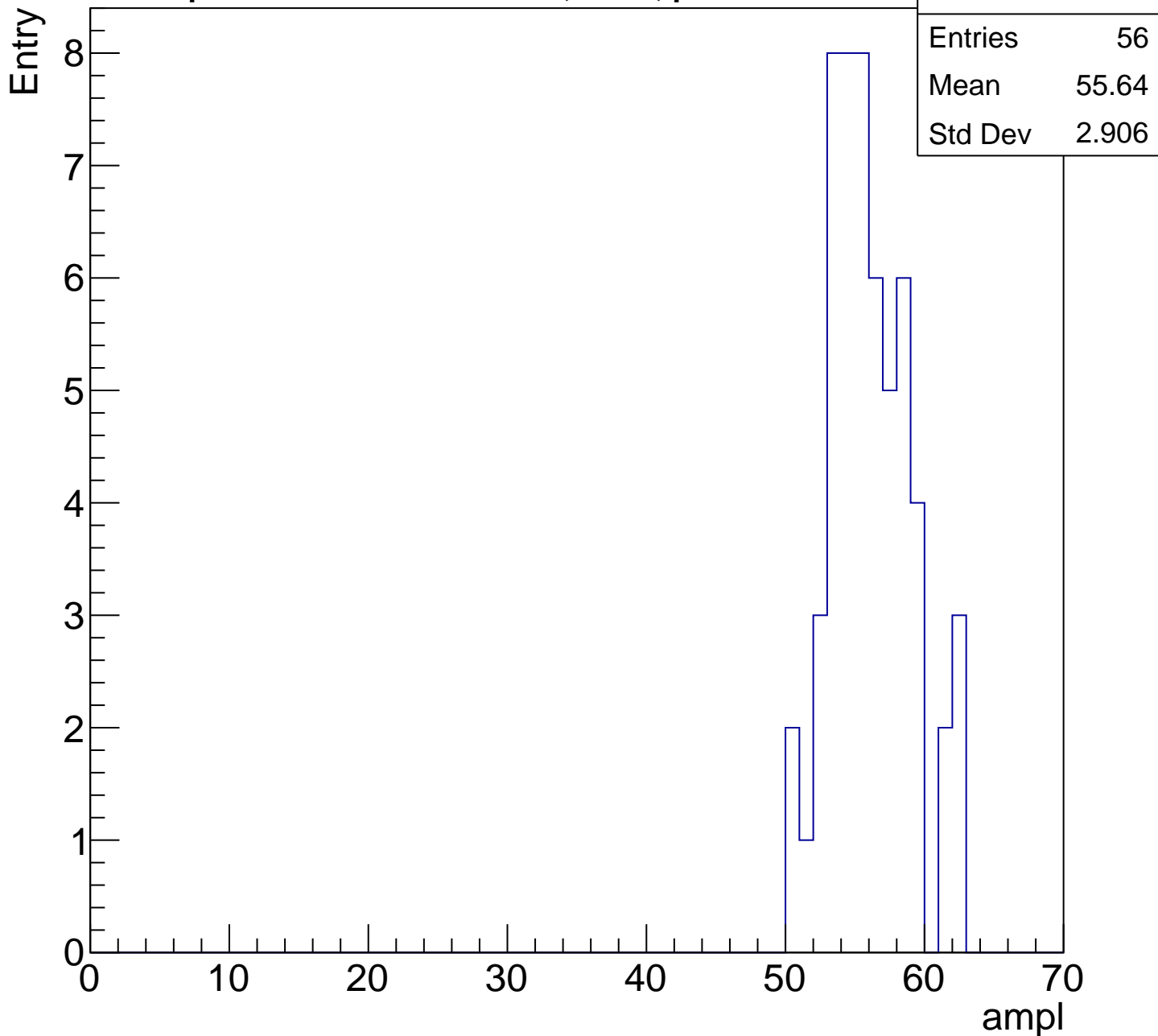
Entry

Entries	61
Mean	49.02
Std Dev	2.934



B0L001S, U18-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

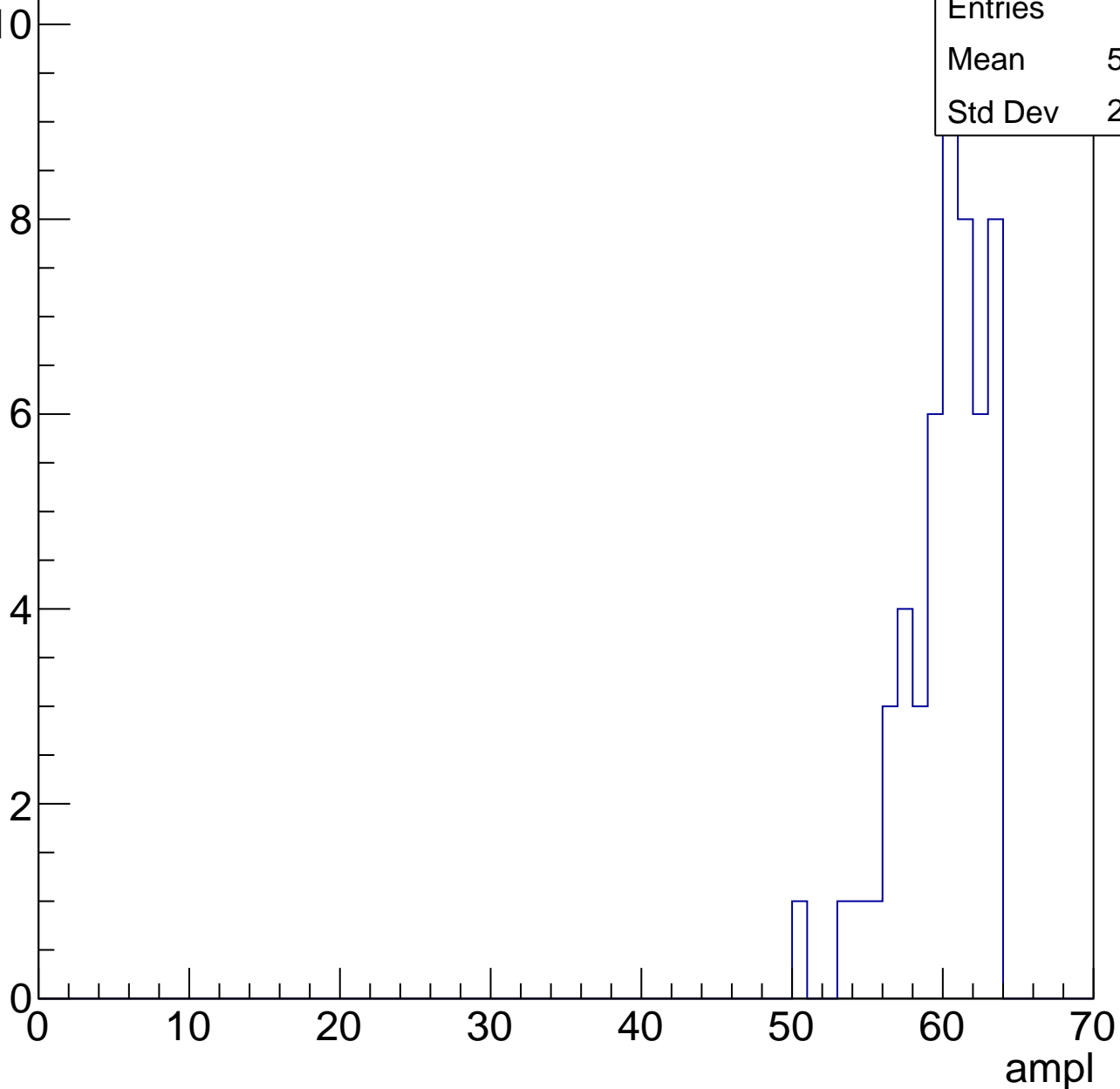


B0L001S, U18-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

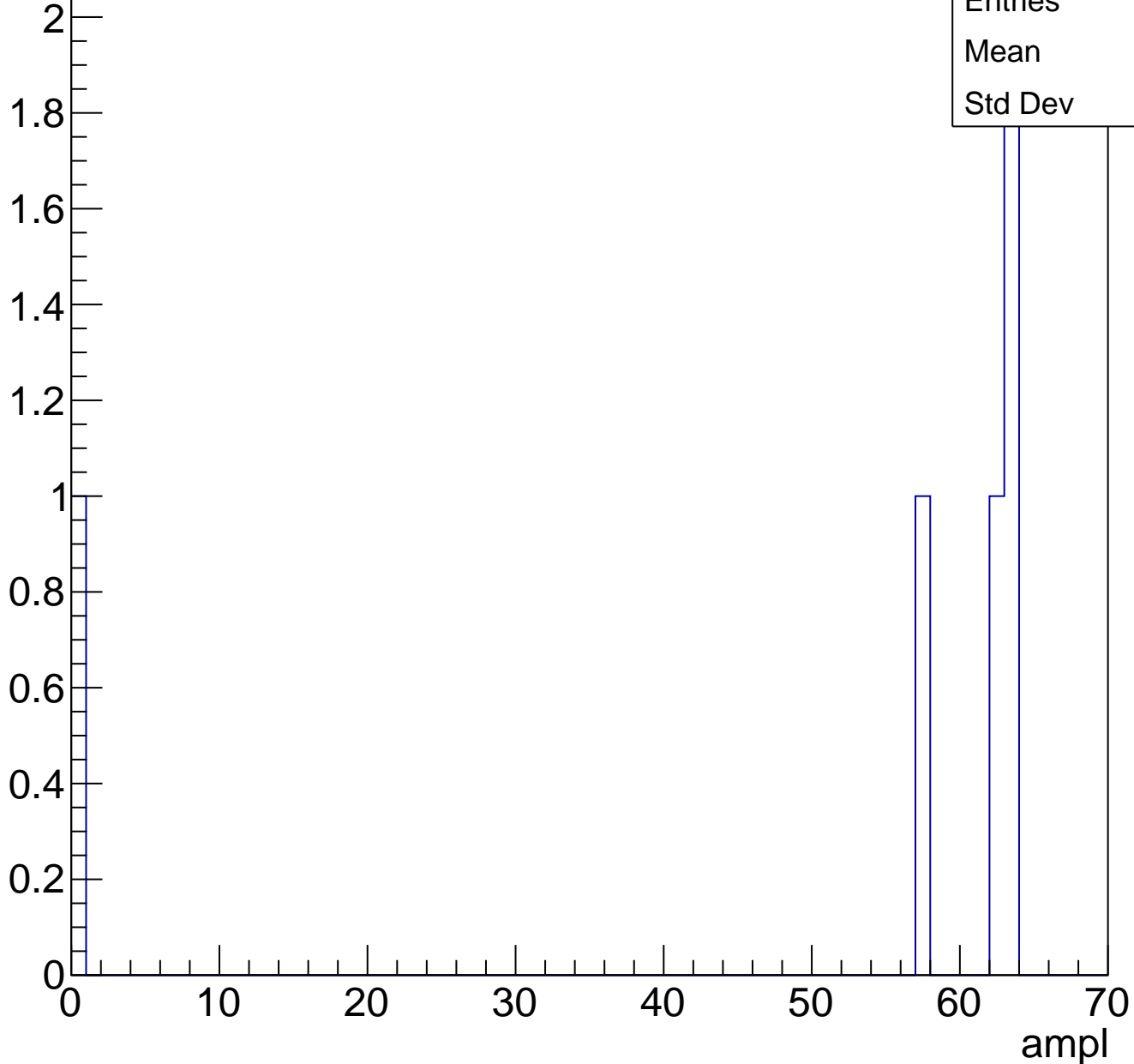
Entries	52
Mean	59.62
Std Dev	2.802



B0L001S, U18-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch70, adc0

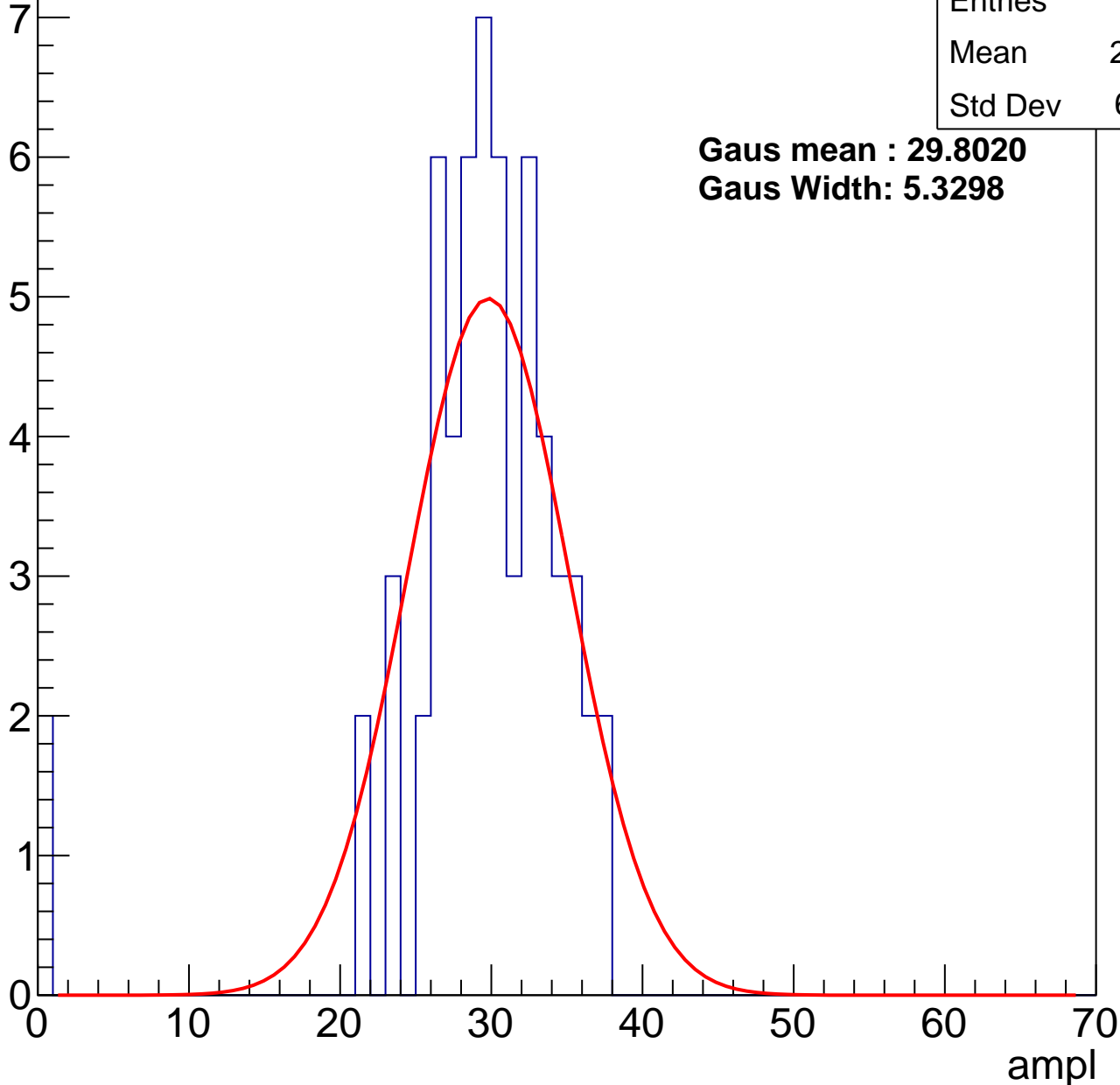
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.62
Std Dev	6.481

Gaus mean : 29.8020

Gaus Width: 5.3298



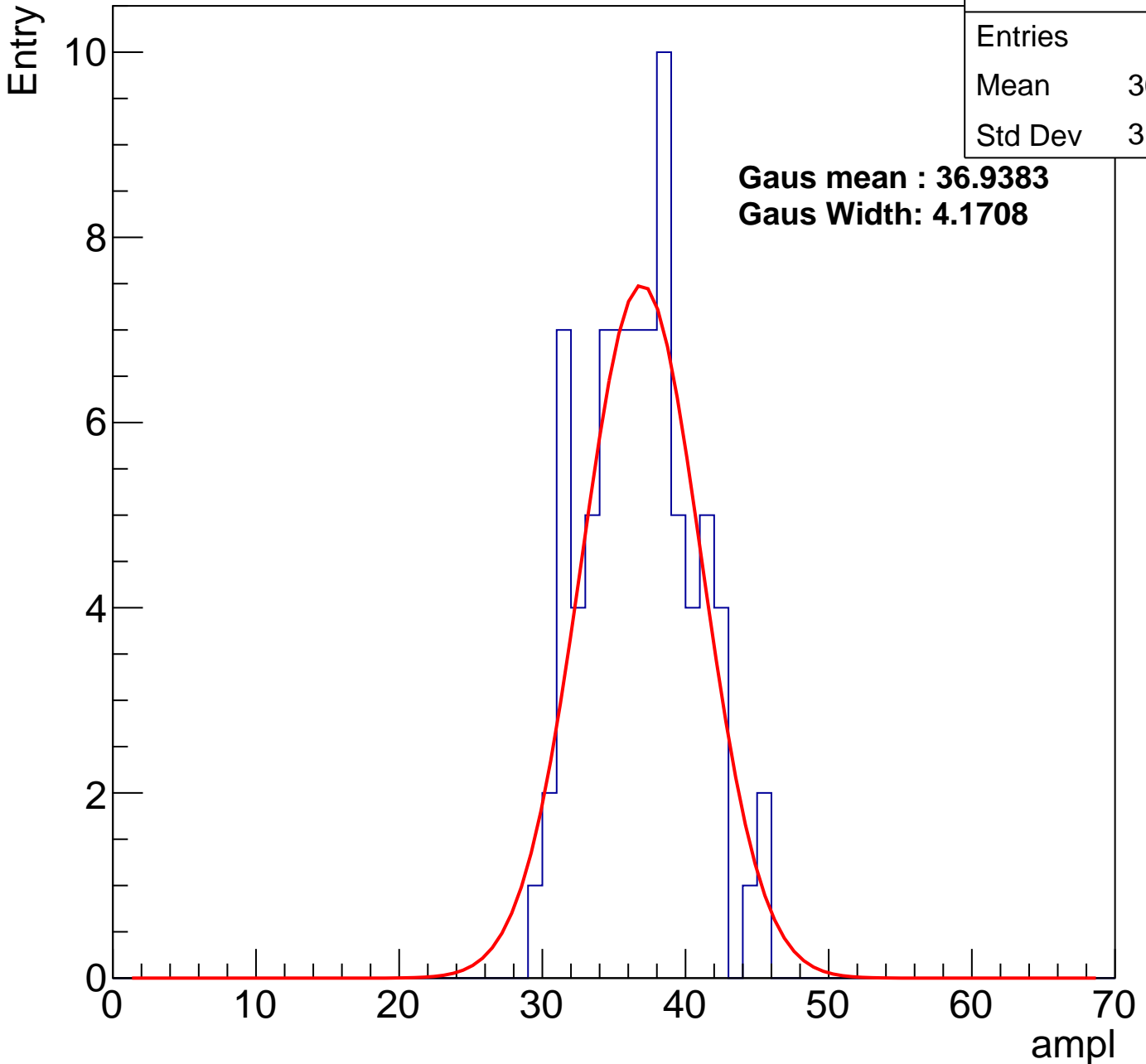
B0L001S, U18-ch70, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	36.35
Std Dev	3.724

Gaus mean : 36.9383

Gaus Width: 4.1708



B0L001S, U18-ch70, adc2

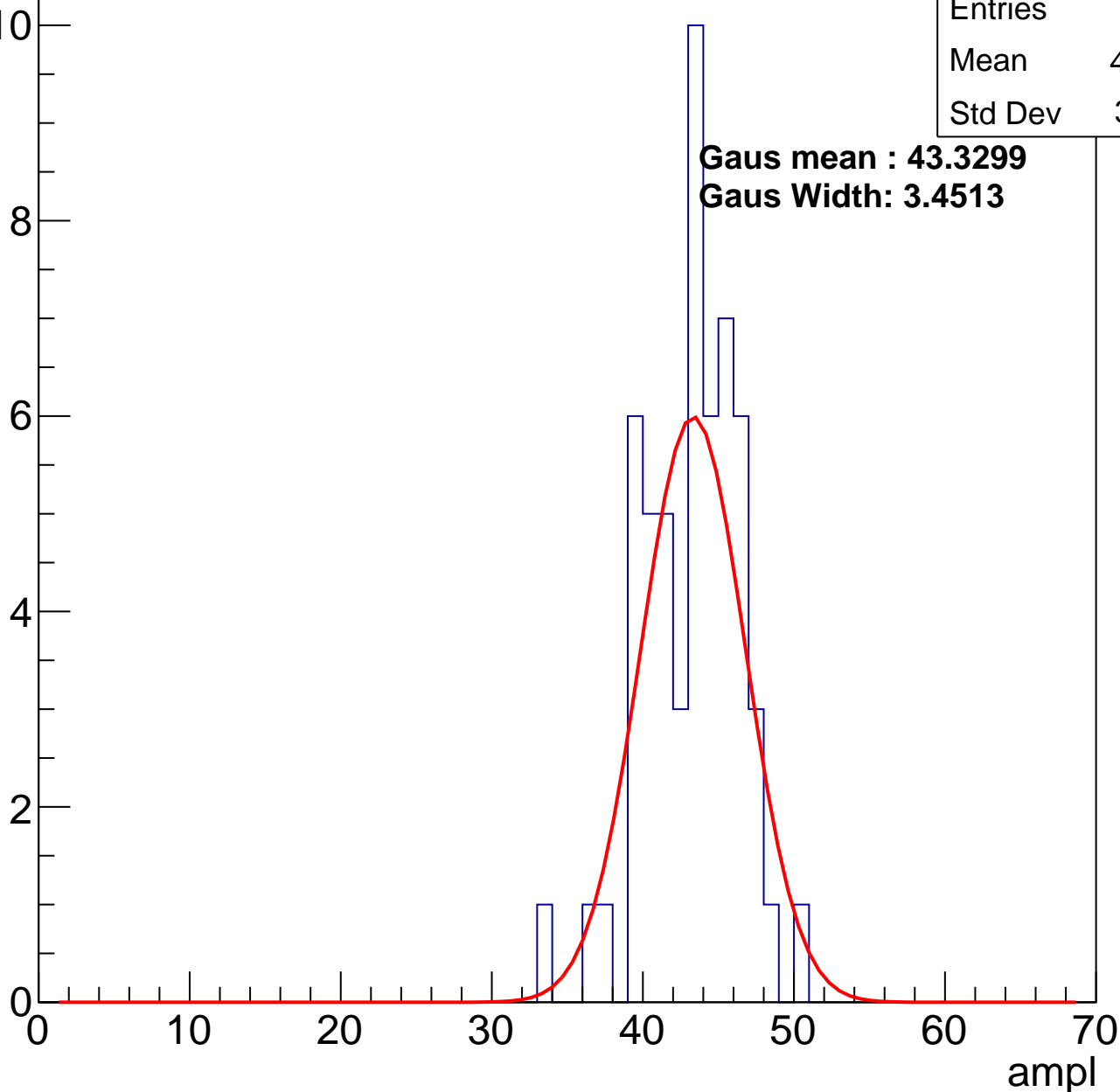
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	42.77
Std Dev	3.151

Gaus mean : 43.3299

Gaus Width: 3.4513

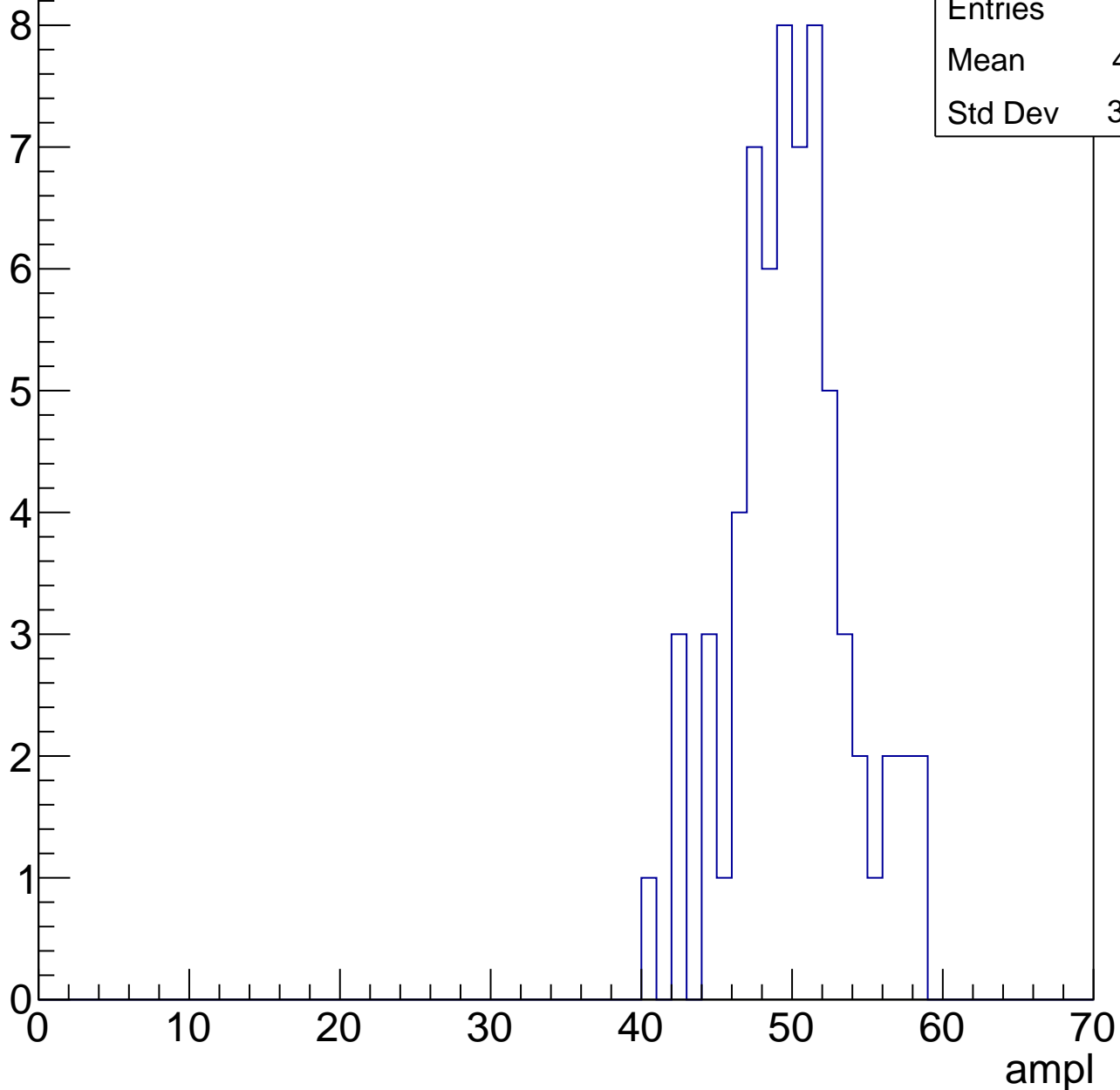


B0L001S, U18-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.51
Std Dev	3.879

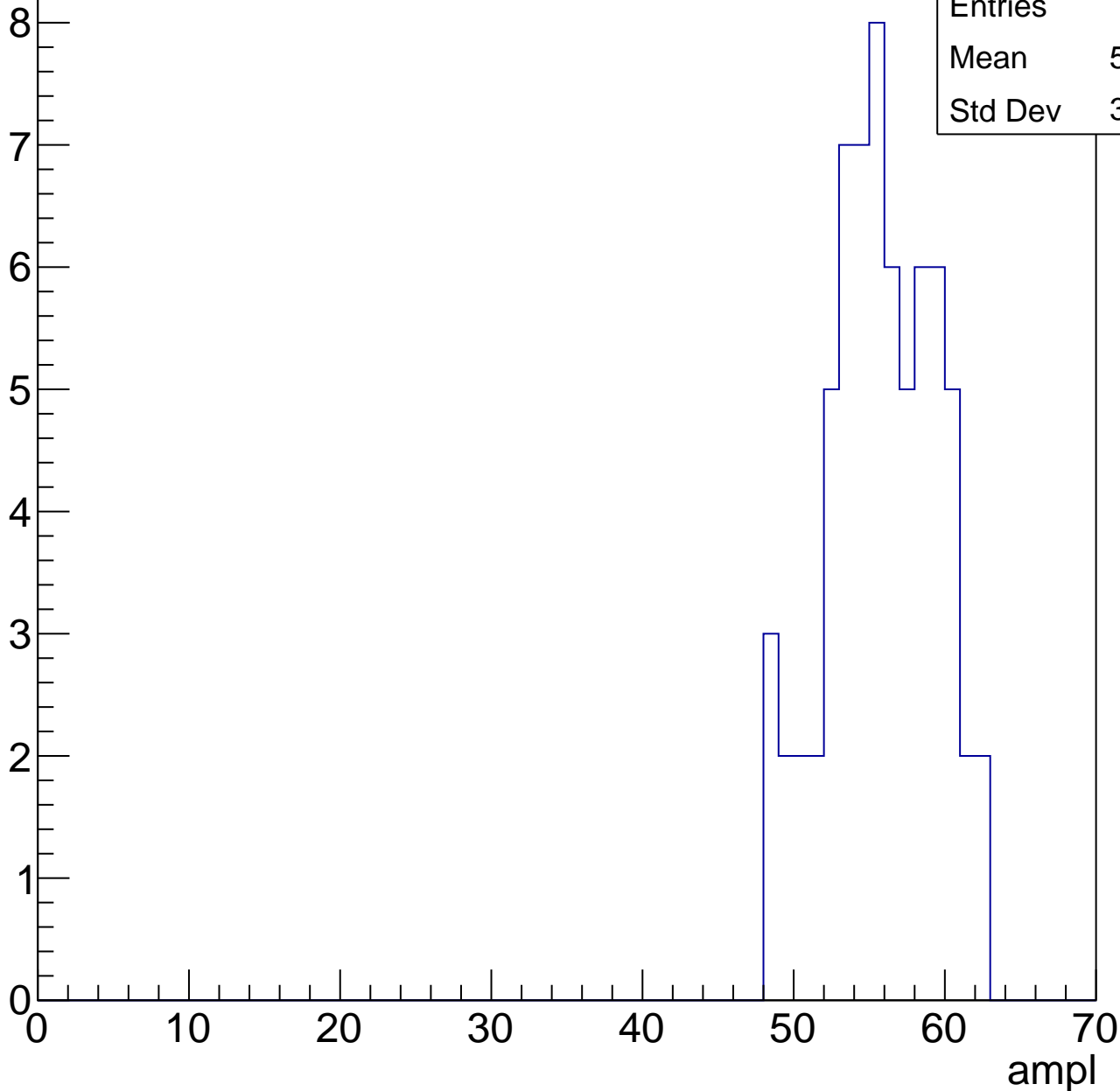


B0L001S, U18-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	55.32
Std Dev	3.525

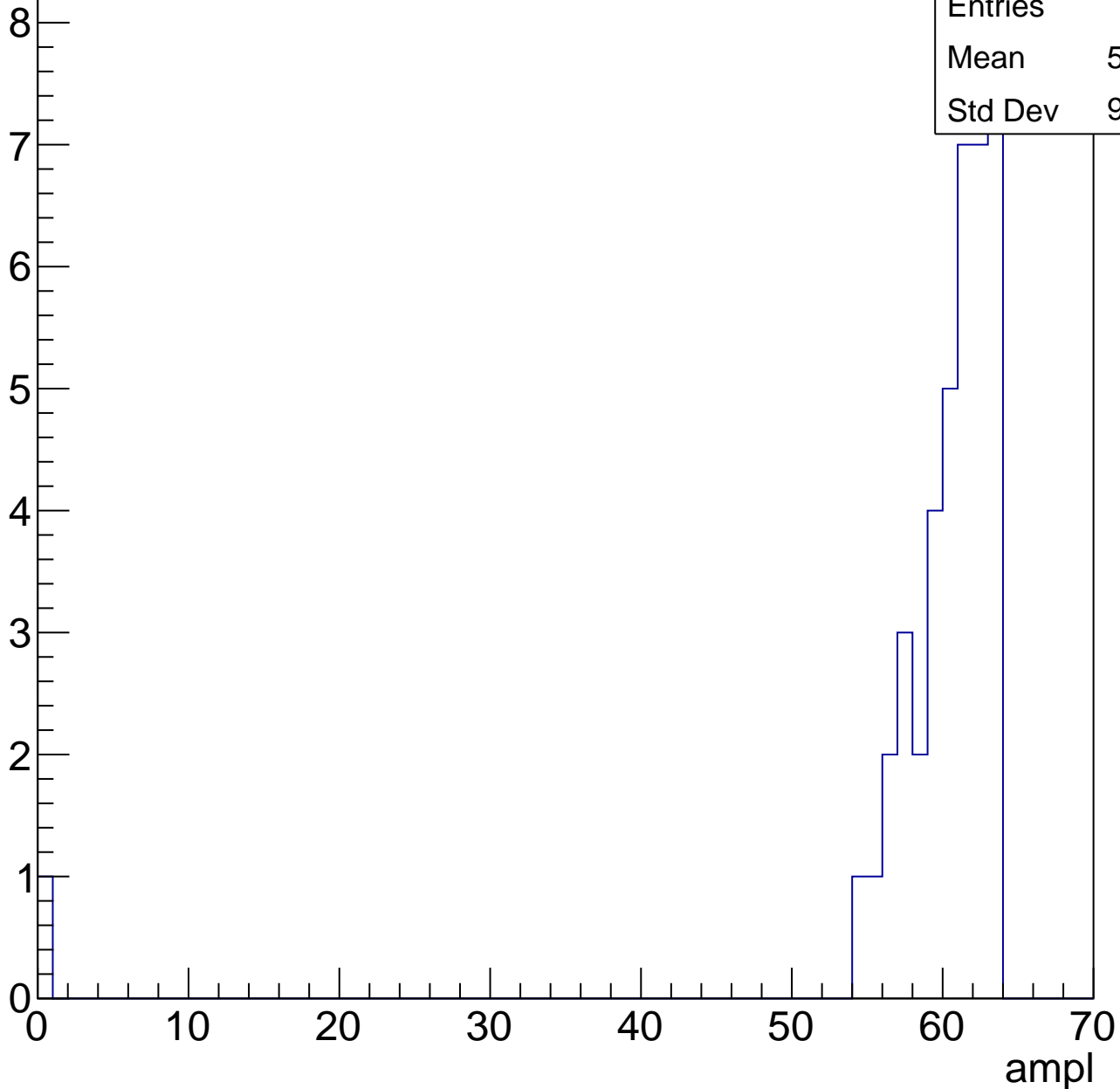


B0L001S, U18-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.76
Std Dev	9.596



B0L001S, U18-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch71, adc0

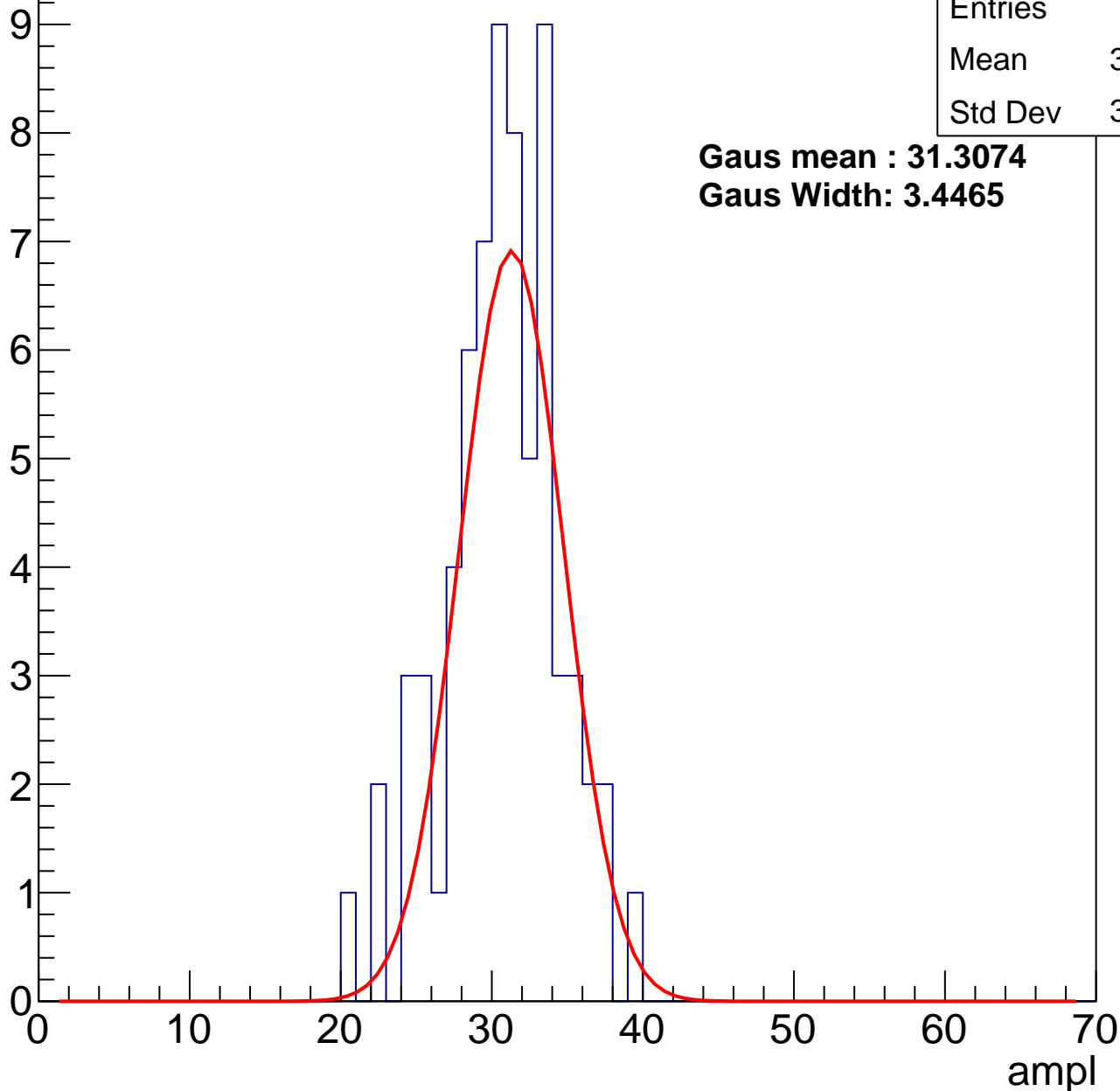
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.19
Std Dev	3.758

Gaus mean : 31.3074

Gaus Width: 3.4465



B0L001S, U18-ch71, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	36.07
Std Dev	3.329

Gaus mean : 37.2966

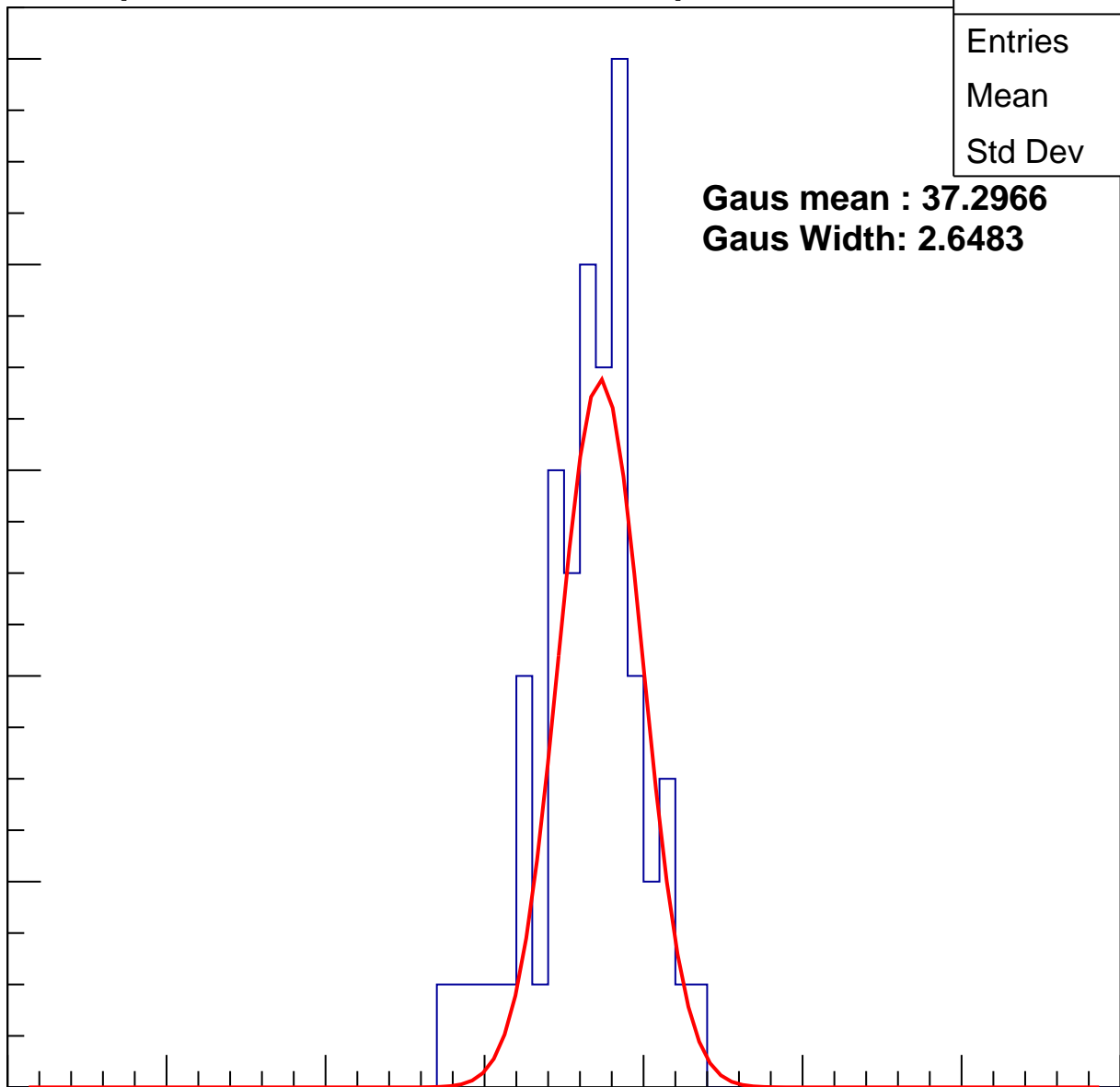
Gaus Width: 2.6483

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch71, adc2

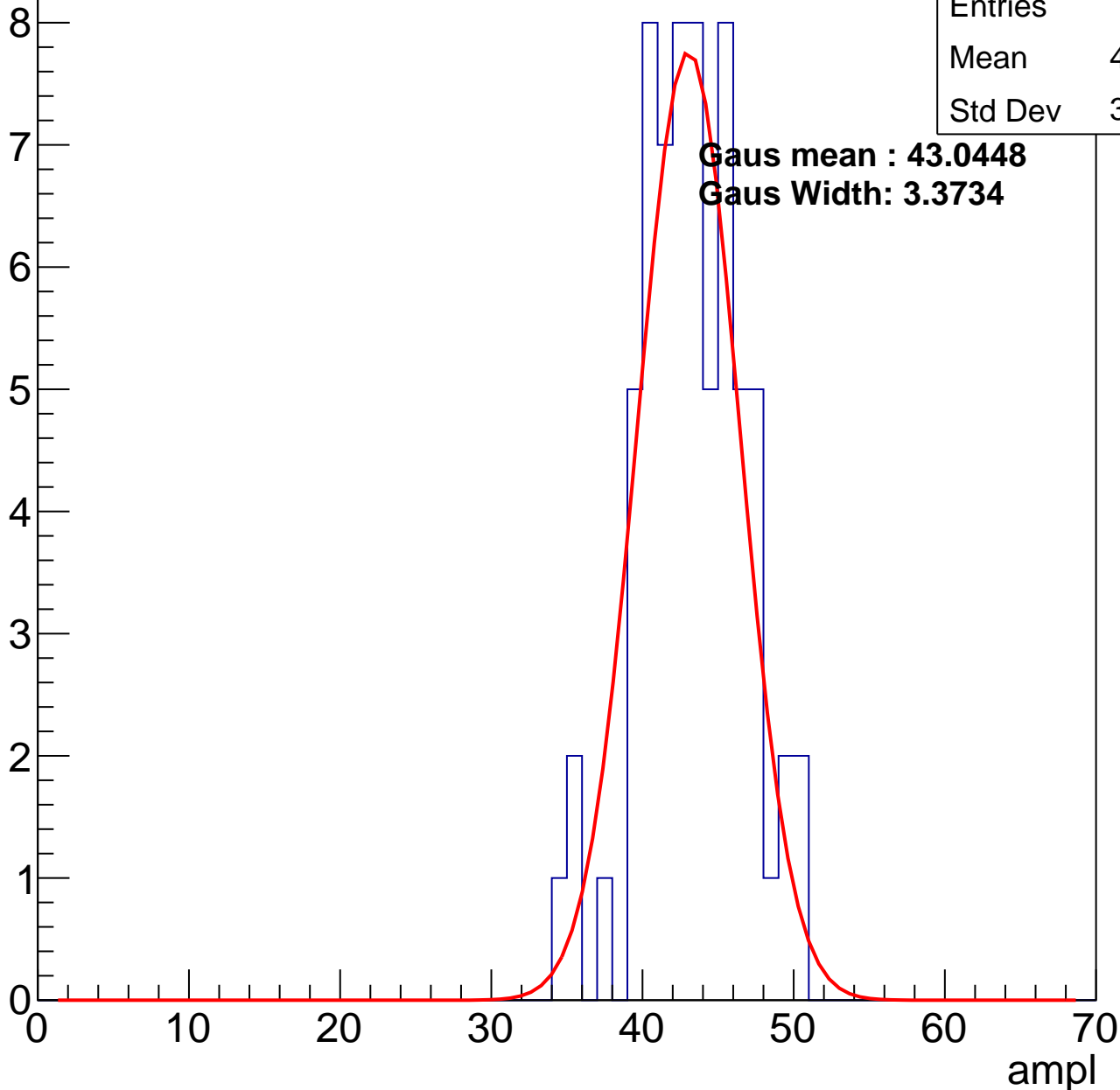
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	42.85
Std Dev	3.405

Gaus mean : 43.0448

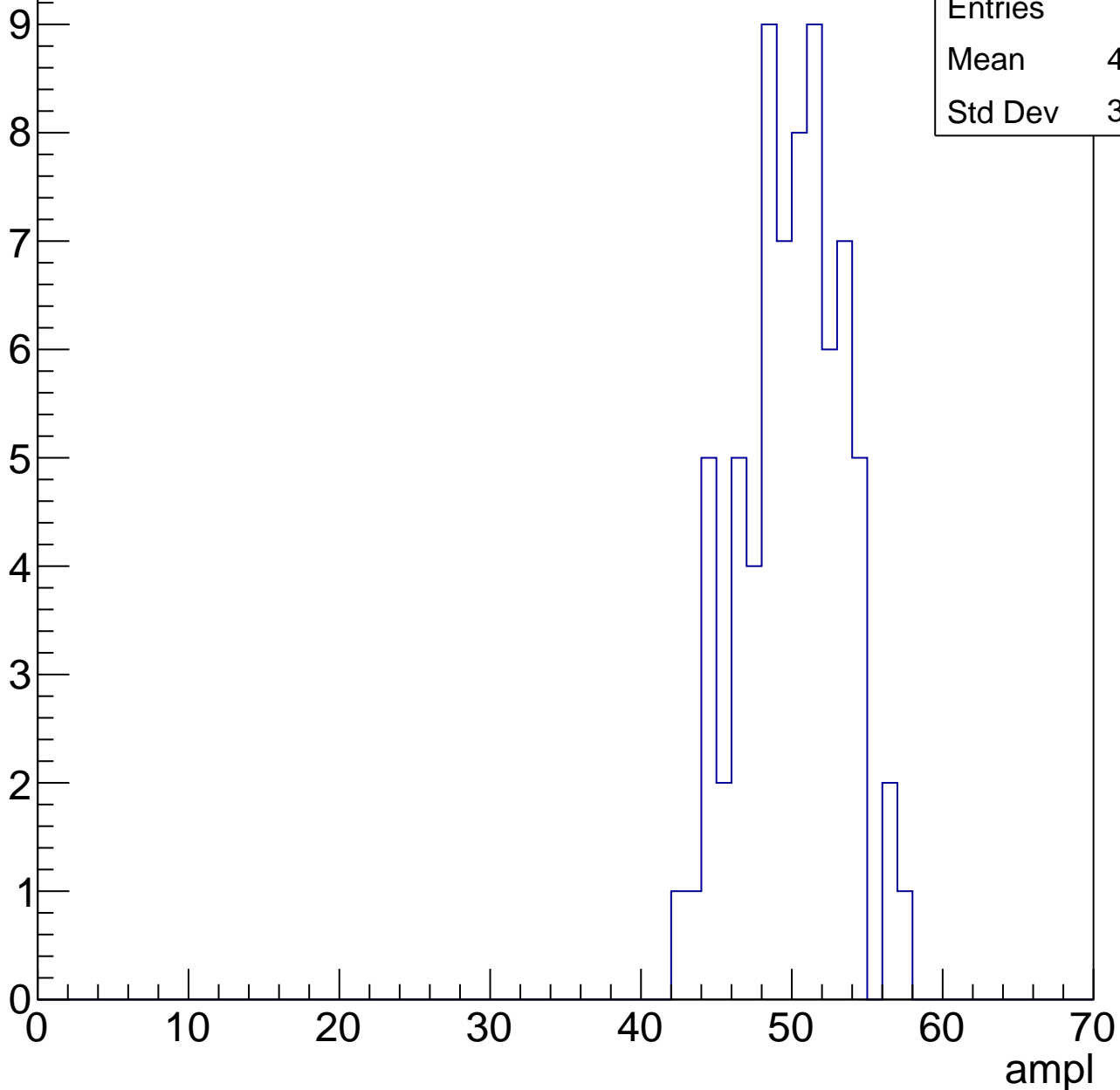
Gaus Width: 3.3734



B0L001S, U18-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



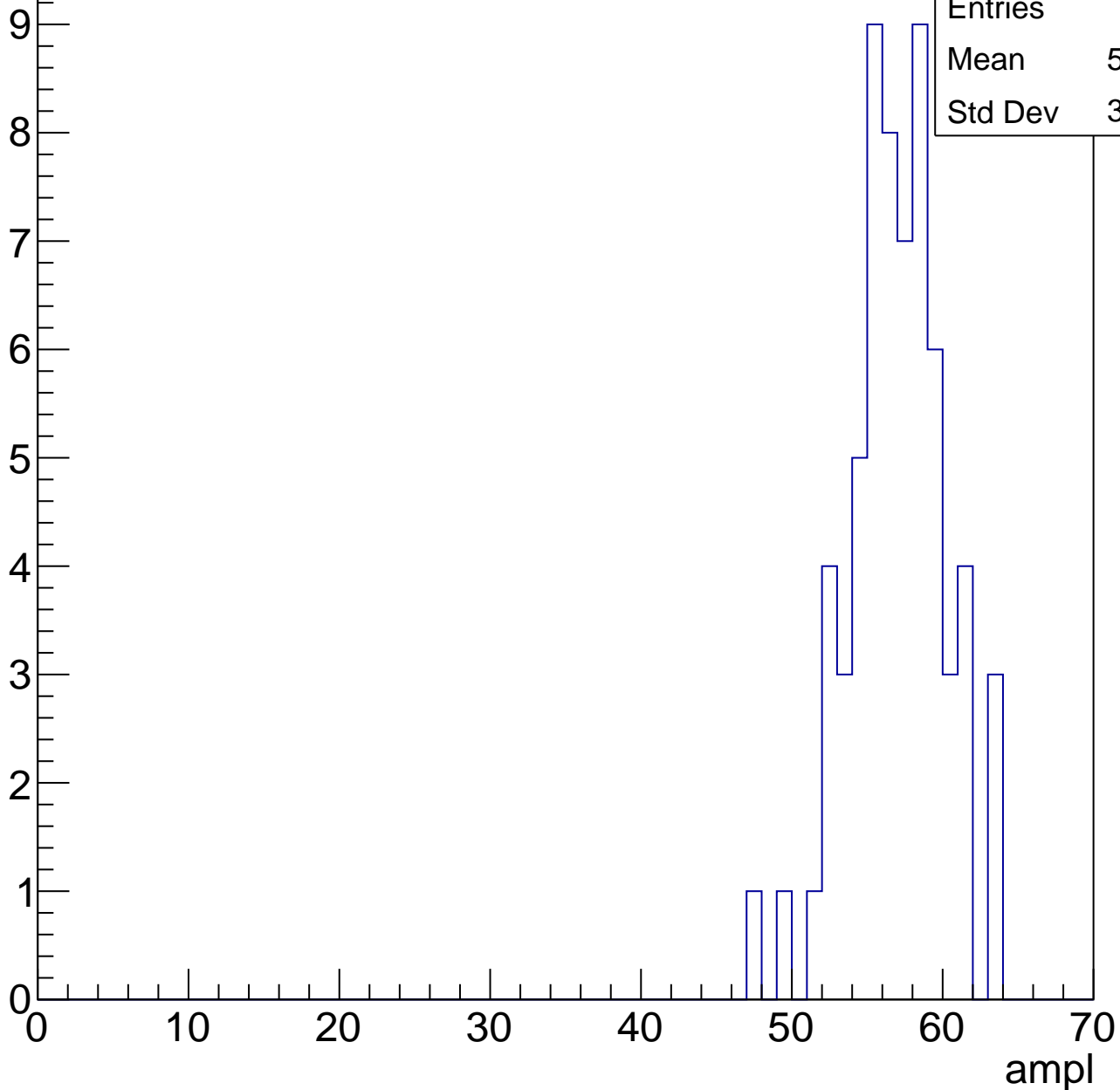
Entries	72
Mean	49.57
Std Dev	3.303

B0L001S, U18-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.48
Std Dev	3.192

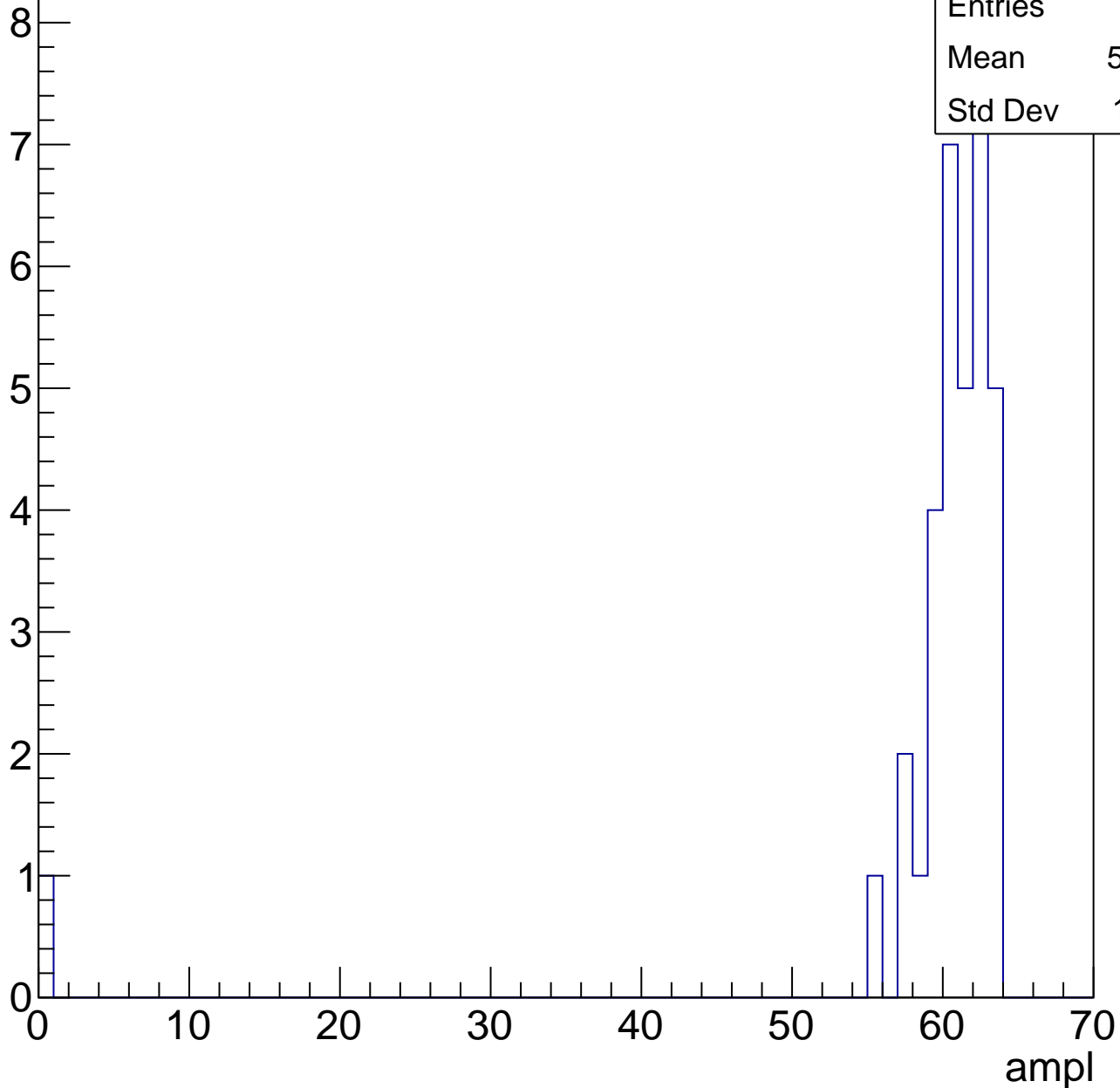


B0L001S, U18-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.79
Std Dev	10.41



B0L001S, U18-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch72, adc0

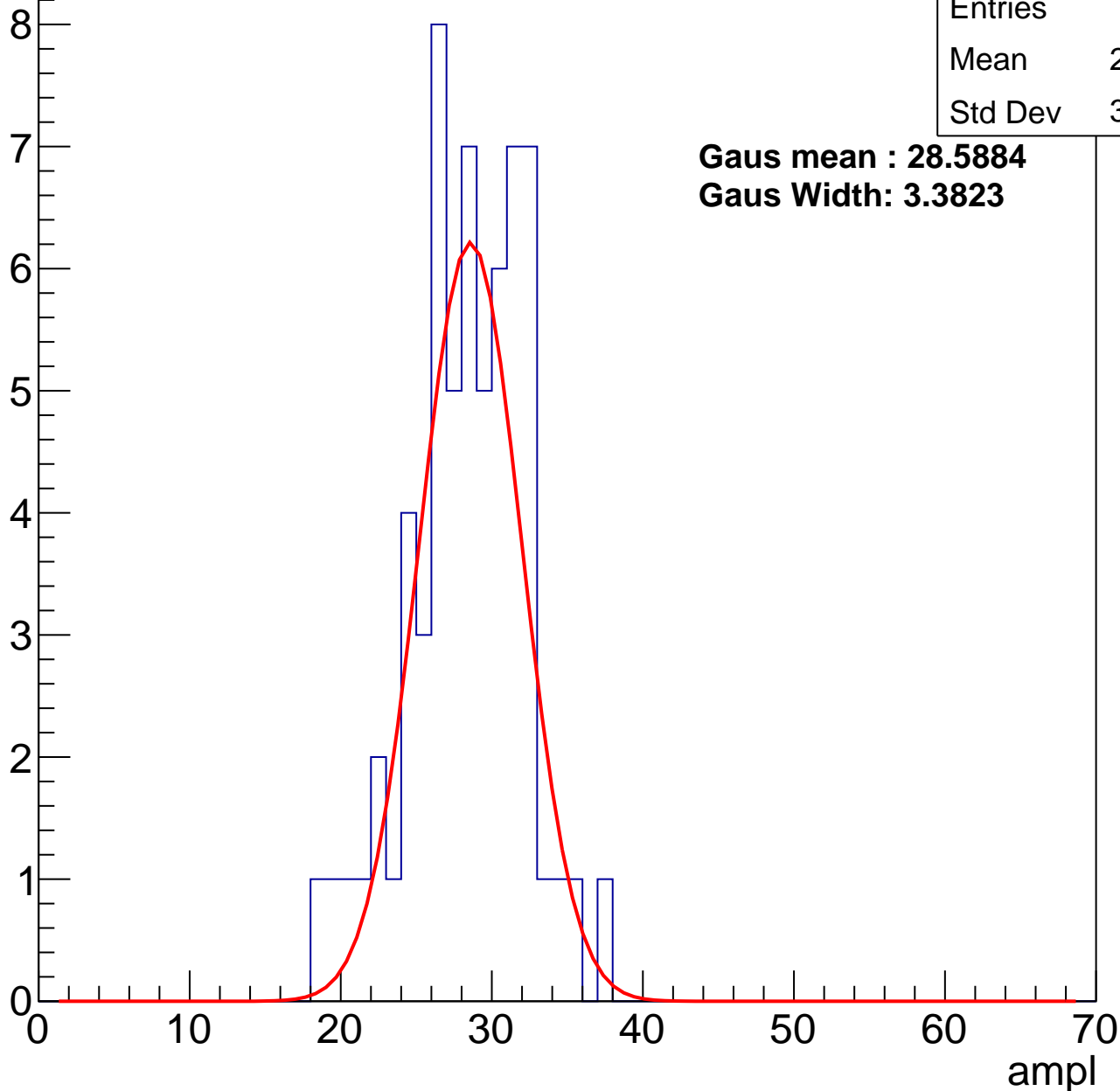
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	27.94
Std Dev	3.808

Gaus mean : 28.5884

Gaus Width: 3.3823



B0L001S, U18-ch72, adc1

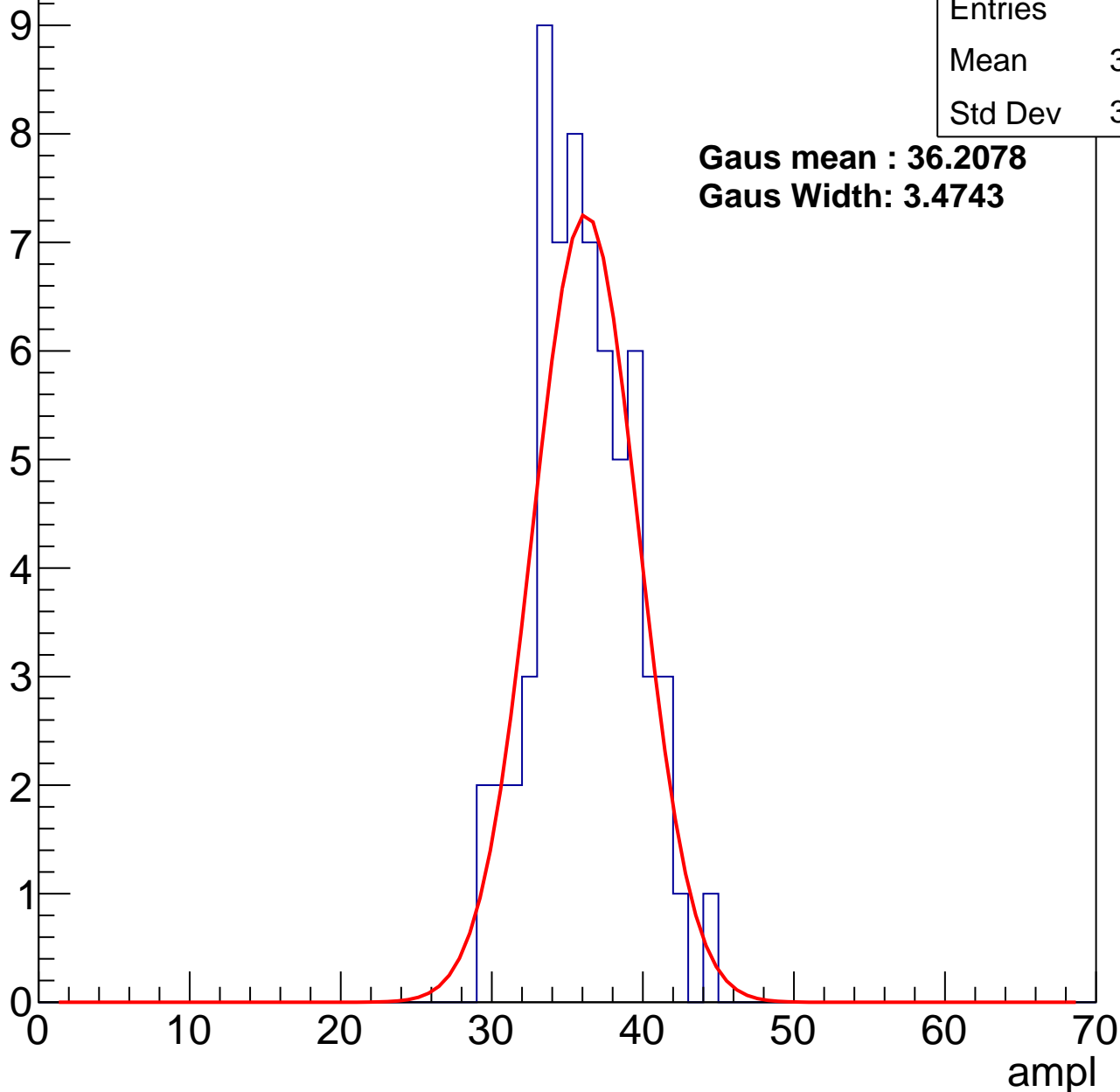
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	35.66
Std Dev	3.236

Gaus mean : 36.2078

Gaus Width: 3.4743



B0L001S, U18-ch72, adc2

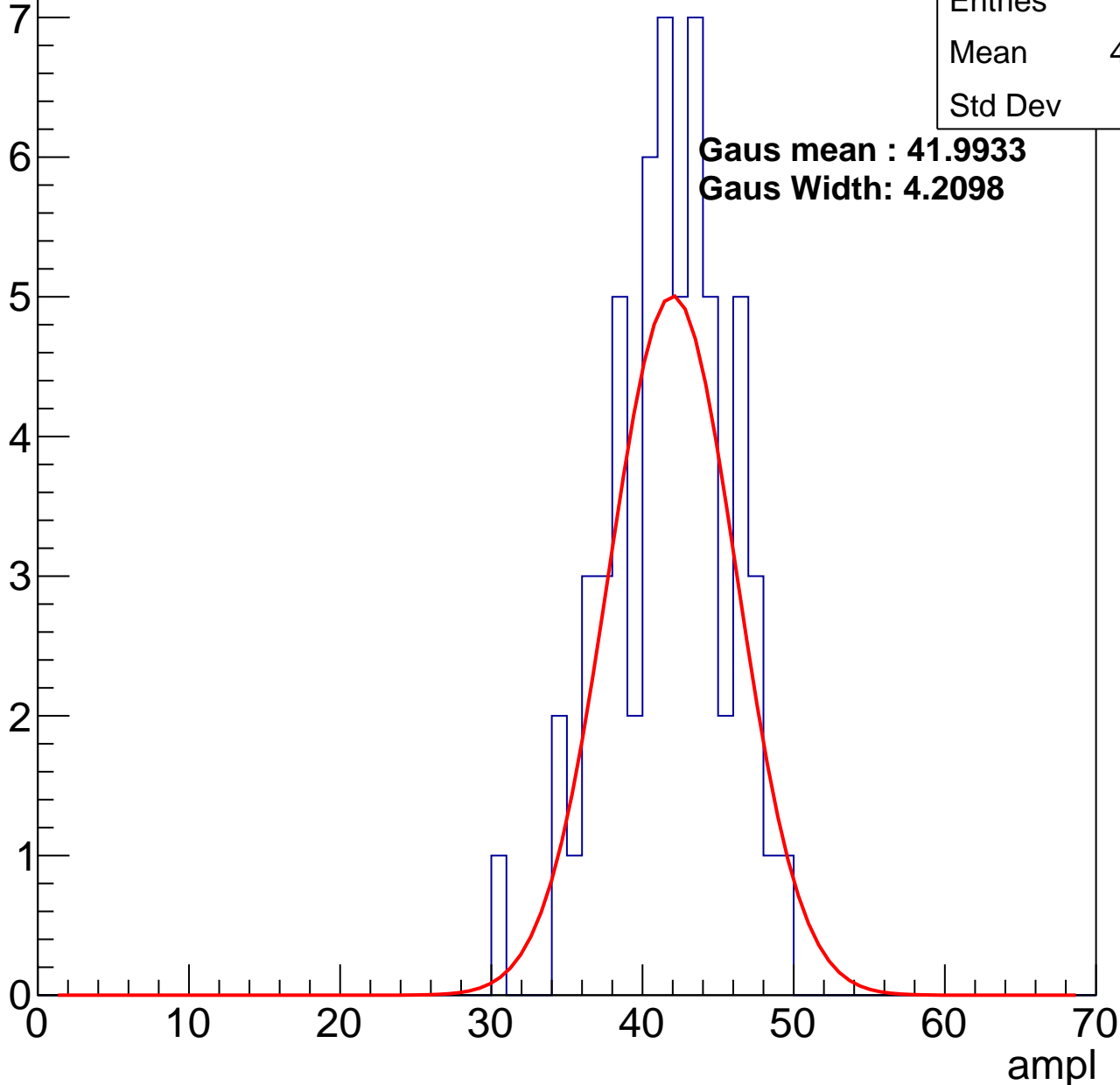
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	41.29
Std Dev	3.88

Gaus mean : 41.9933

Gaus Width: 4.2098

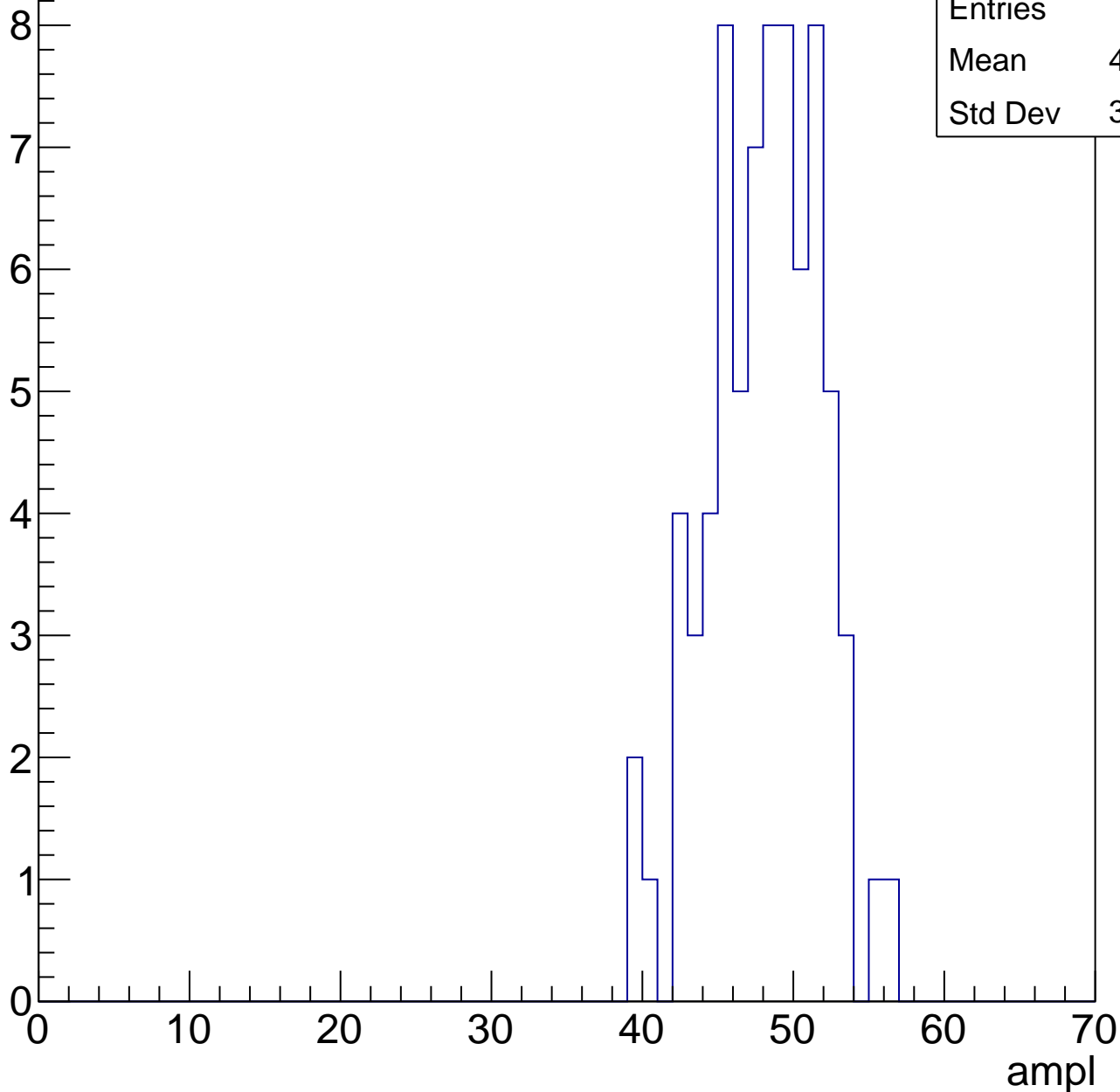


B0L001S, U18-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

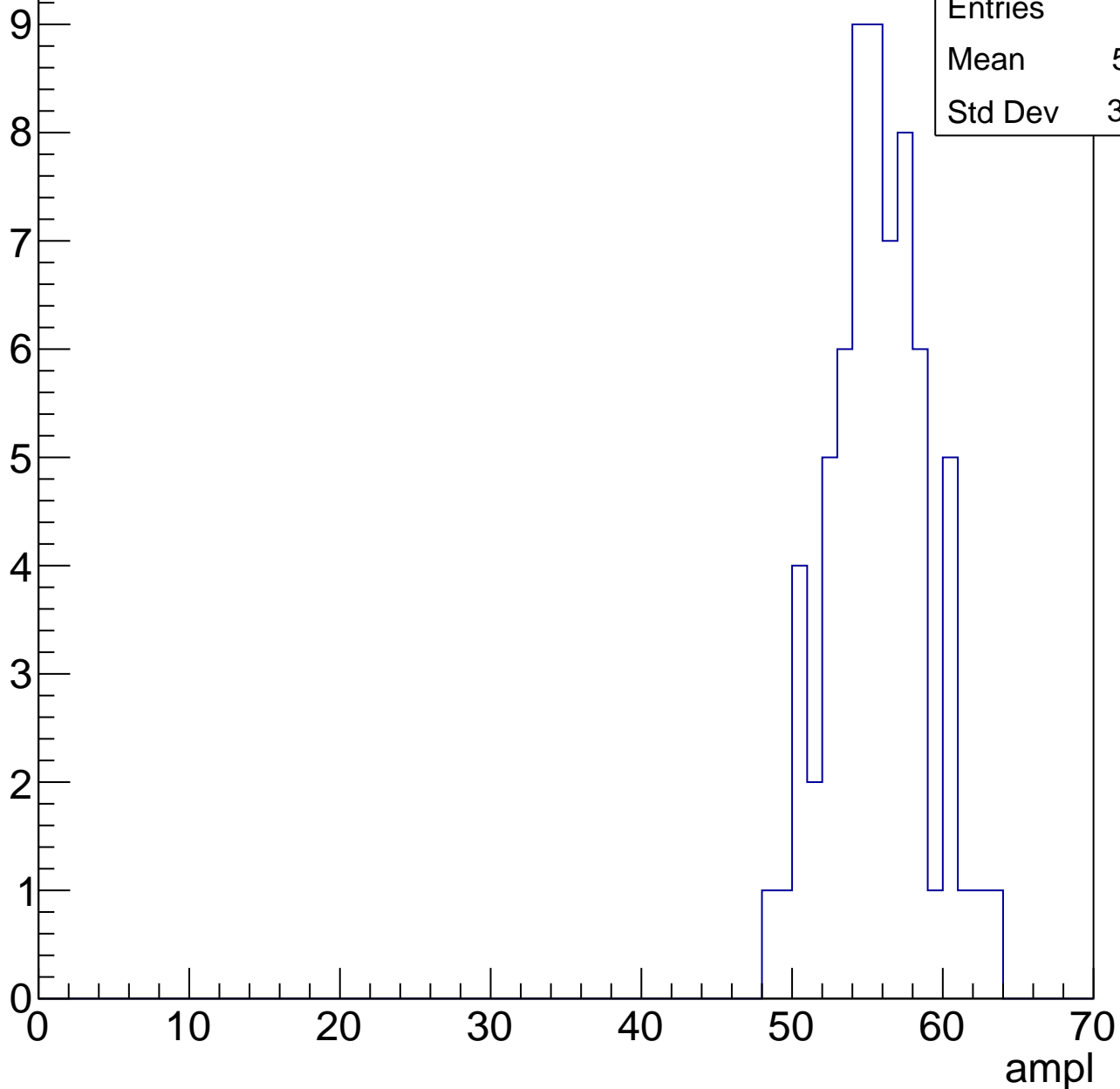
Entries	74
Mean	47.62
Std Dev	3.619



B0L001S, U18-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

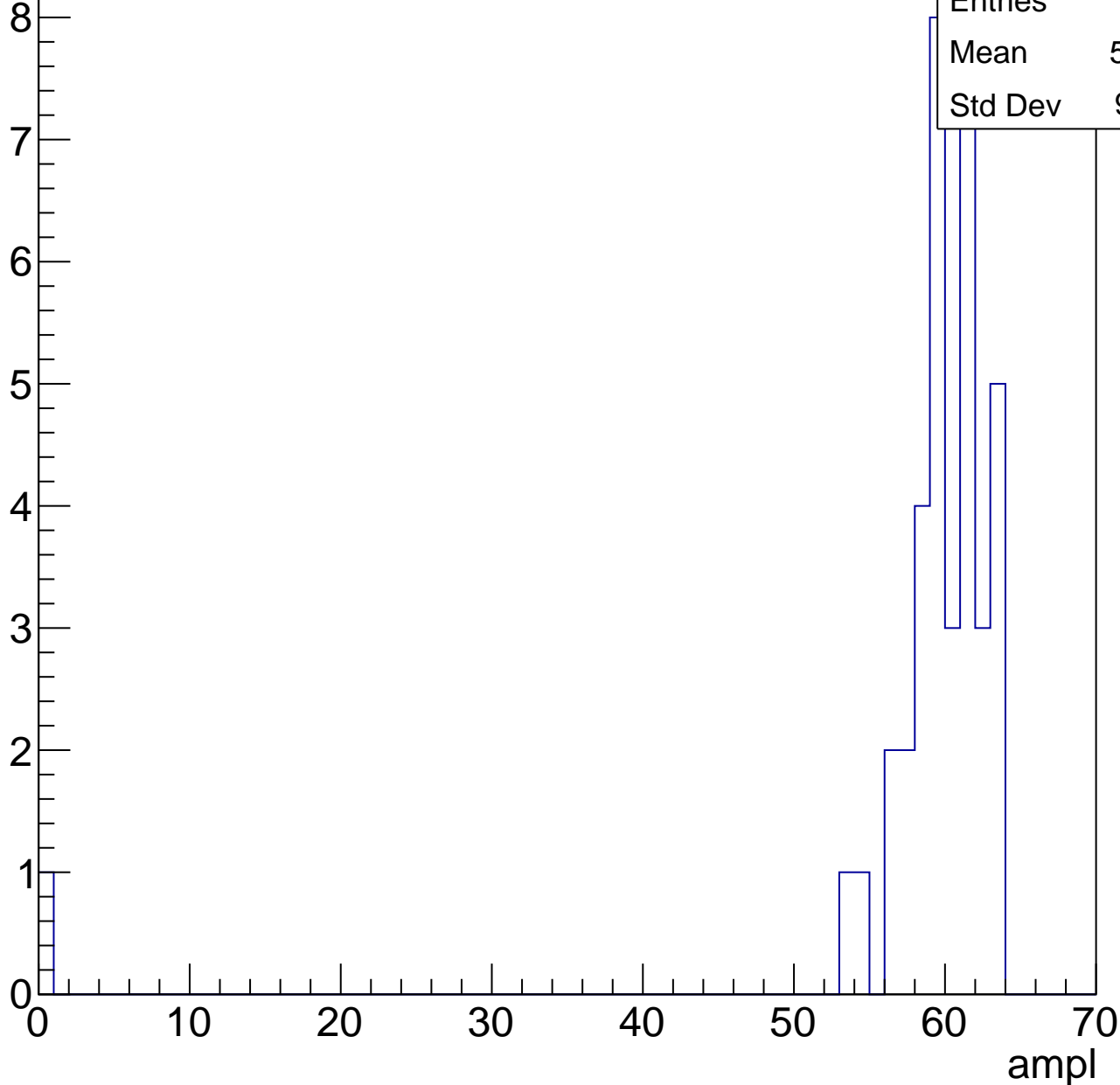


B0L001S, U18-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.05
Std Dev	9.841

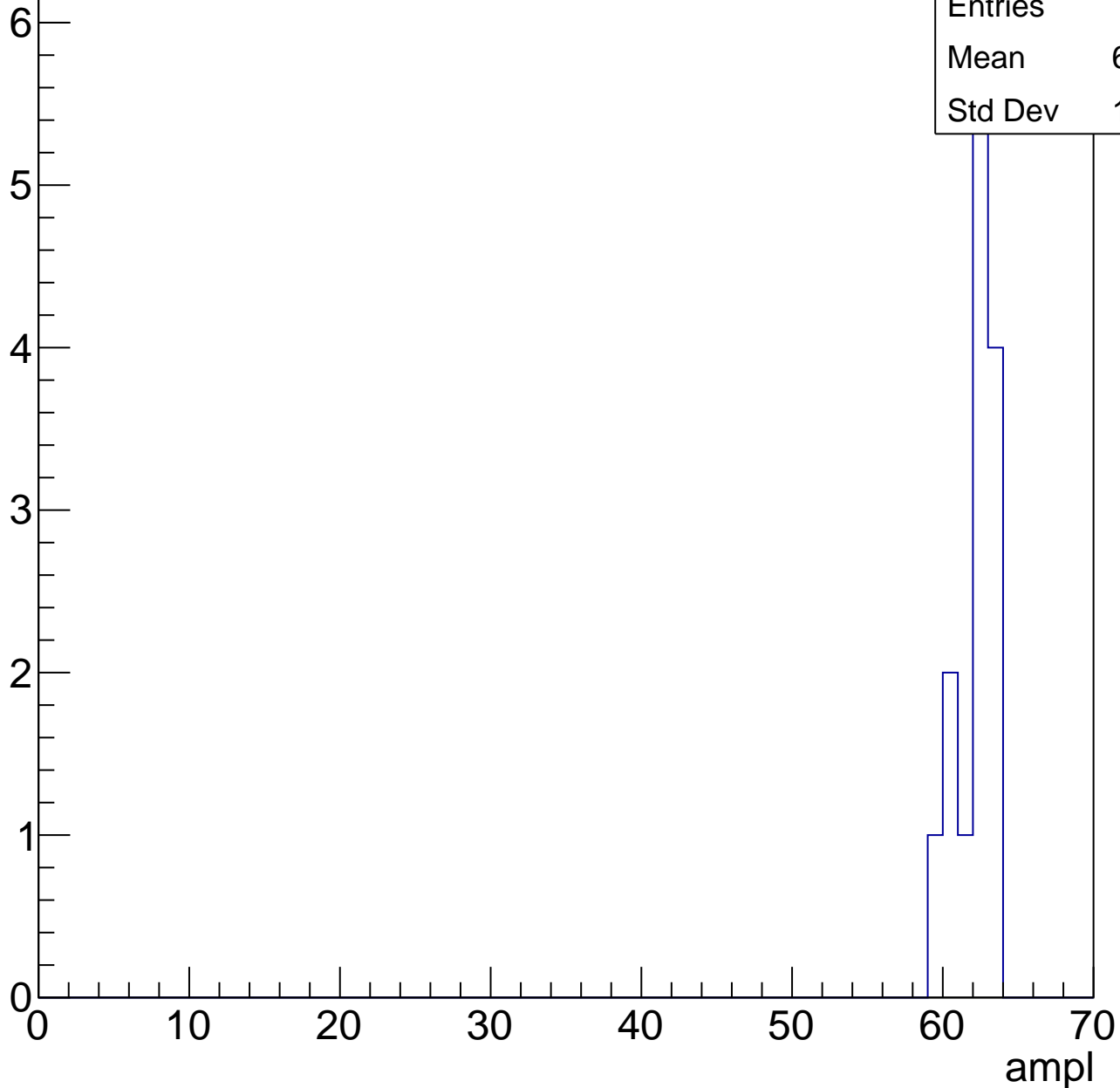


B0L001S, U18-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	61.71
Std Dev	1.221



B0L001S, U18-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch73, adc0

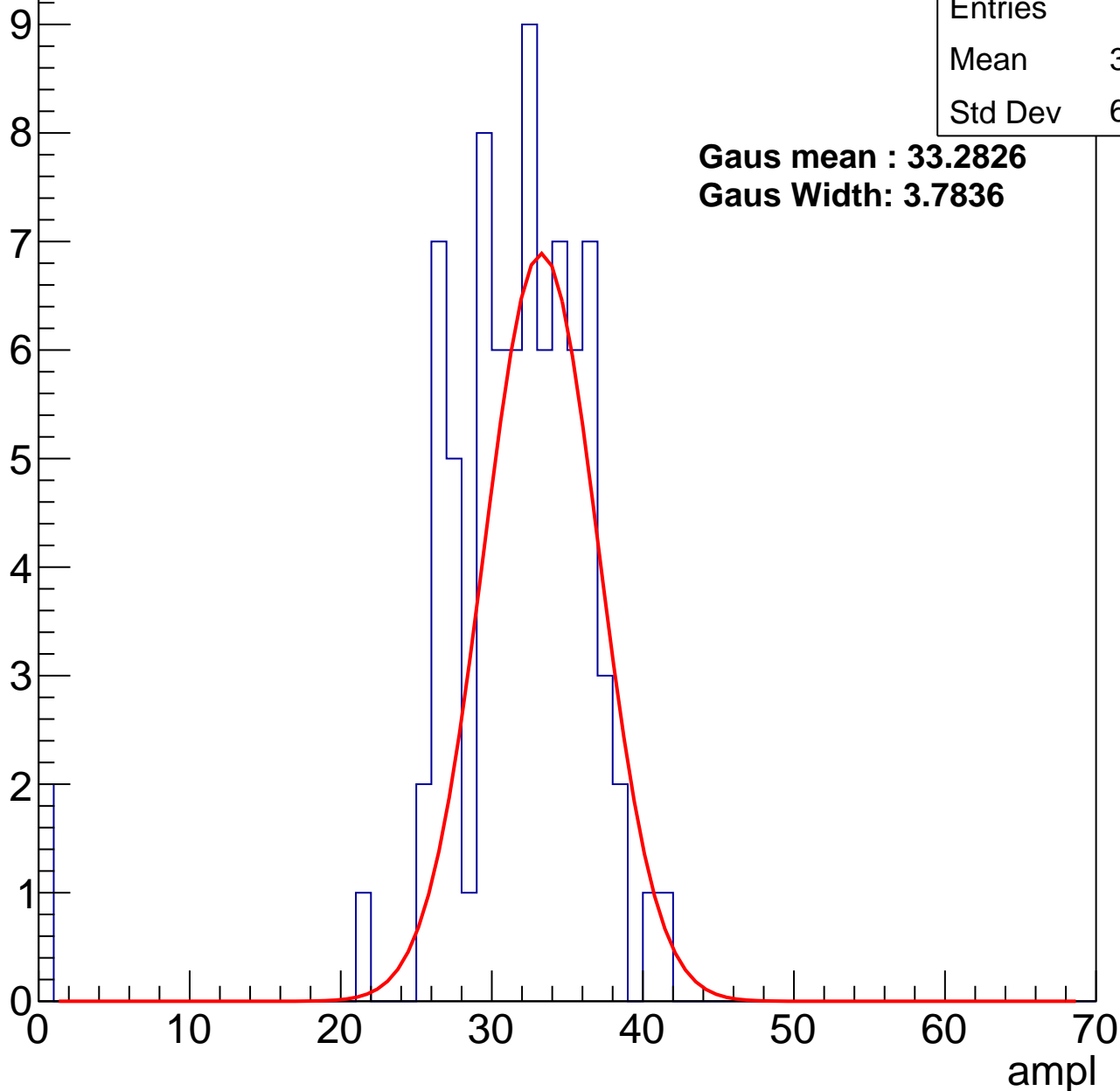
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.85
Std Dev	6.273

Gaus mean : 33.2826

Gaus Width: 3.7836



B0L001S, U18-ch73, adc1

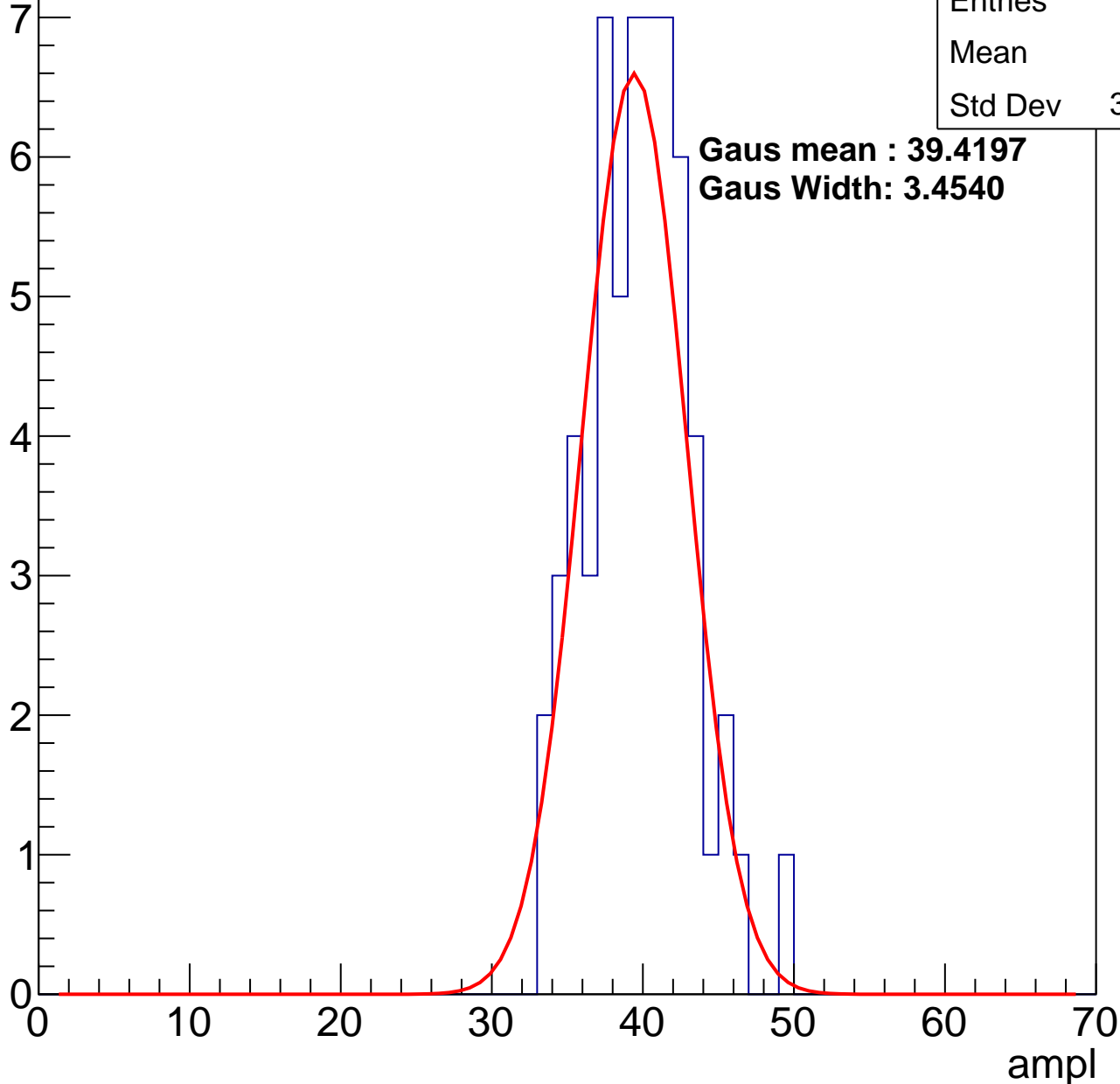
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	39.3
Std Dev	3.328

Gaus mean : 39.4197

Gaus Width: 3.4540



B0L001S, U18-ch73, adc2

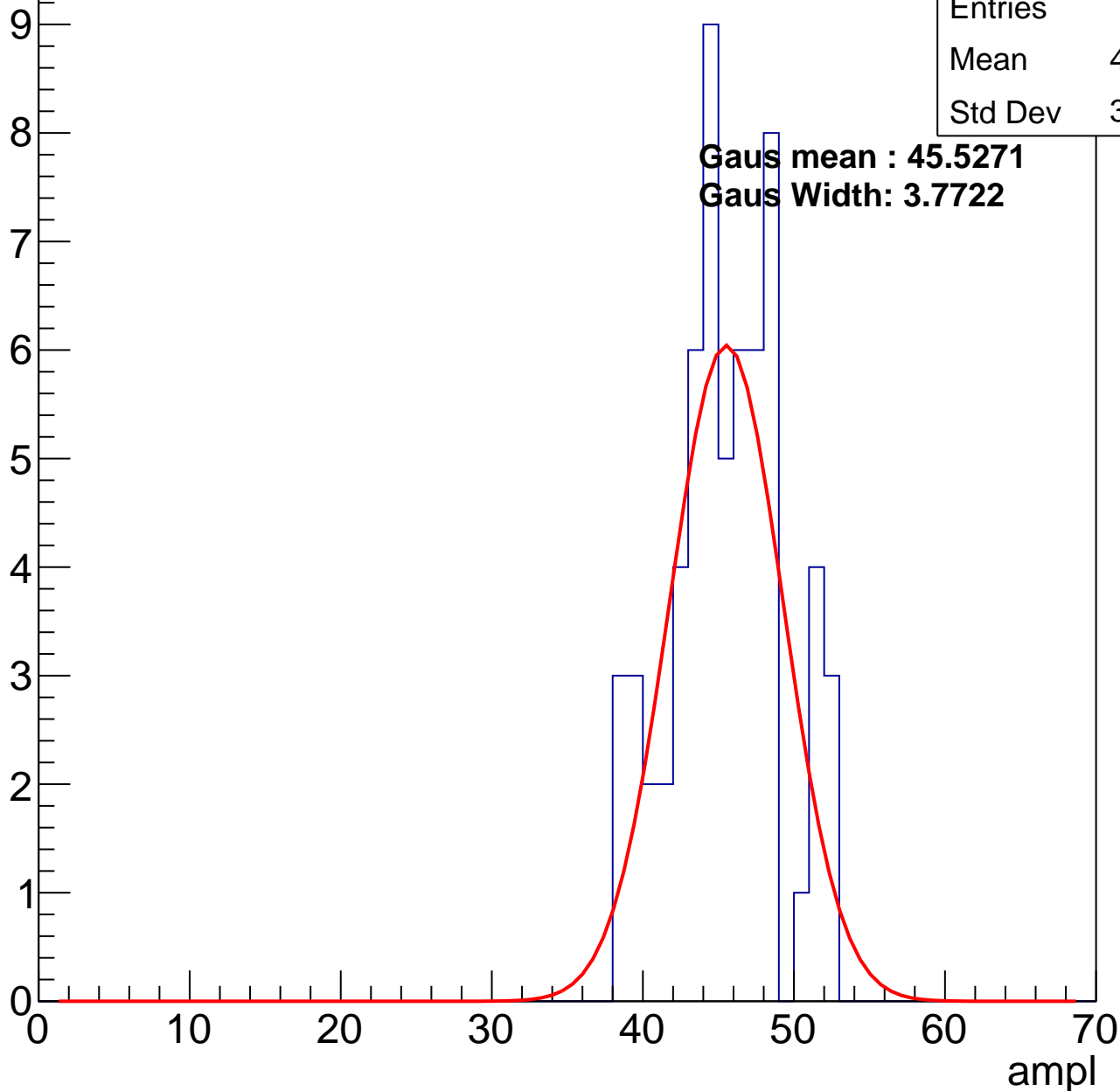
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	45.03
Std Dev	3.645

Gaus mean : 45.5271

Gaus Width: 3.7722

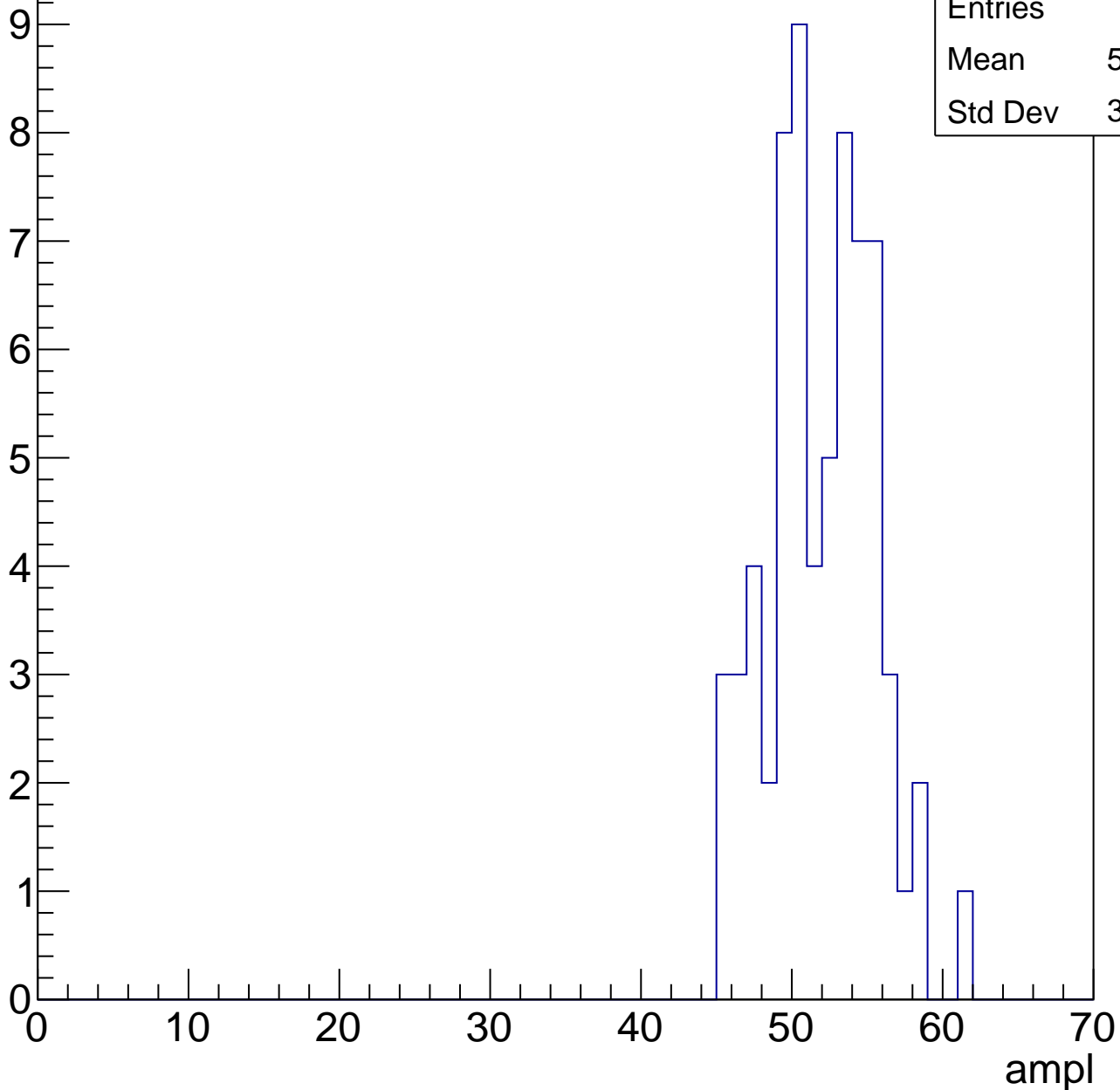


B0L001S, U18-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	51.52
Std Dev	3.474

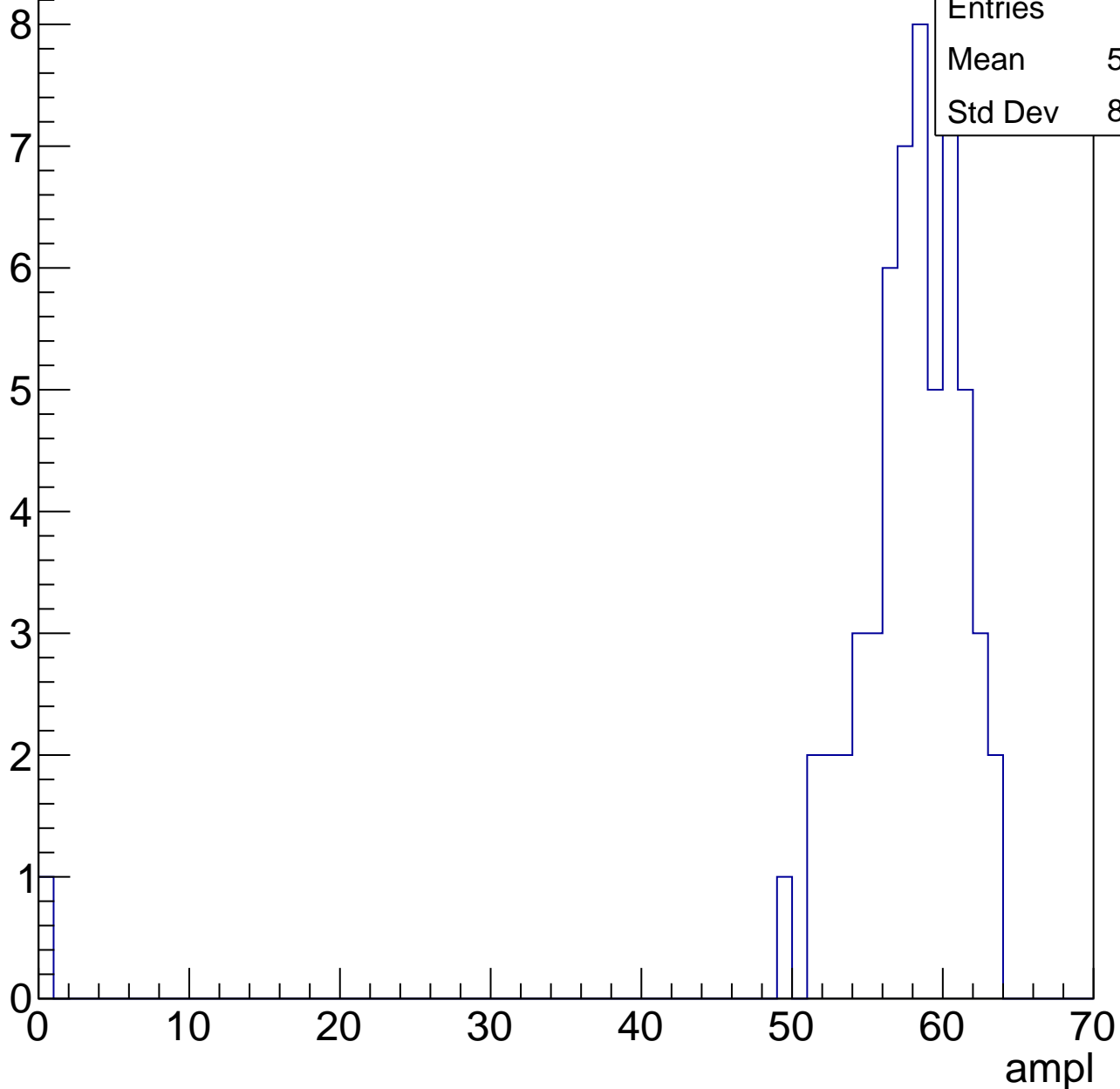


B0L001S, U18-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.53
Std Dev	8.122

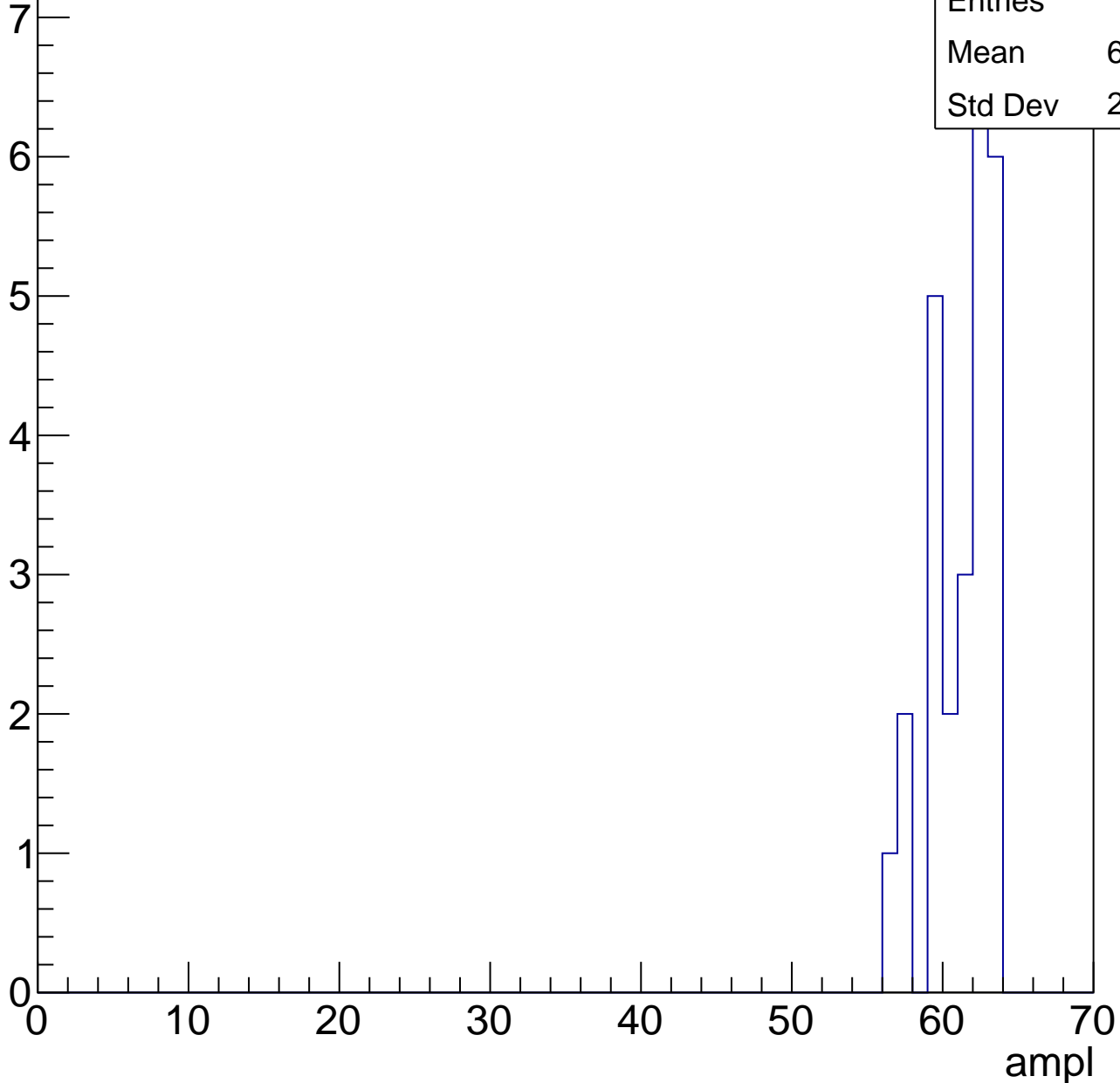


B0L001S, U18-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	60.77
Std Dev	2.044



B0L001S, U18-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L001S, U18-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch74, adc0

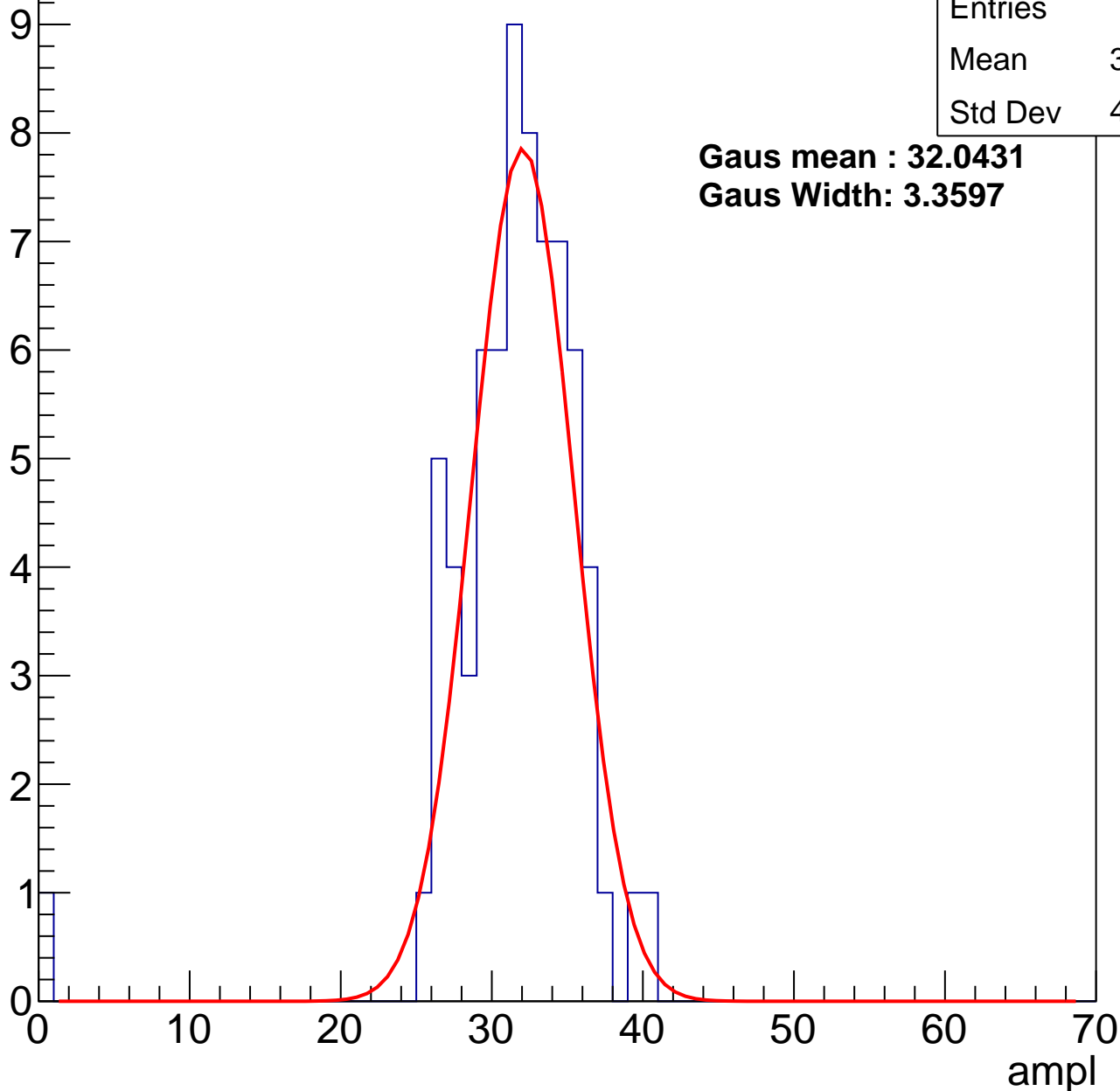
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	31.07
Std Dev	4.955

Gaus mean : 32.0431

Gaus Width: 3.3597



B0L001S, U18-ch74, adc1

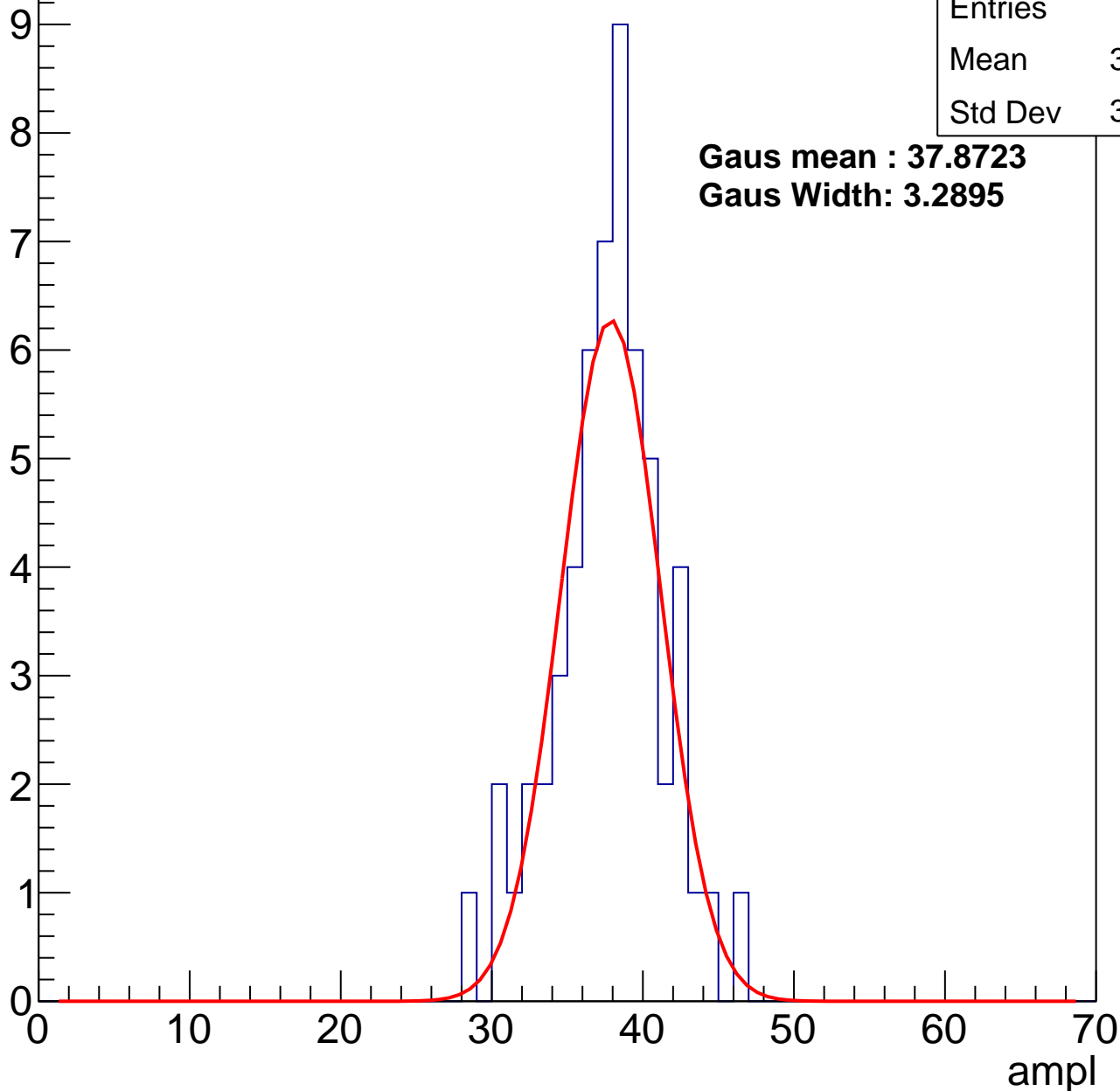
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.28
Std Dev	3.548

Gaus mean : 37.8723

Gaus Width: 3.2895



B0L001S, U18-ch74, adc2

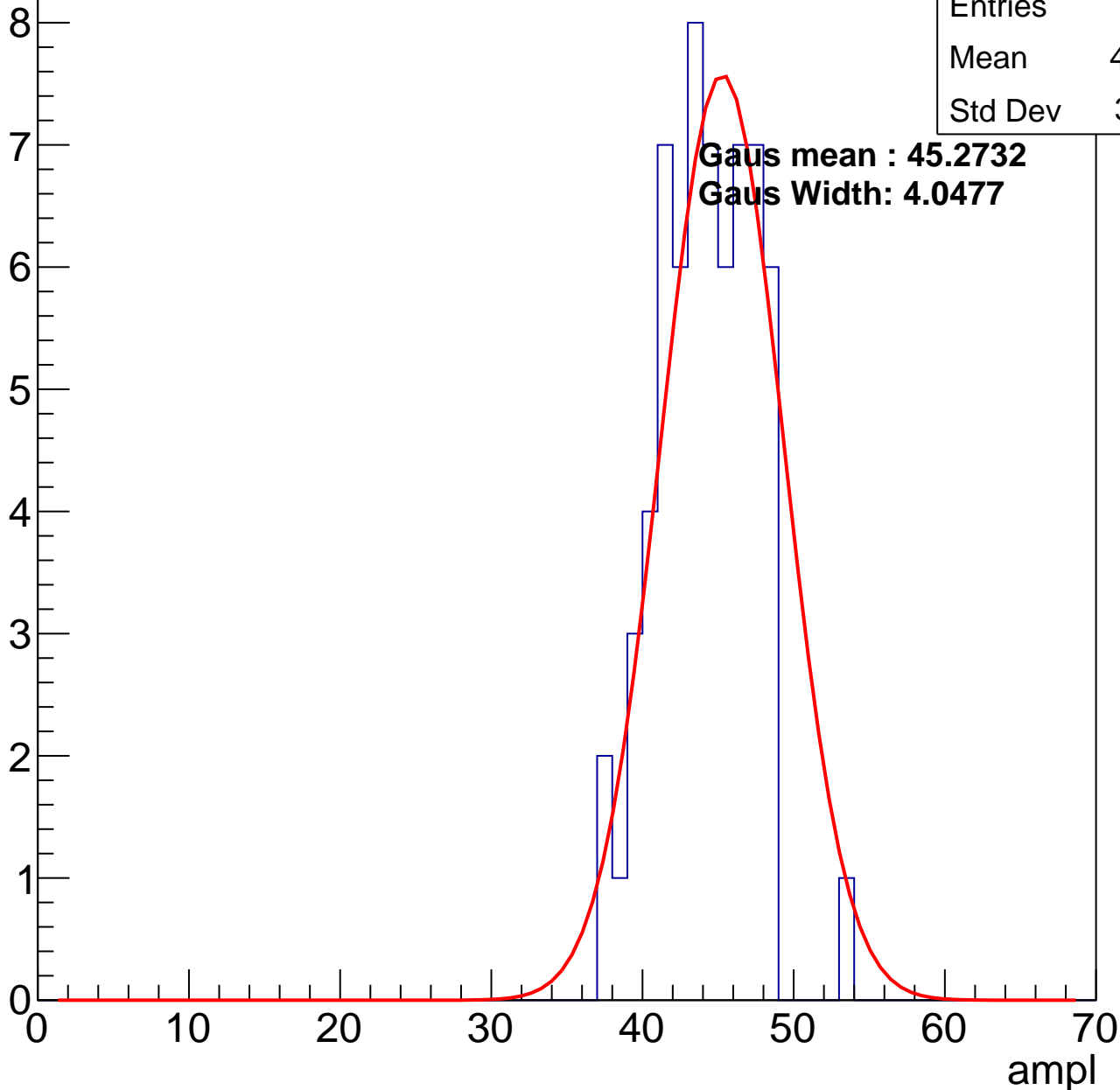
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.72
Std Dev	3.131

Gaus mean : 45.2732

Gaus Width: 4.0477



B0L001S, U18-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

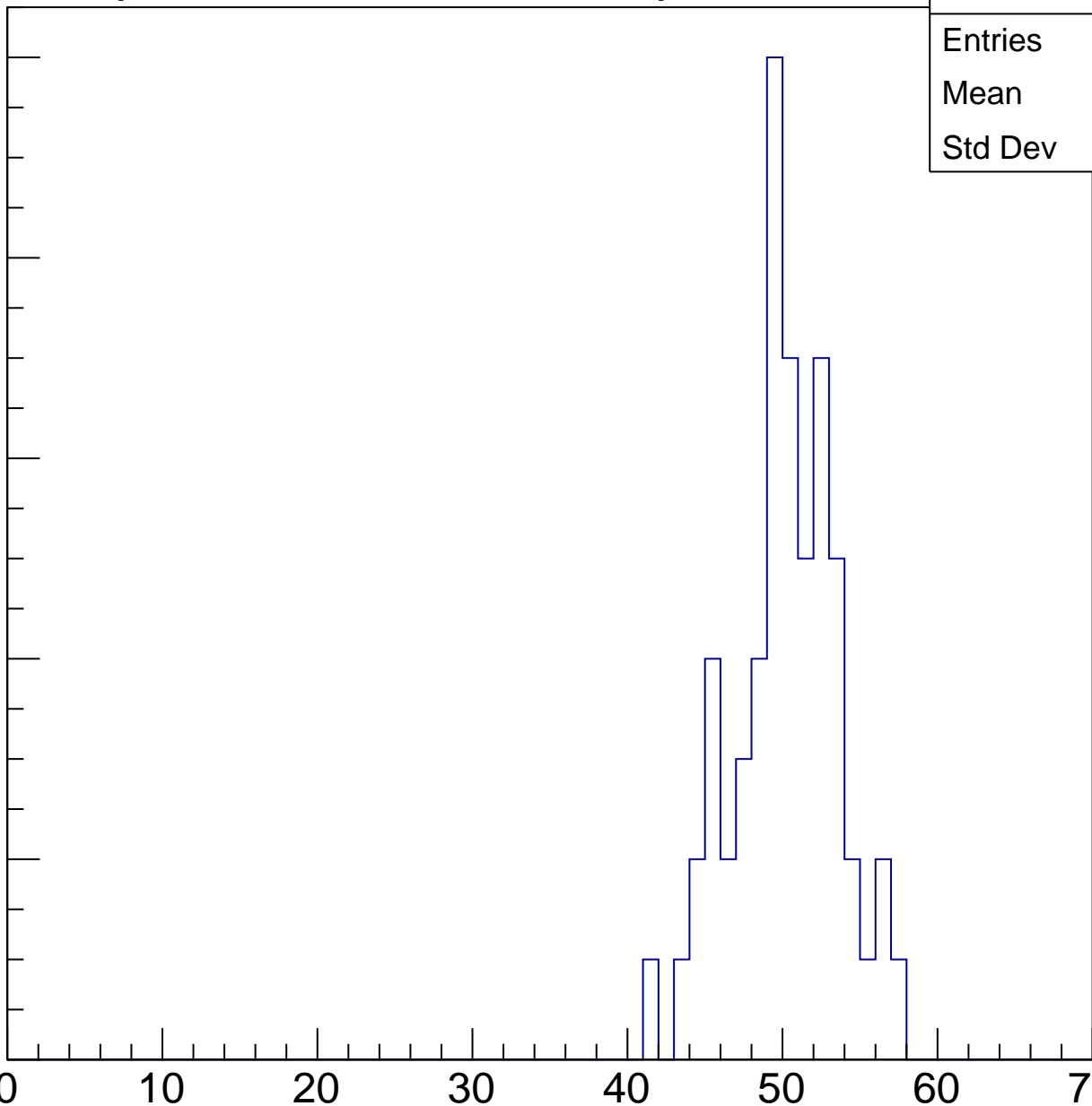
Entries	57
Mean	49.7
Std Dev	3.345

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

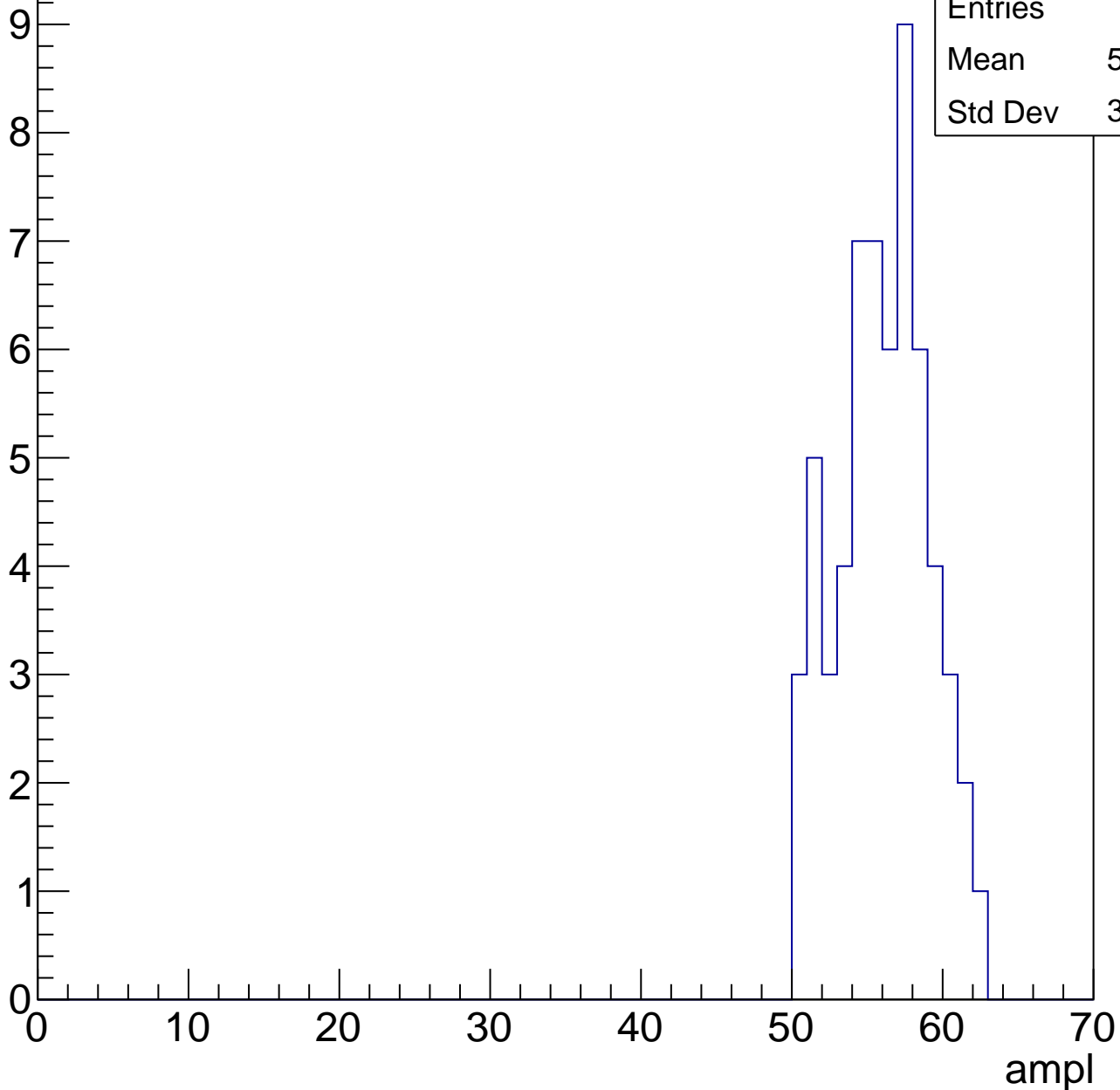


B0L001S, U18-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.55
Std Dev	3.008

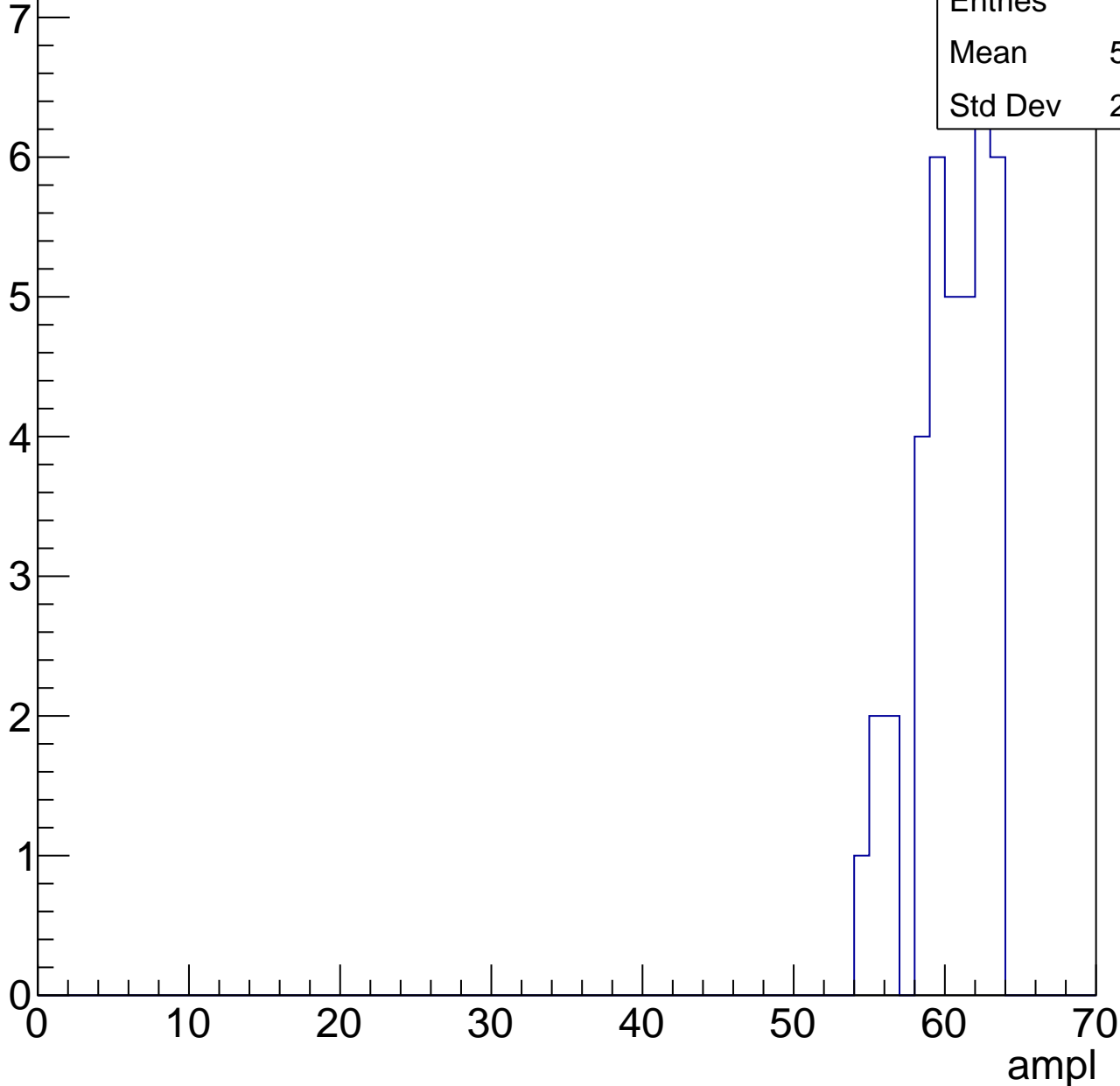


B0L001S, U18-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

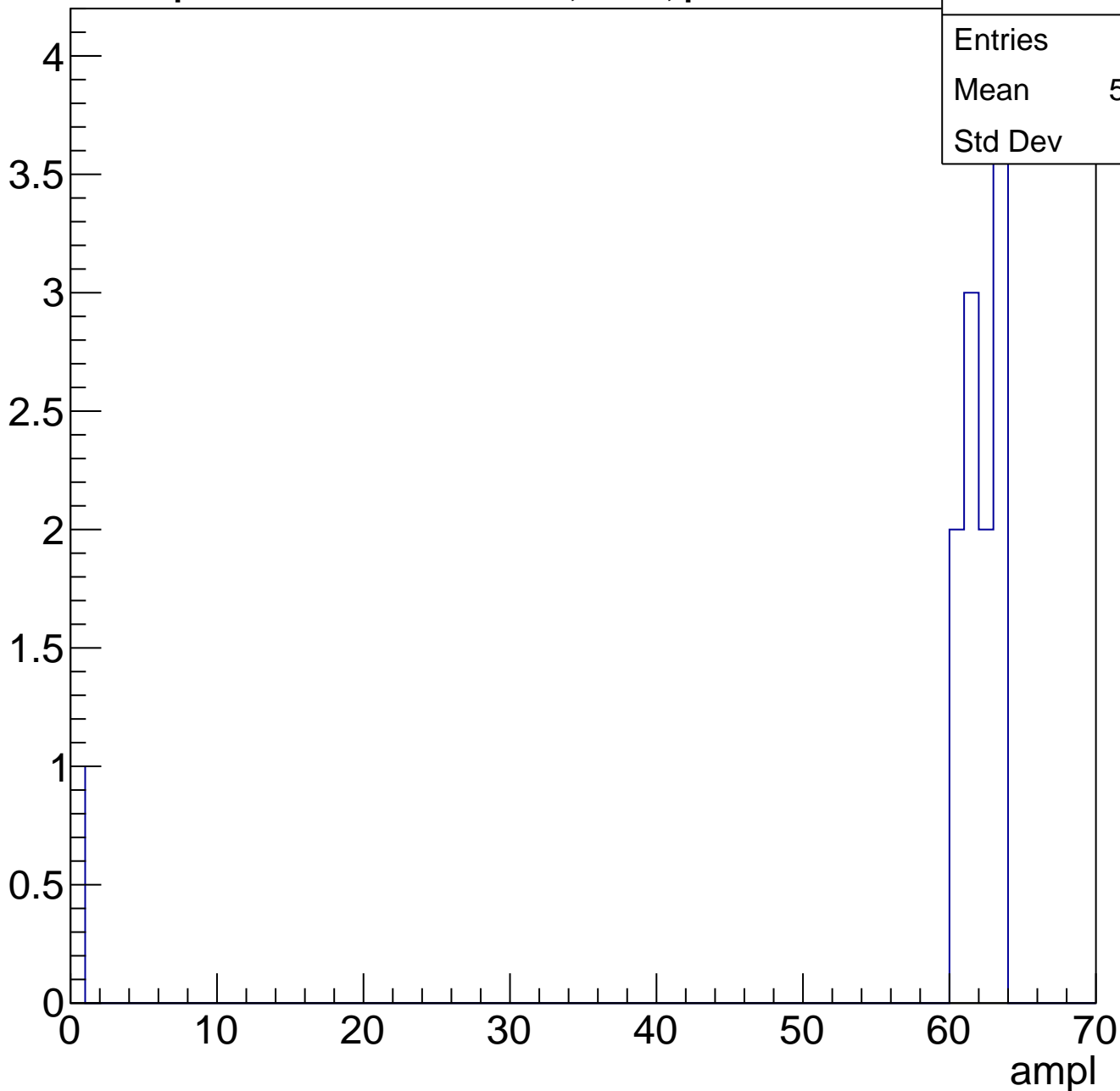
Entries	38
Mean	59.97
Std Dev	2.444



B0L001S, U18-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch75, adc0

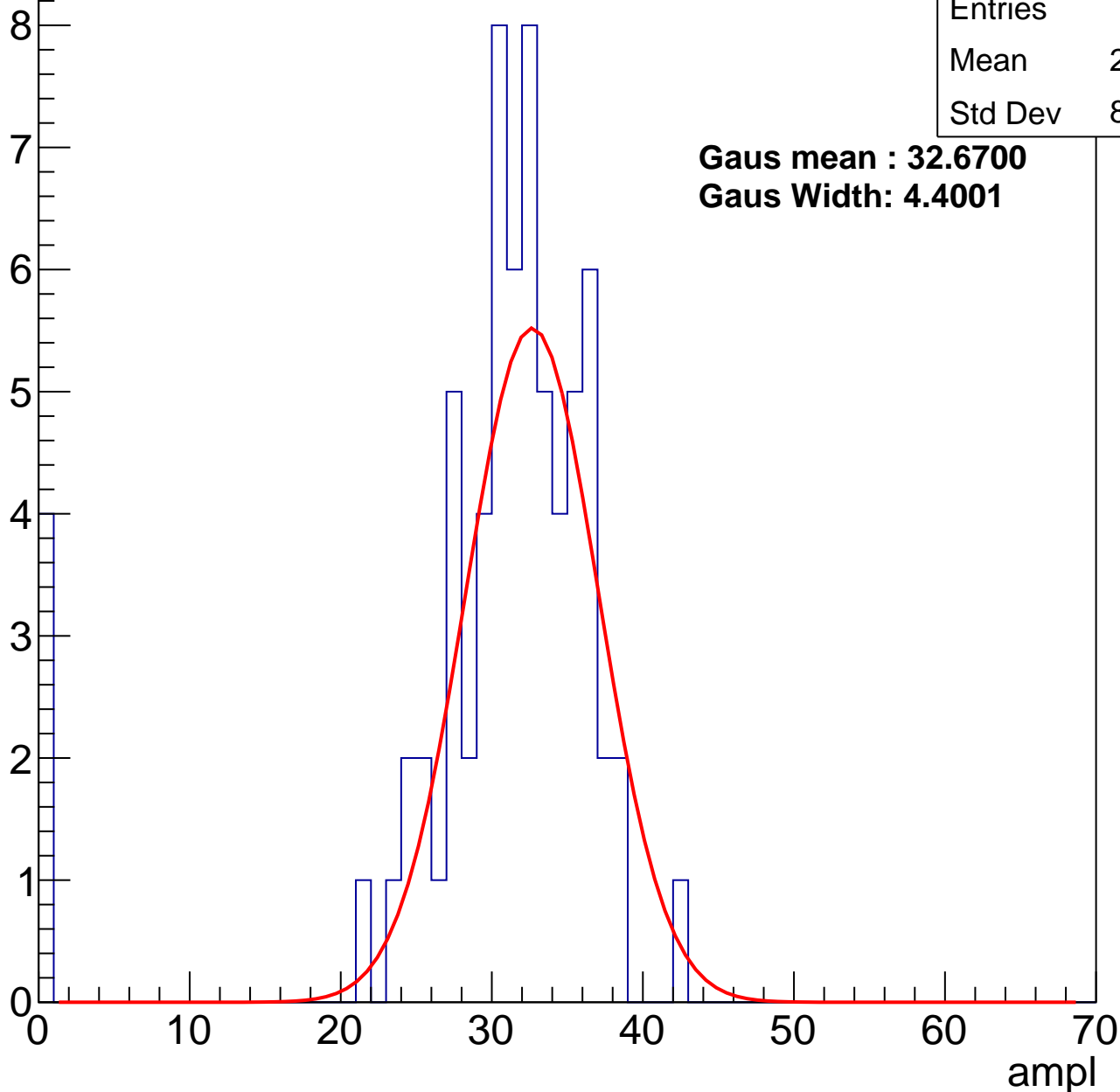
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	29.58
Std Dev	8.315

Gaus mean : 32.6700

Gaus Width: 4.4001



B0L001S, U18-ch75, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	38.87
Std Dev	3.372

Gaus mean : 39.3453

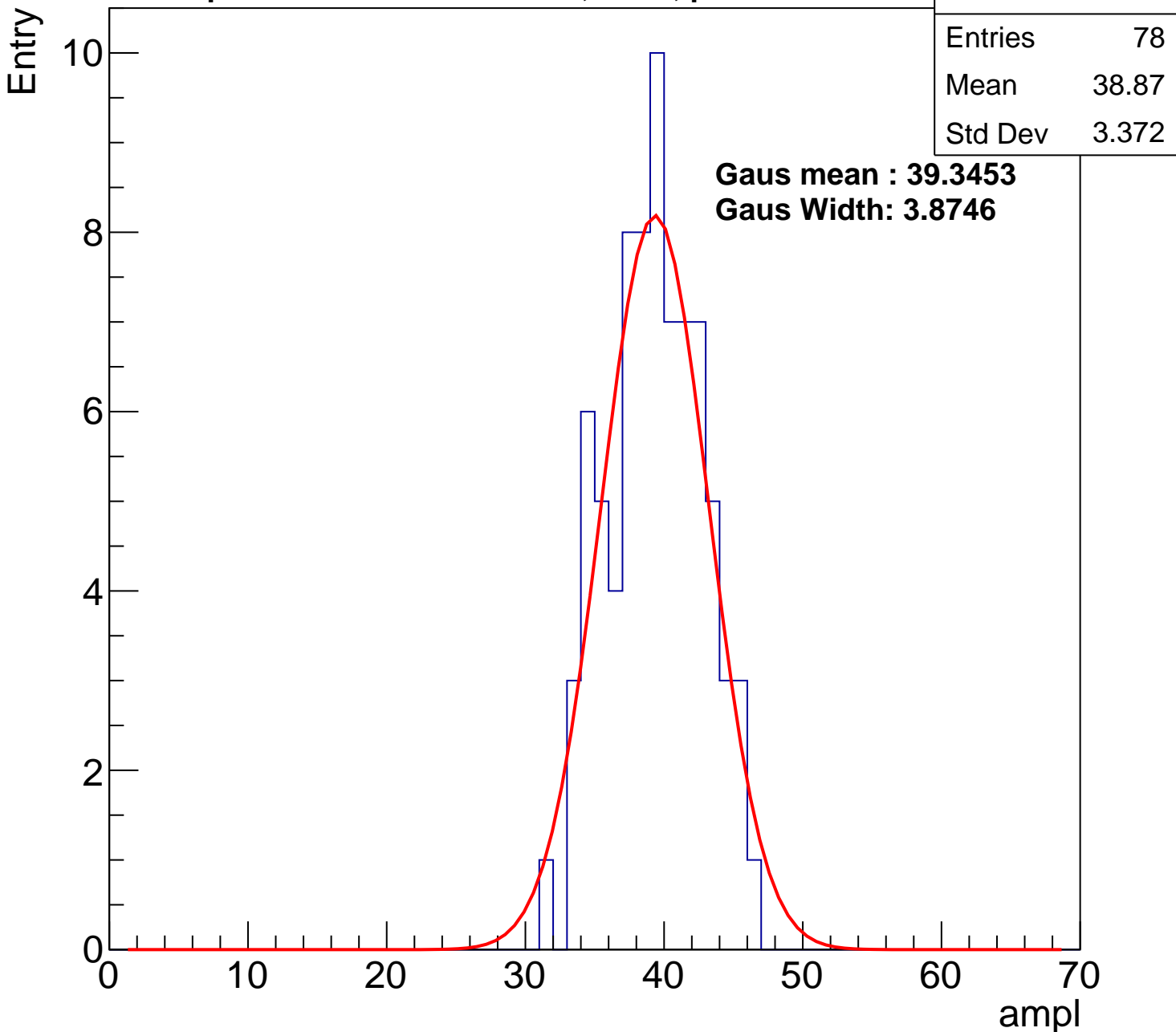
Gaus Width: 3.8746

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch75, adc2

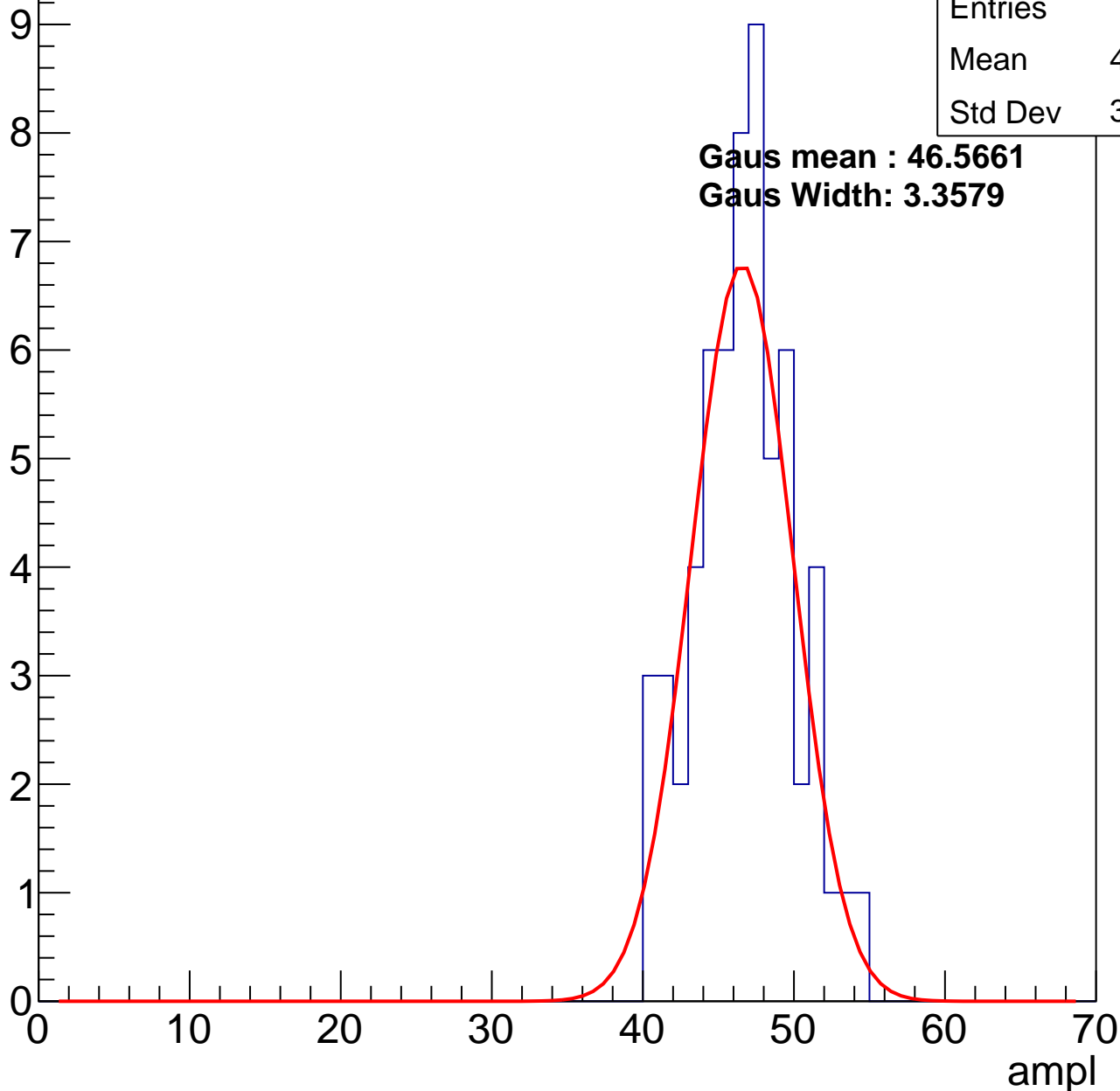
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	46.25
Std Dev	3.242

Gaus mean : 46.5661

Gaus Width: 3.3579

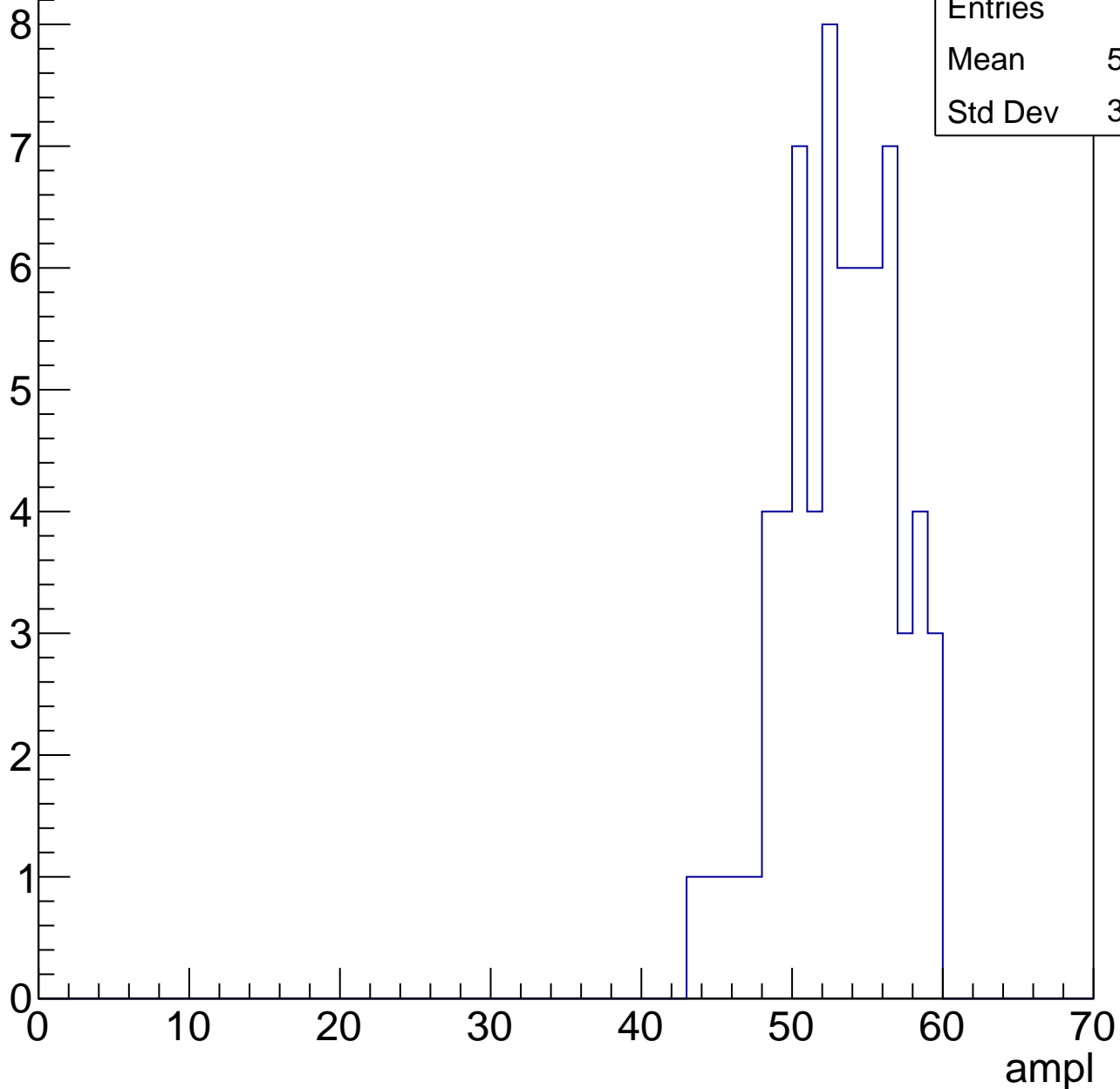


B0L001S, U18-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	52.64
Std Dev	3.708

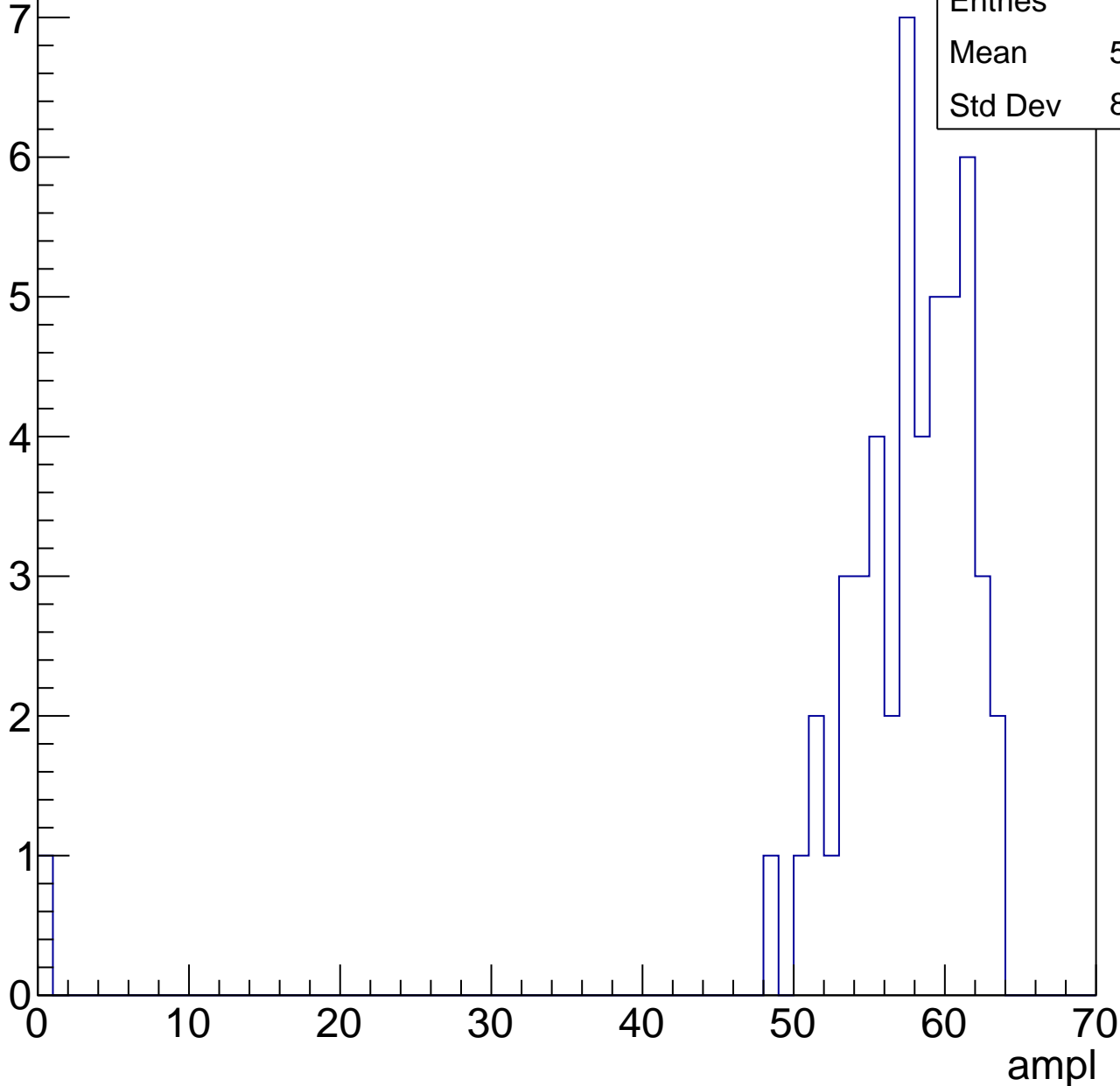


B0L001S, U18-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.18
Std Dev	8.772

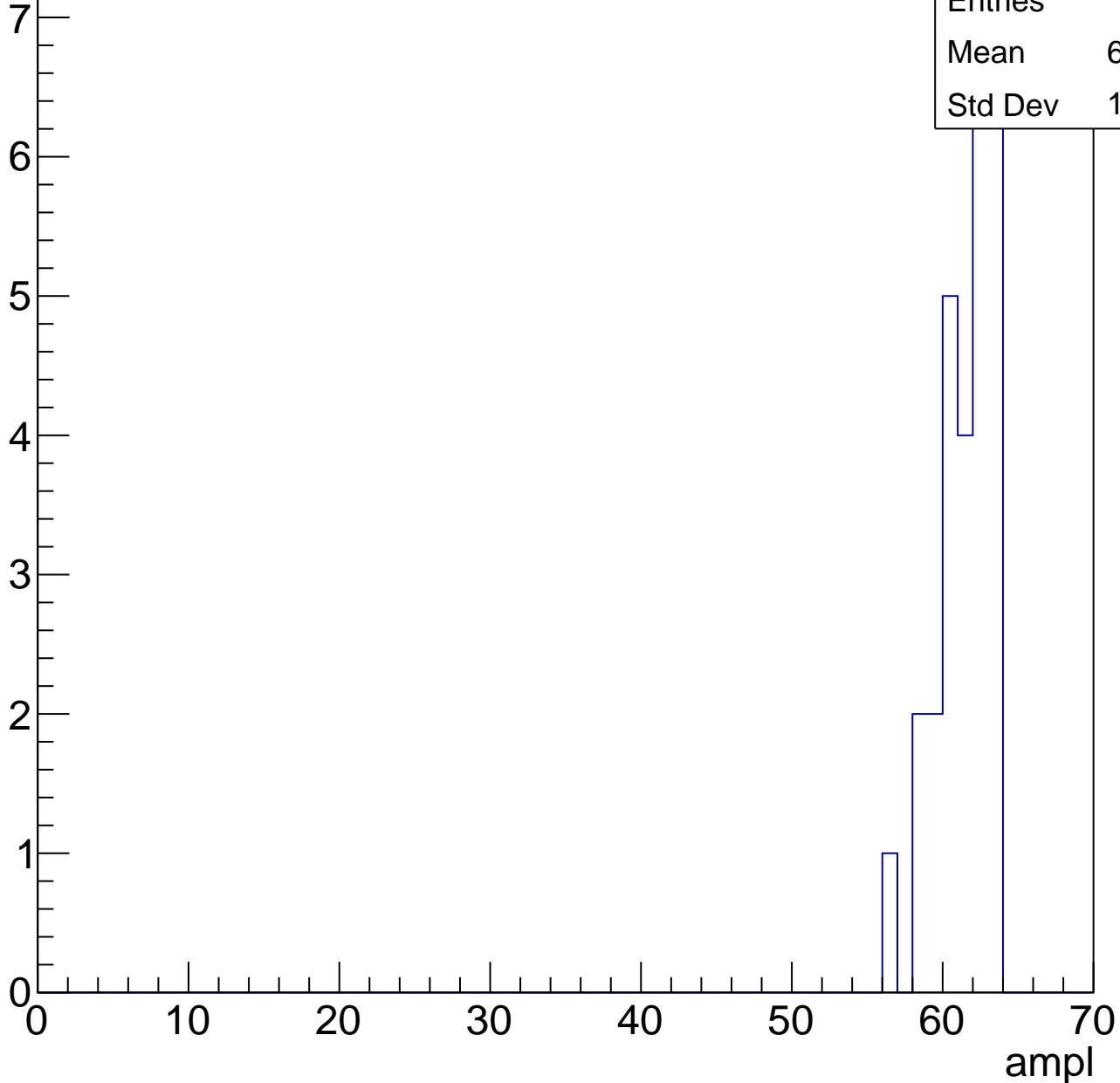


B0L001S, U18-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	61.04
Std Dev	1.802



B0L001S, U18-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch76, adc0

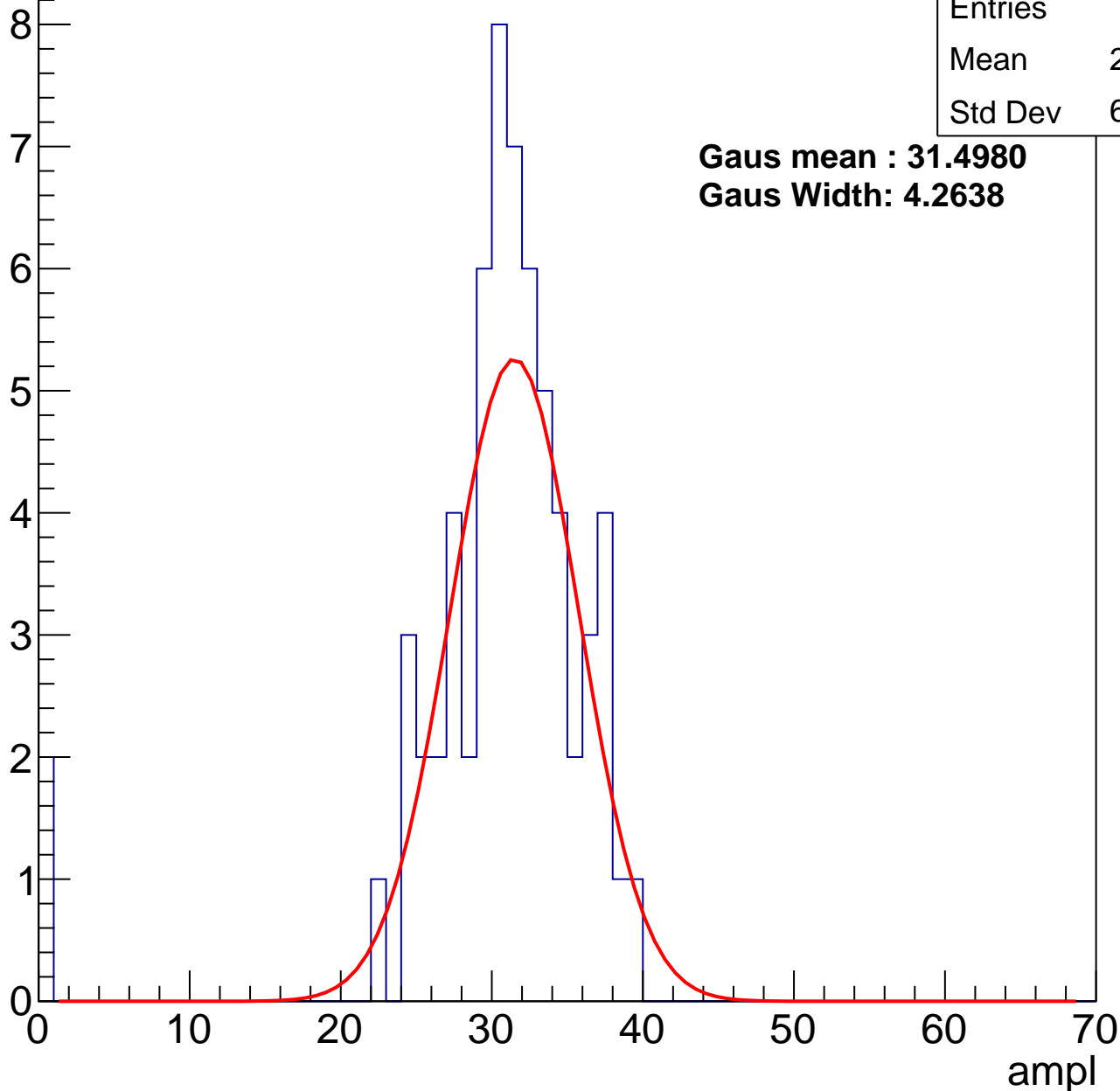
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.95
Std Dev	6.598

Gaus mean : 31.4980

Gaus Width: 4.2638



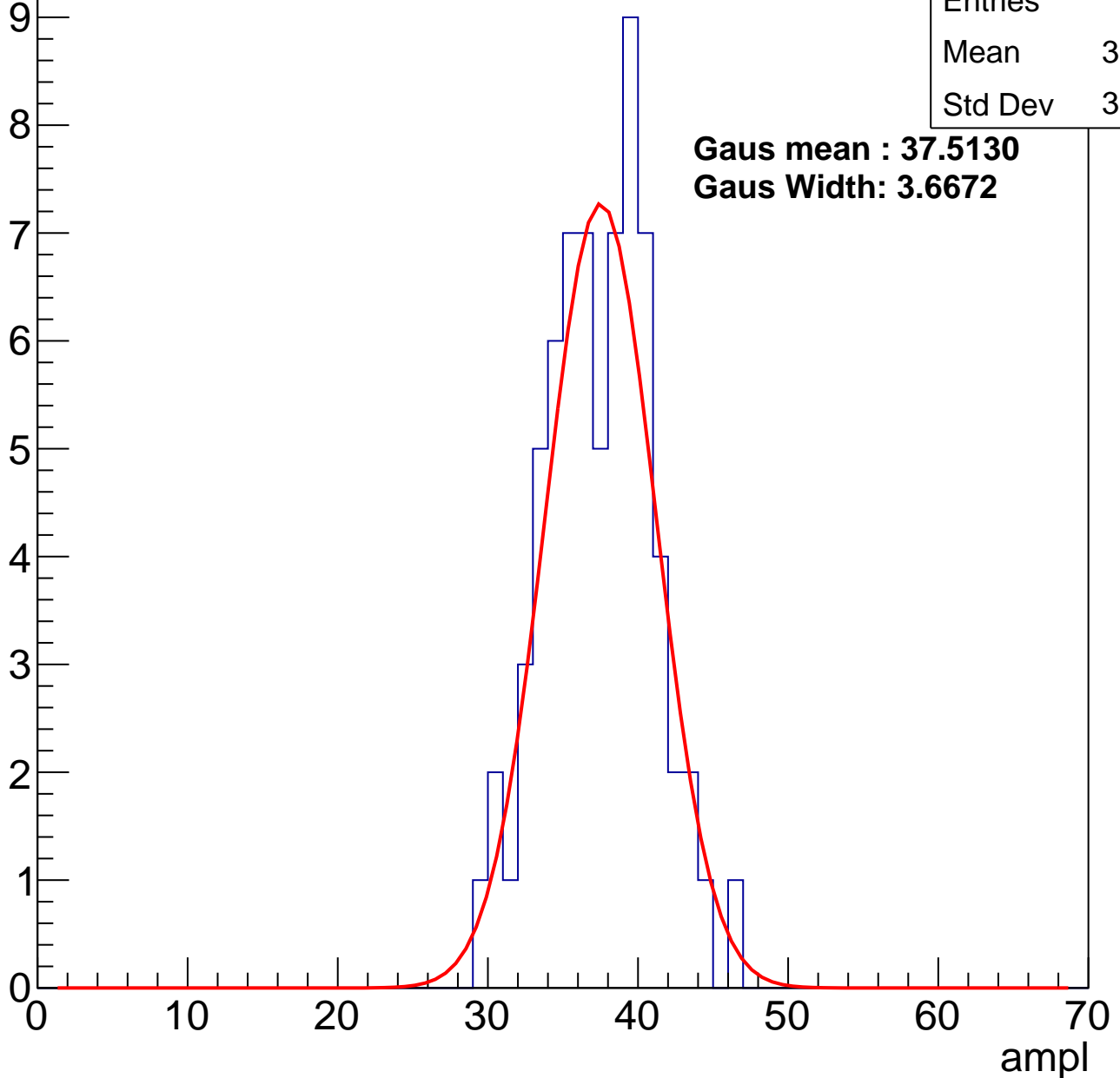
B0L001S, U18-ch76, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.97
Std Dev	3.513

Gaus mean : 37.5130
Gaus Width: 3.6672



B0L001S, U18-ch76, adc2

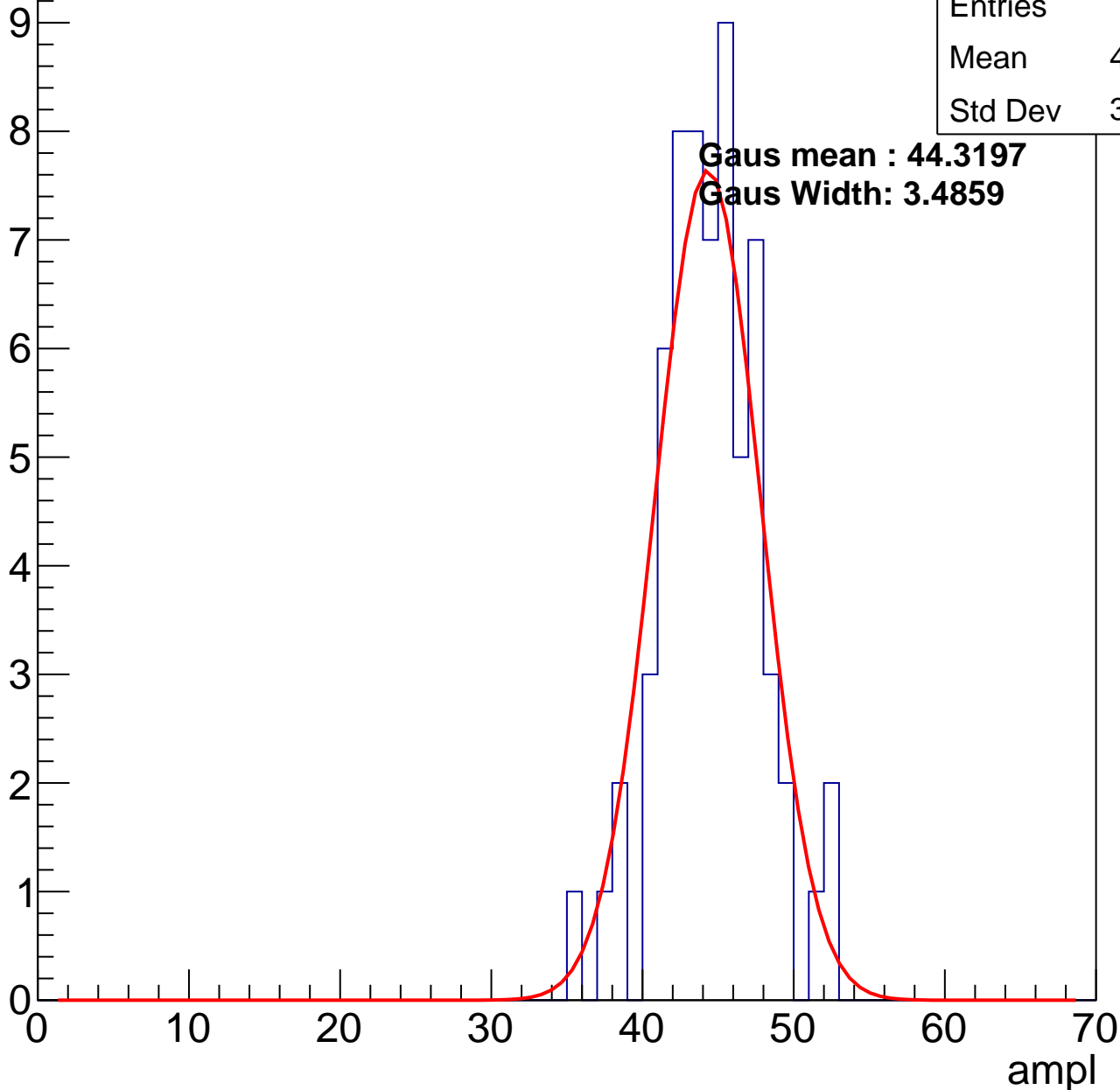
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.05
Std Dev	3.307

Gaus mean : 44.3197

Gaus Width: 3.4859

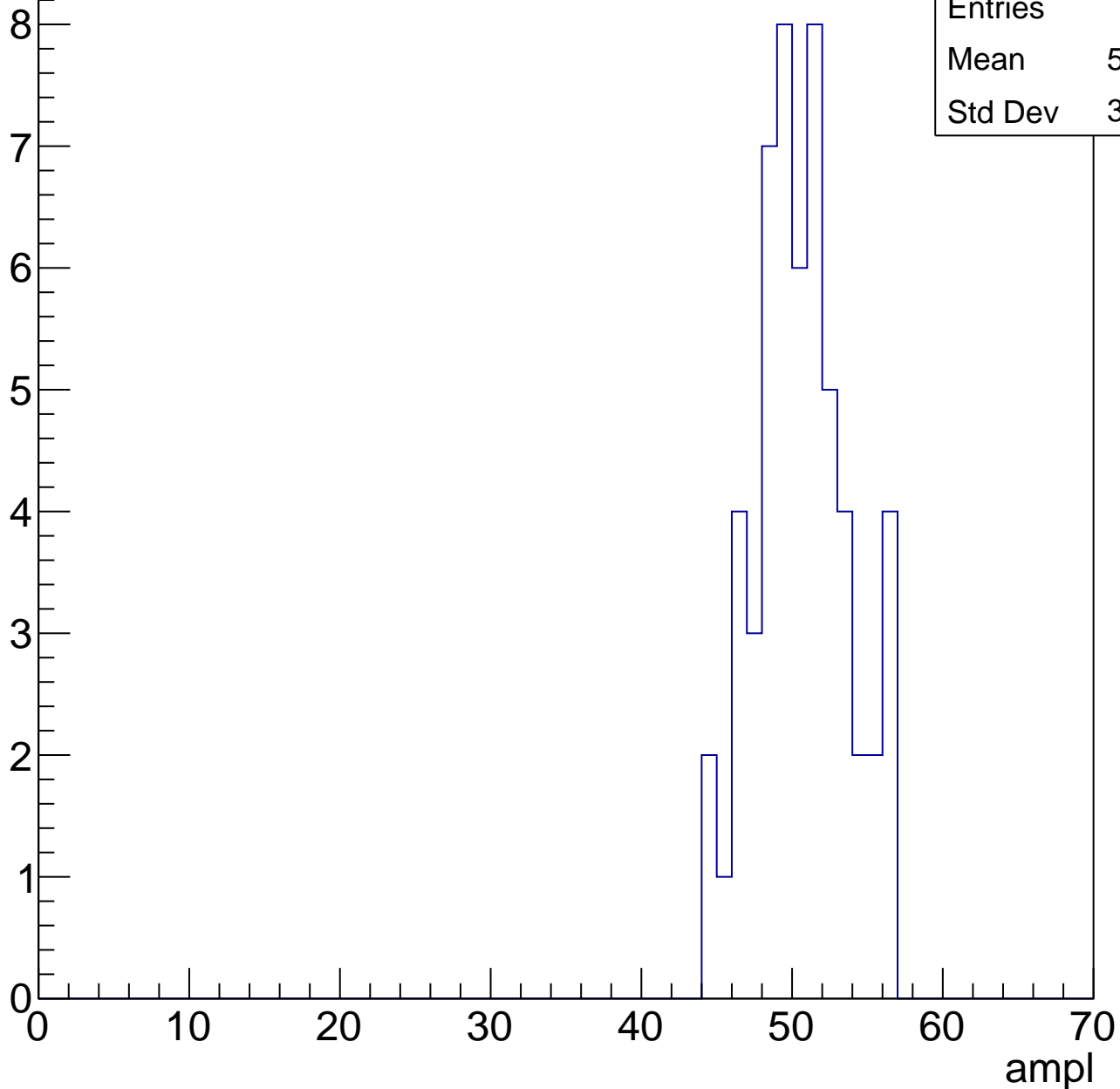


B0L001S, U18-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	50.14
Std Dev	3.026

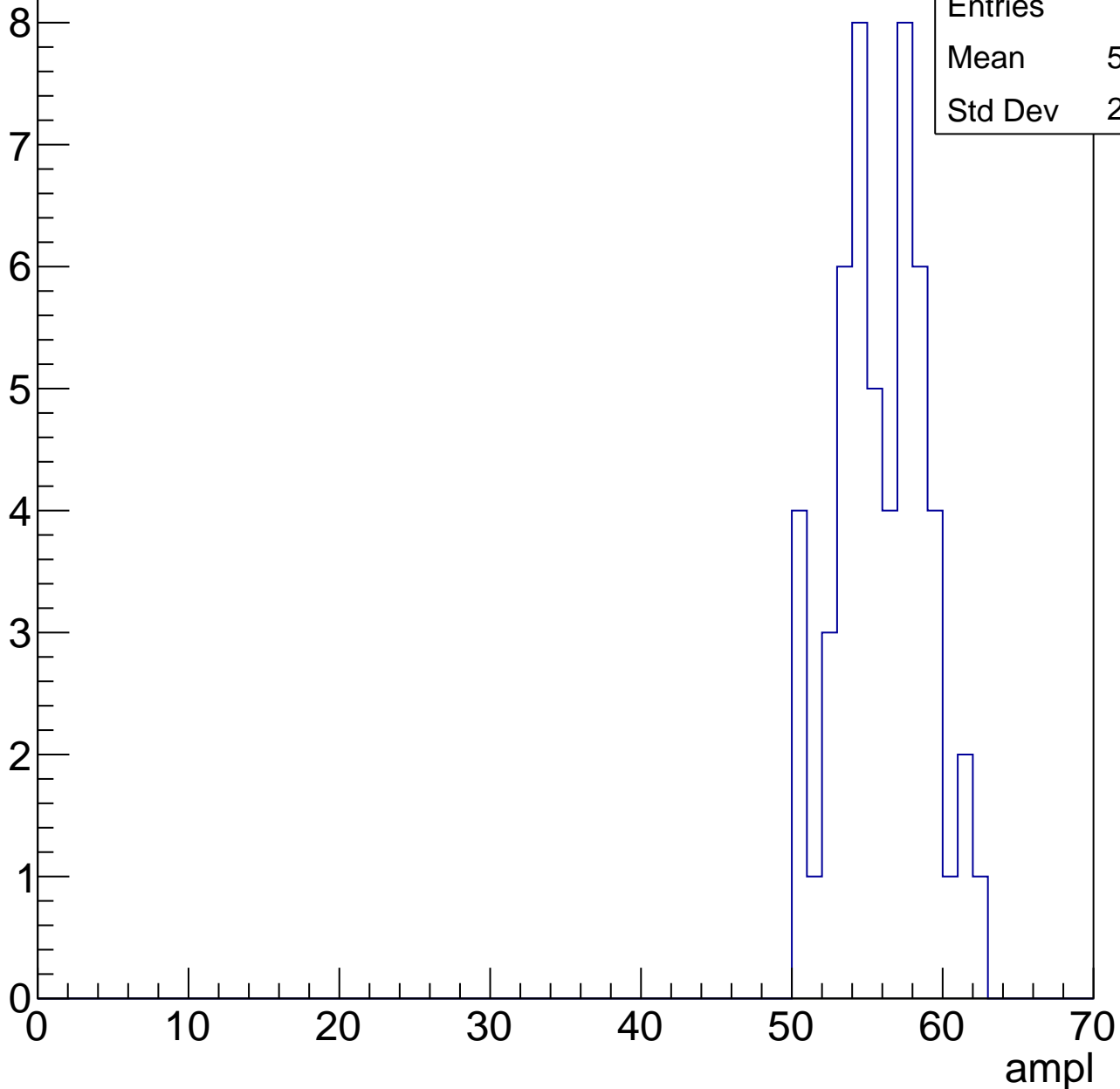


B0L001S, U18-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.47
Std Dev	2.956



B0L001S, U18-ch76, adc5

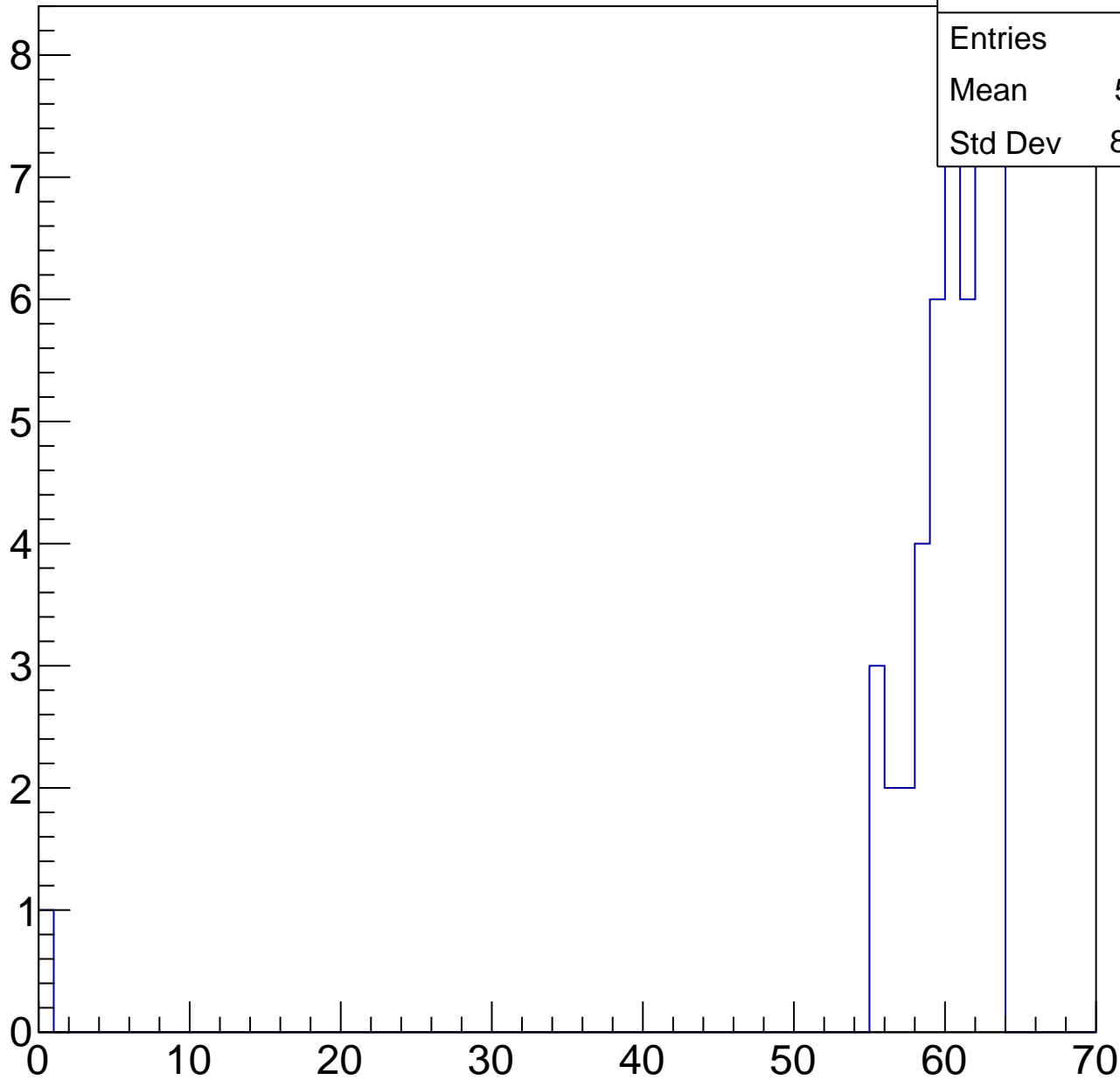
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.81
Std Dev	8.885

ampl



B0L001S, U18-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch77, adc0

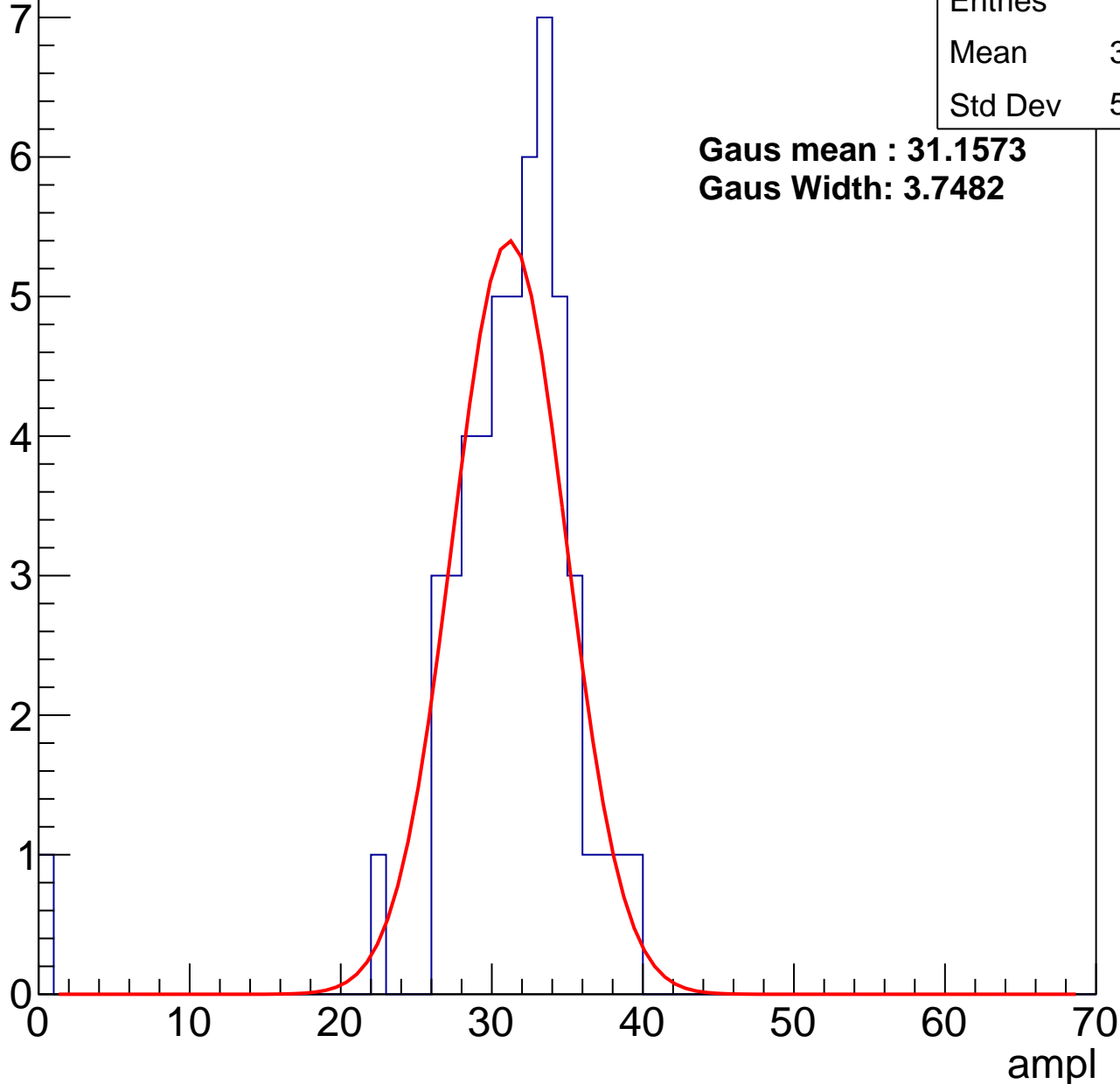
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30.63
Std Dev	5.452

Gaus mean : 31.1573

Gaus Width: 3.7482



B0L001S, U18-ch77, adc1

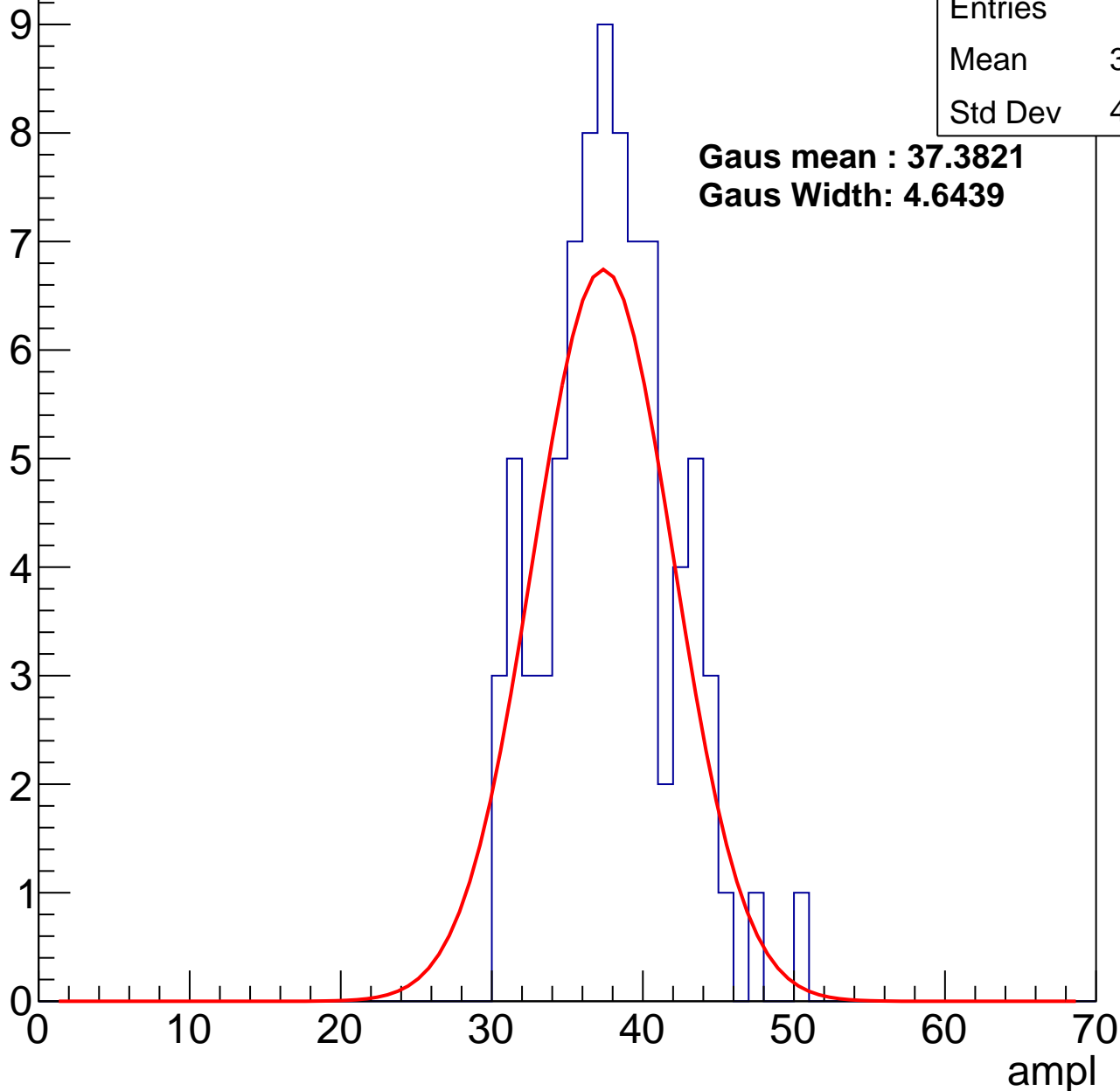
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	37.46
Std Dev	4.138

Gaus mean : 37.3821

Gaus Width: 4.6439



B0L001S, U18-ch77, adc2

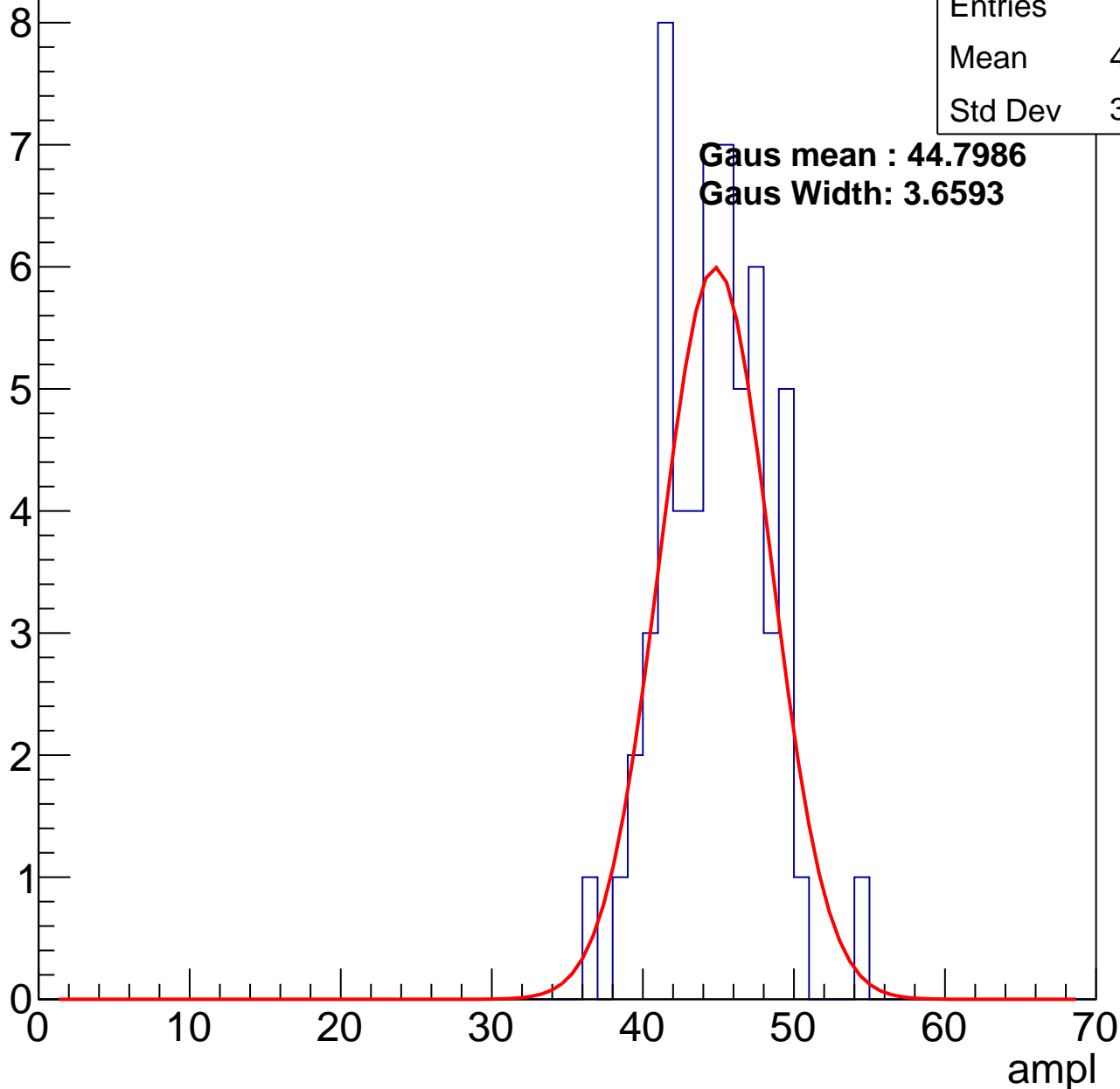
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.28
Std Dev	3.413

Gaus mean : 44.7986

Gaus Width: 3.6593

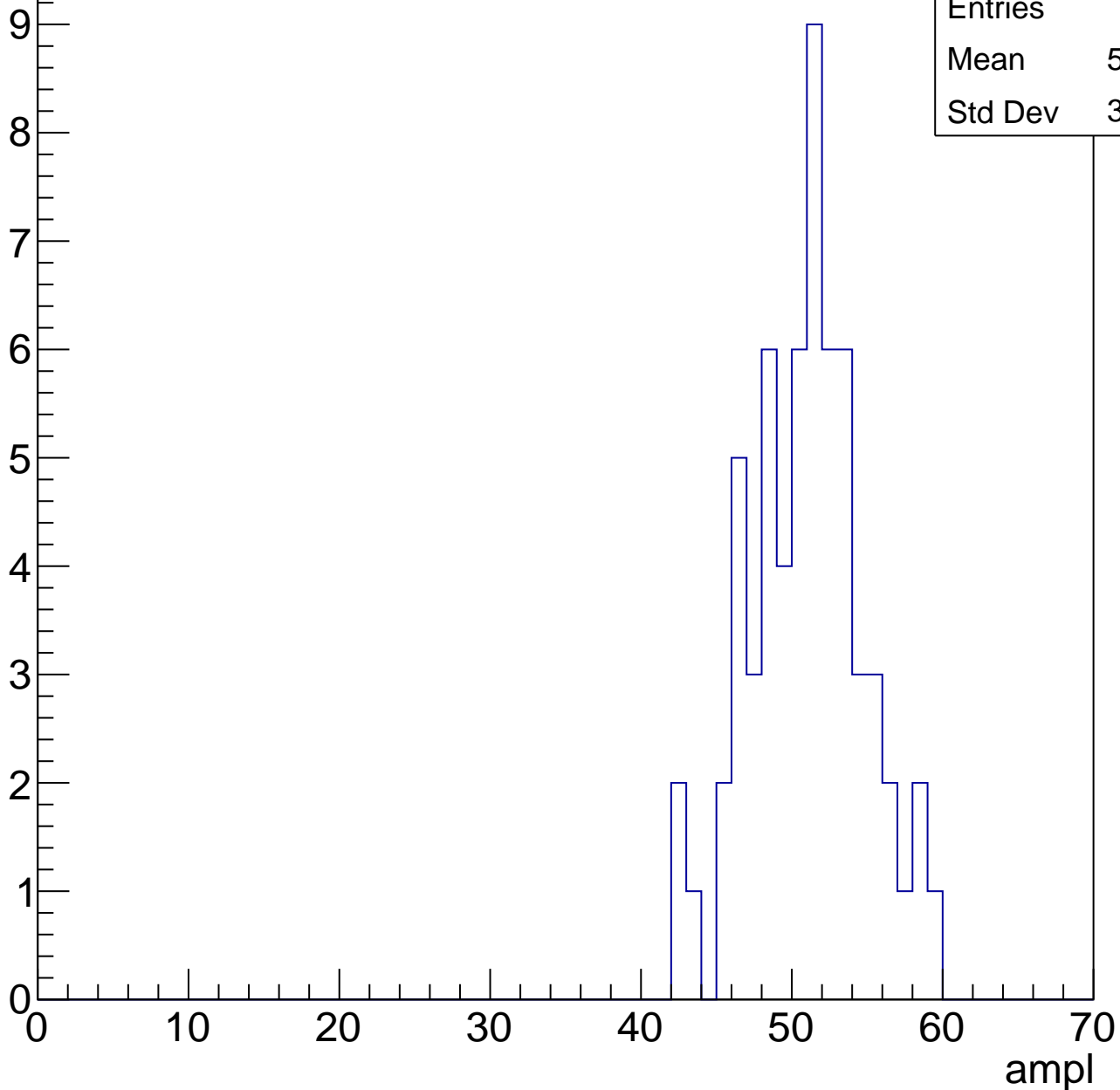


B0L001S, U18-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.52
Std Dev	3.779

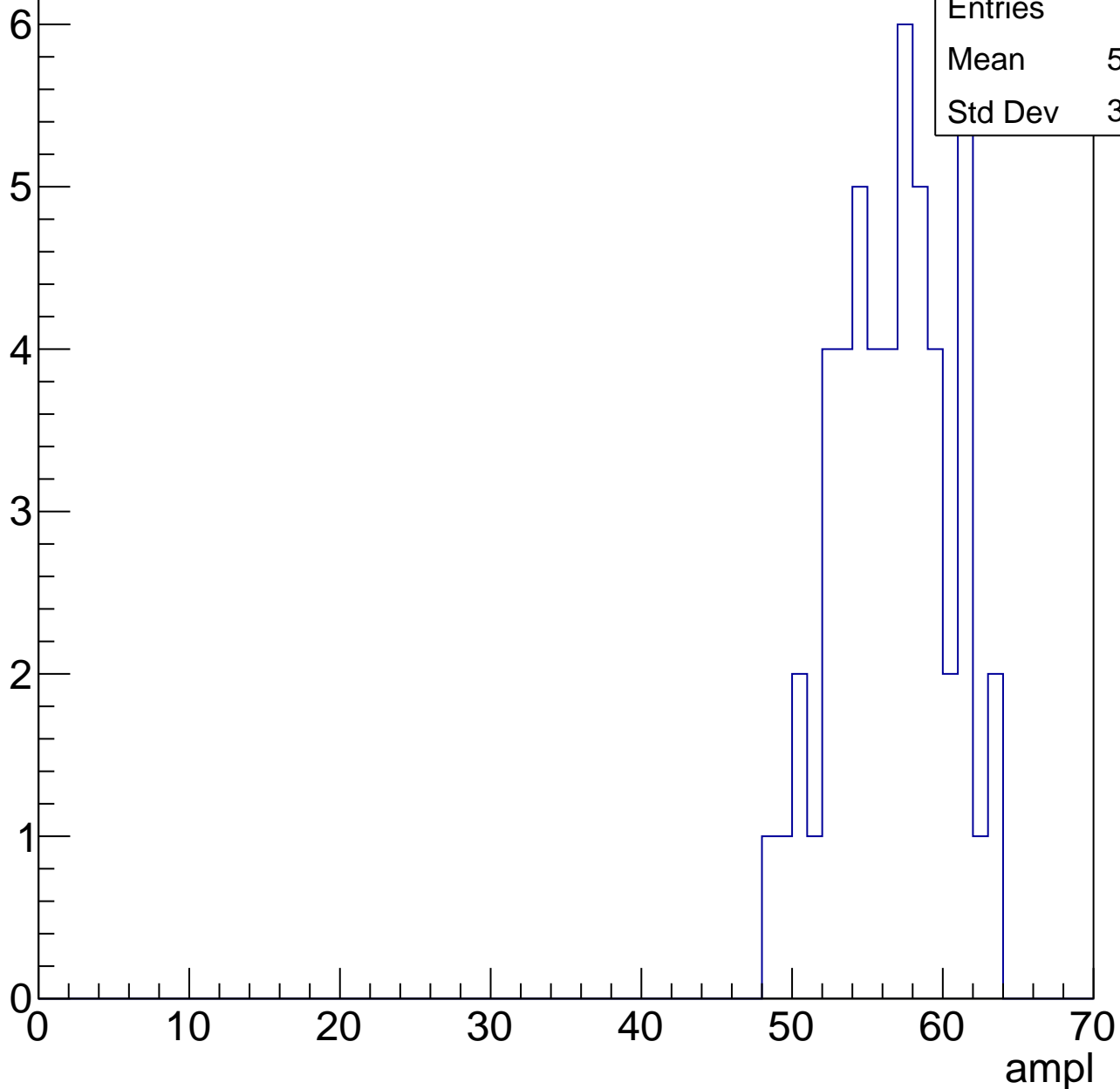


B0L001S, U18-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.23
Std Dev	3.693



B0L001S, U18-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

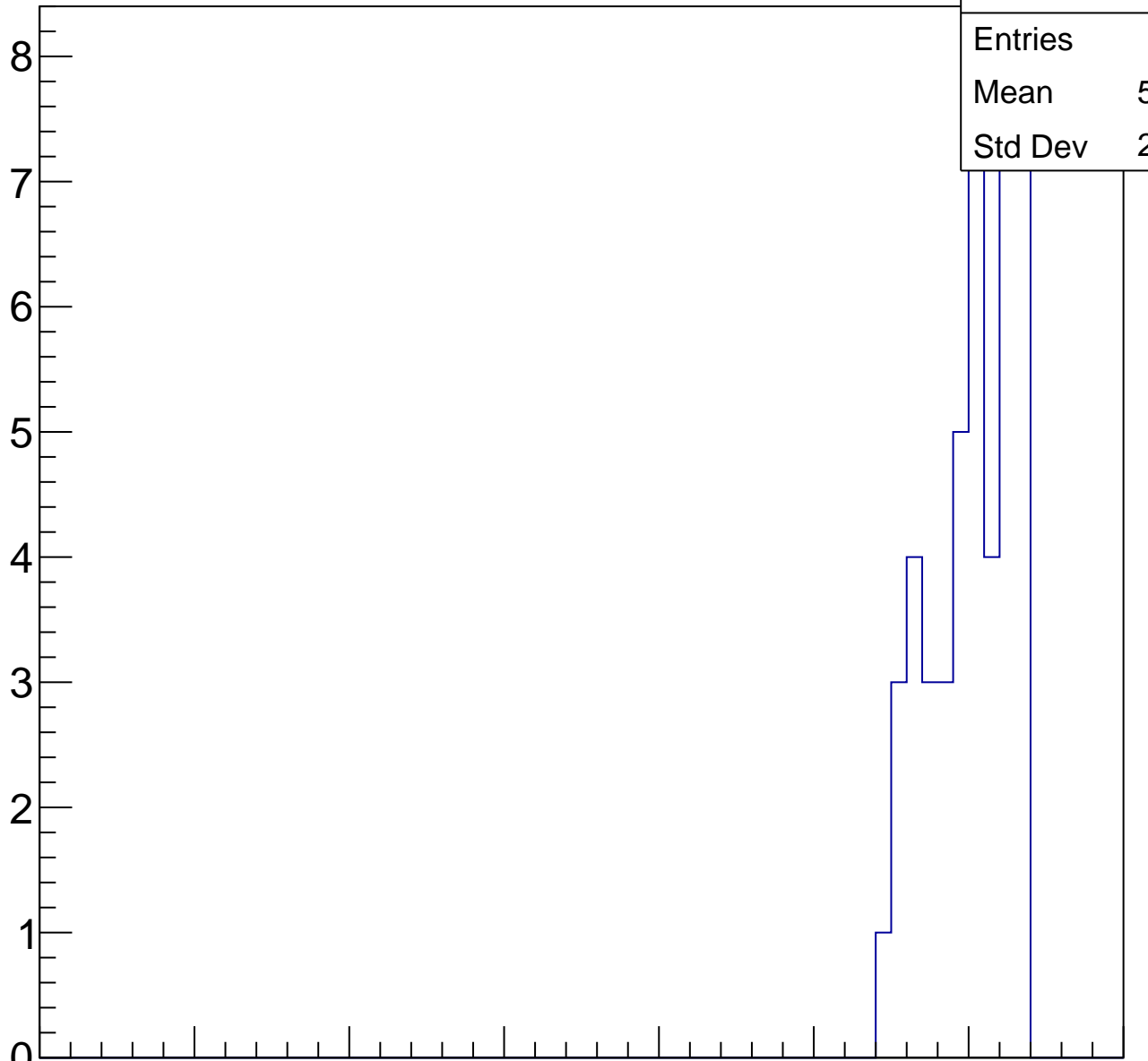
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.72
Std Dev	2.623

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch78, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	28
Std Dev	4.887

Gaus mean : 28.7932

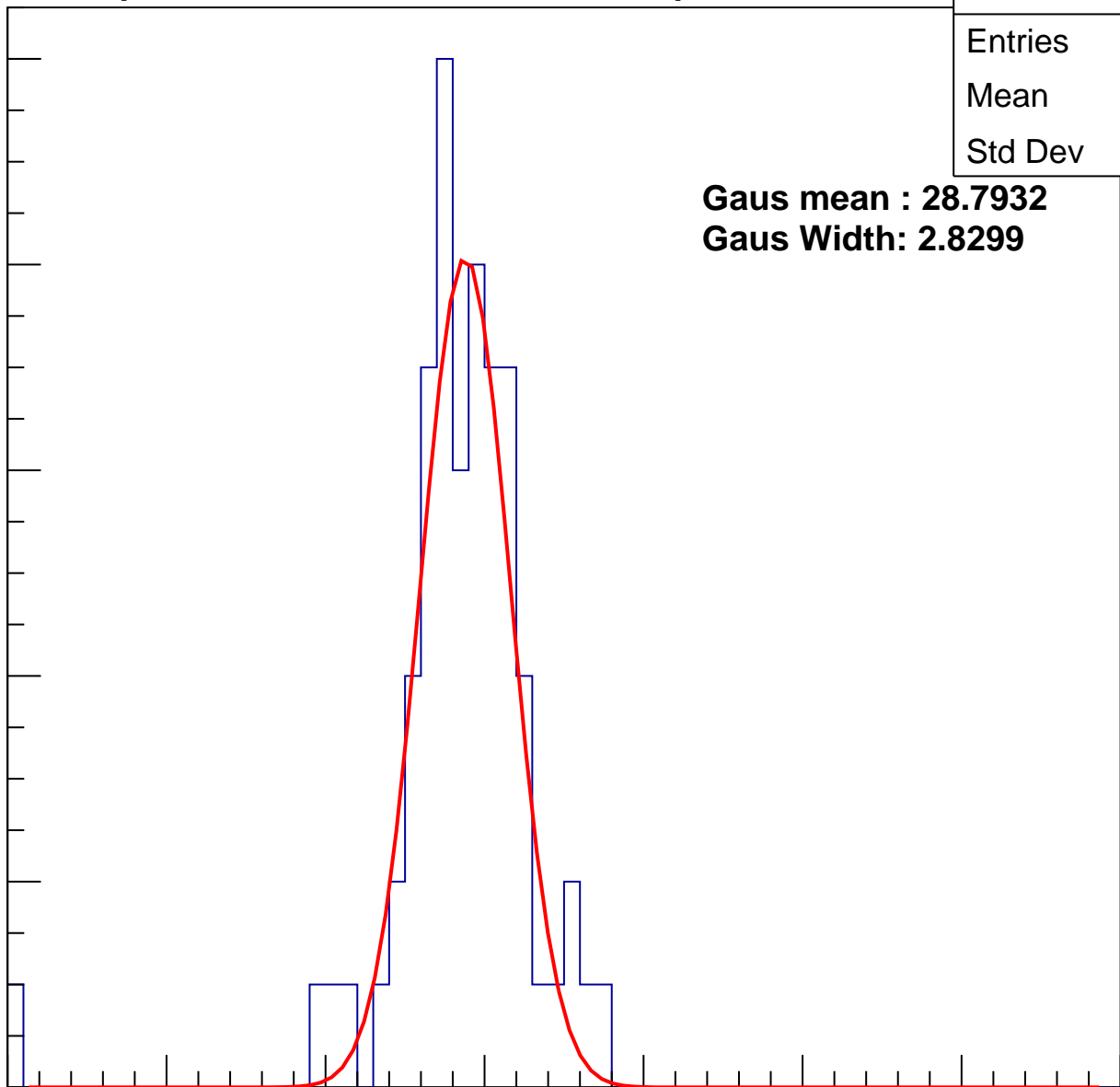
Gaus Width: 2.8299

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch78, adc1

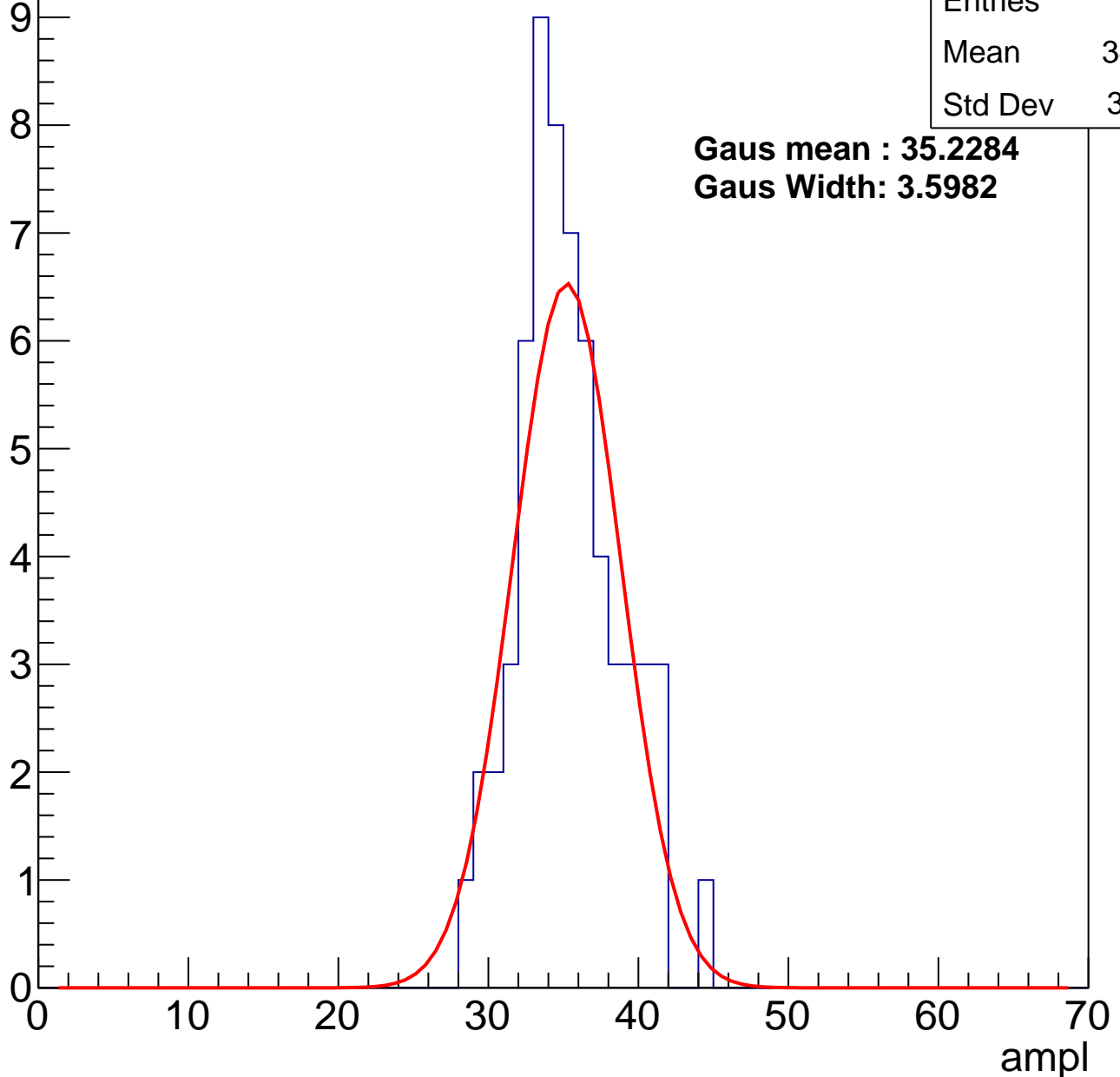
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	34.87
Std Dev	3.331

Gaus mean : 35.2284

Gaus Width: 3.5982



B0L001S, U18-ch78, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

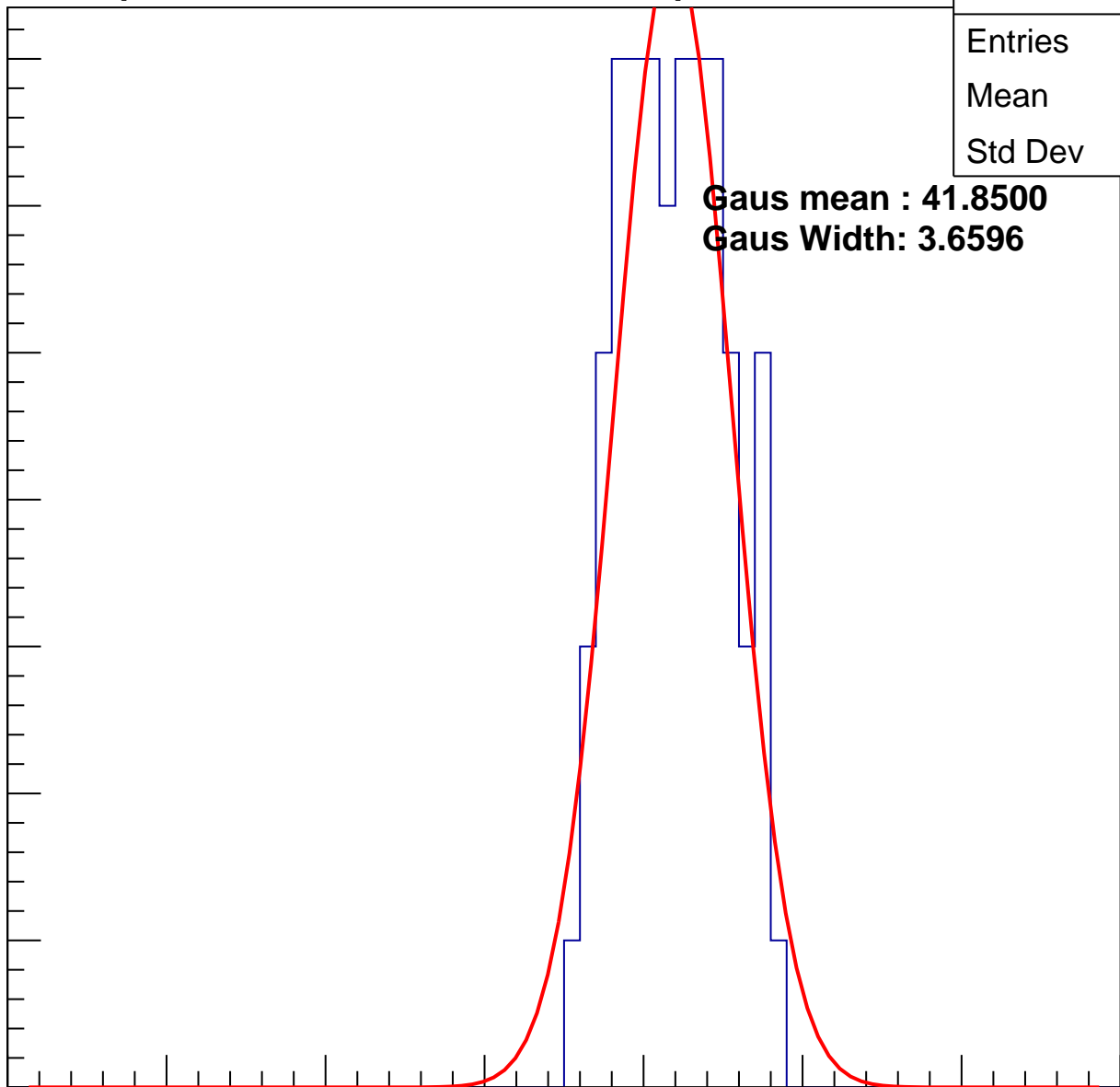
Entries	71
Mean	41.44
Std Dev	3.266

Gaus mean : 41.8500

Gaus Width: 3.6596

0 10 20 30 40 50 60 70

ampl

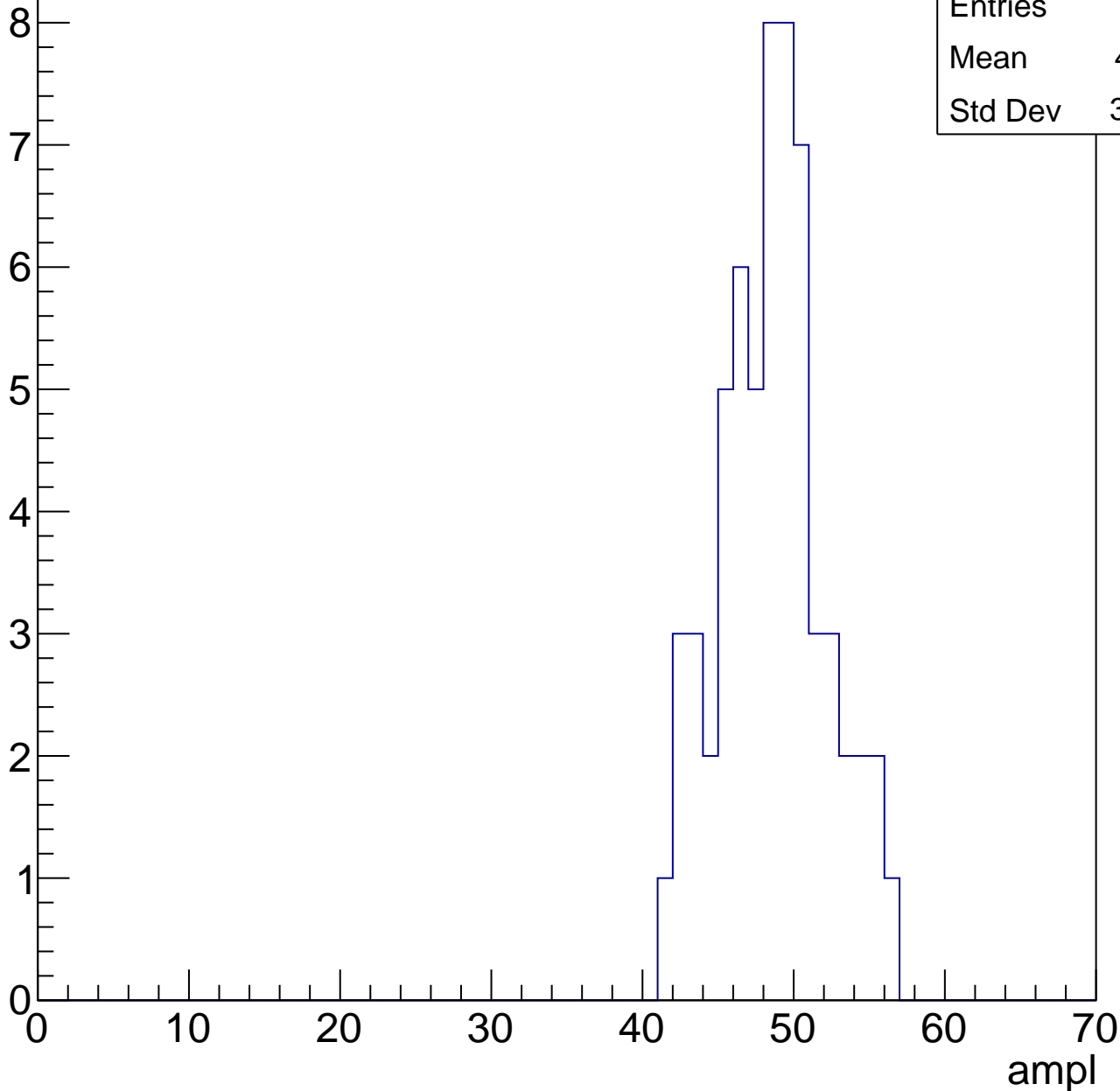


B0L001S, U18-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

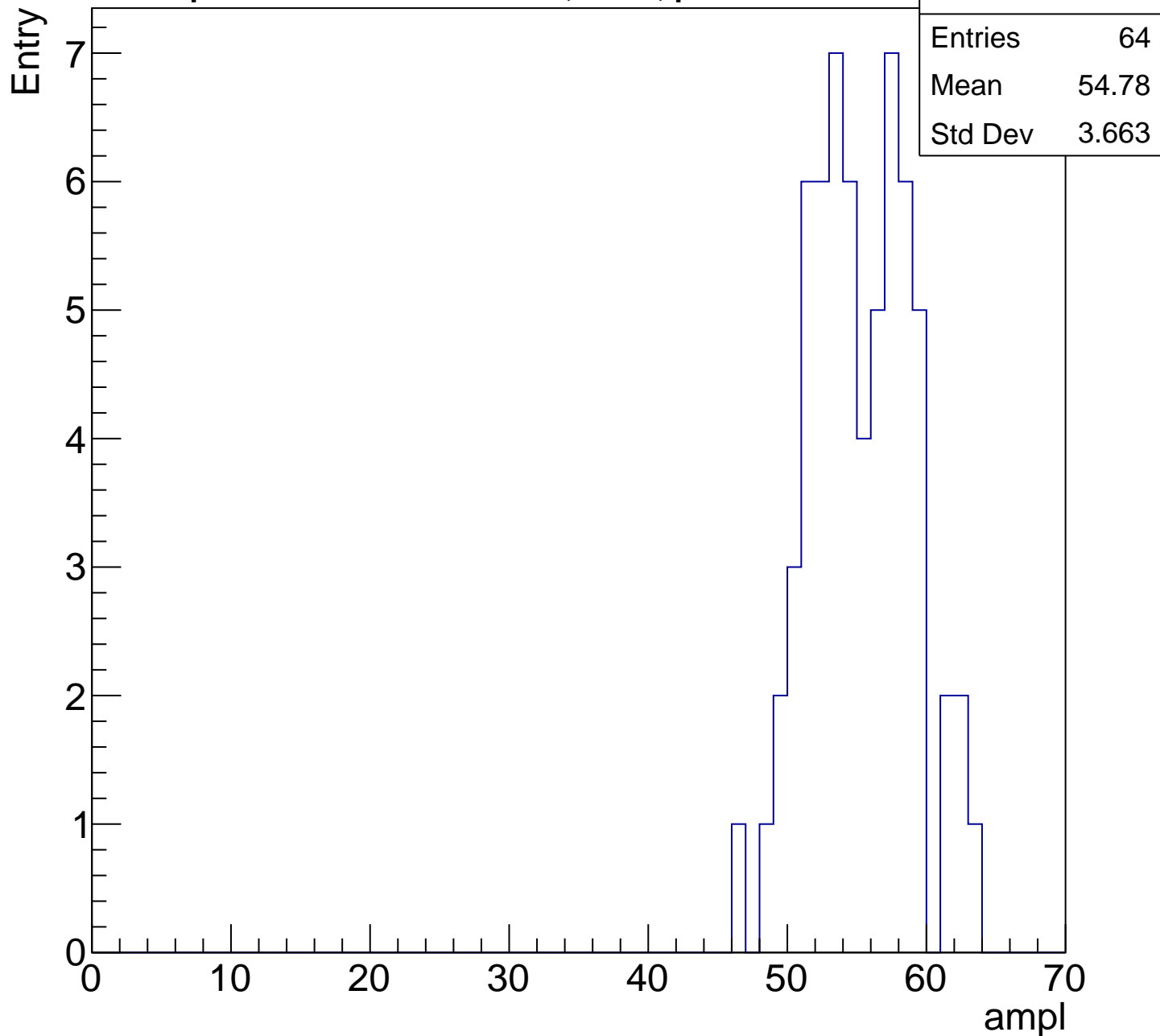
Entry

Entries	61
Mean	48.11
Std Dev	3.465



B0L001S, U18-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U18-ch78, adc5

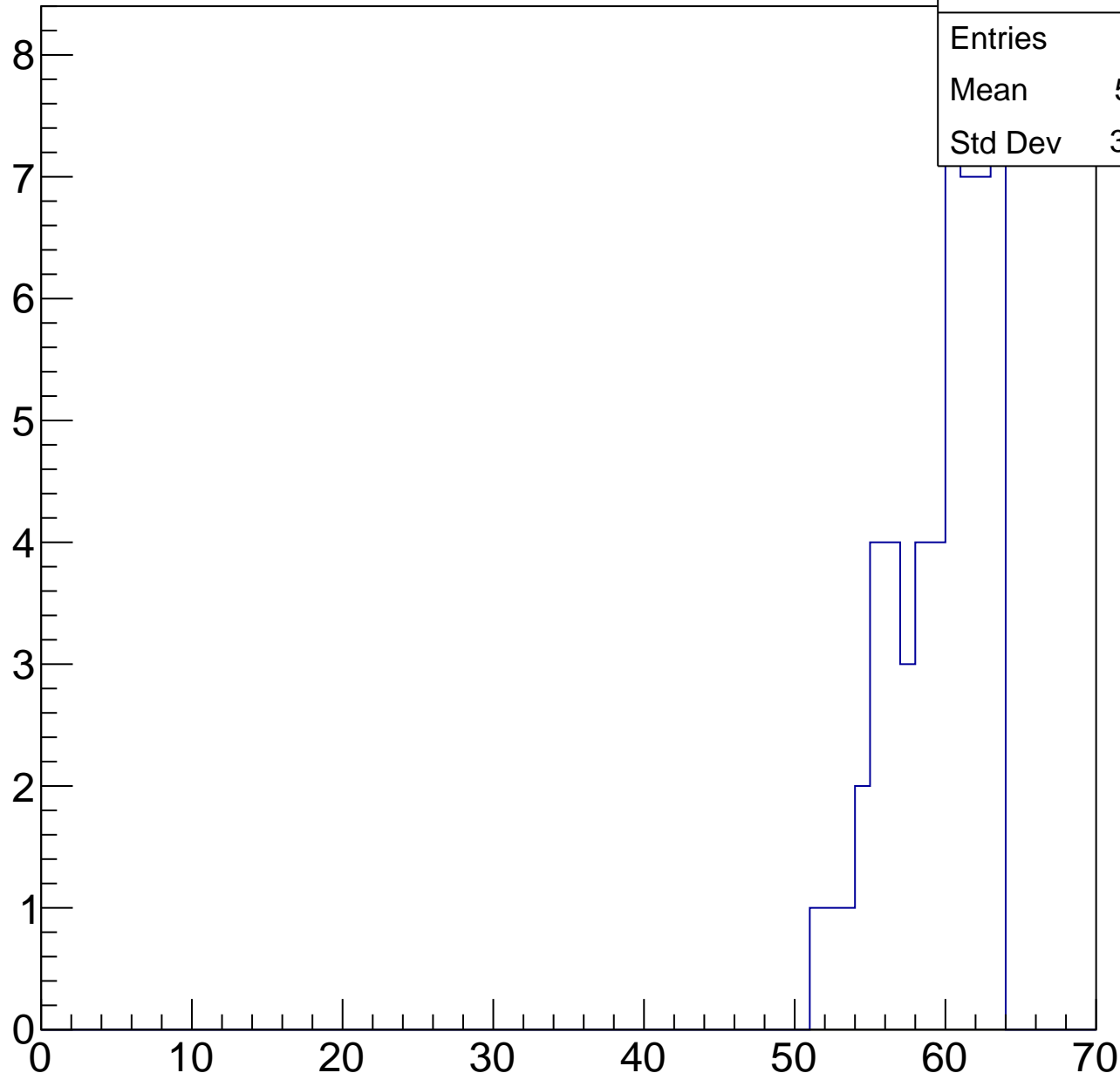
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	59.11
Std Dev	3.166

ampl



B0L001S, U18-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

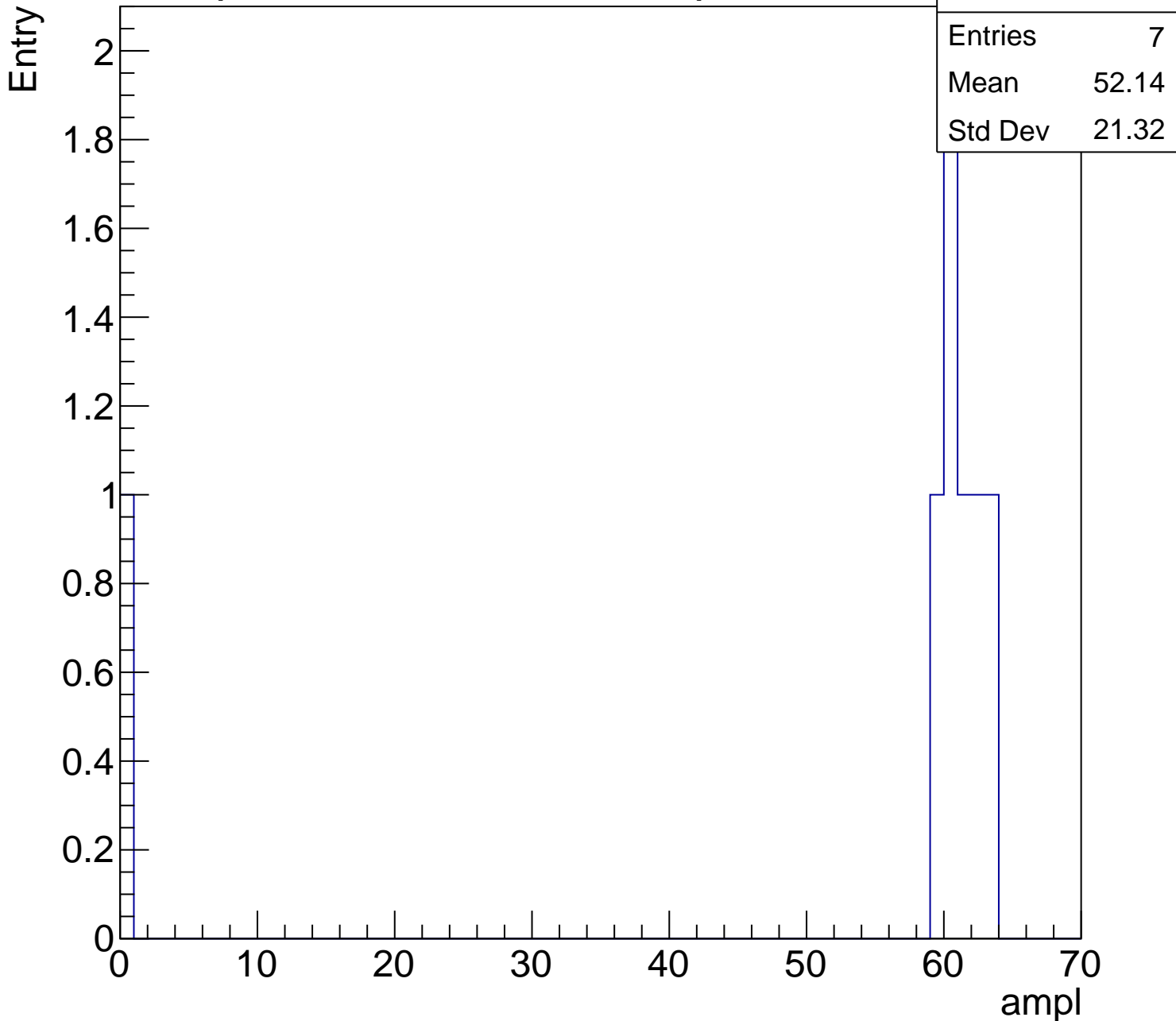
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.14
Std Dev	21.32

0 10 20 30 40 50 60 70

ampl



B0L001S, U18-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch79, adc0

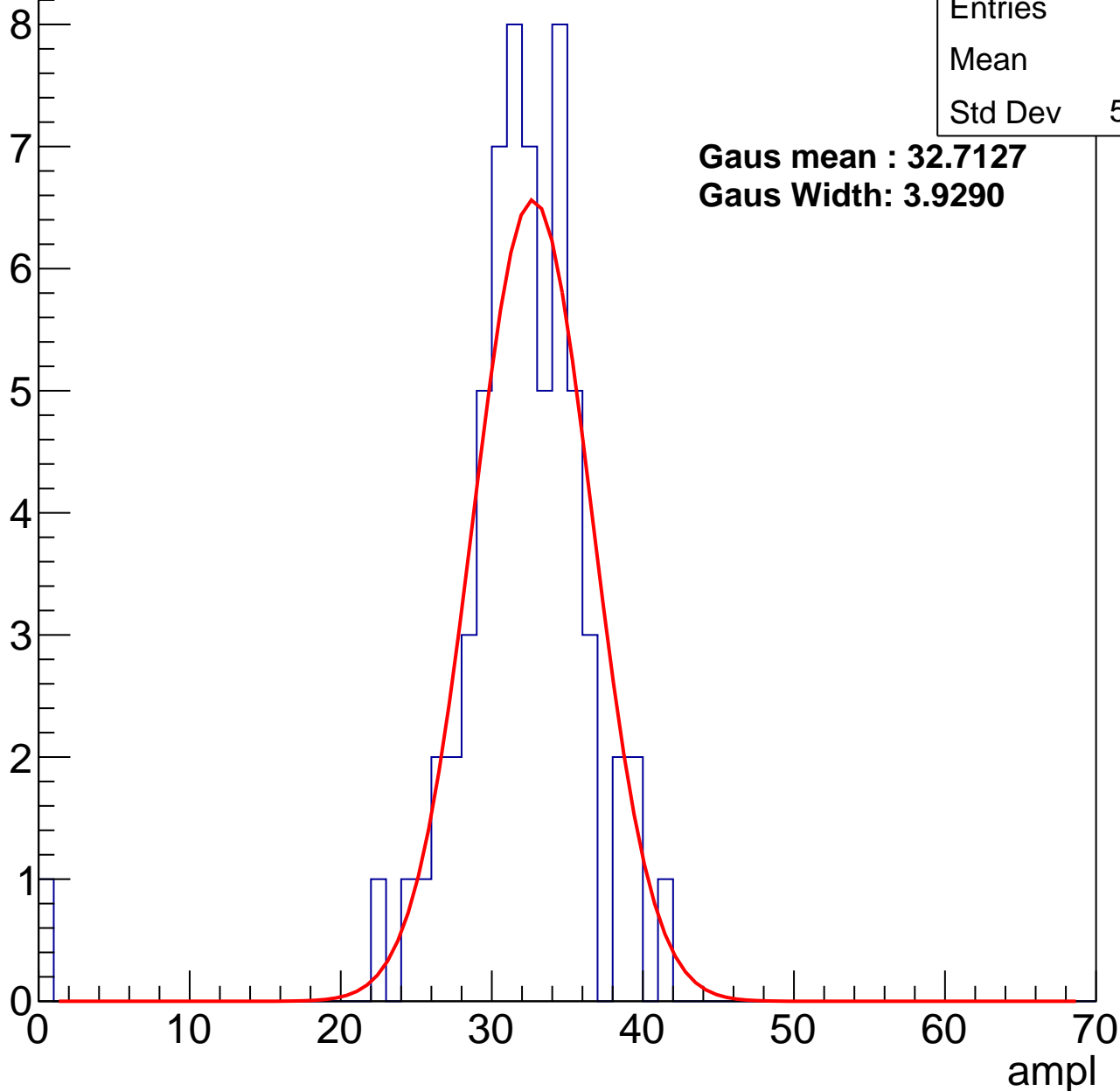
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.3
Std Dev	5.358

Gaus mean : 32.7127

Gaus Width: 3.9290



B0L001S, U18-ch79, adc1

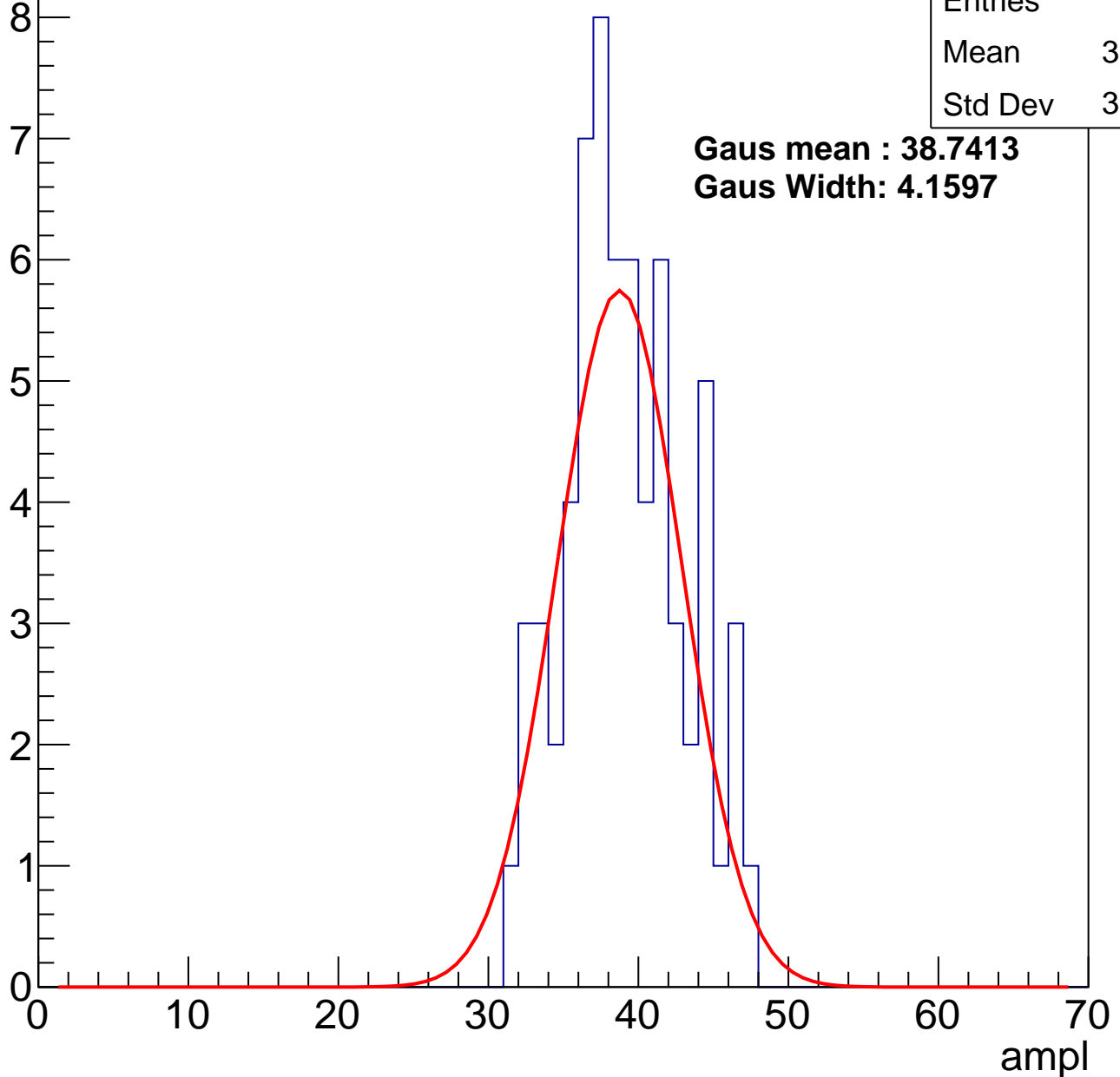
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.65
Std Dev	3.893

Gaus mean : 38.7413

Gaus Width: 4.1597

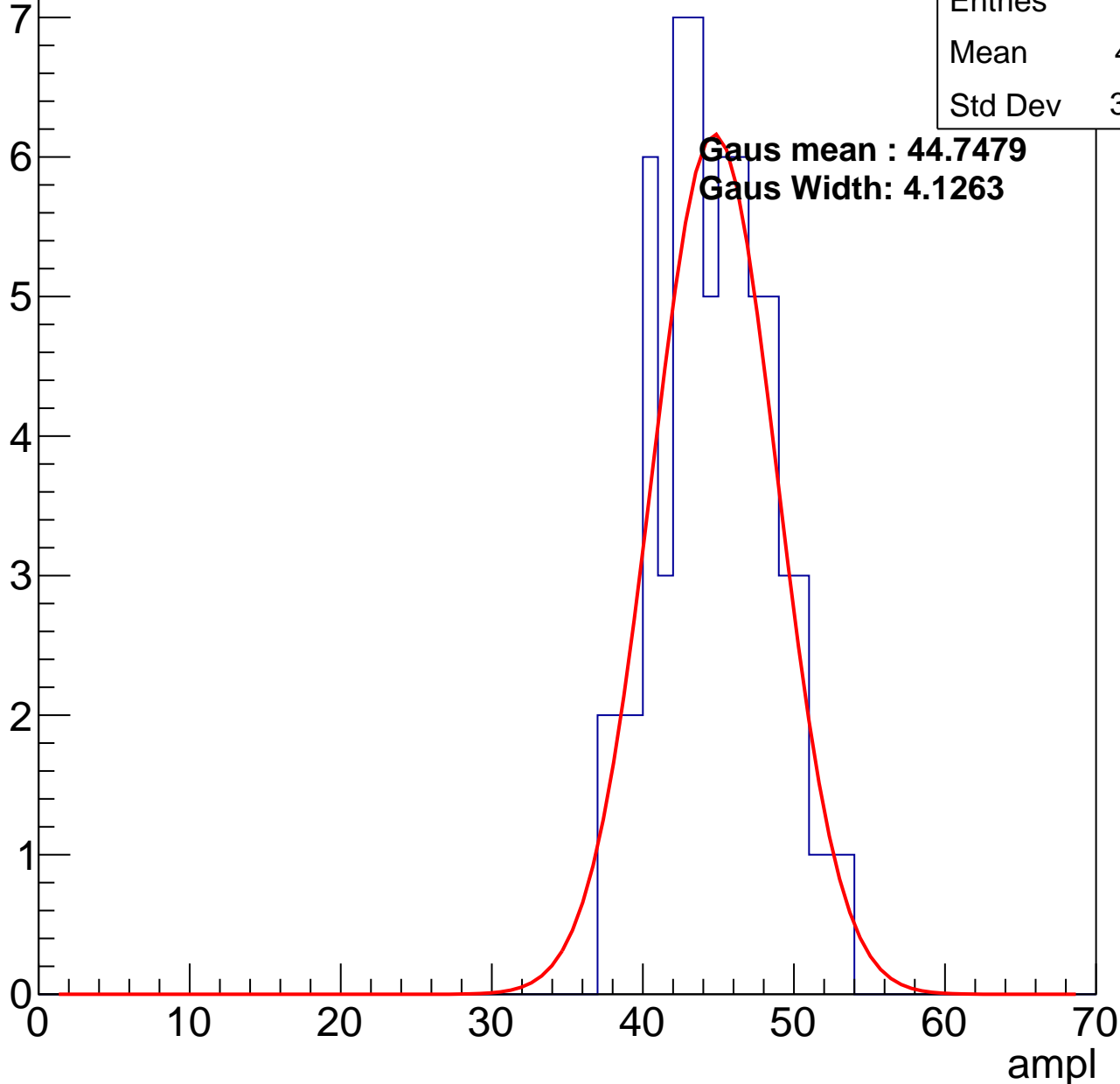


B0L001S, U18-ch79, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.31
Std Dev	3.729

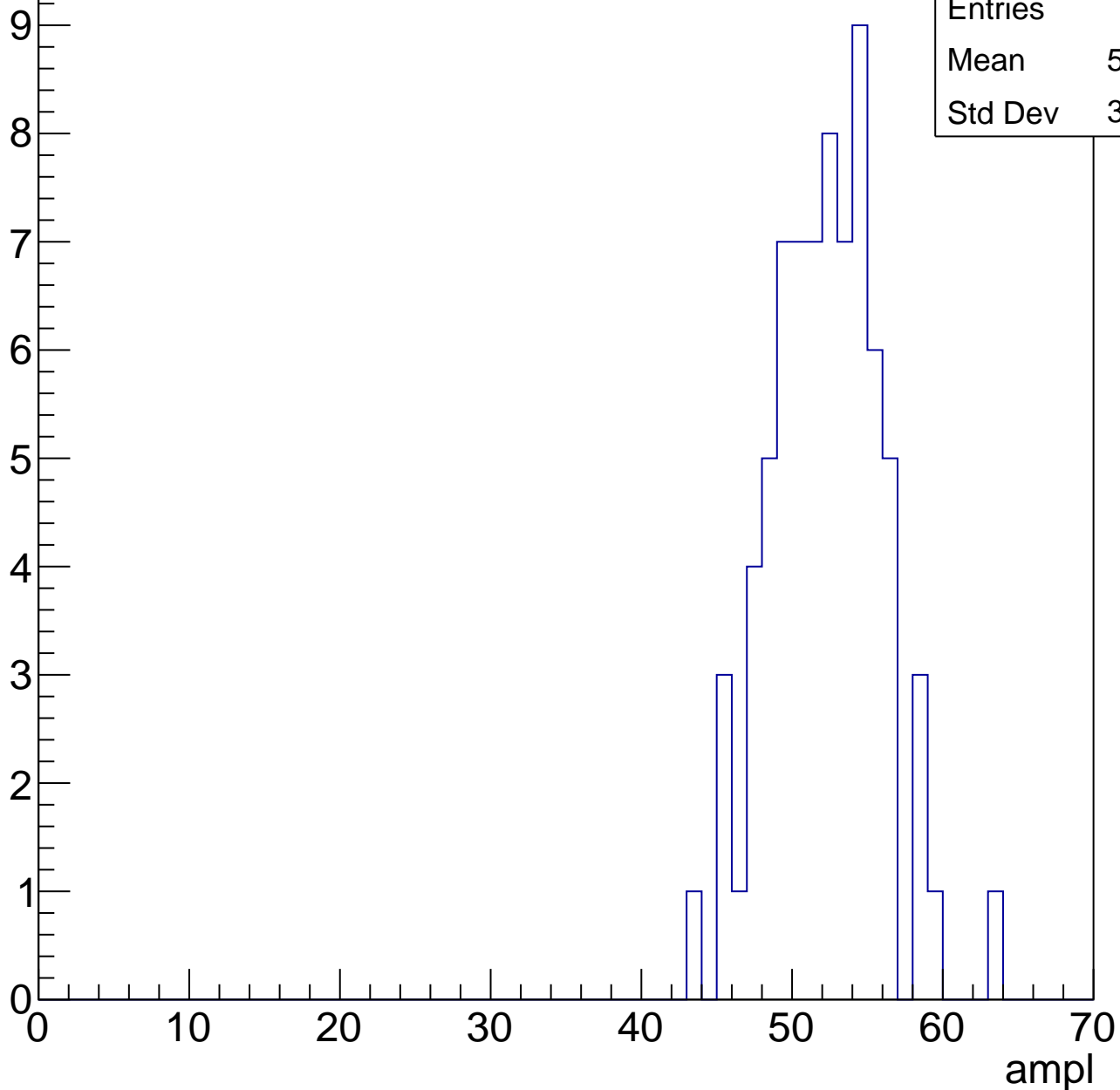


B0L001S, U18-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	51.75
Std Dev	3.637

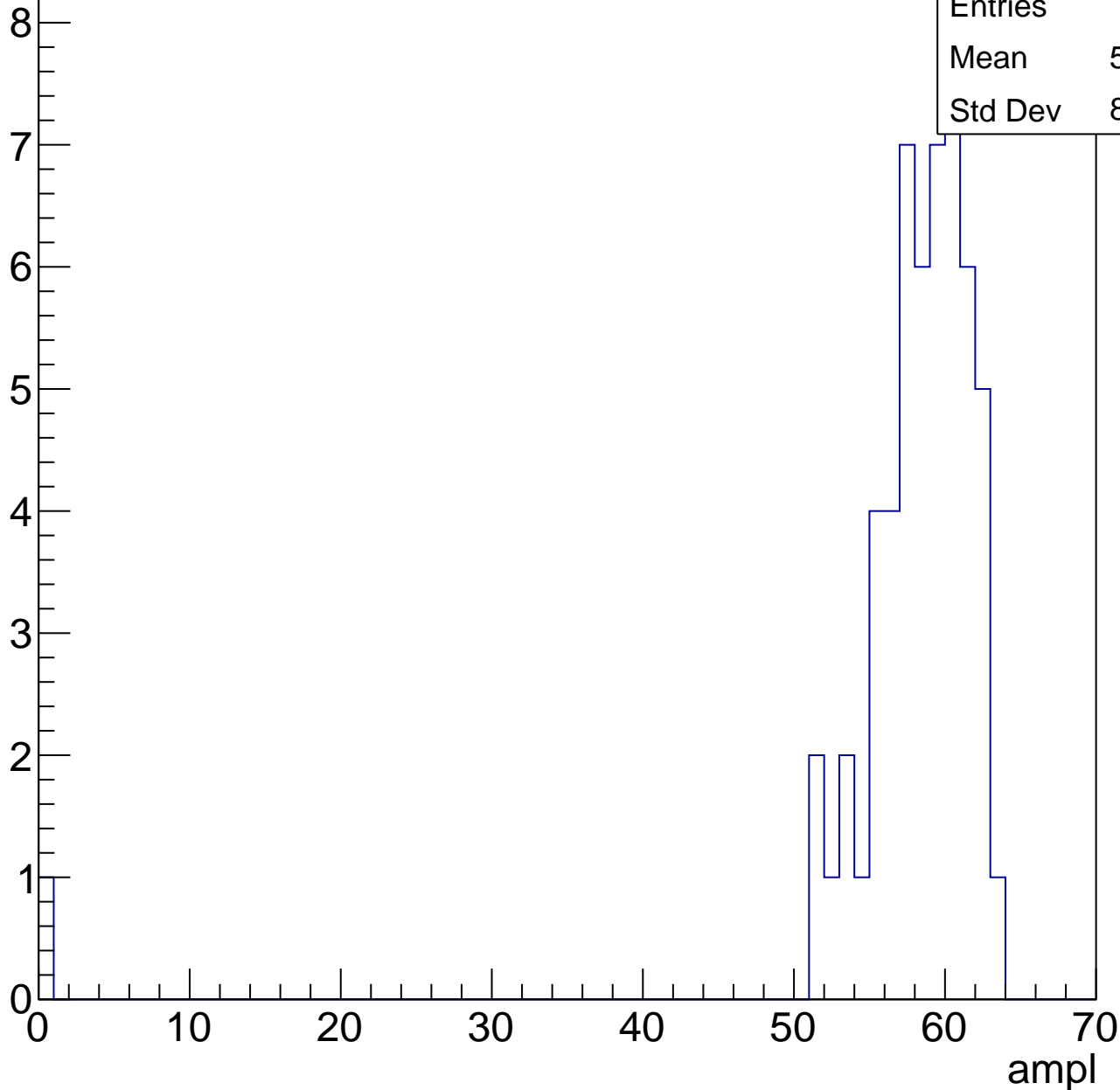


B0L001S, U18-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.04
Std Dev	8.279

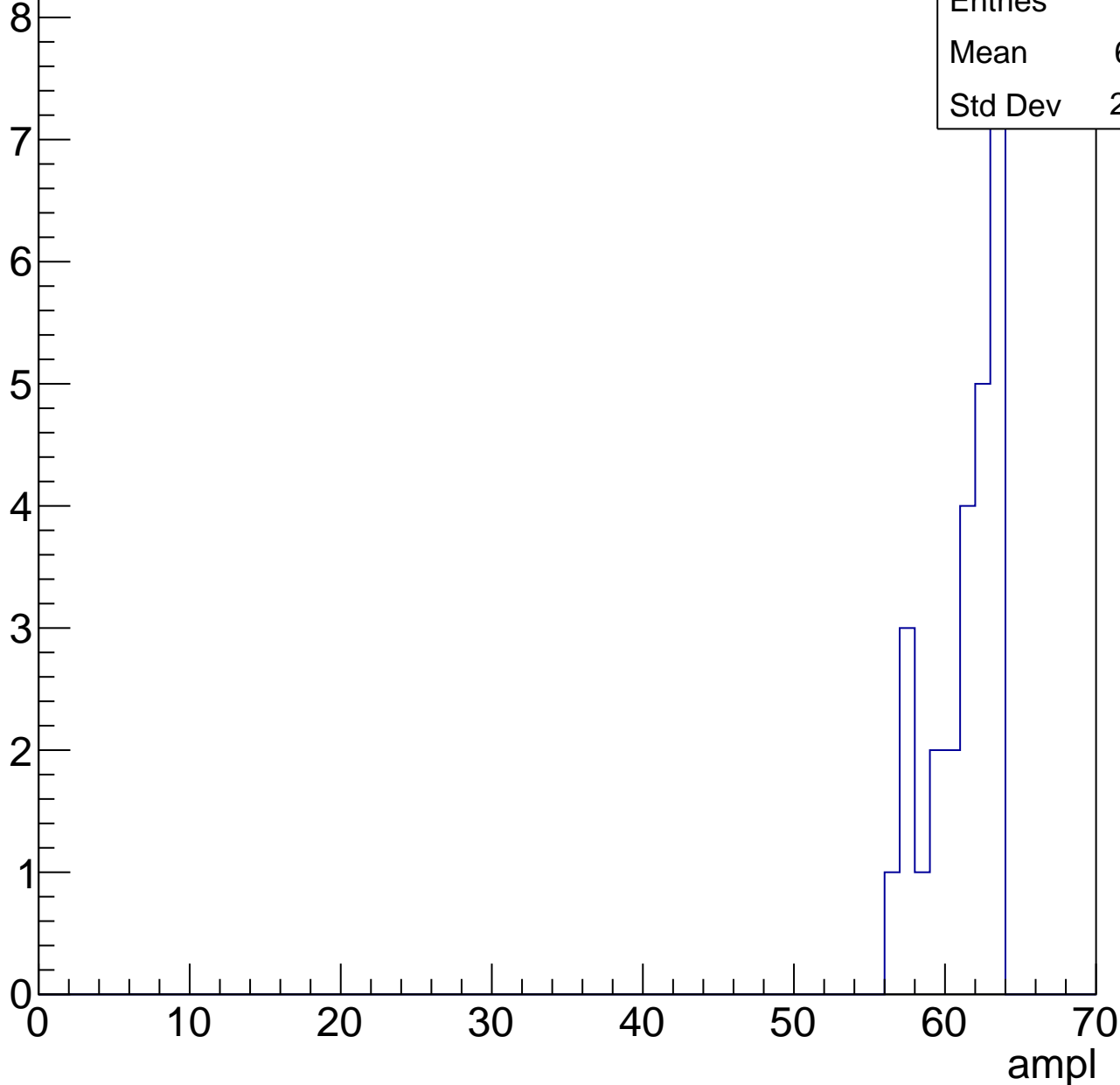


B0L001S, U18-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	60.81
Std Dev	2.219



B0L001S, U18-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch80, adc0

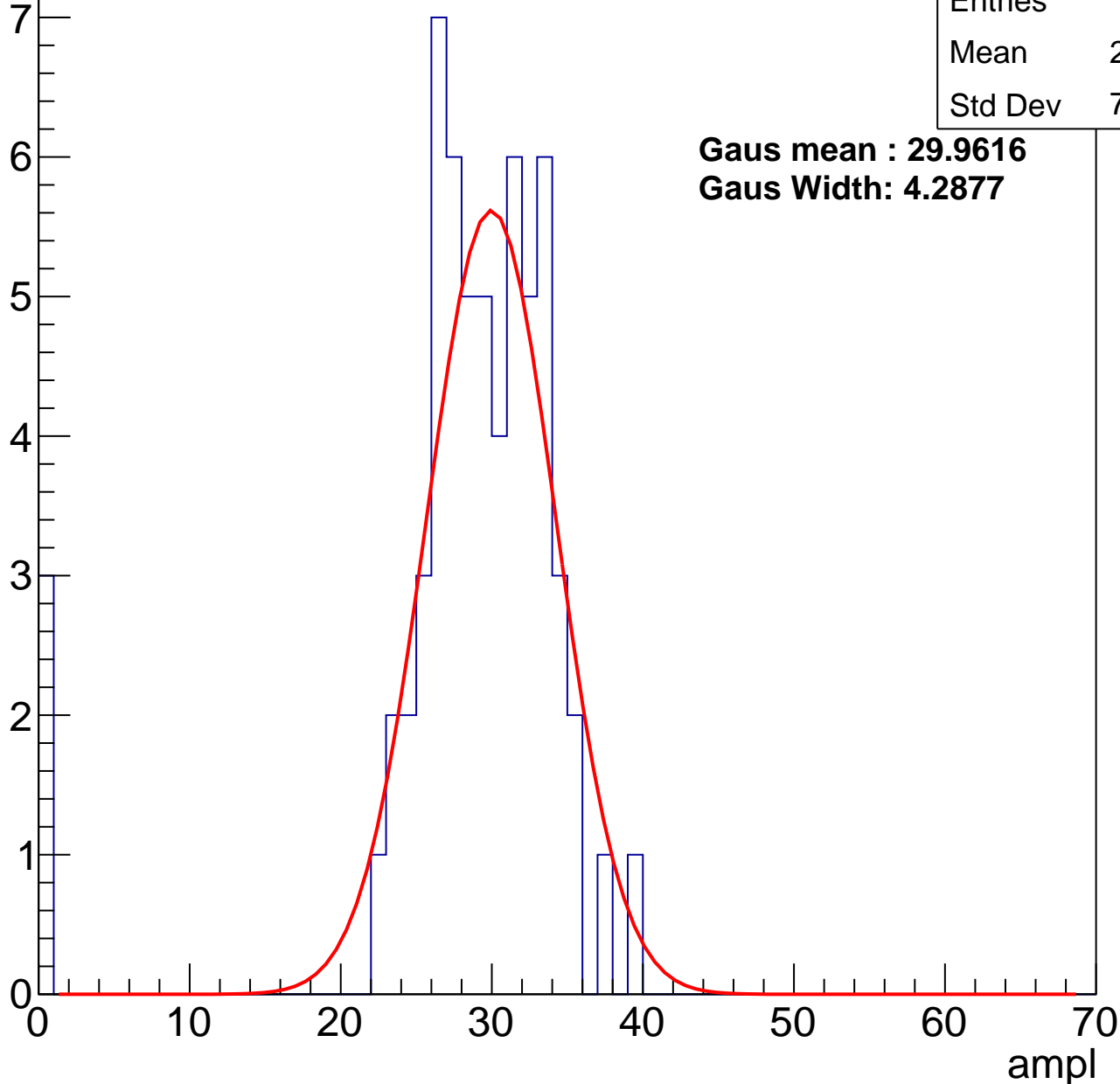
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	27.94
Std Dev	7.233

Gaus mean : 29.9616

Gaus Width: 4.2877



B0L001S, U18-ch80, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	36.45
Std Dev	3.015

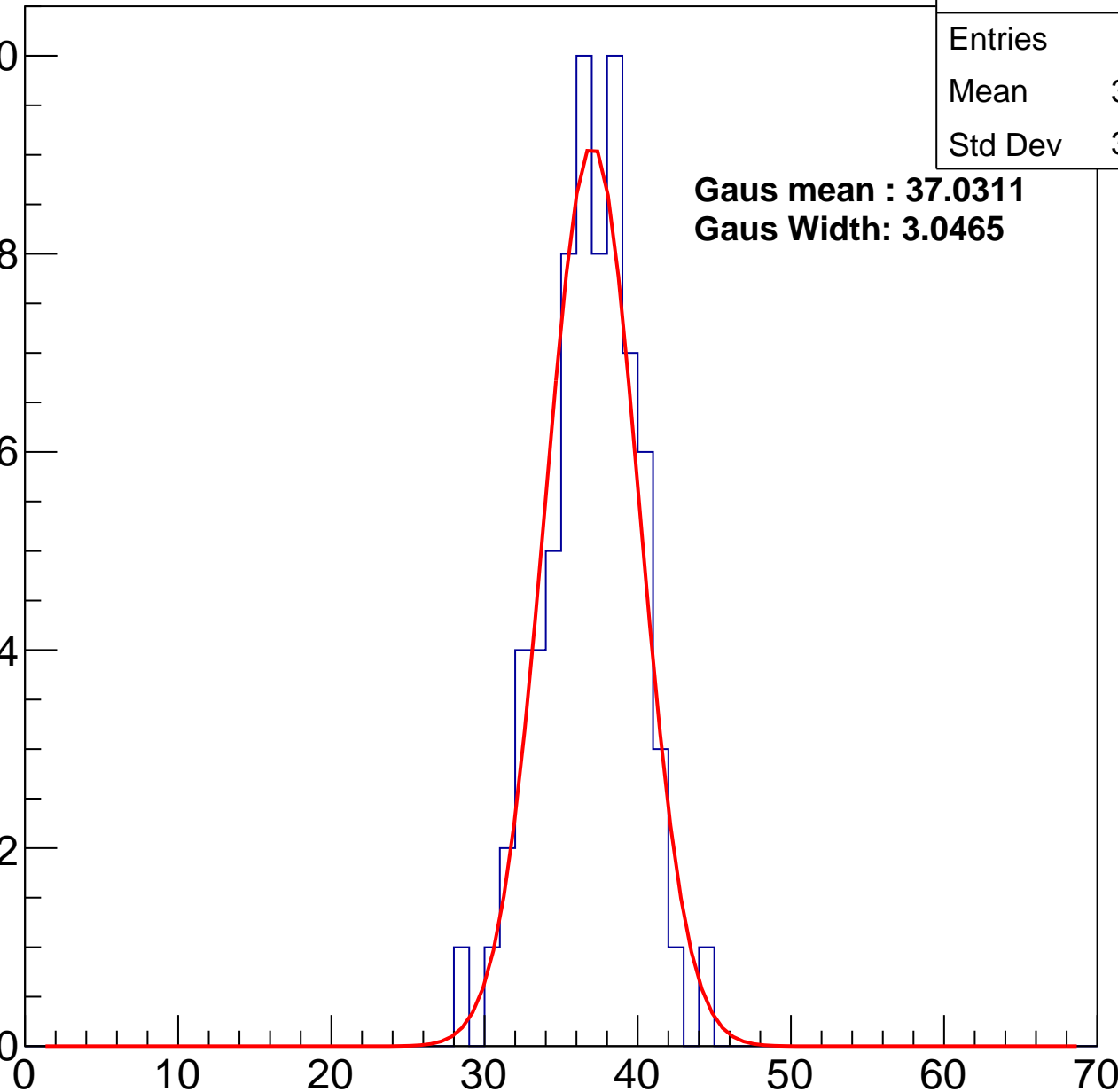
Gaus mean : 37.0311

Gaus Width: 3.0465

Entry

10
8
6
4
2
0

ampl



B0L001S, U18-ch80, adc2

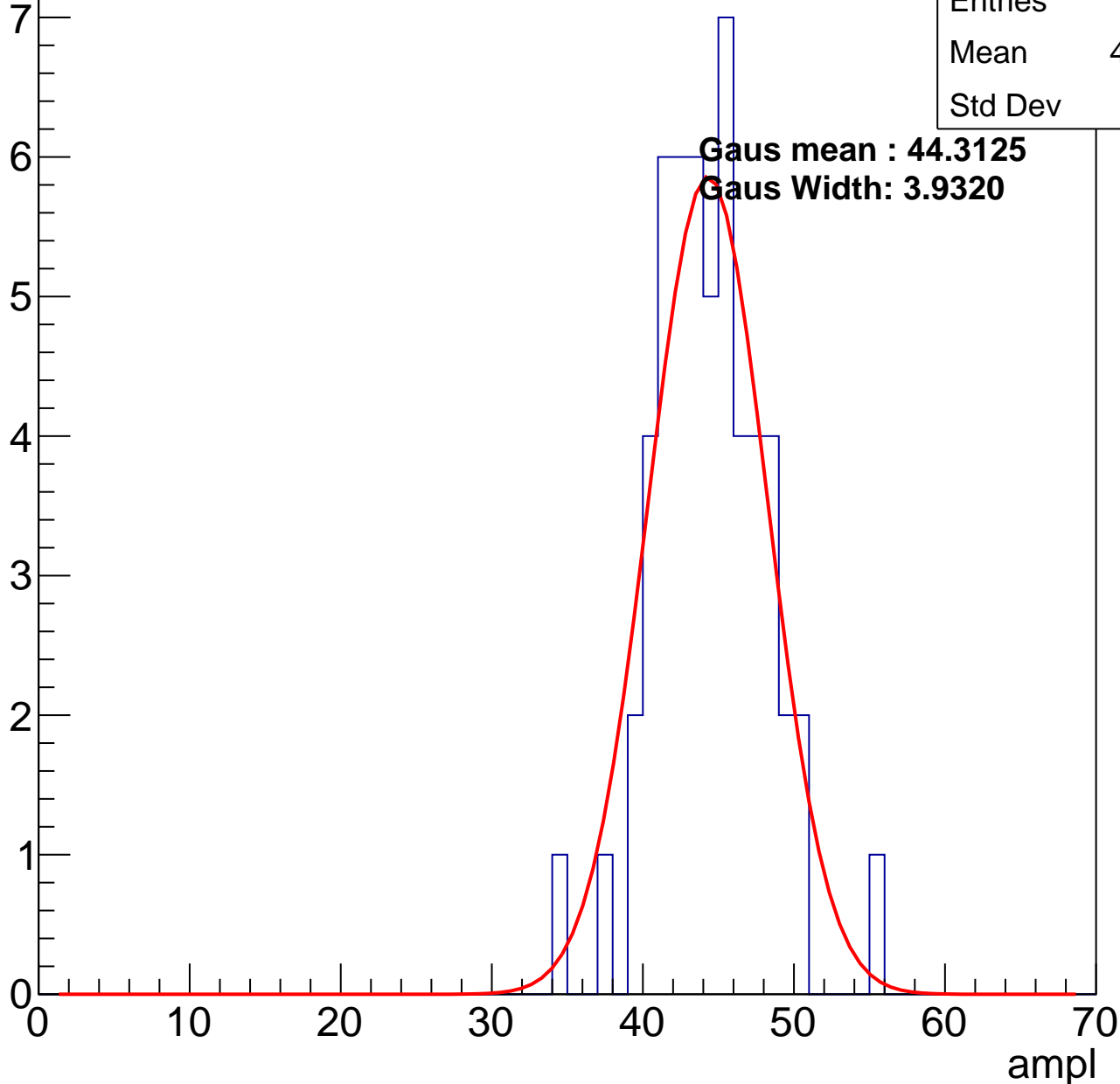
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.95
Std Dev	3.6

Gaus mean : 44.3125

Gaus Width: 3.9320

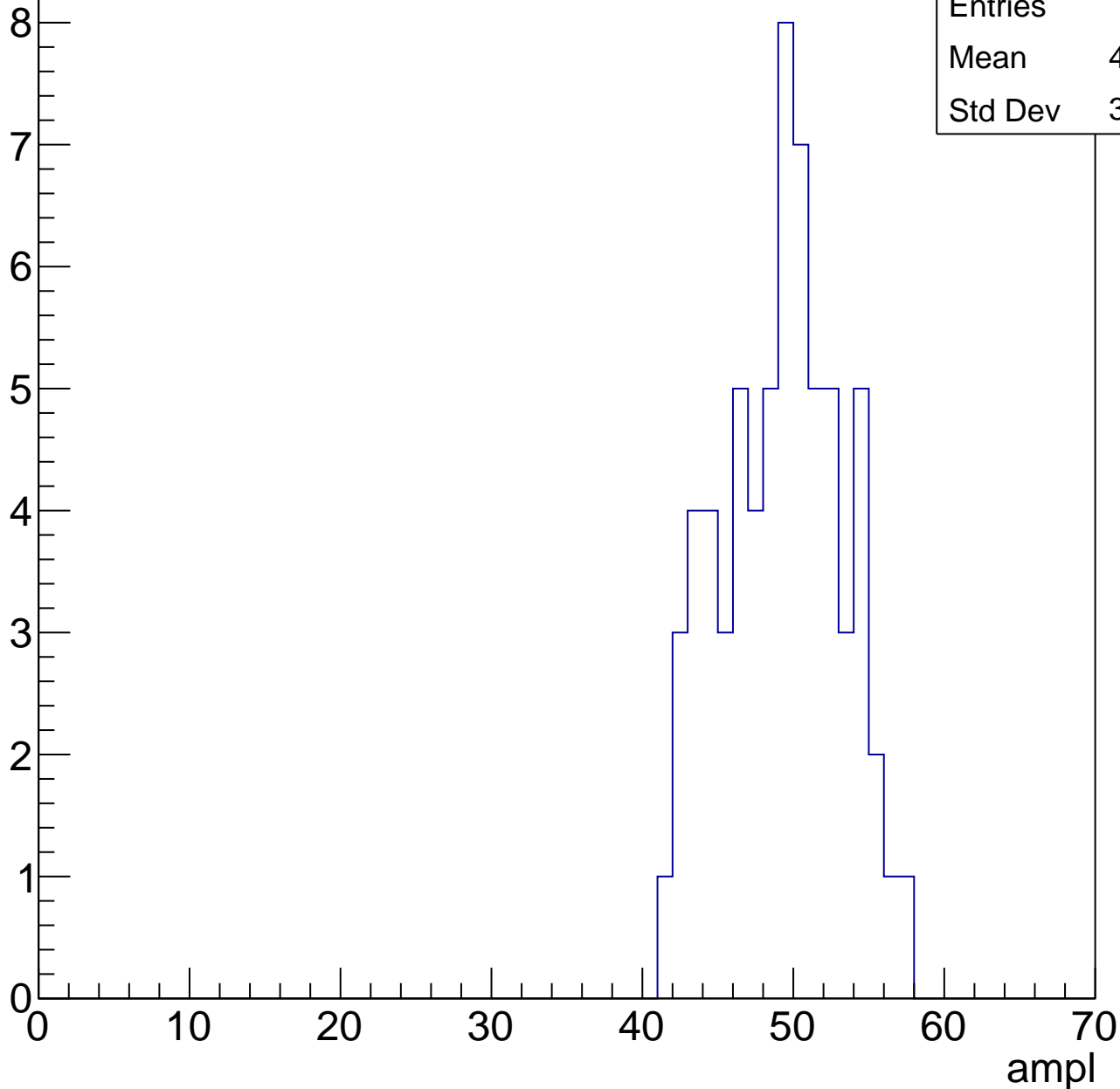


B0L001S, U18-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	48.74
Std Dev	3.882

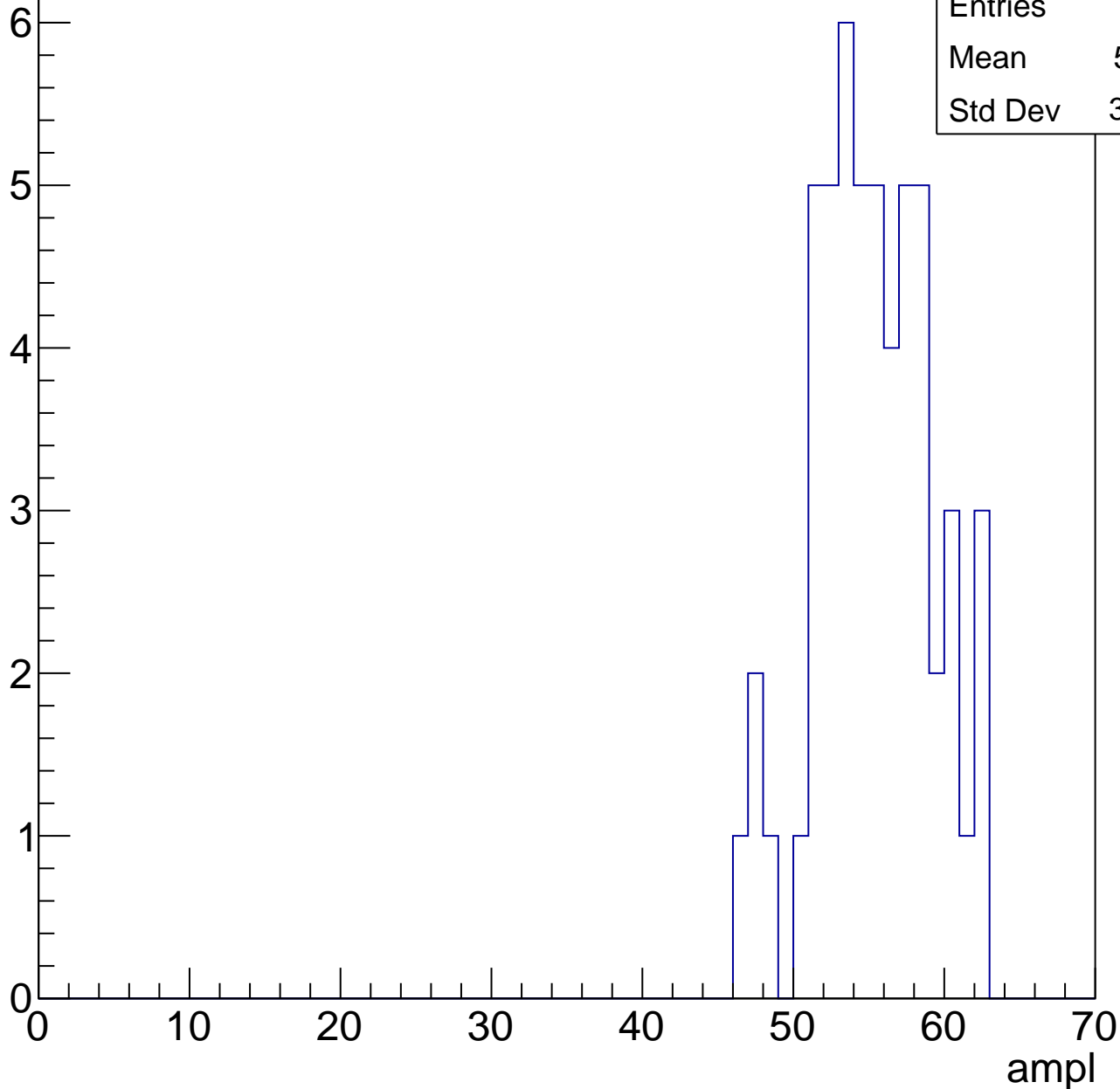


B0L001S, U18-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	54.81
Std Dev	3.835

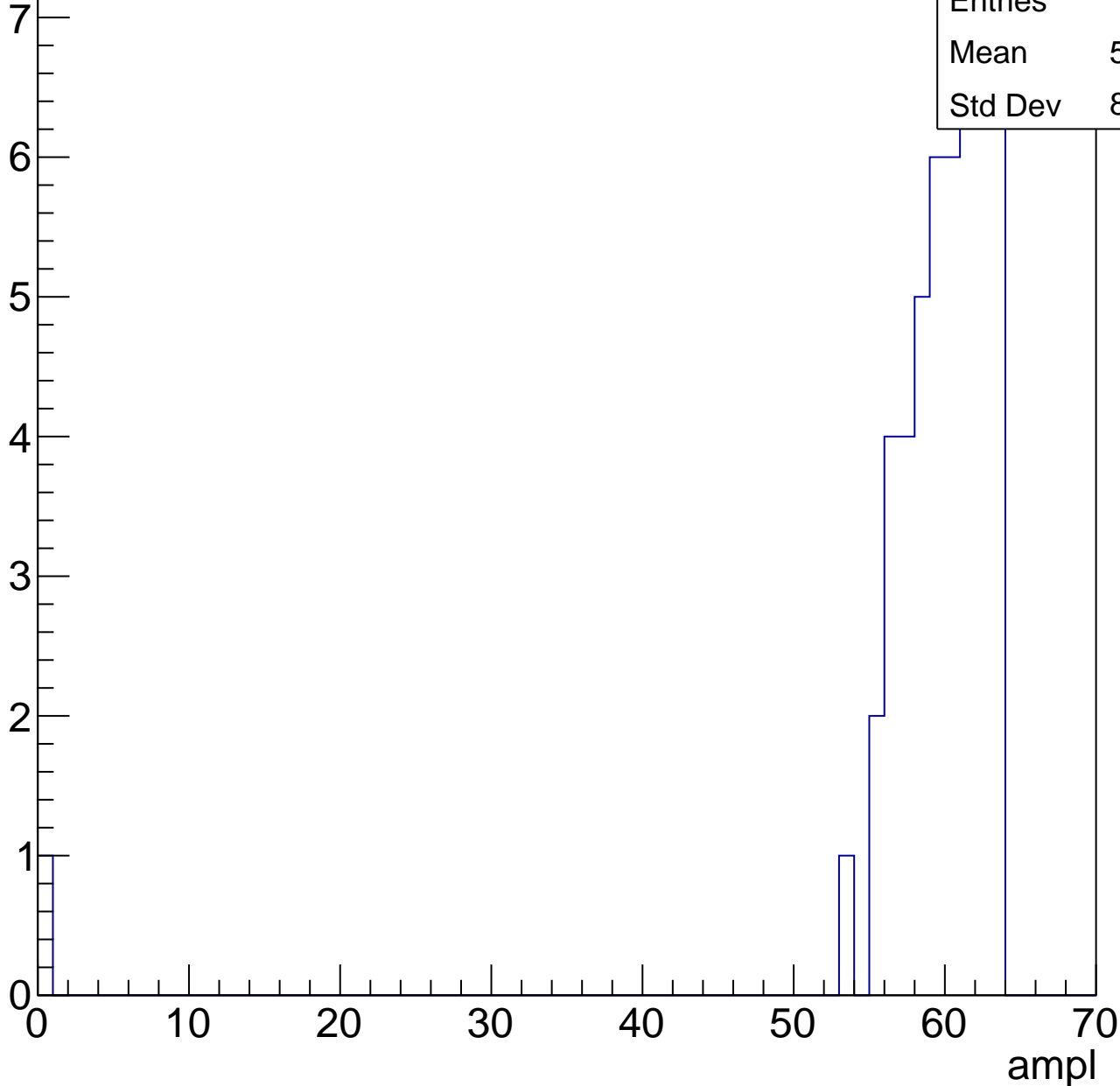


B0L001S, U18-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

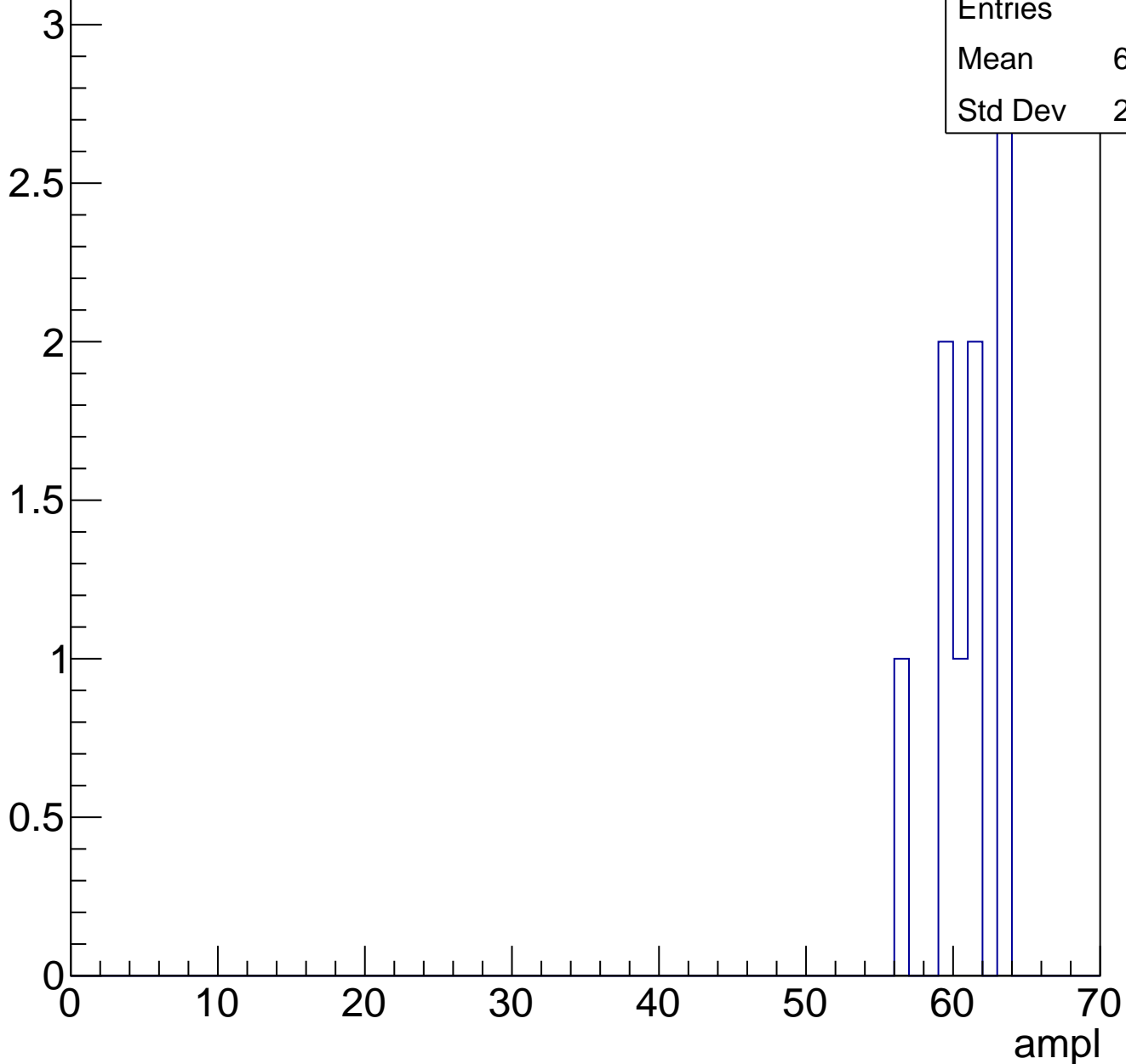
Entries	50
Mean	58.42
Std Dev	8.716



B0L001S, U18-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch81, adc0

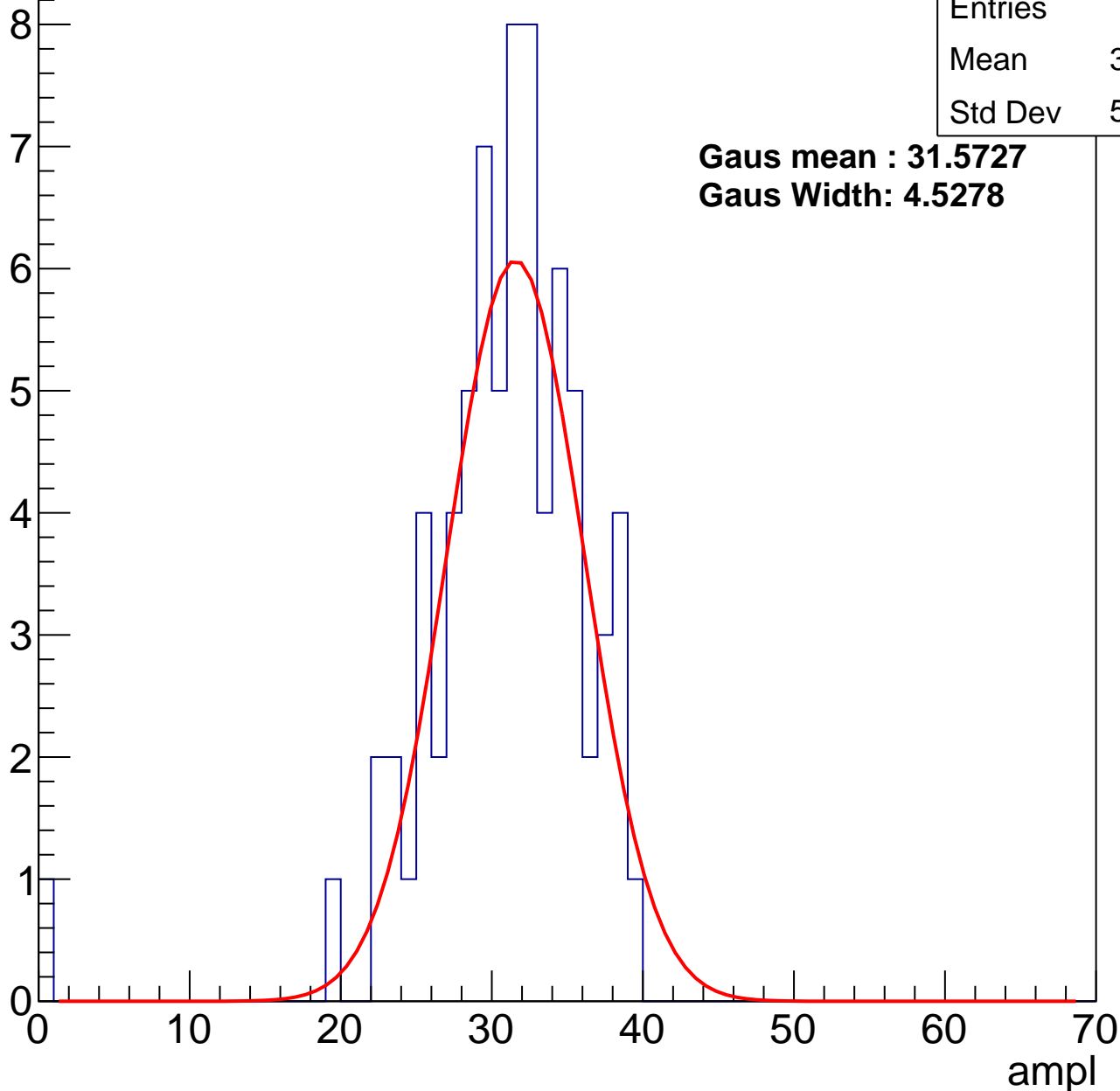
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	30.33
Std Dev	5.565

Gaus mean : 31.5727

Gaus Width: 4.5278



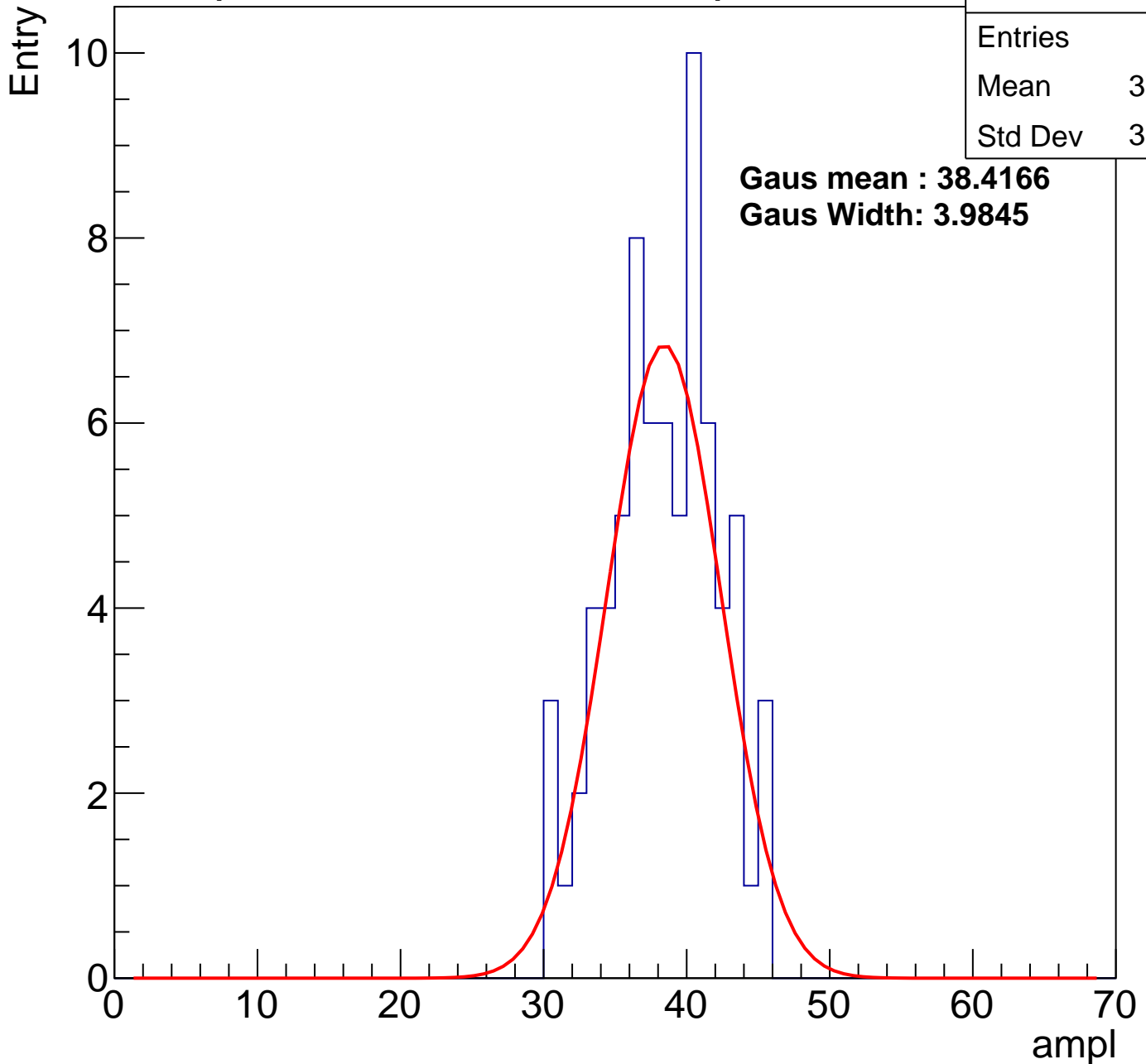
B0L001S, U18-ch81, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	37.93
Std Dev	3.757

Gaus mean : 38.4166

Gaus Width: 3.9845



B0L001S, U18-ch81, adc2

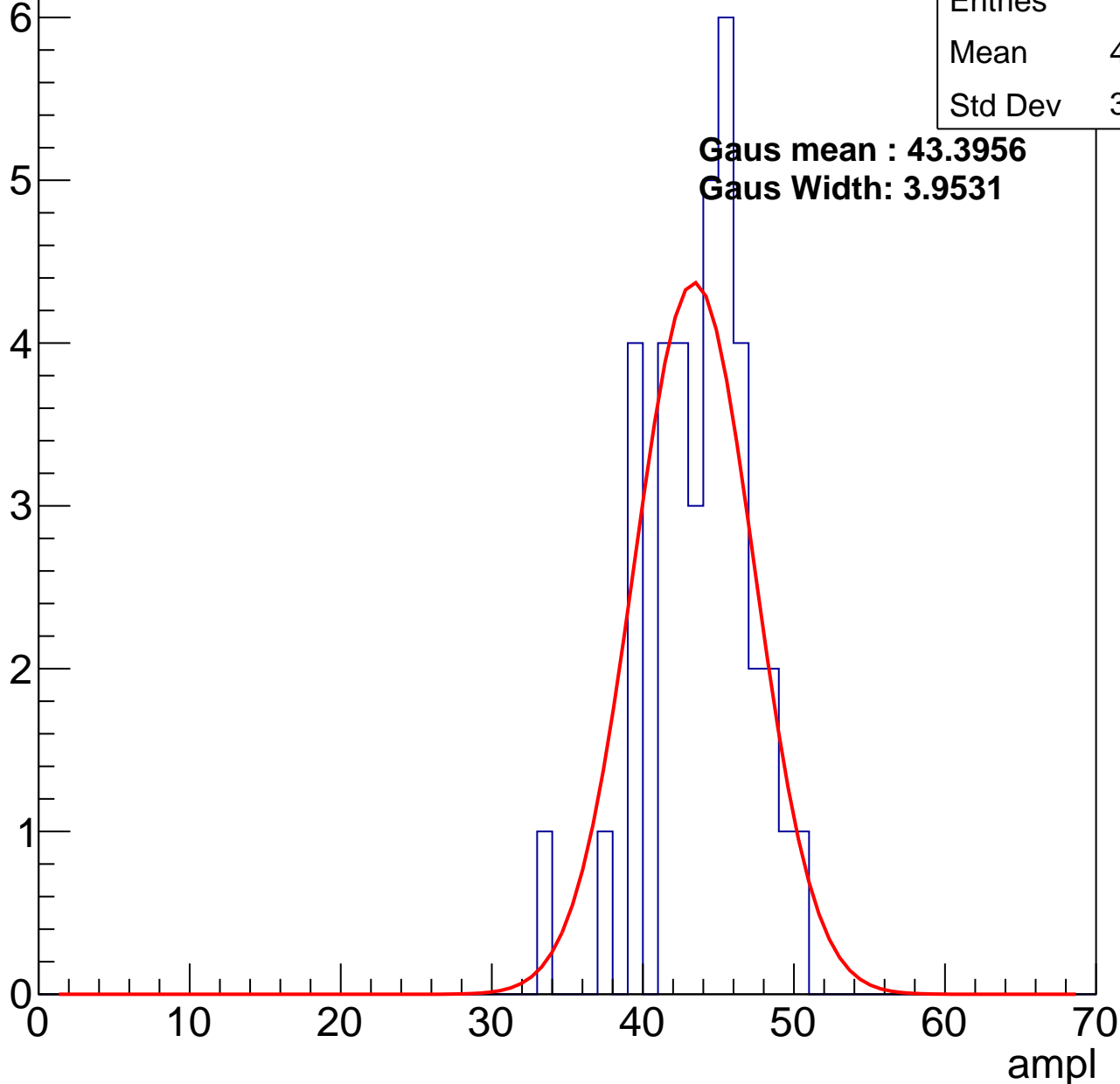
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	43.42
Std Dev	3.423

Gaus mean : 43.3956

Gaus Width: 3.9531

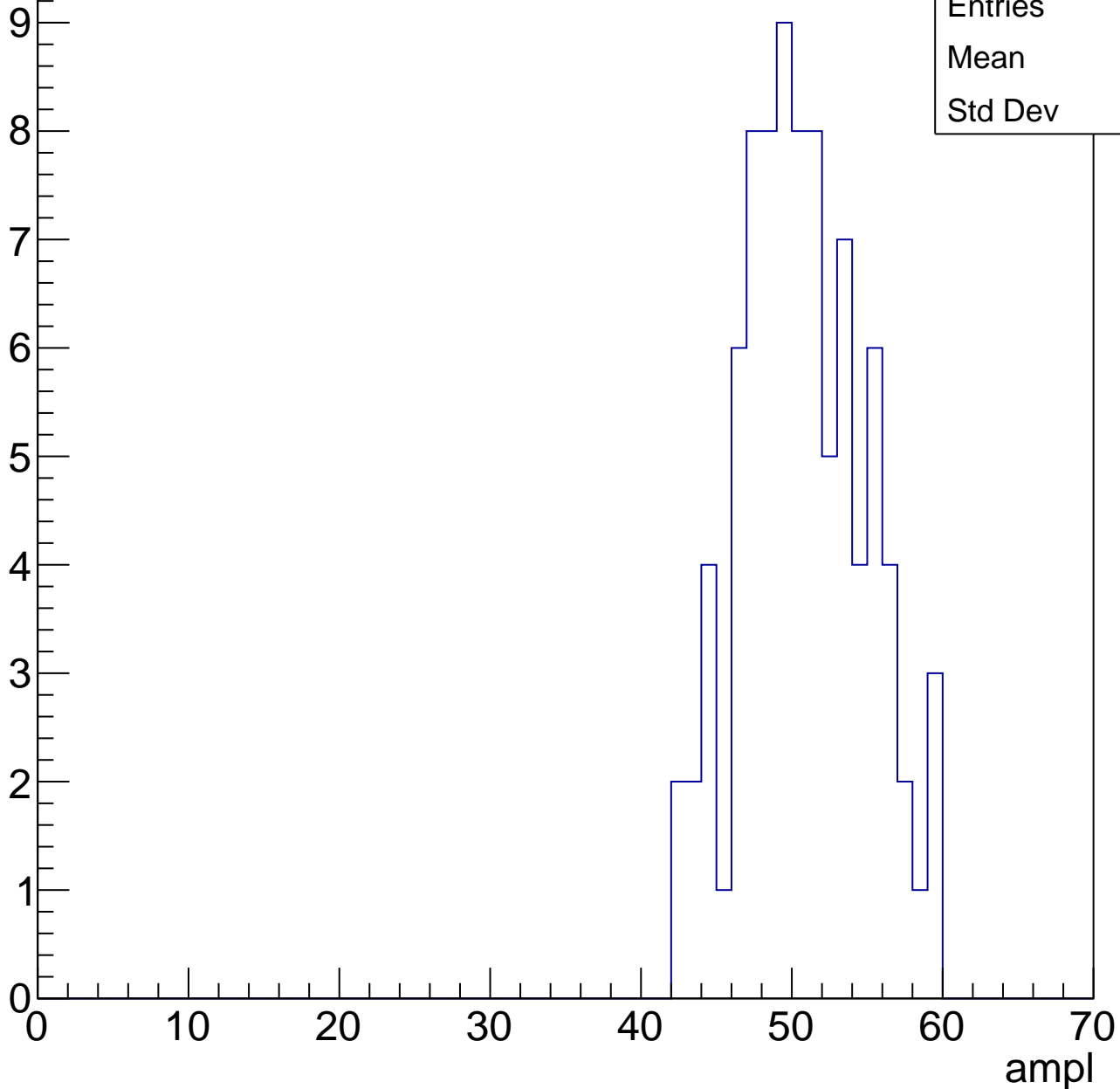


B0L001S, U18-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

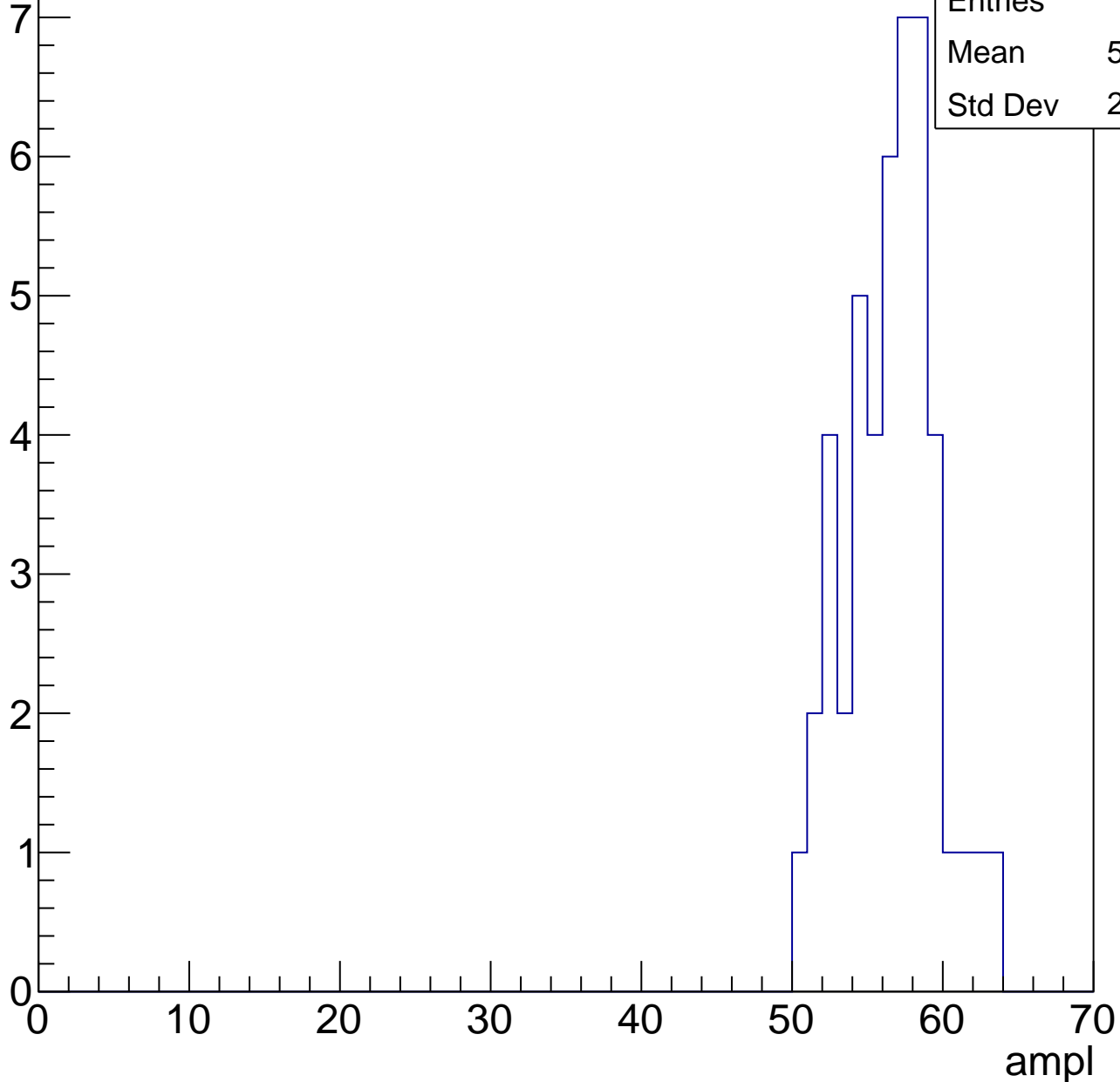
Entries	88
Mean	50.3
Std Dev	4.06



B0L001S, U18-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

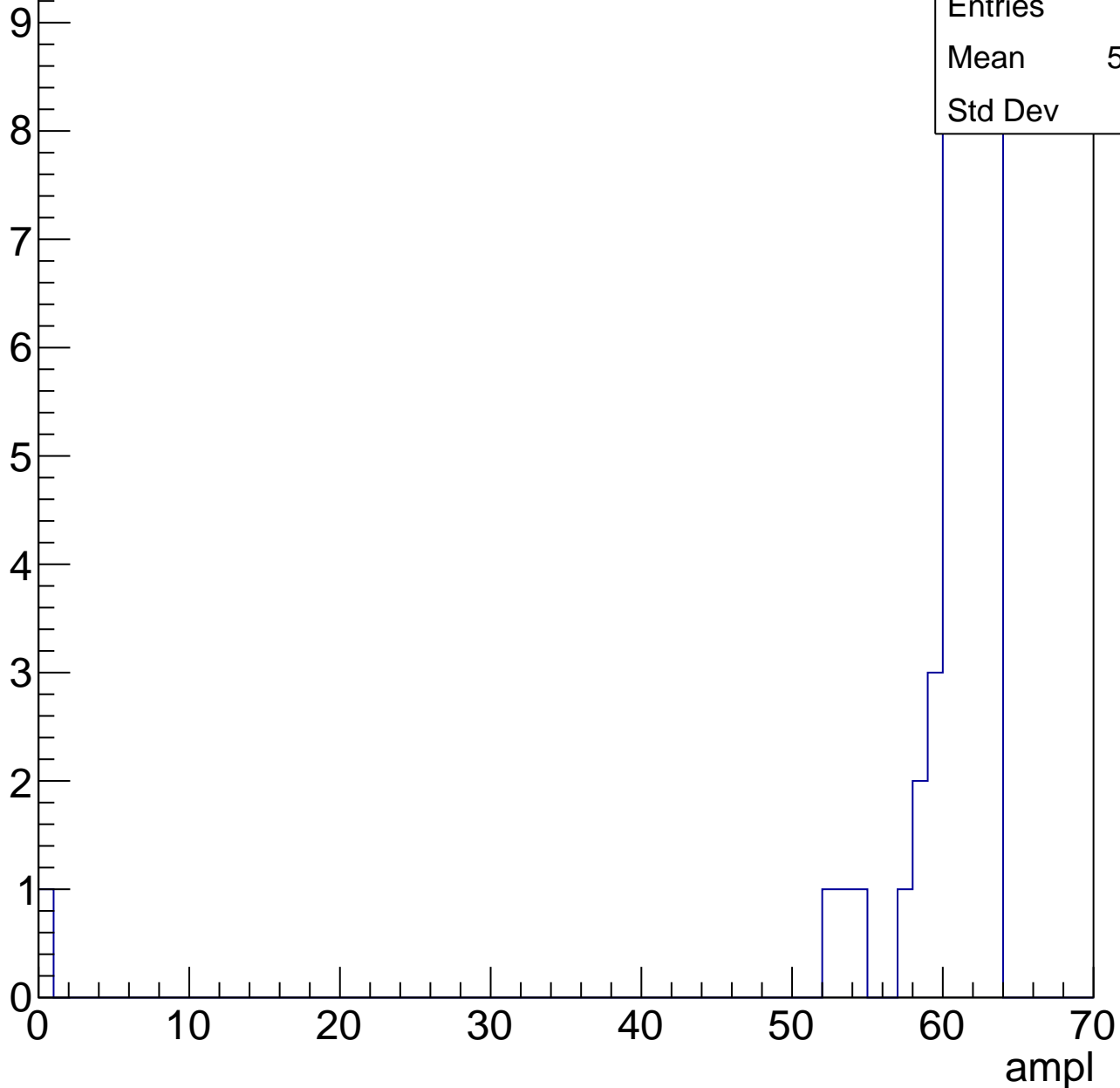


Entries	46
Mean	56.07
Std Dev	2.907

B0L001S, U18-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch82, adc0

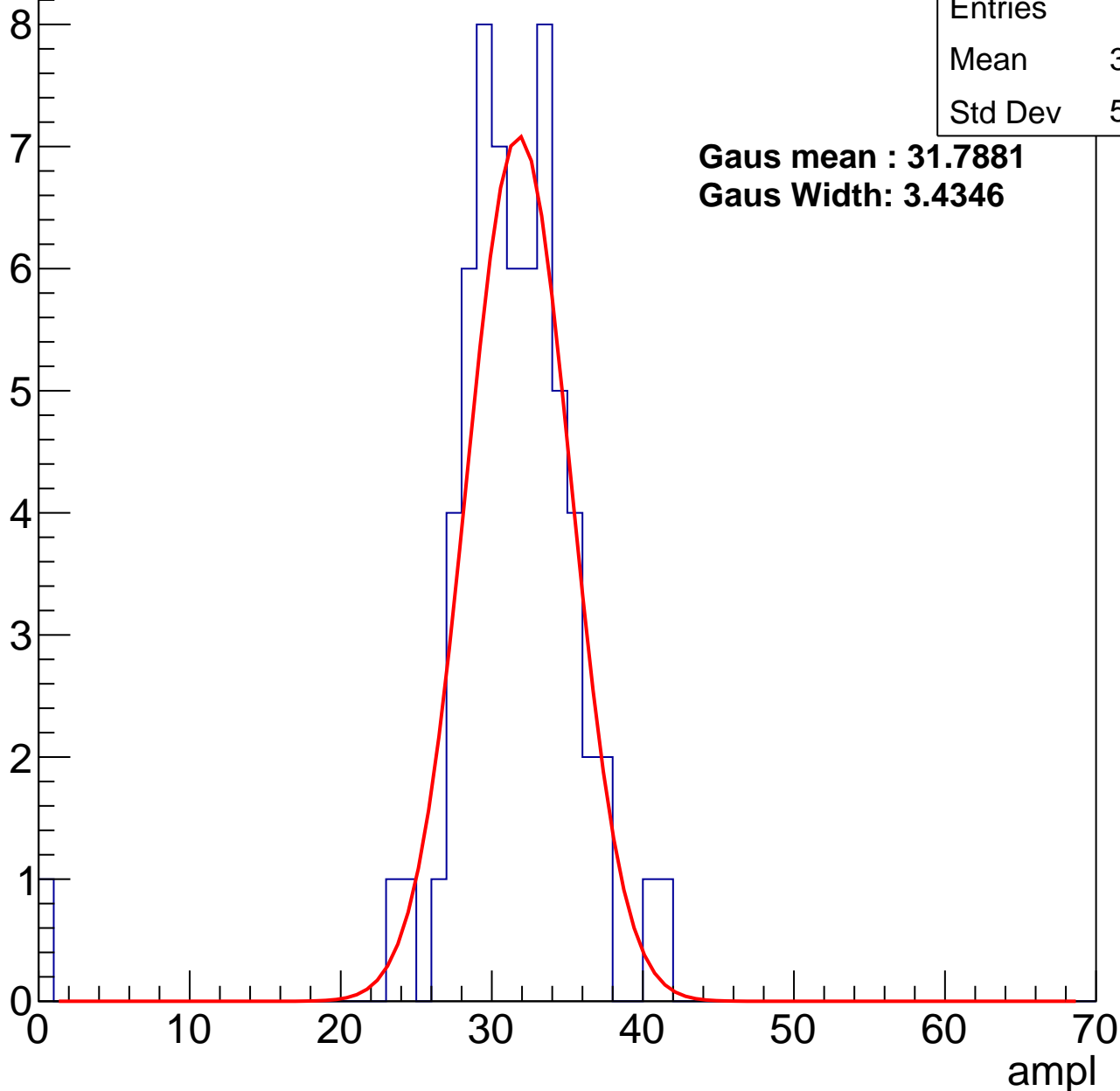
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.78
Std Dev	5.158

Gaus mean : 31.7881

Gaus Width: 3.4346



B0L001S, U18-ch82, adc1

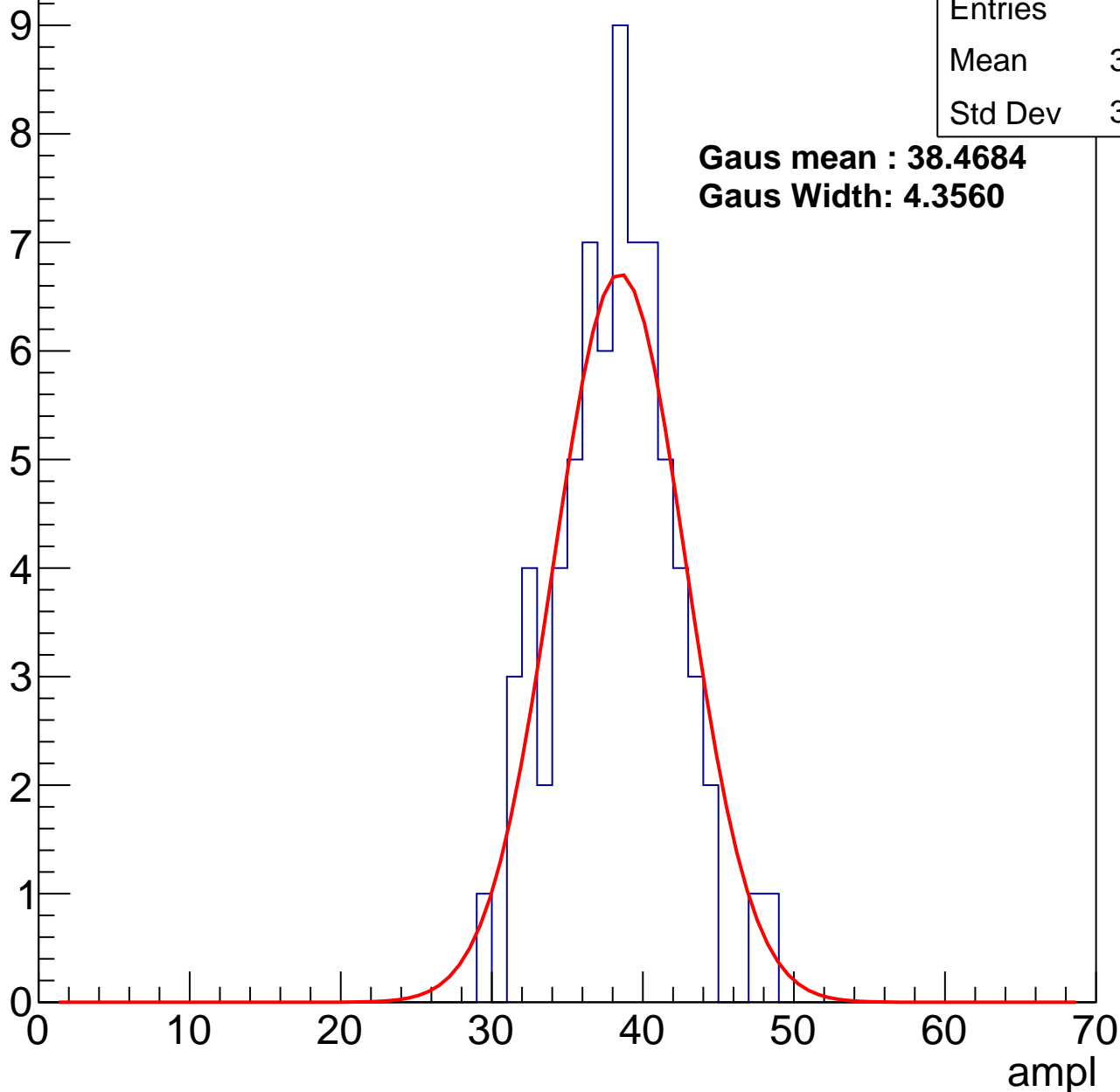
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.76
Std Dev	3.814

Gaus mean : 38.4684

Gaus Width: 4.3560



B0L001S, U18-ch82, adc2

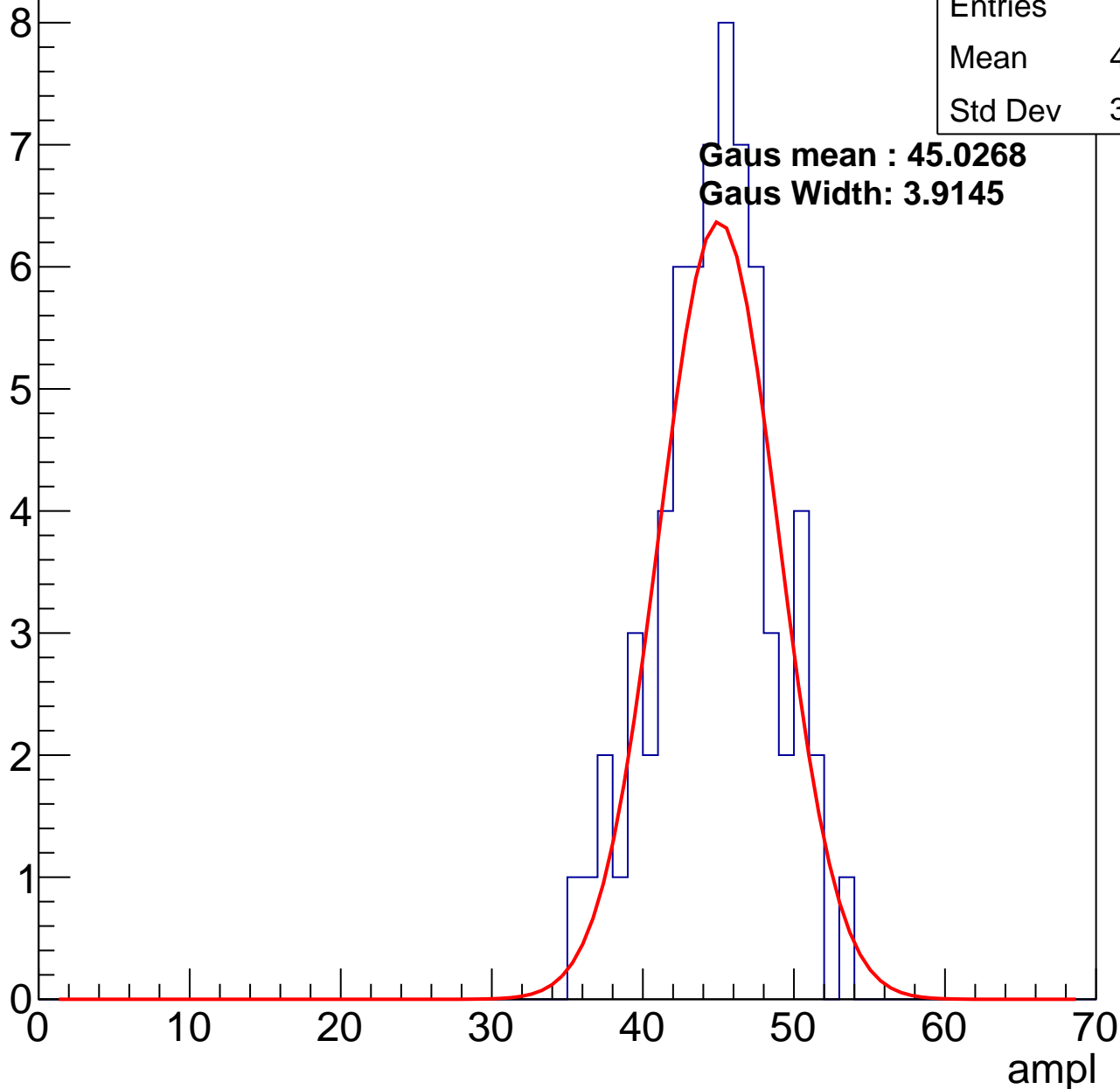
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.29
Std Dev	3.809

Gaus mean : 45.0268

Gaus Width: 3.9145

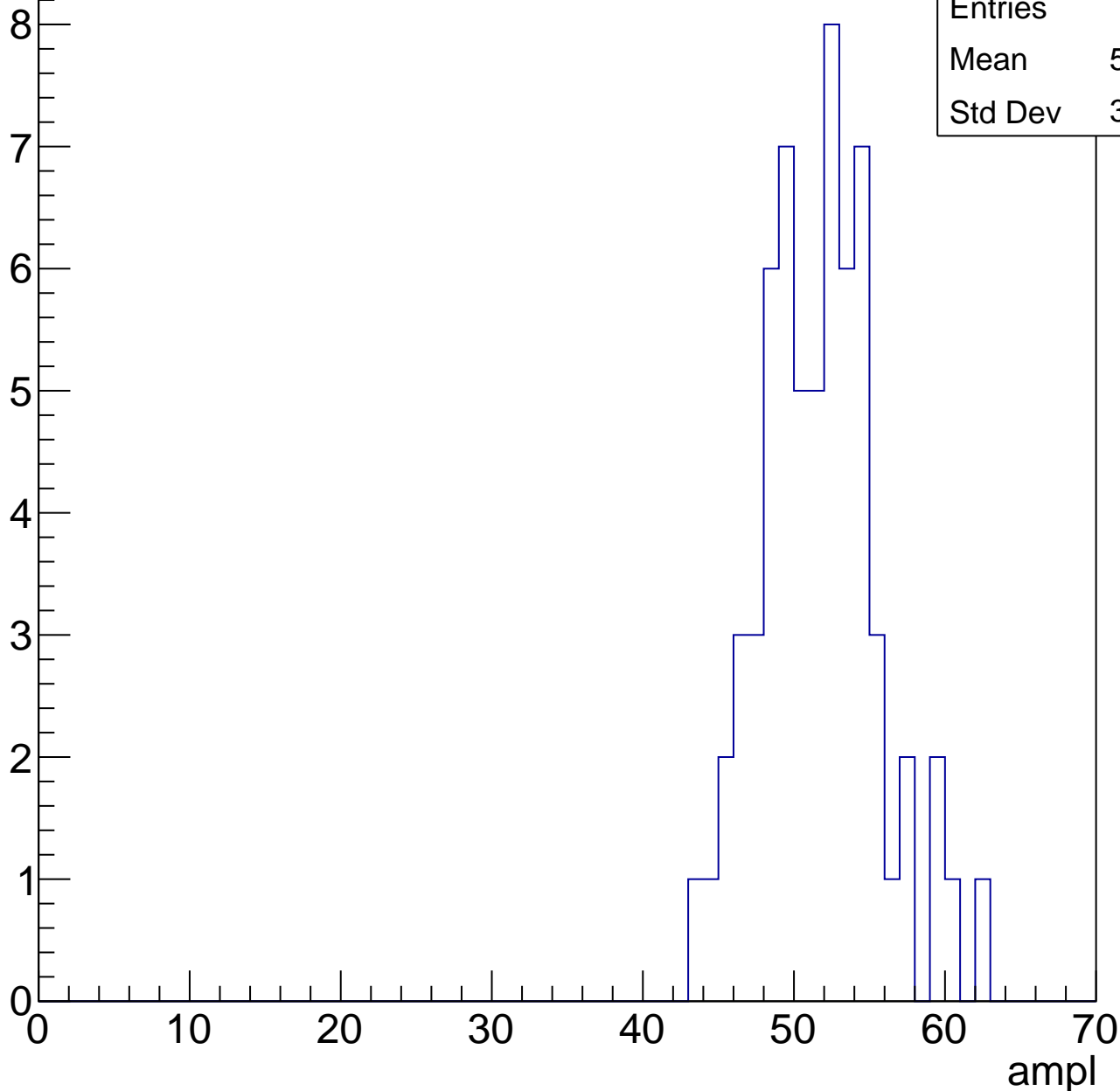


B0L001S, U18-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

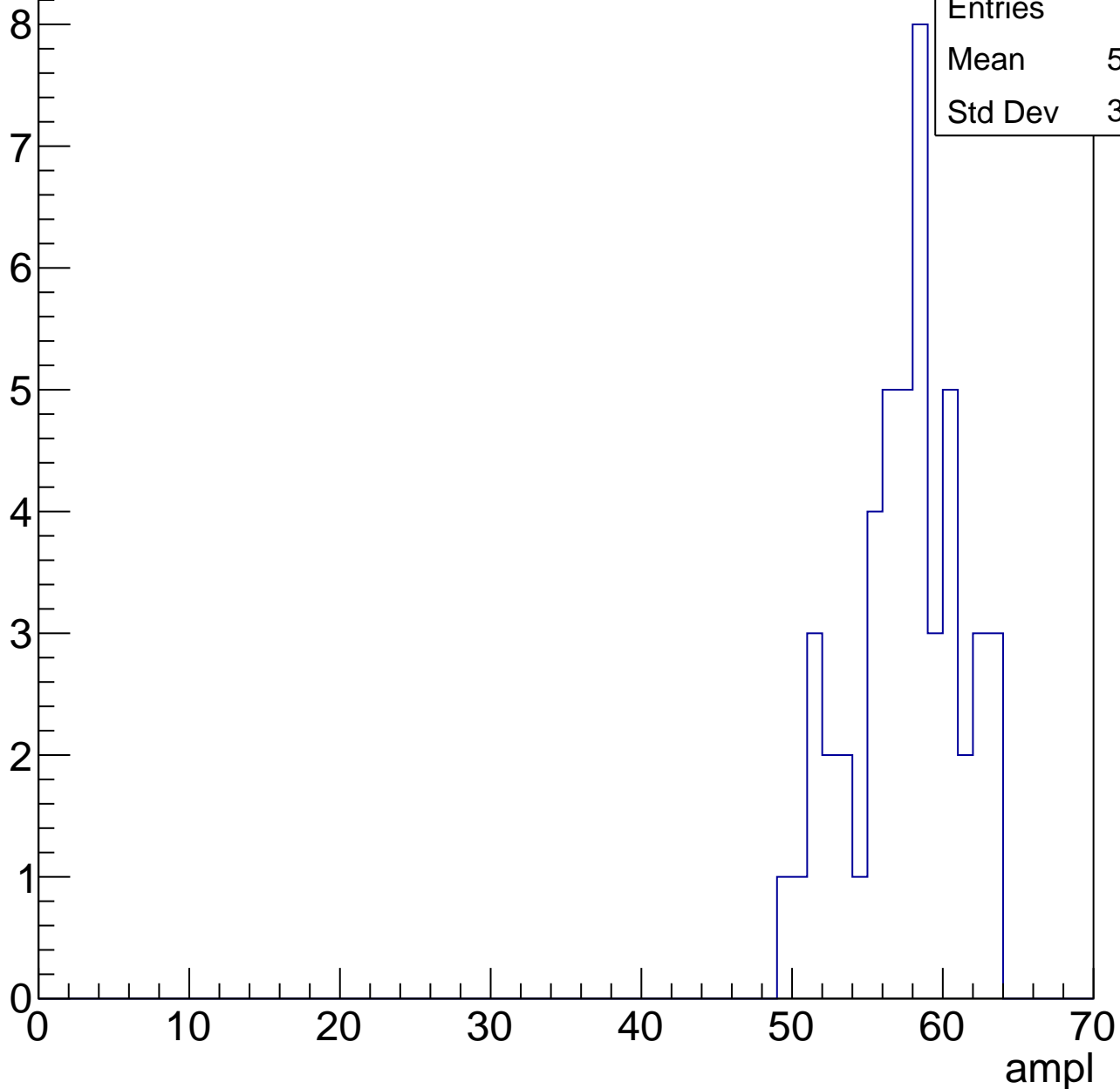
Entries	64
Mean	51.23
Std Dev	3.876



B0L001S, U18-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



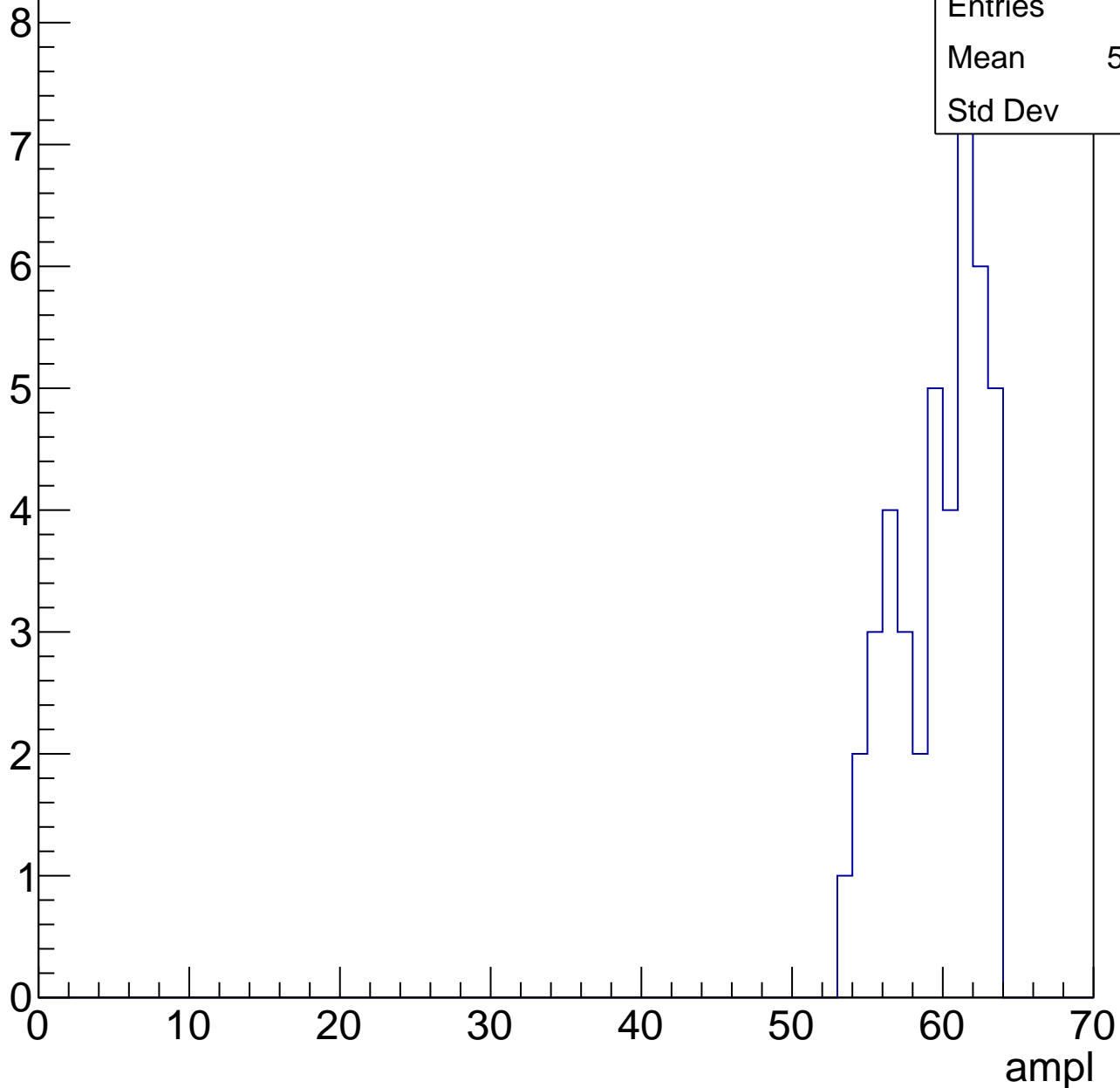
Entries	48
Mean	57.06
Std Dev	3.573

B0L001S, U18-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	59.23
Std Dev	2.86



B0L001S, U18-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch83, adc0

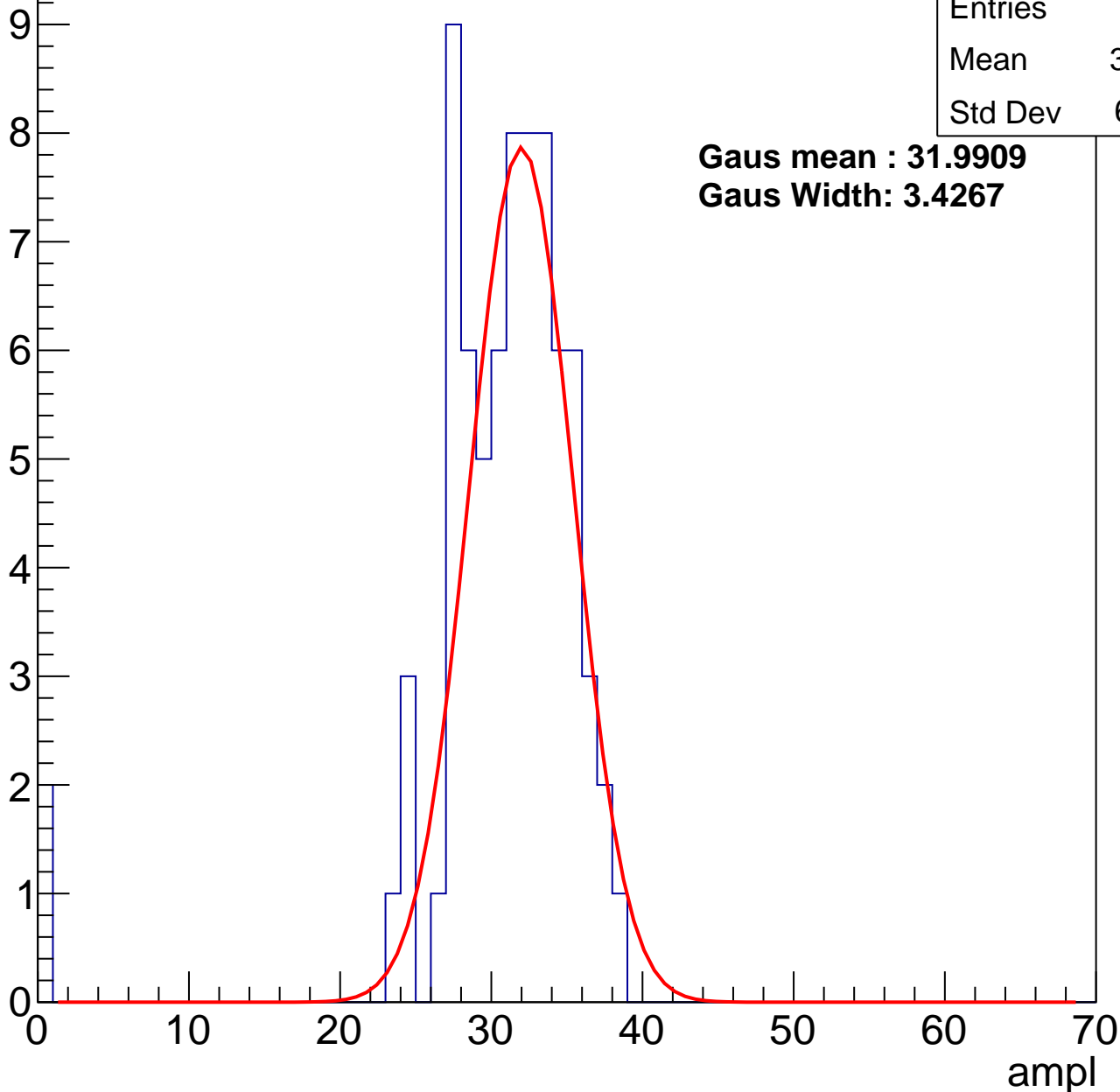
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	30.12
Std Dev	6.011

Gaus mean : 31.9909

Gaus Width: 3.4267



B0L001S, U18-ch83, adc1

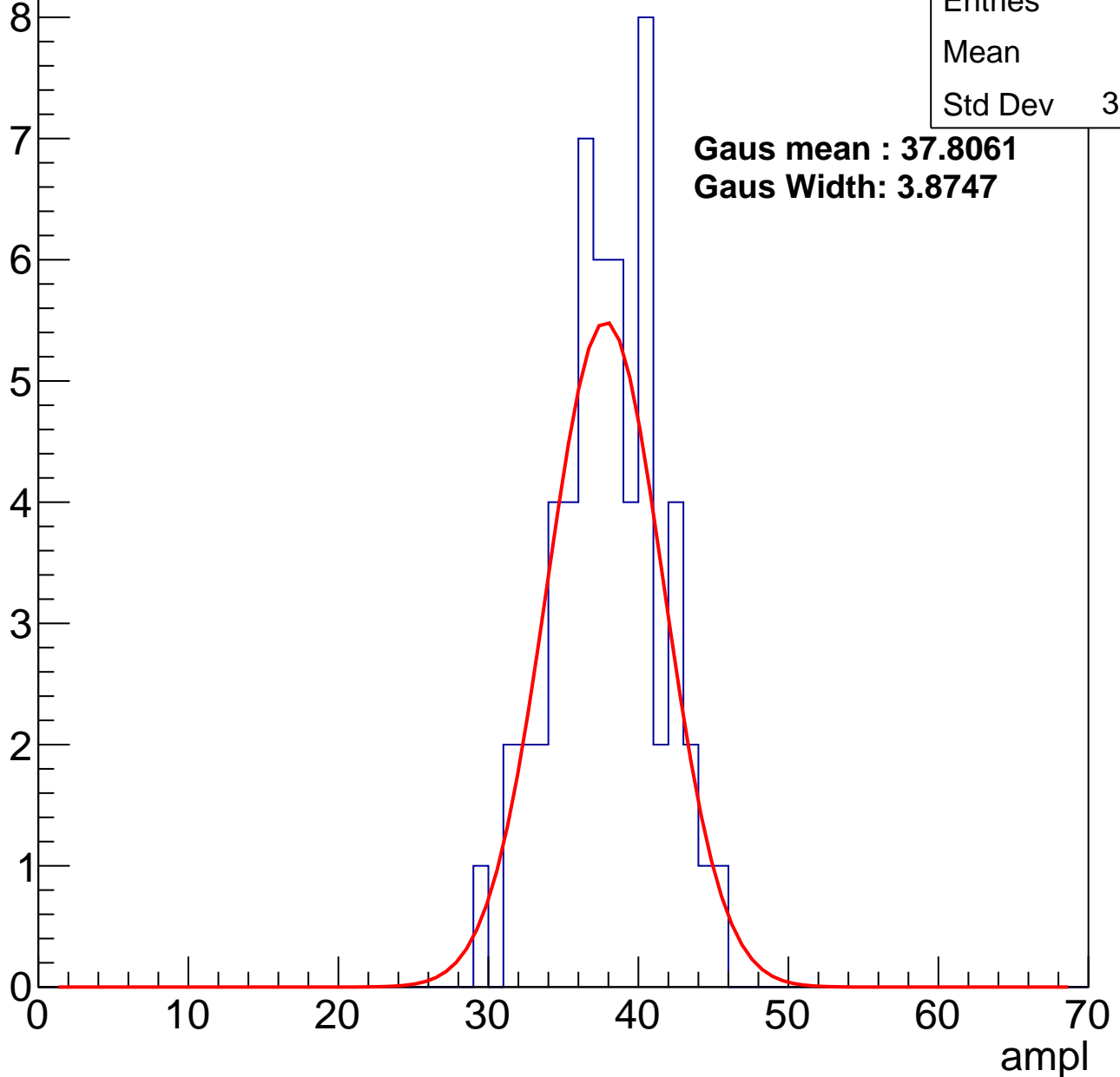
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	37.5
Std Dev	3.469

Gaus mean : 37.8061

Gaus Width: 3.8747



B0L001S, U18-ch83, adc2

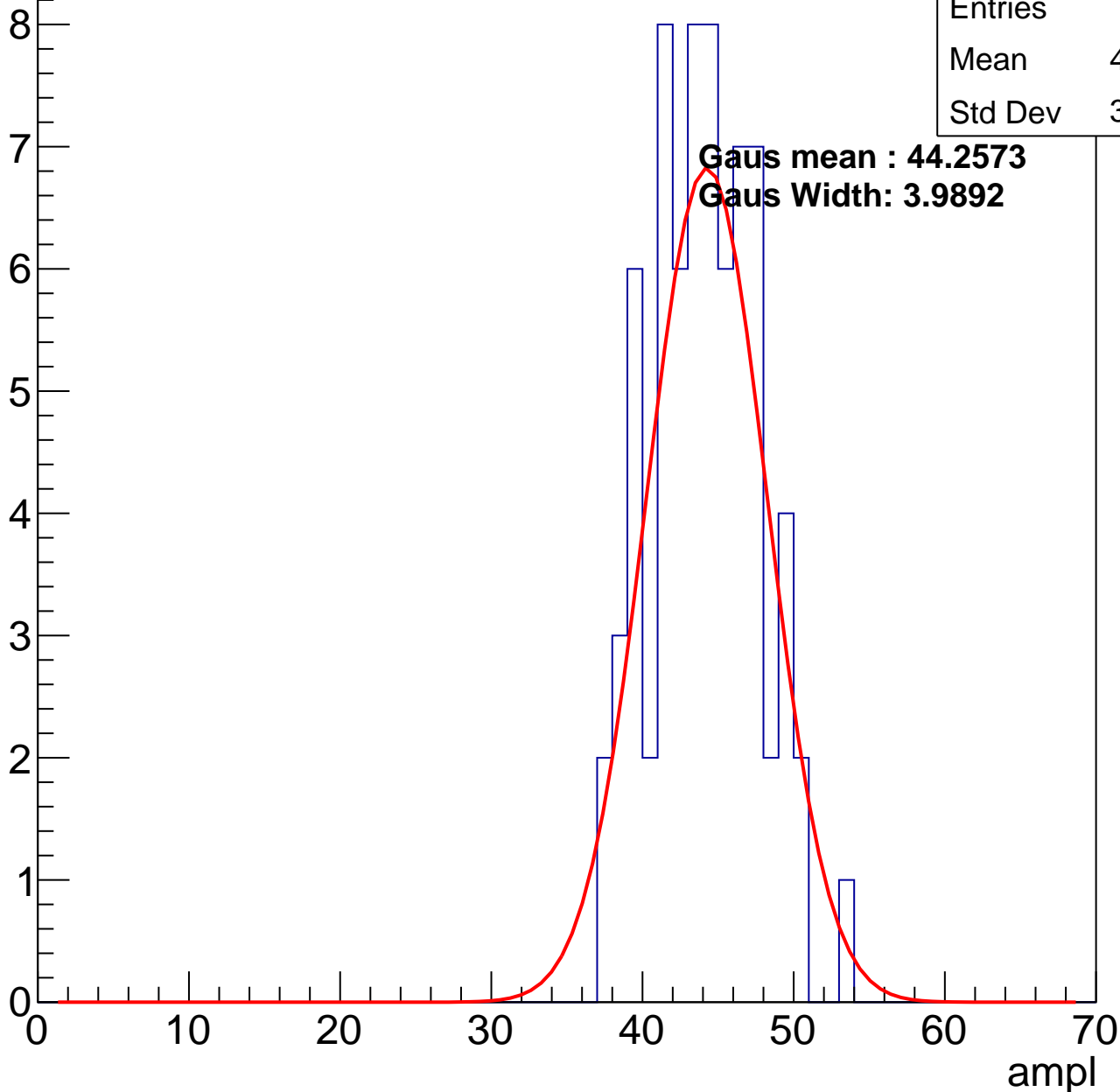
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.67
Std Dev	3.468

Gaus mean : 44.2573

Gaus Width: 3.9892

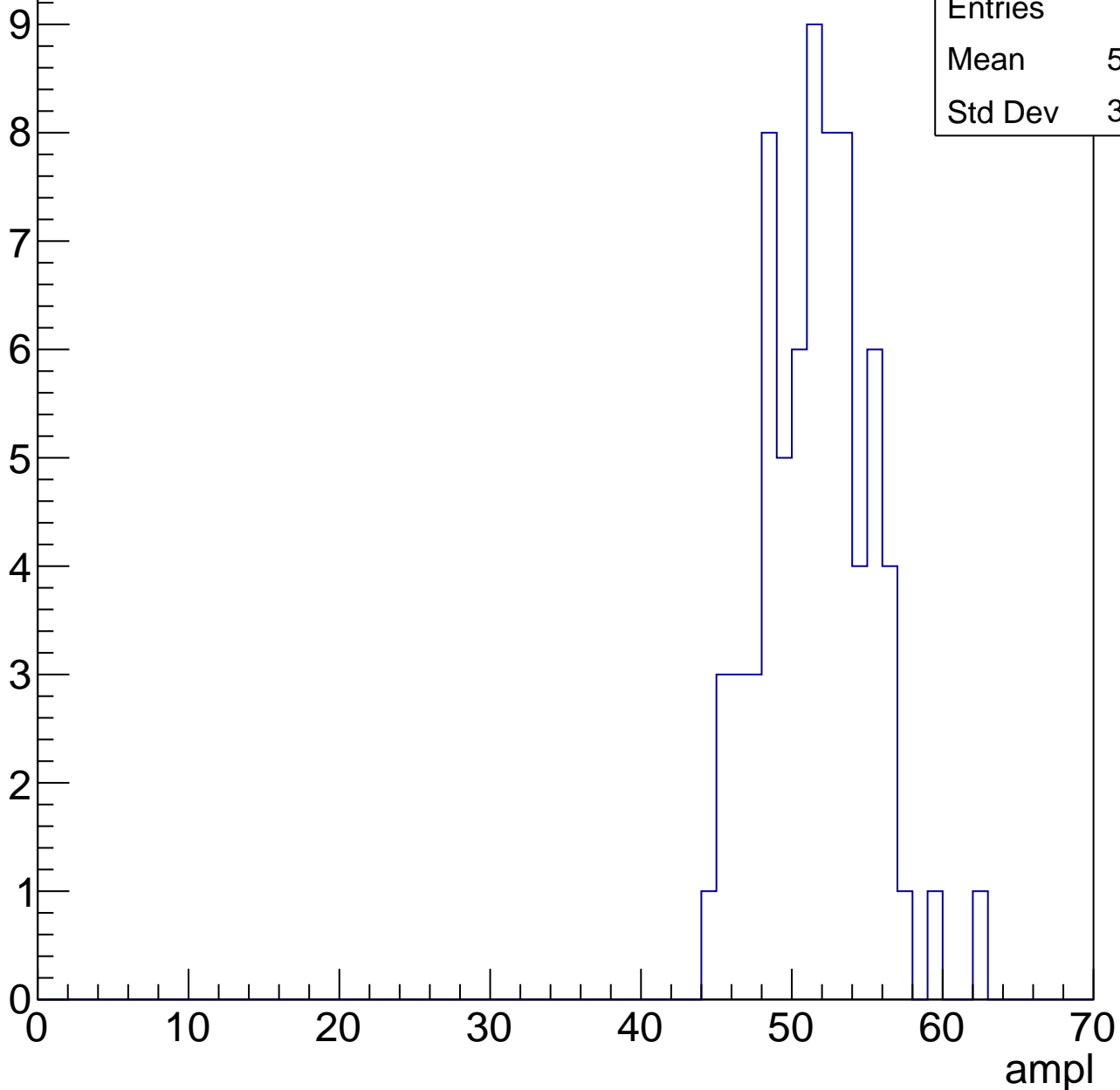


B0L001S, U18-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.18
Std Dev	3.498

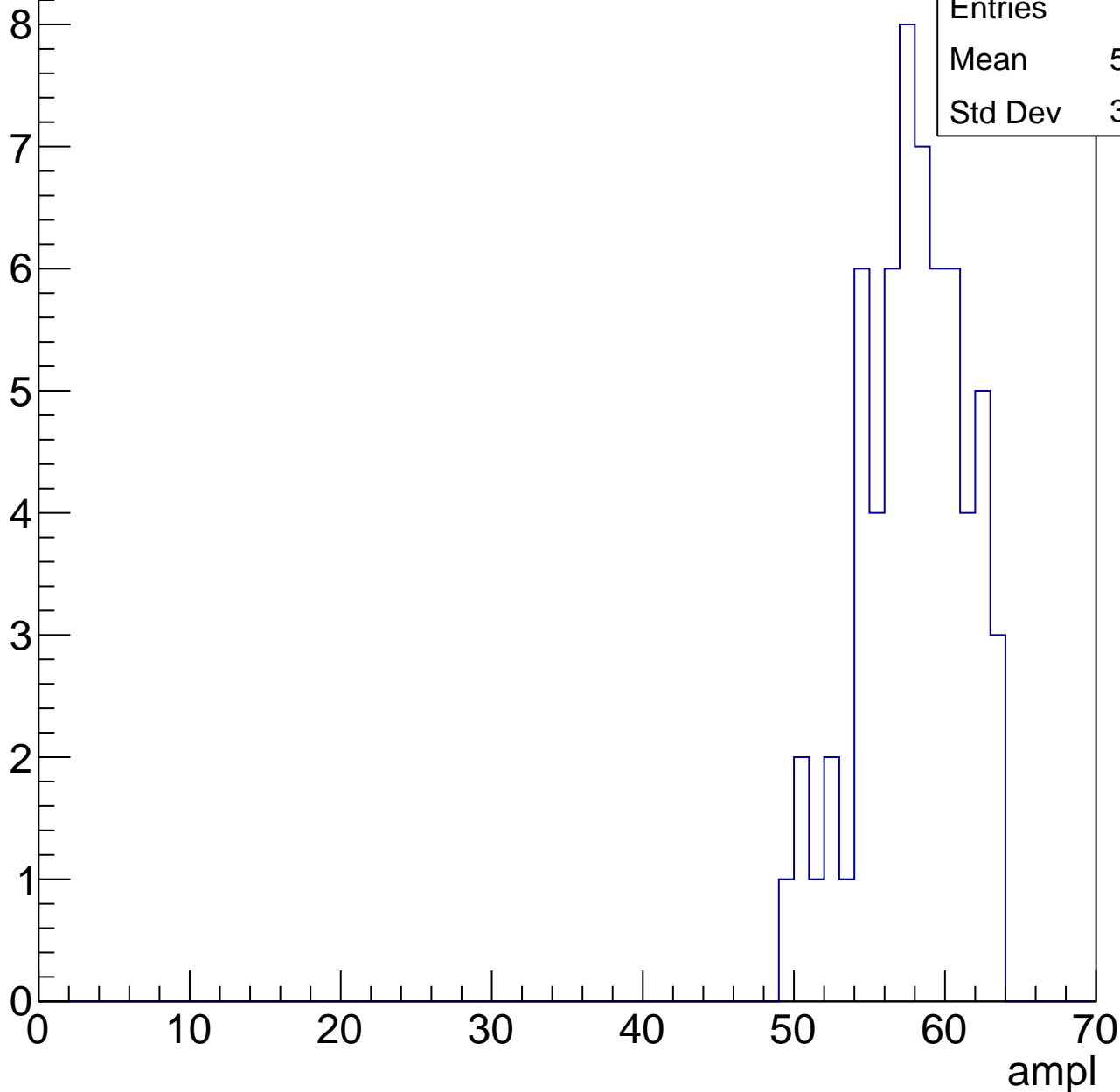


B0L001S, U18-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	57.35
Std Dev	3.394

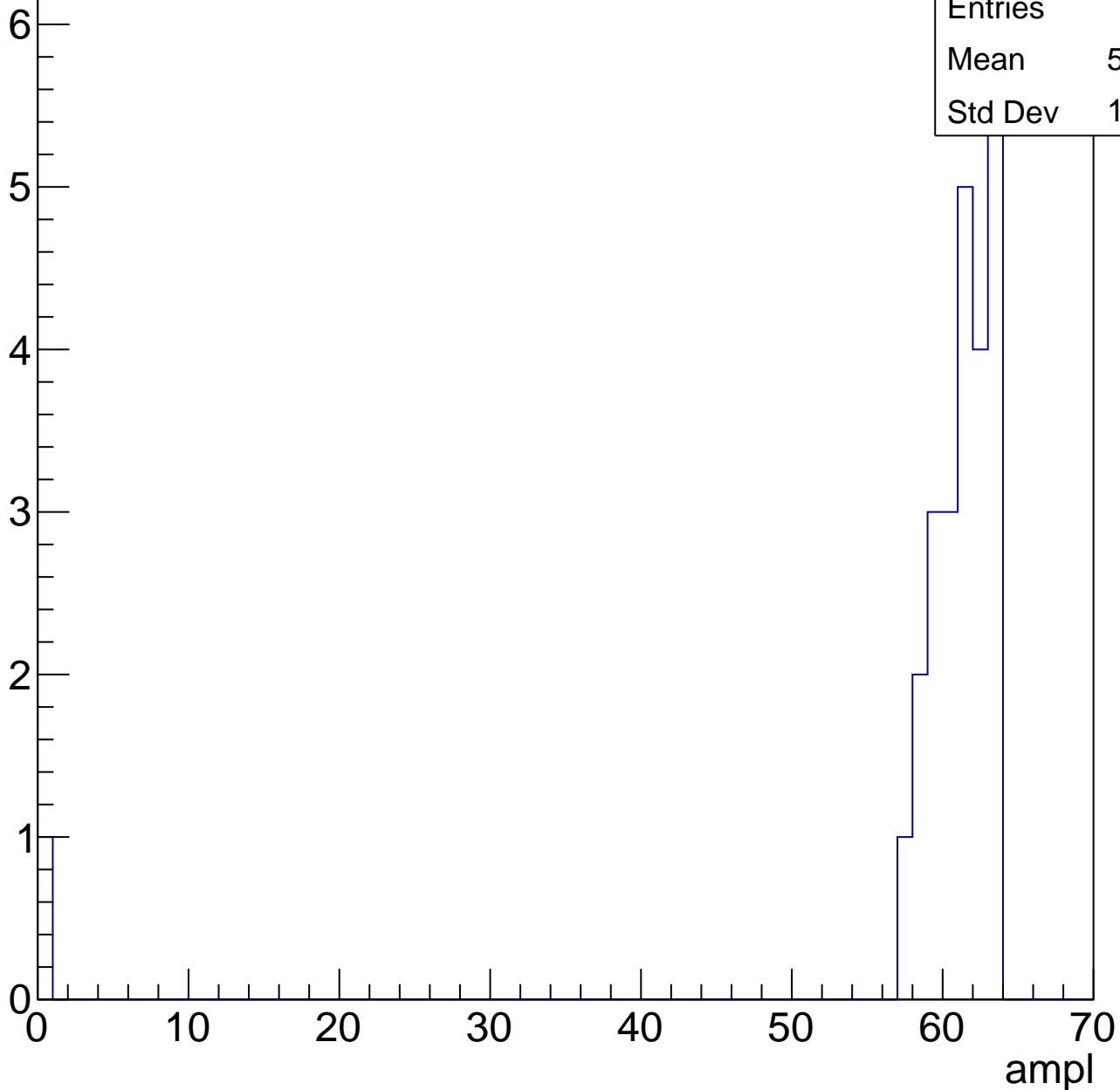


B0L001S, U18-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

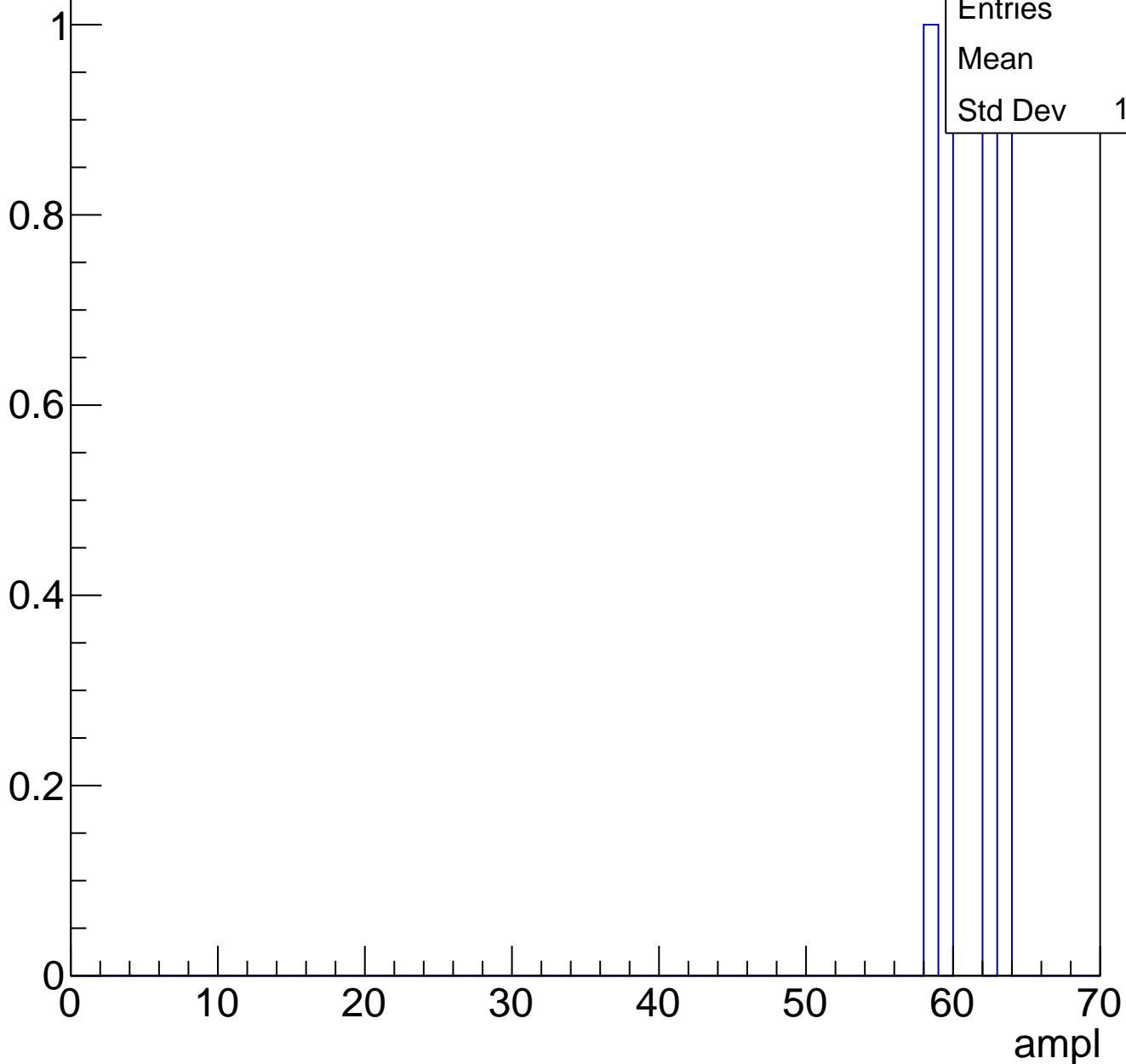
Entries	25
Mean	58.44
Std Dev	12.06



B0L001S, U18-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch84, adc0

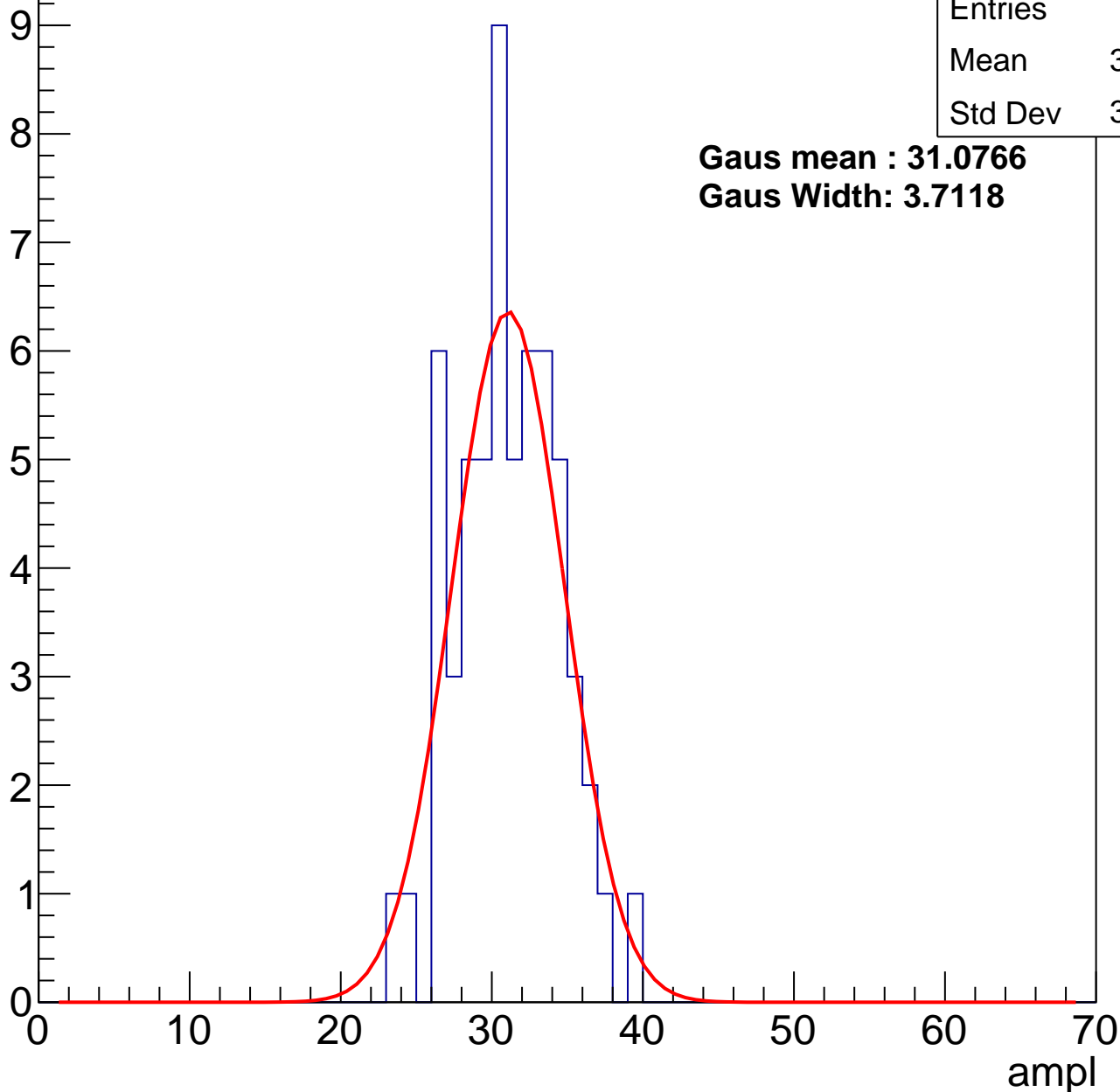
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	30.63
Std Dev	3.319

Gaus mean : 31.0766

Gaus Width: 3.7118



B0L001S, U18-ch84, adc1

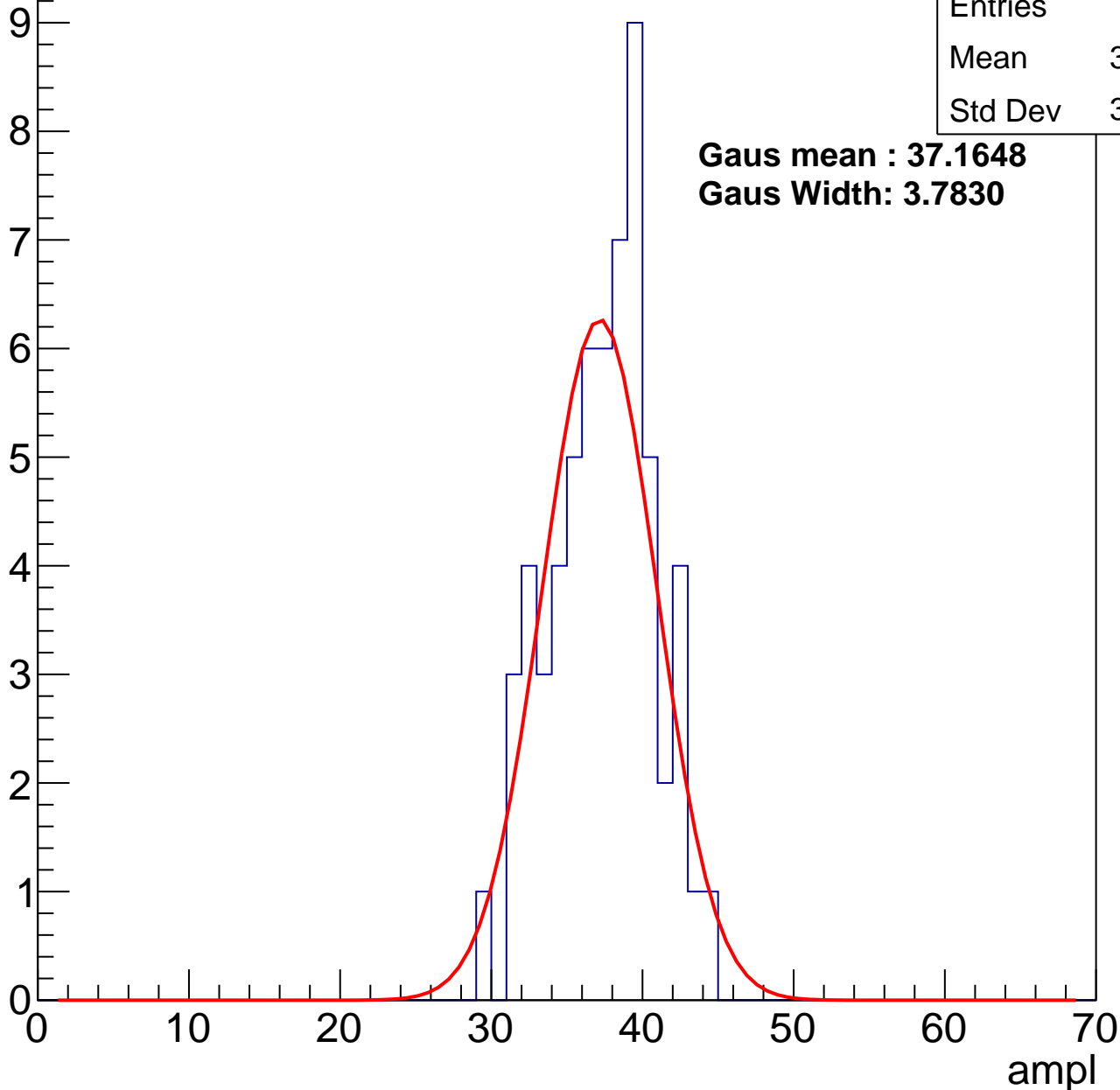
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.92
Std Dev	3.355

Gaus mean : 37.1648

Gaus Width: 3.7830



B0L001S, U18-ch84, adc2

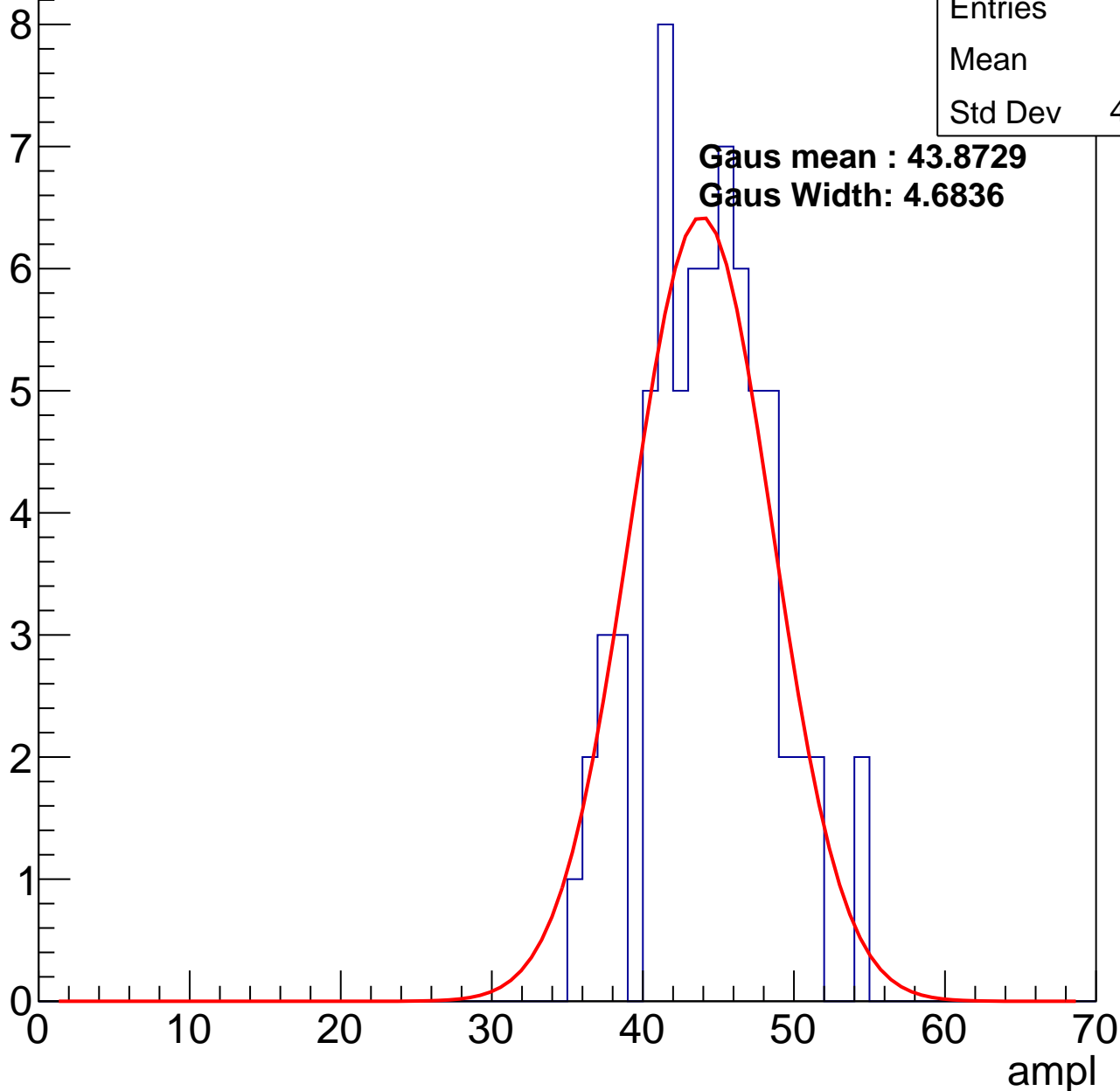
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.8
Std Dev	4.167

Gaus mean : 43.8729

Gaus Width: 4.6836

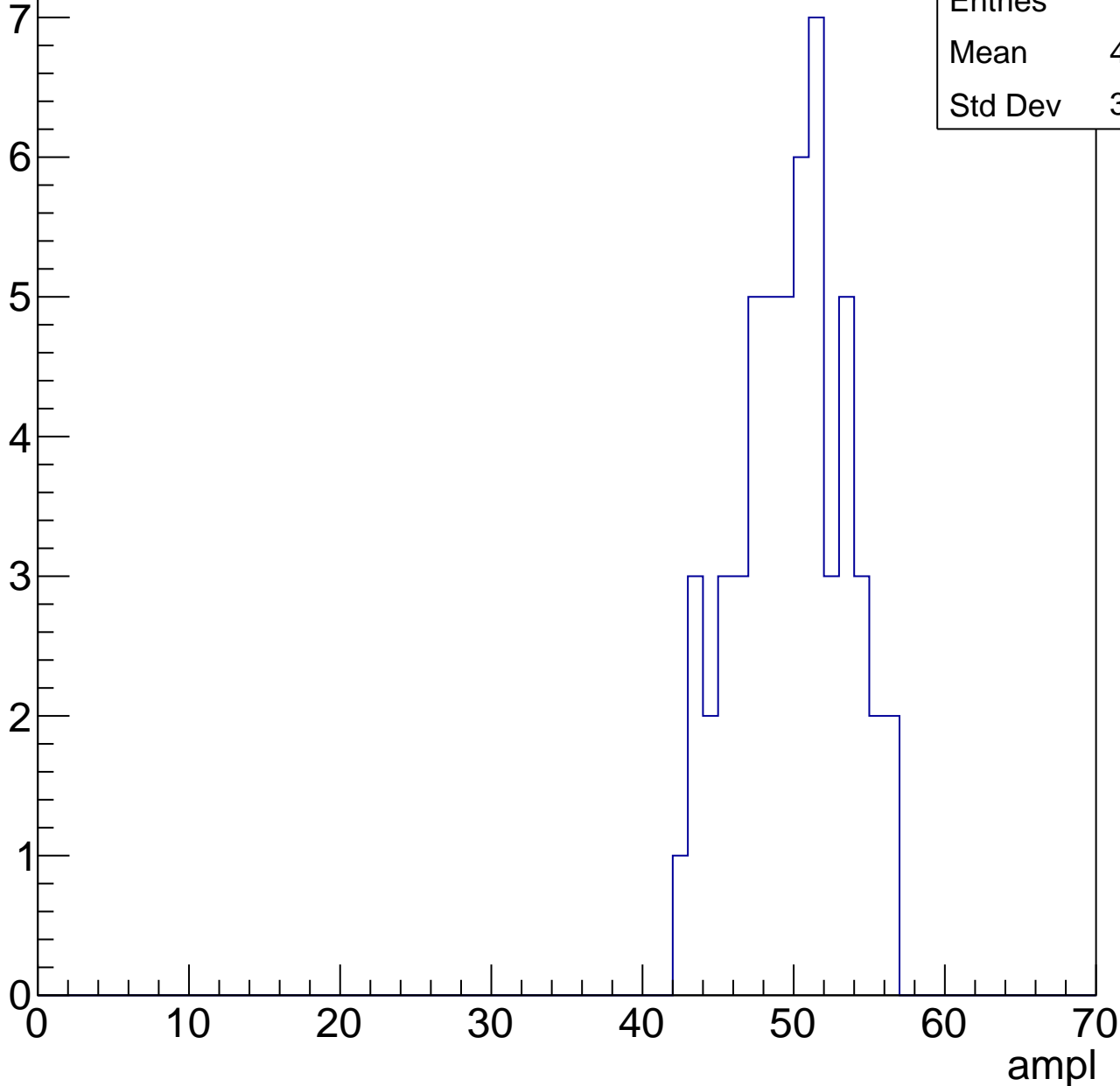


B0L001S, U18-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.35
Std Dev	3.533

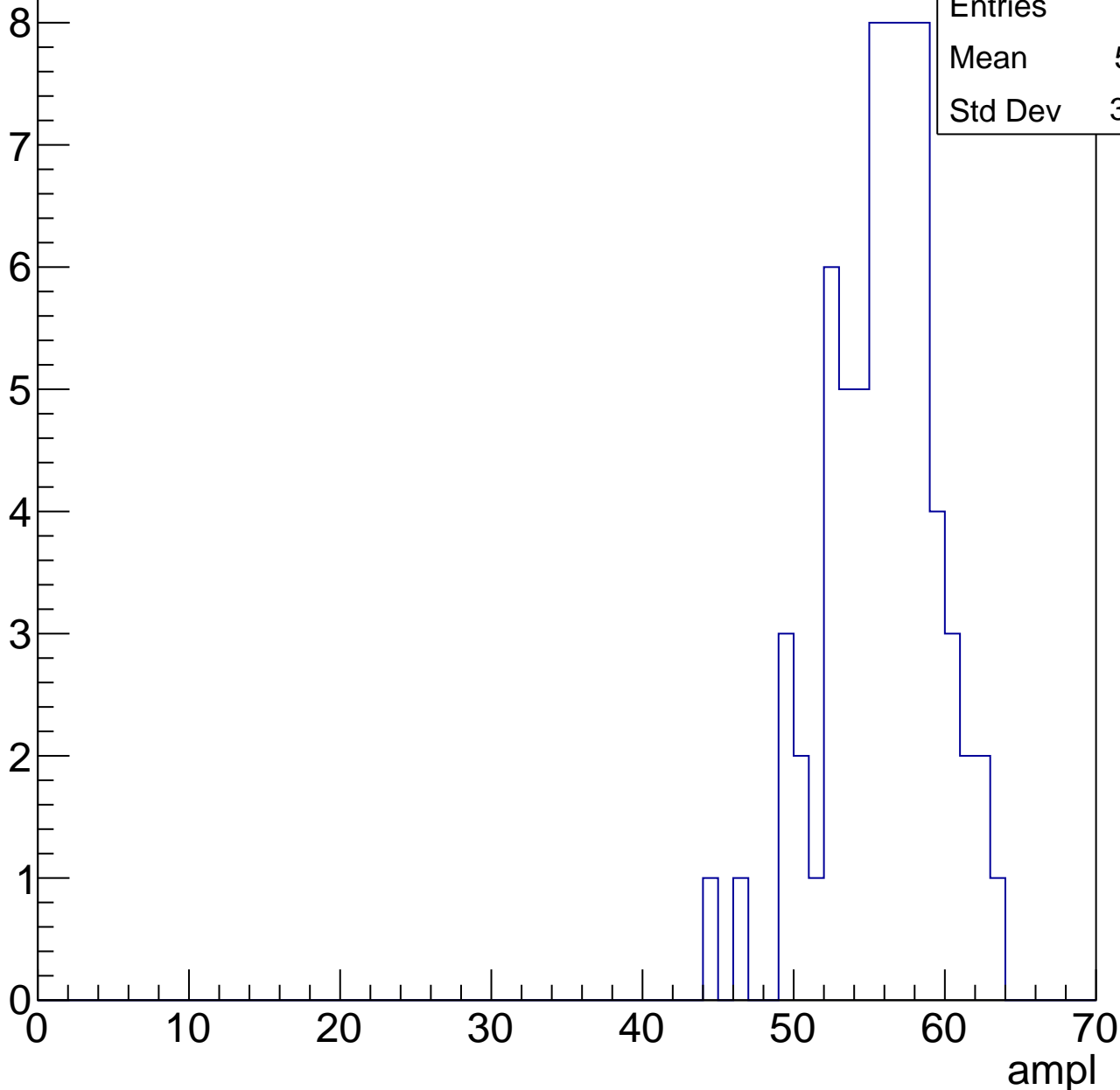


B0L001S, U18-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	55.41
Std Dev	3.695



B0L001S, U18-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

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Entries 37

Mean 58.81

Std Dev 10.05

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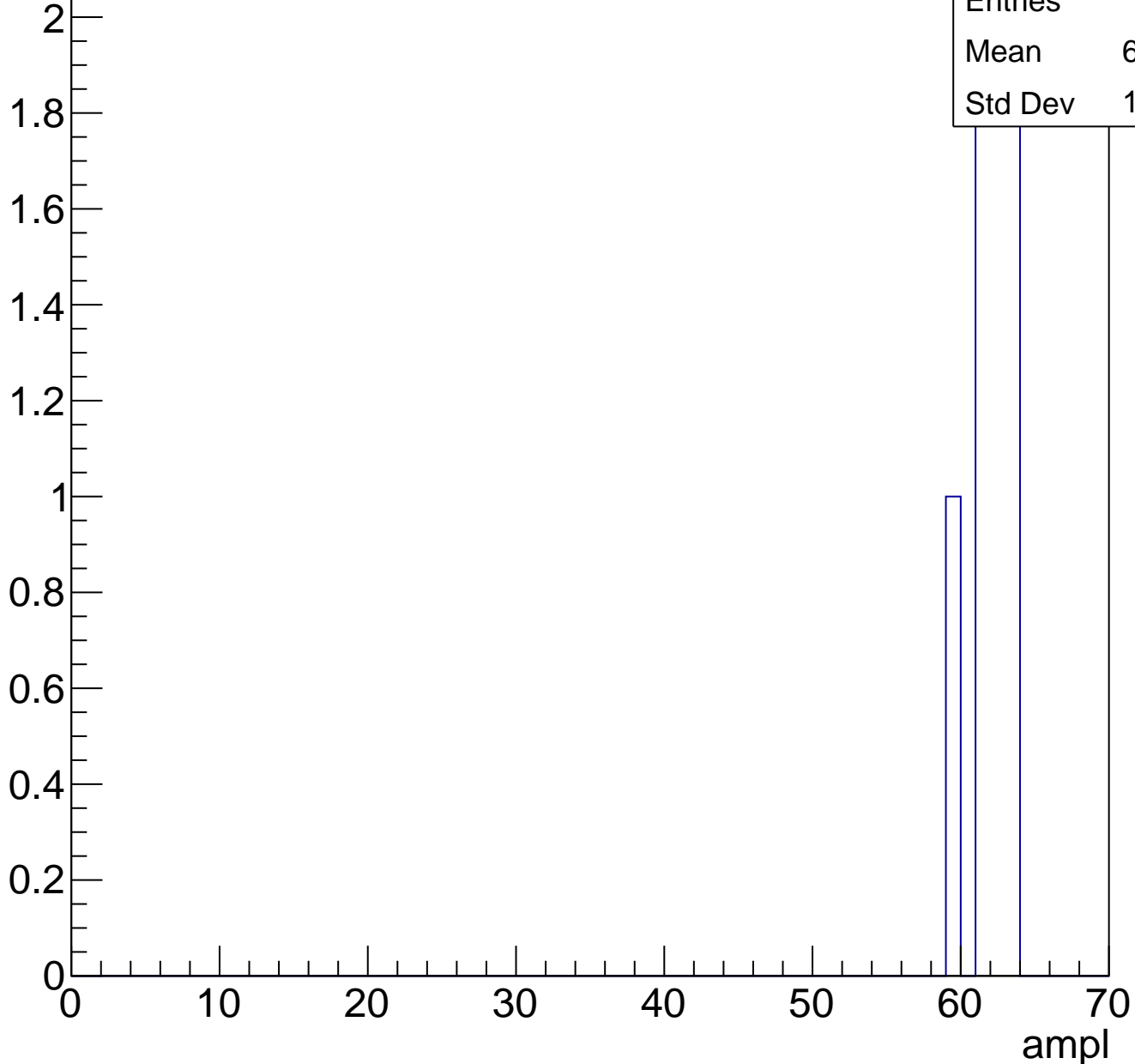
315

316

B0L001S, U18-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch85, adc0

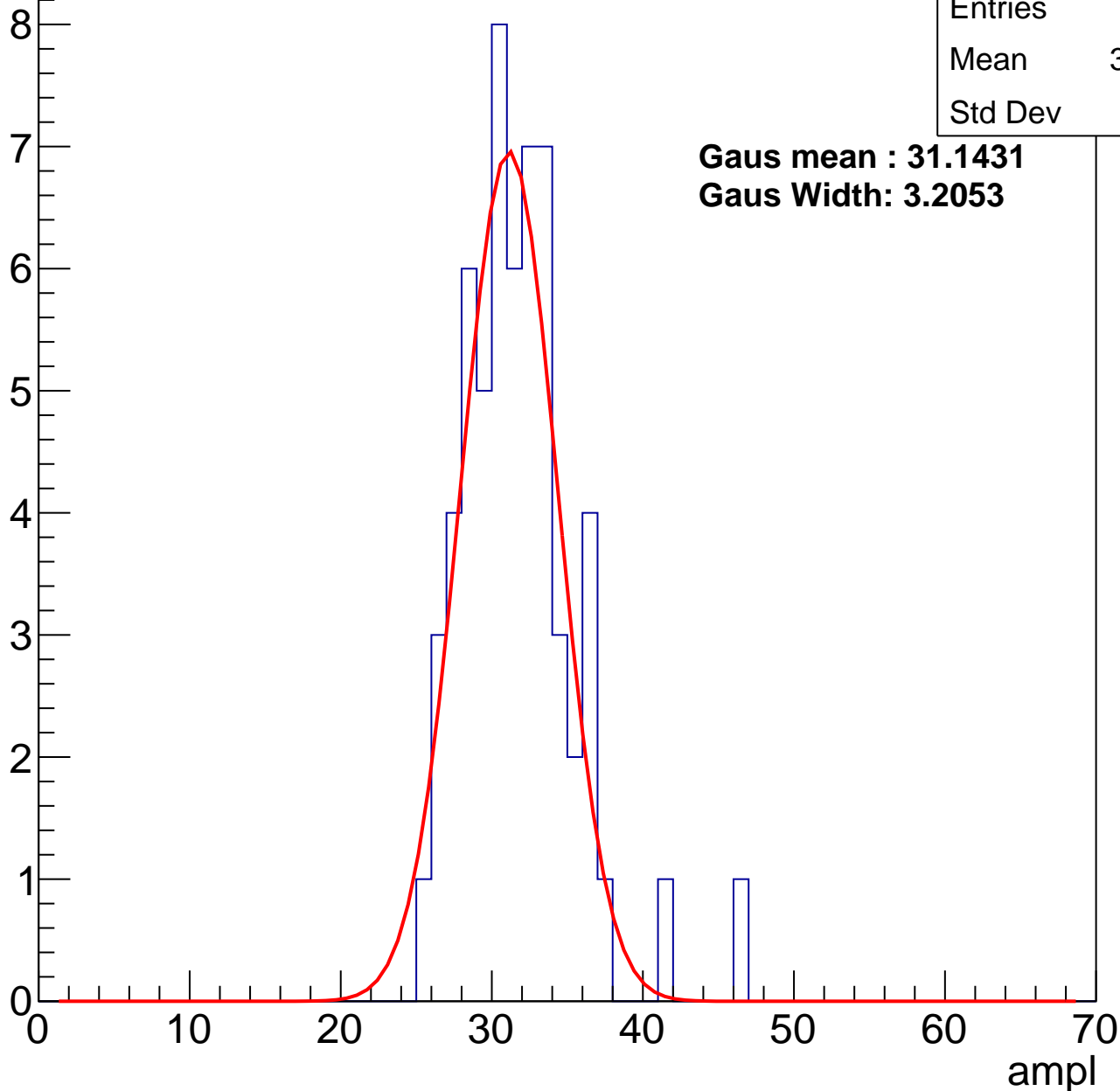
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.27
Std Dev	3.7

Gaus mean : 31.1431

Gaus Width: 3.2053



B0L001S, U18-ch85, adc1

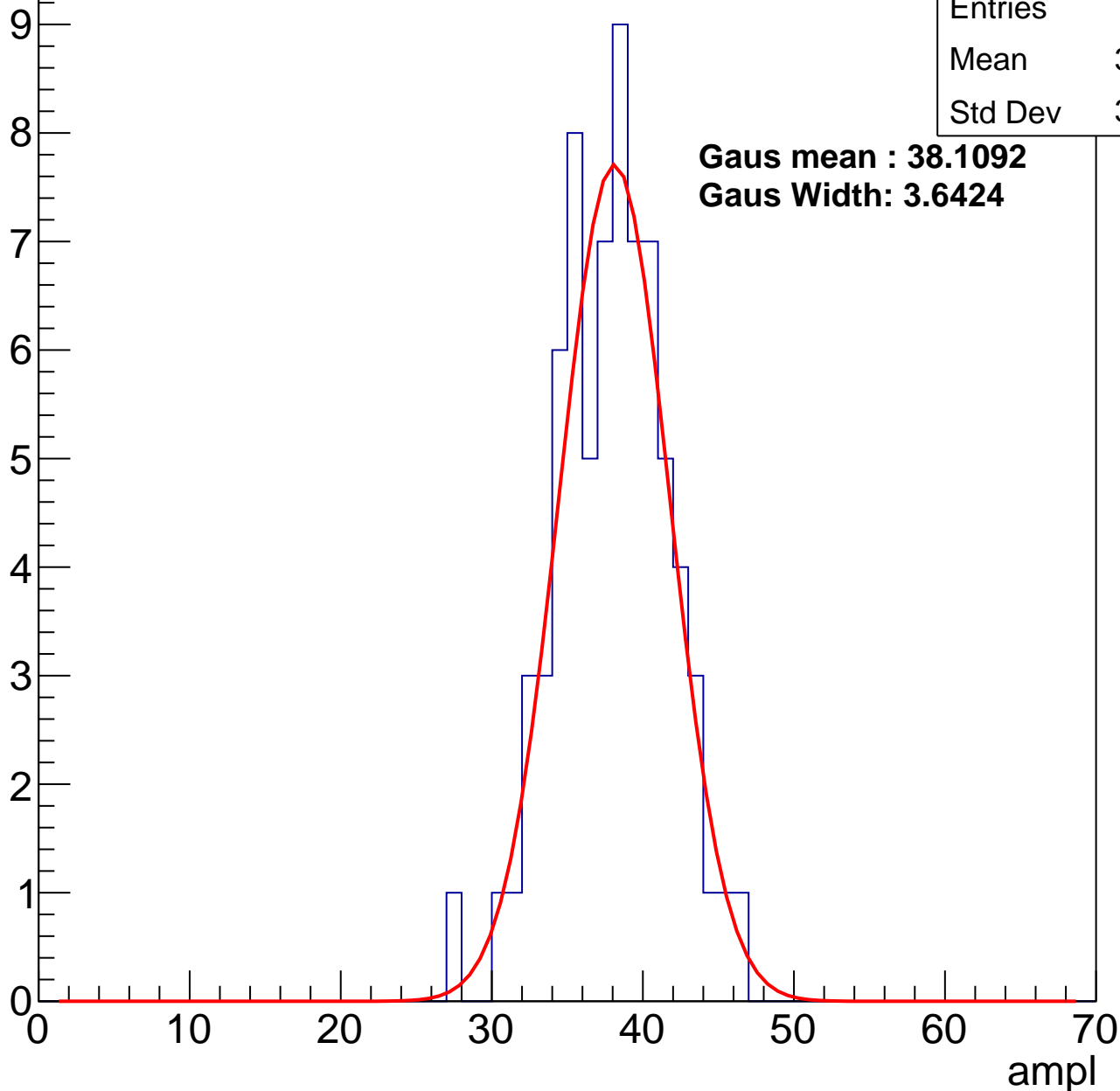
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.51
Std Dev	3.631

Gaus mean : 38.1092

Gaus Width: 3.6424



B0L001S, U18-ch85, adc2

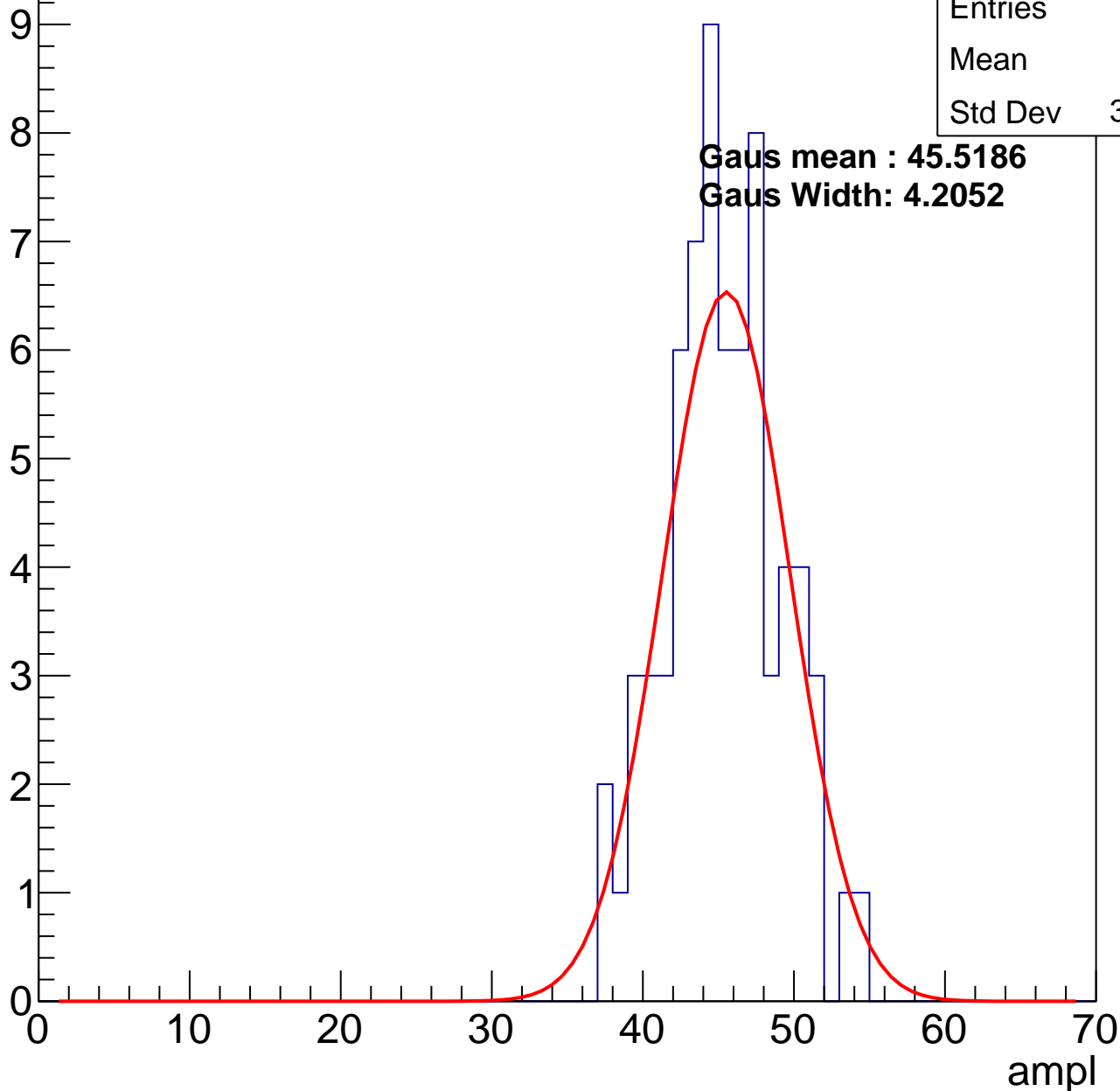
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.9
Std Dev	3.742

Gaus mean : 45.5186

Gaus Width: 4.2052

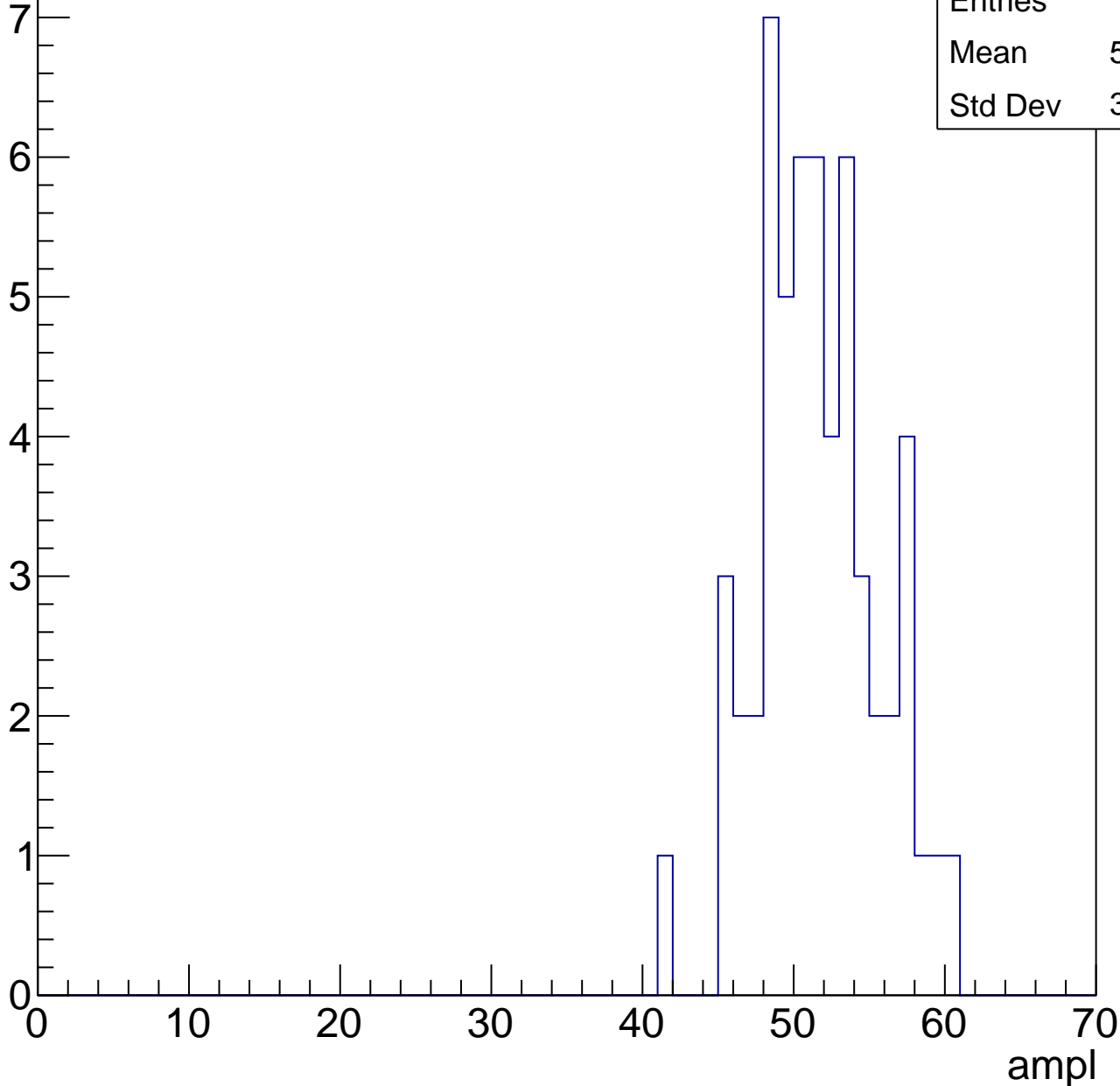


B0L001S, U18-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

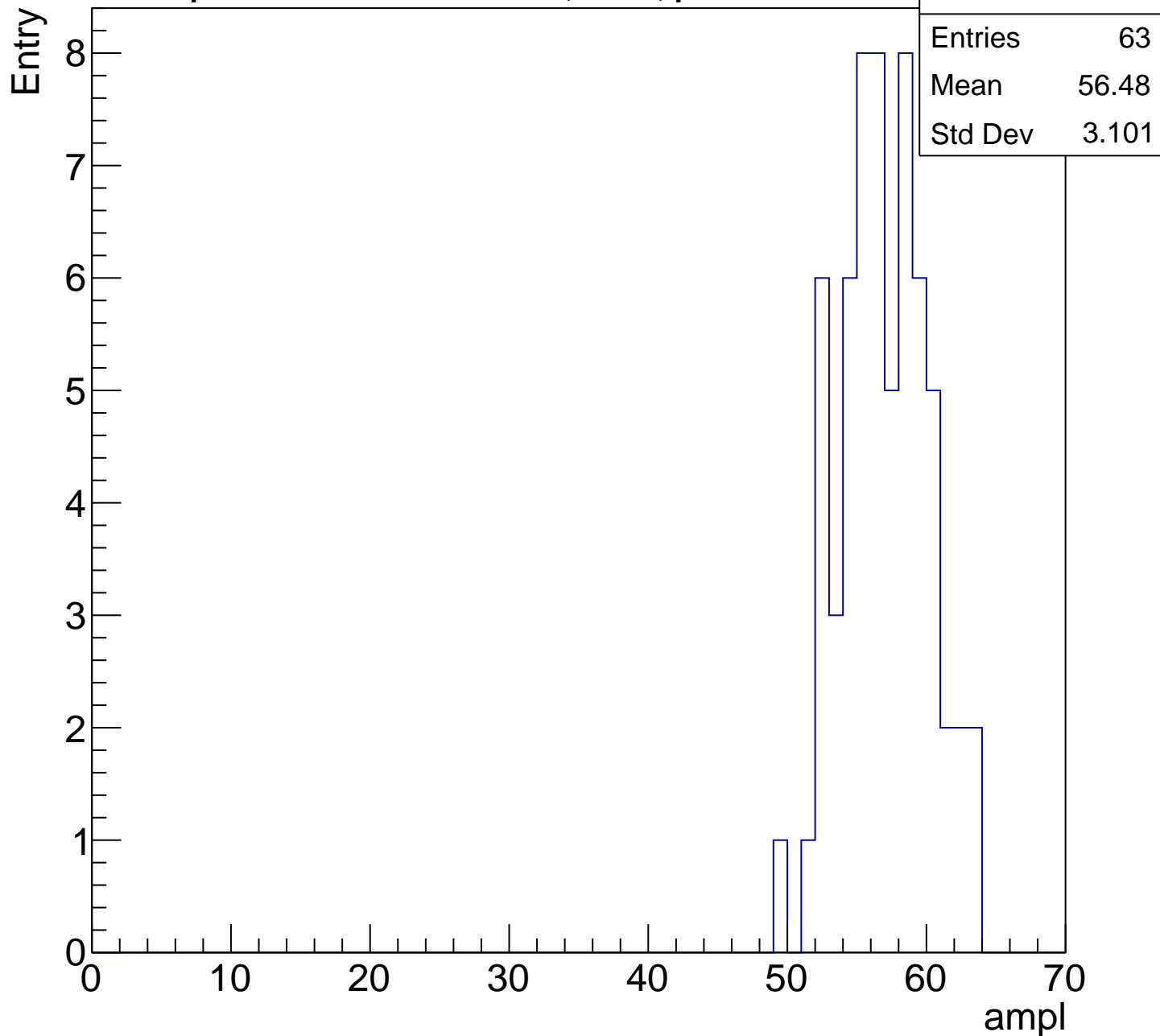
Entry

Entries	56
Mean	51.14
Std Dev	3.903



B0L001S, U18-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

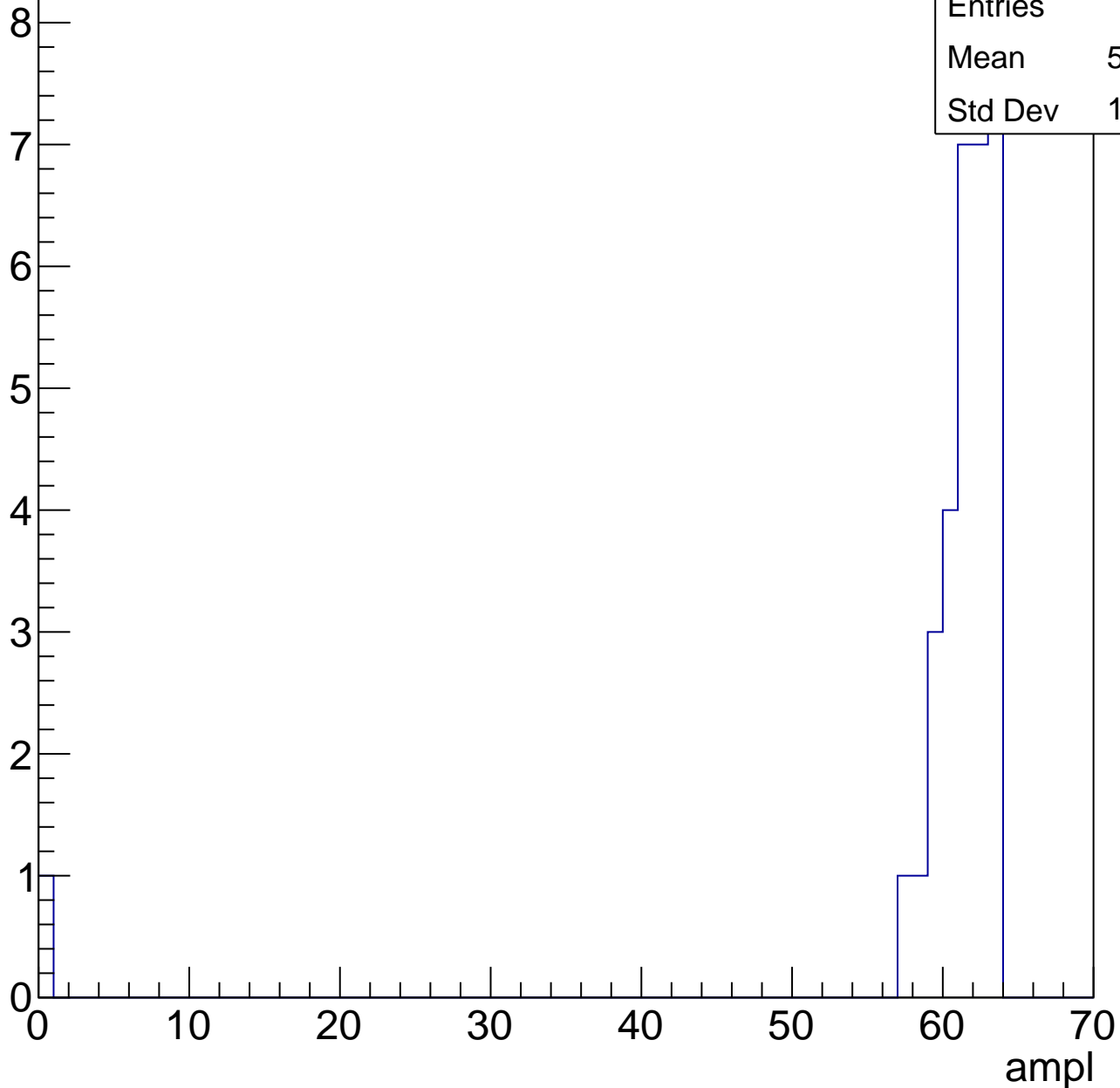


B0L001S, U18-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59.28
Std Dev	10.76



B0L001S, U18-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U18-ch86, adc0

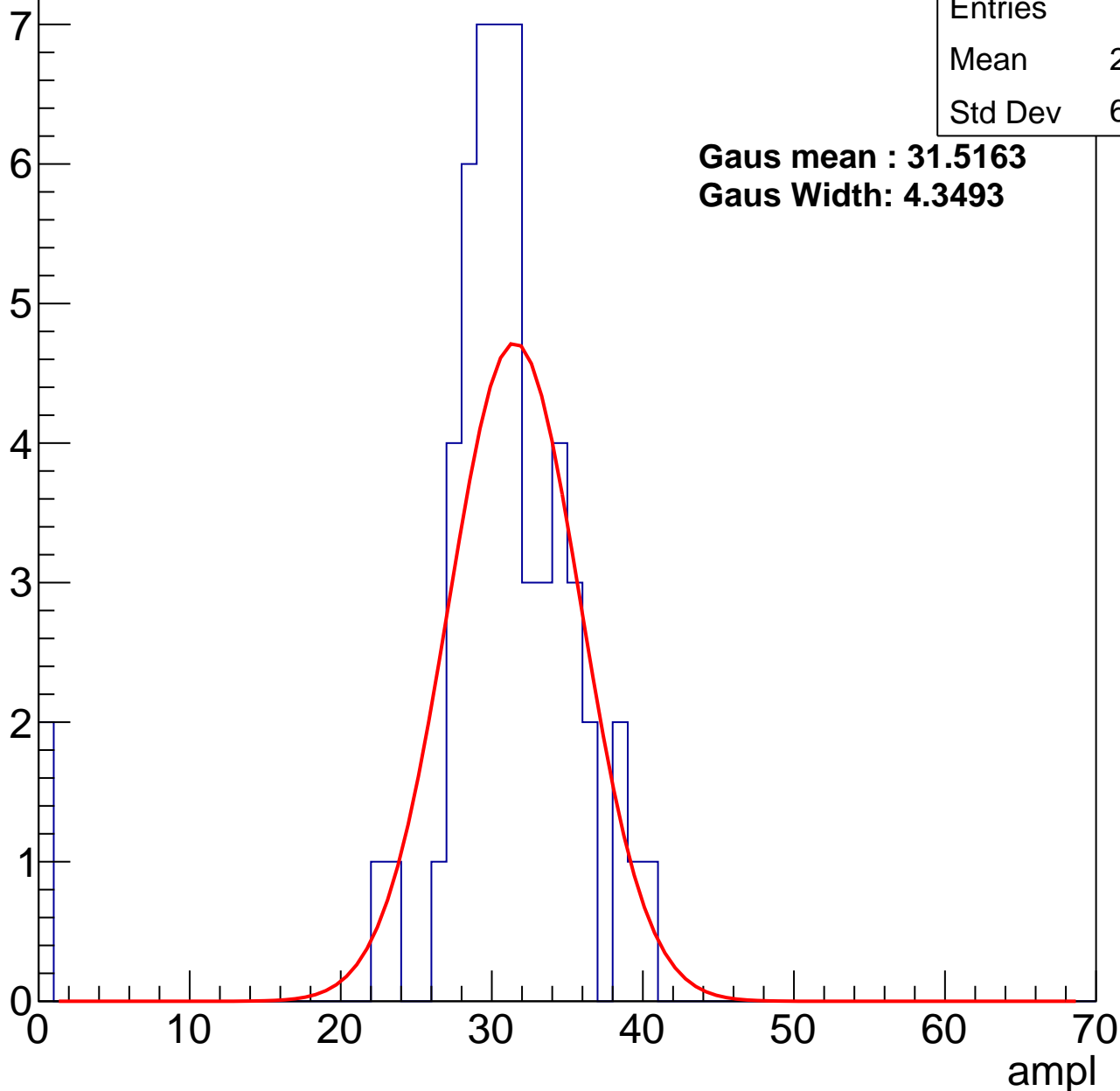
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.82
Std Dev	6.823

Gaus mean : 31.5163

Gaus Width: 4.3493



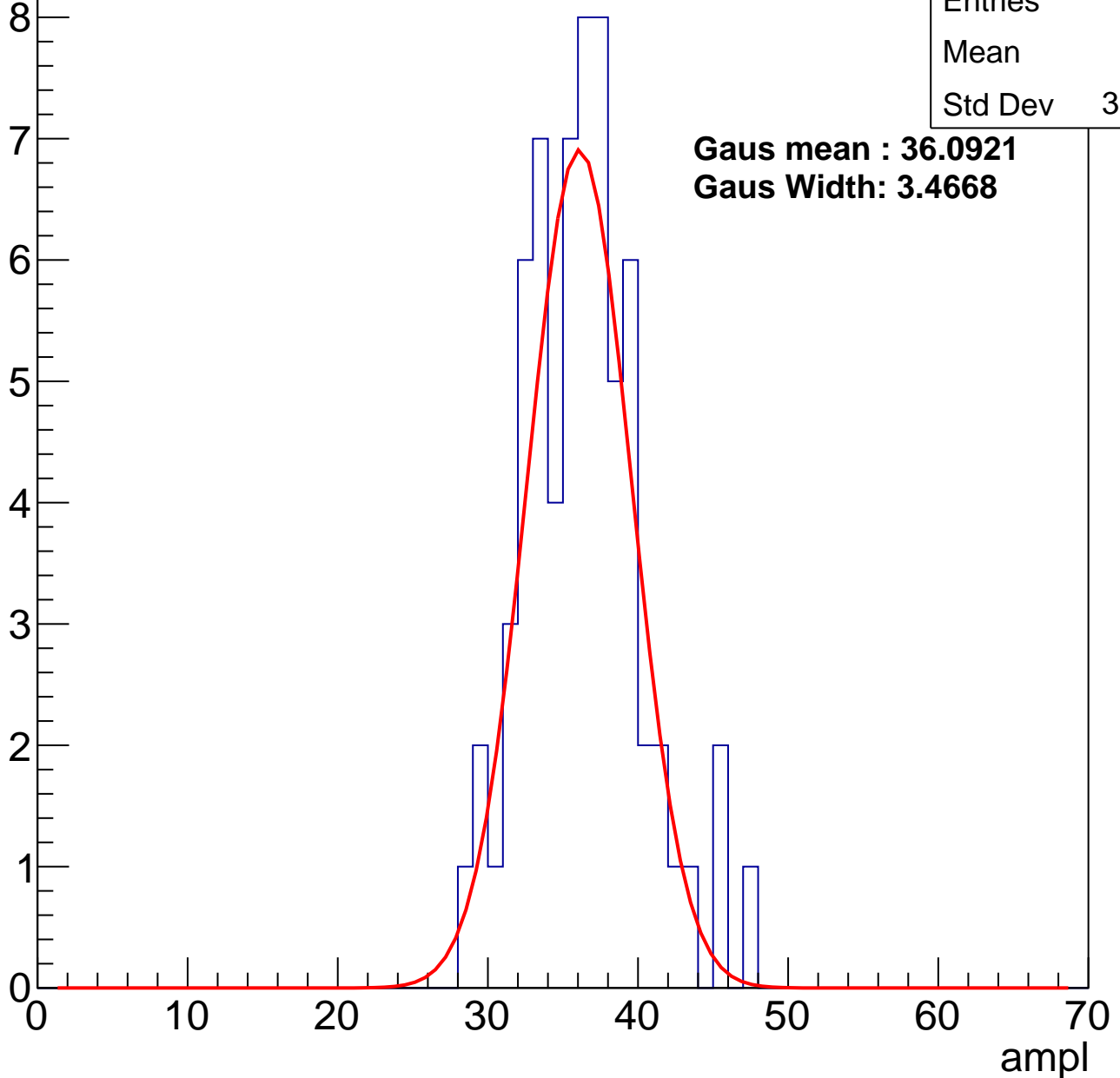
B0L001S, U18-ch86, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	35.9
Std Dev	3.837

Gaus mean : 36.0921
Gaus Width: 3.4668



B0L001S, U18-ch86, adc2

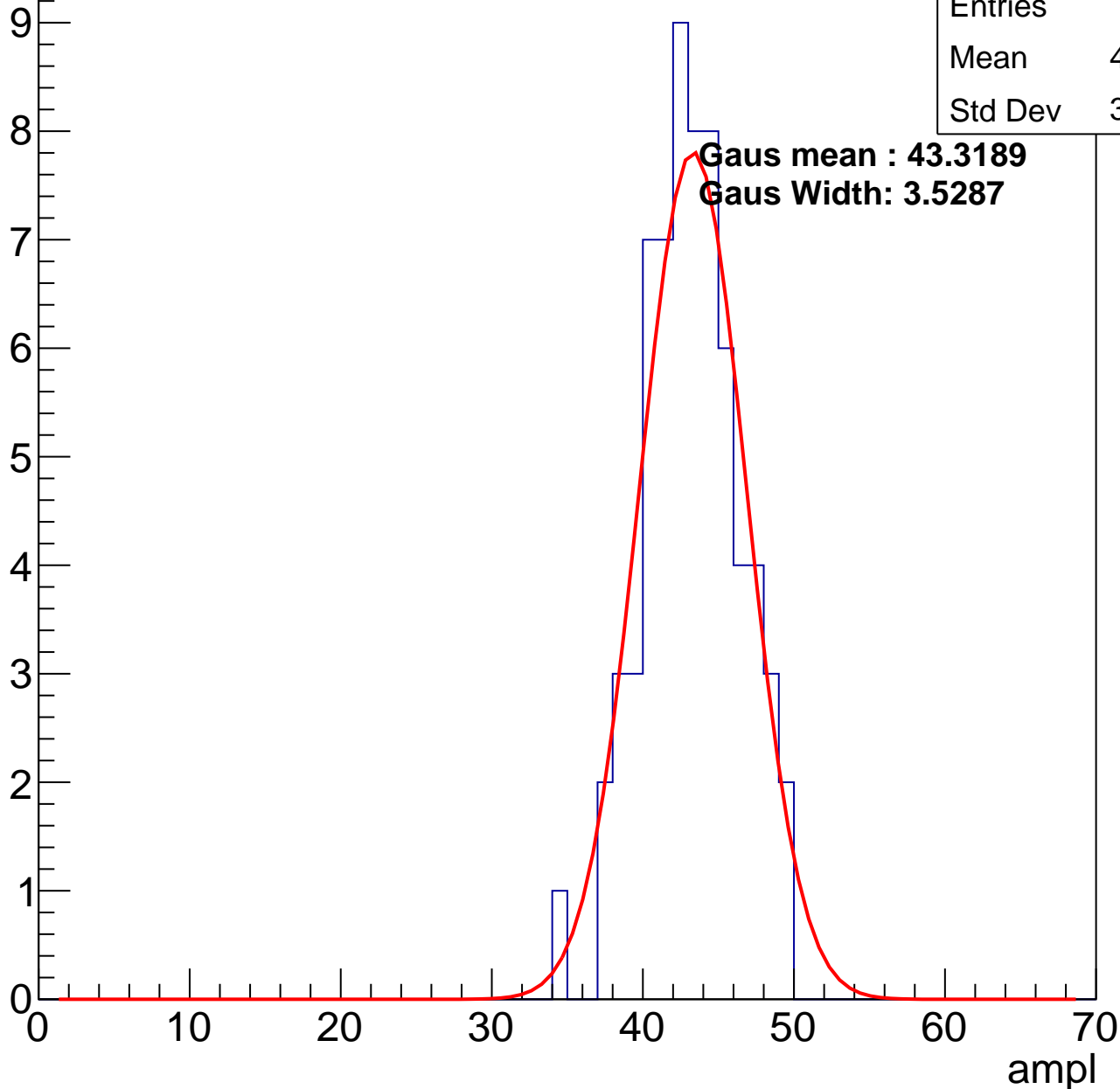
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.75
Std Dev	3.116

Gaus mean : 43.3189

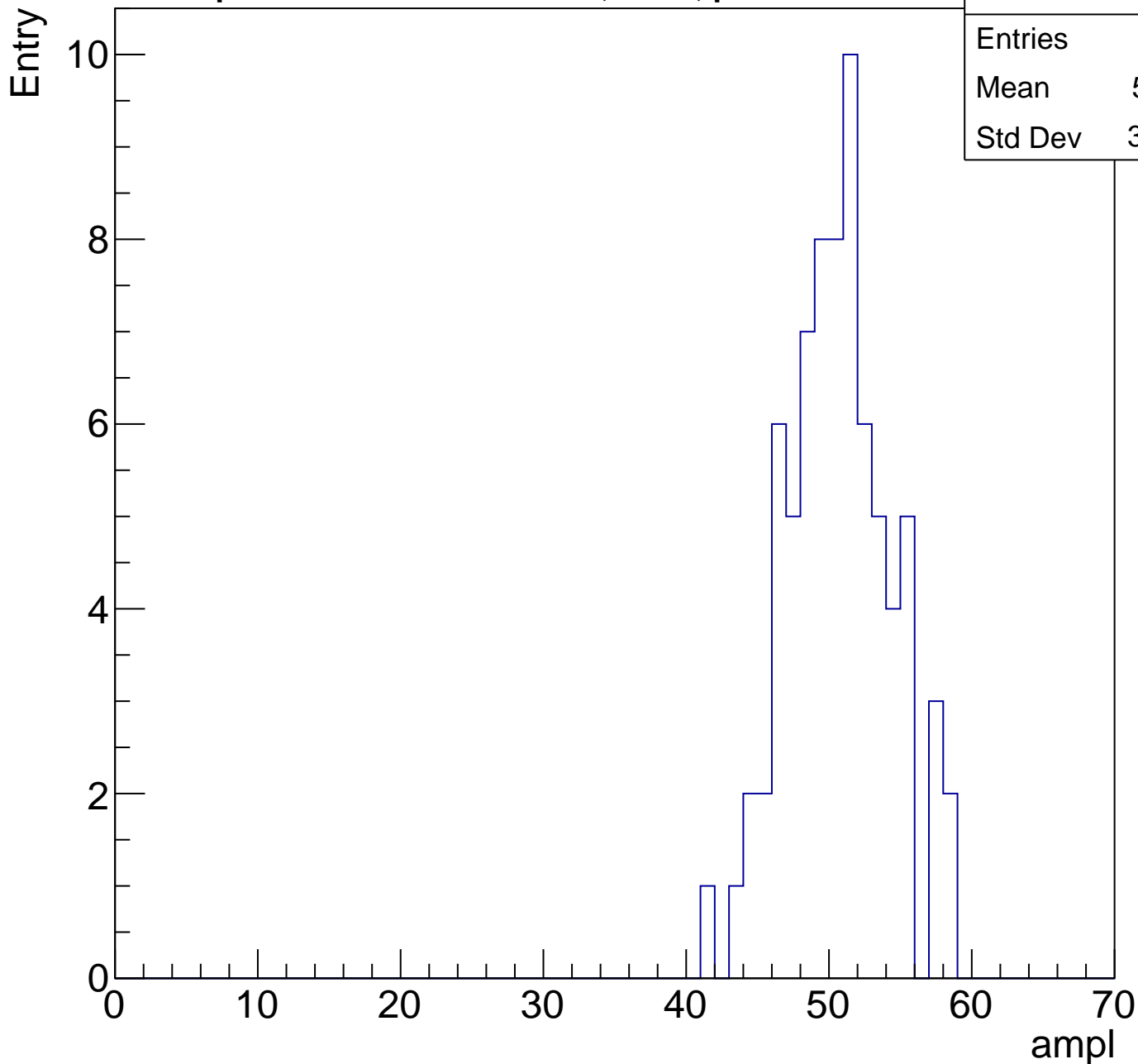
Gaus Width: 3.5287



B0L001S, U18-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	50.21
Std Dev	3.594

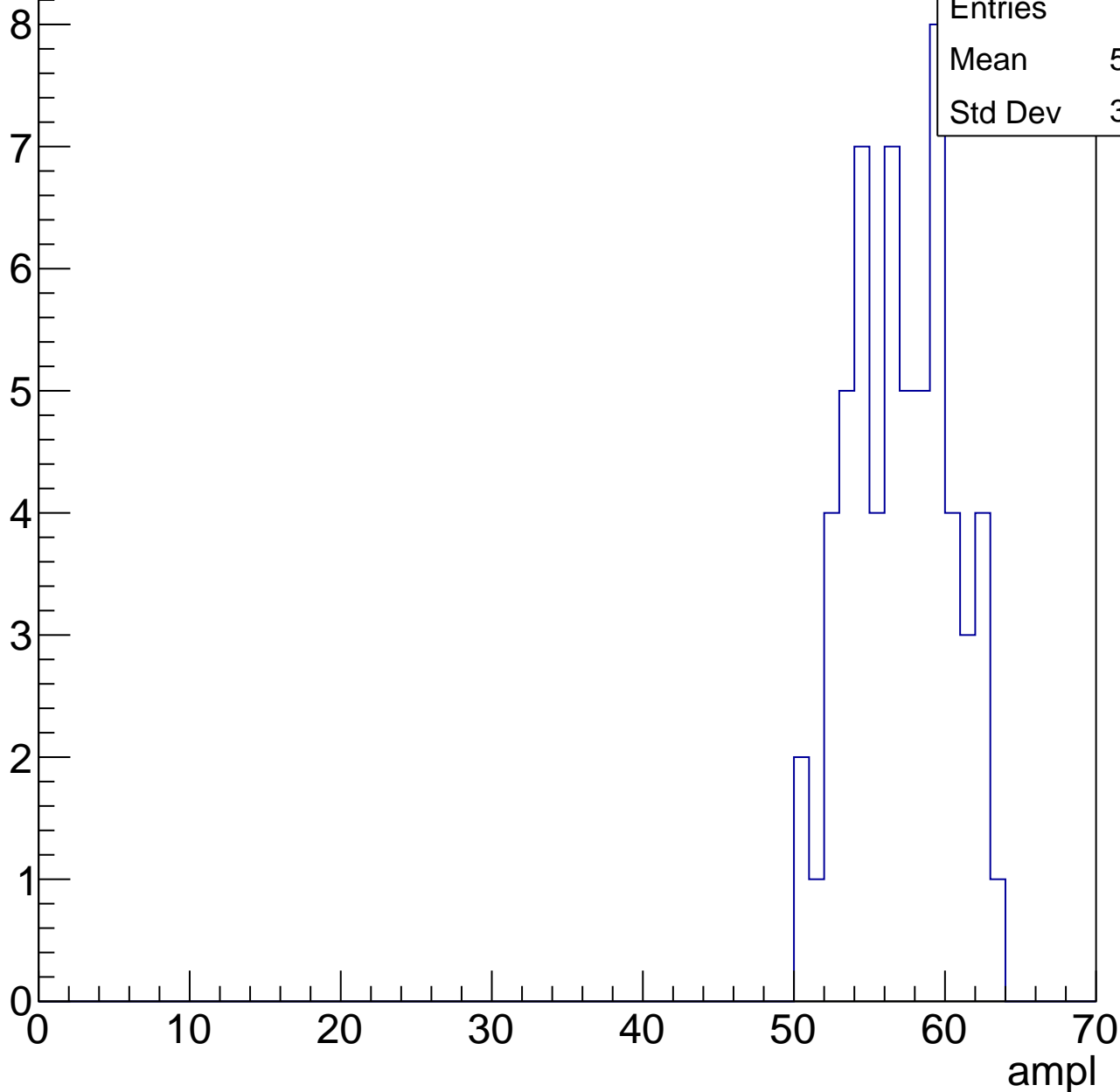


B0L001S, U18-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.58
Std Dev	3.283

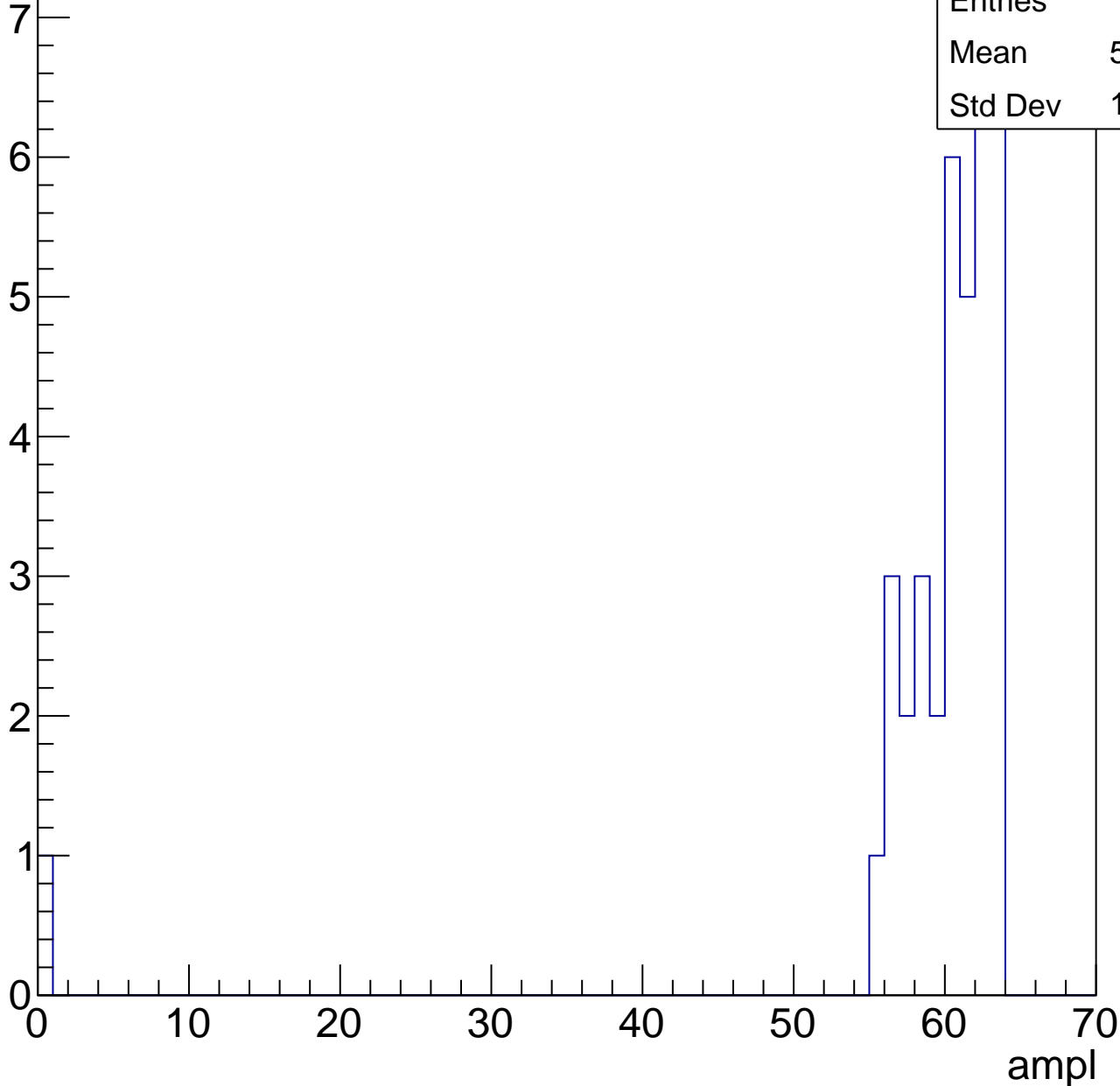


B0L001S, U18-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

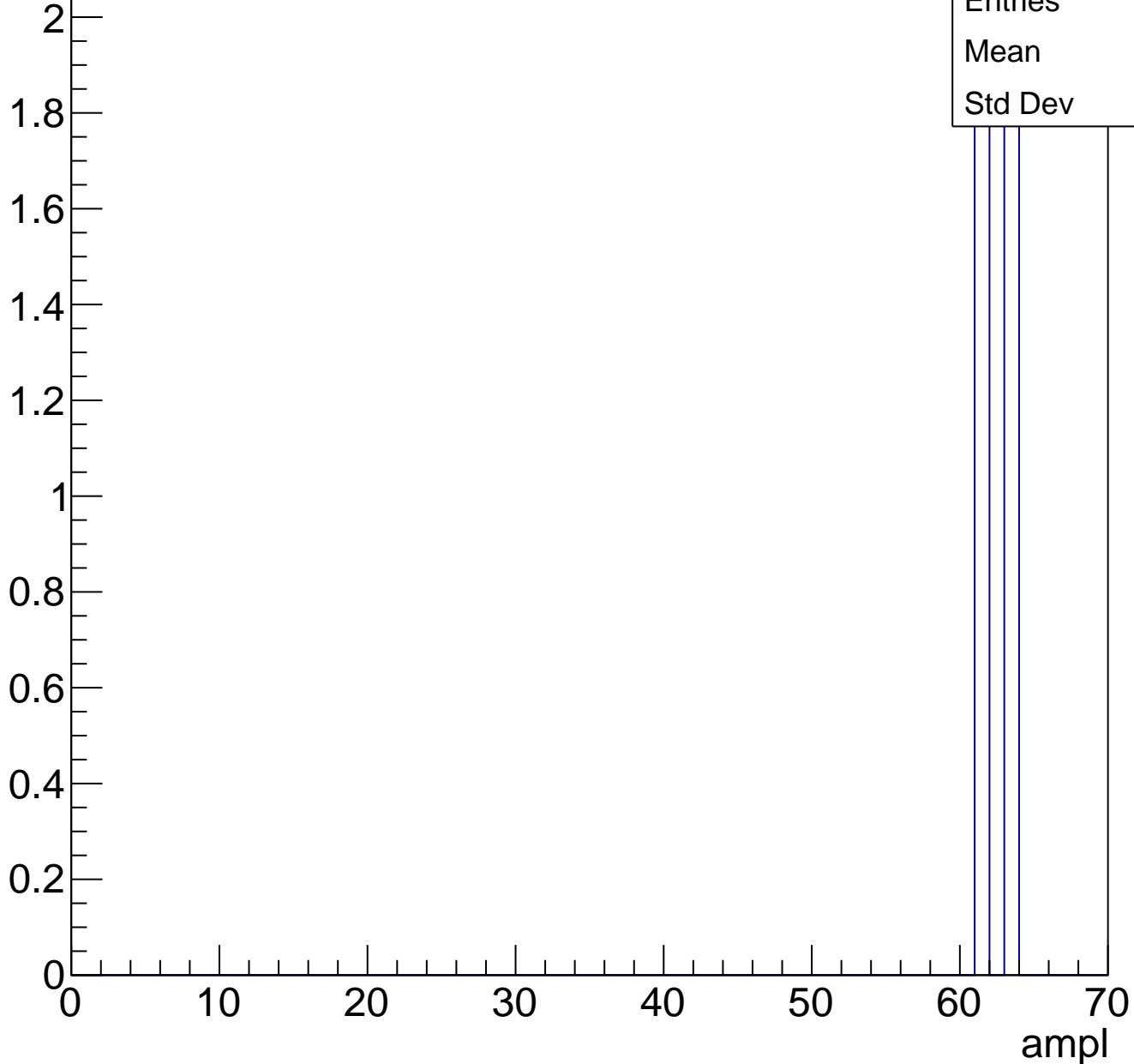
Entries	37
Mean	58.62
Std Dev	10.04



B0L001S, U18-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch87, adc0

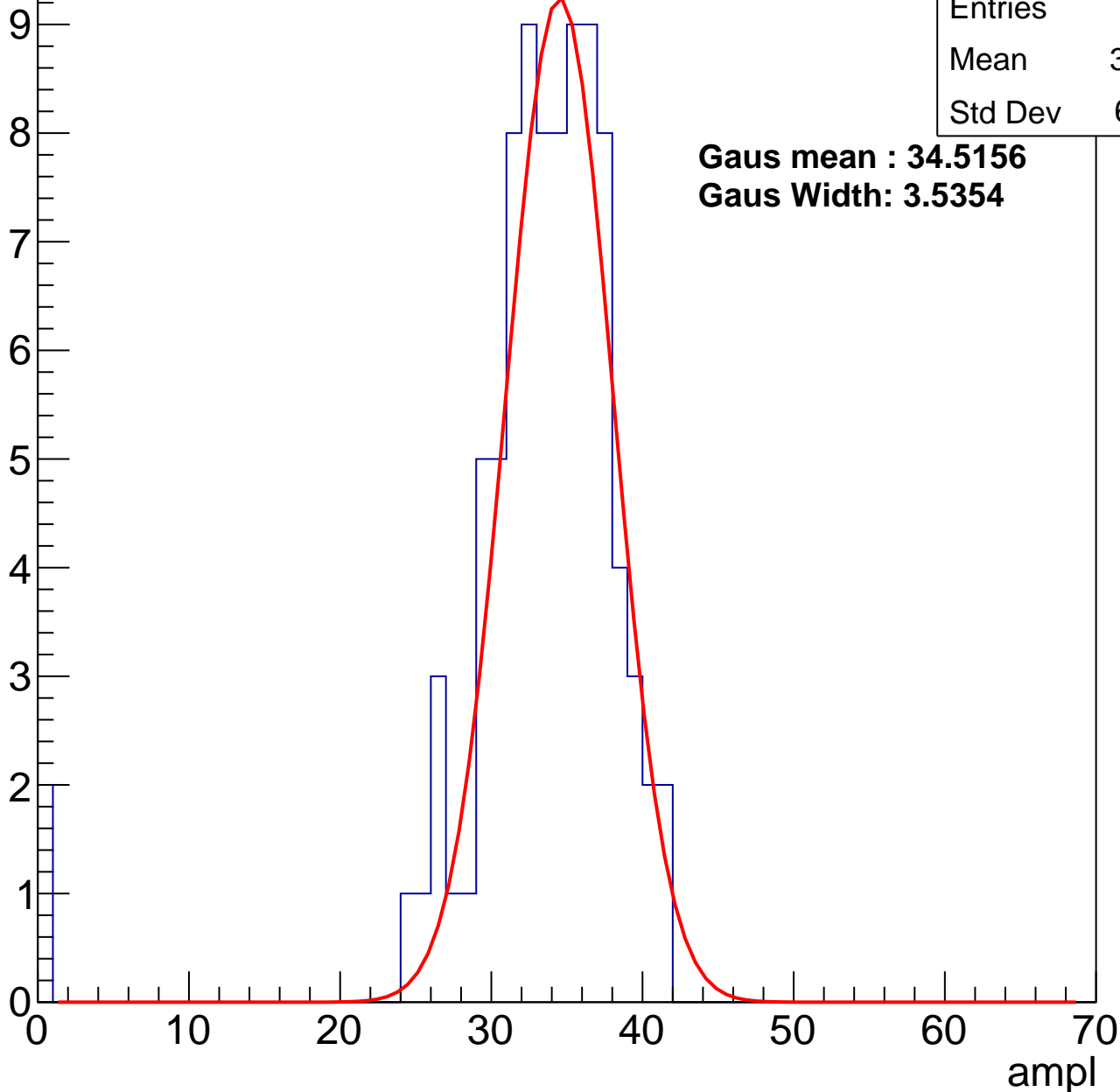
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	89
Mean	32.75
Std Dev	6.161

Gaus mean : 34.5156

Gaus Width: 3.5354



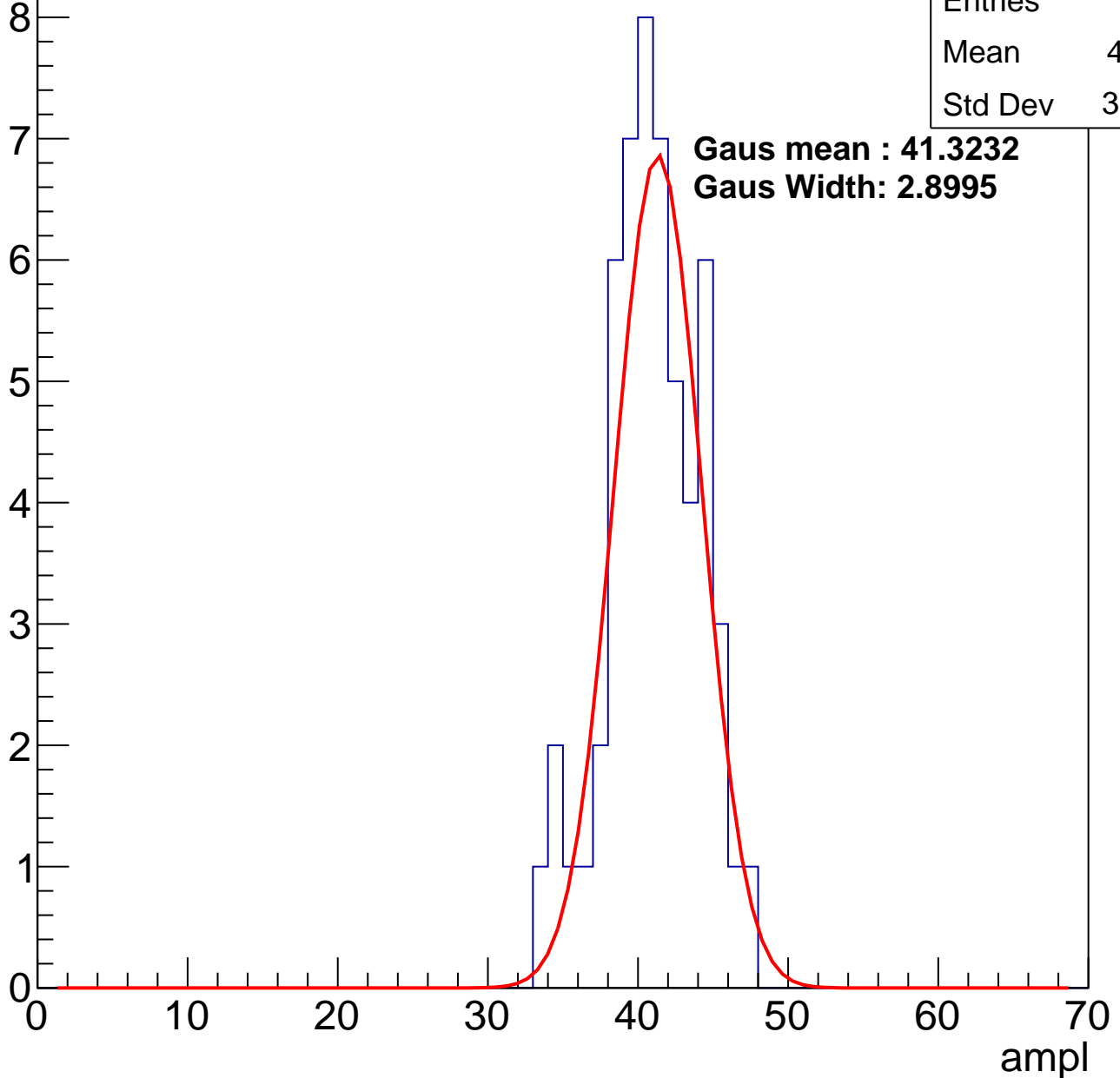
B0L001S, U18-ch87, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	40.51
Std Dev	3.062

Gaus mean : 41.3232
Gaus Width: 2.8995



B0L001S, U18-ch87, adc2

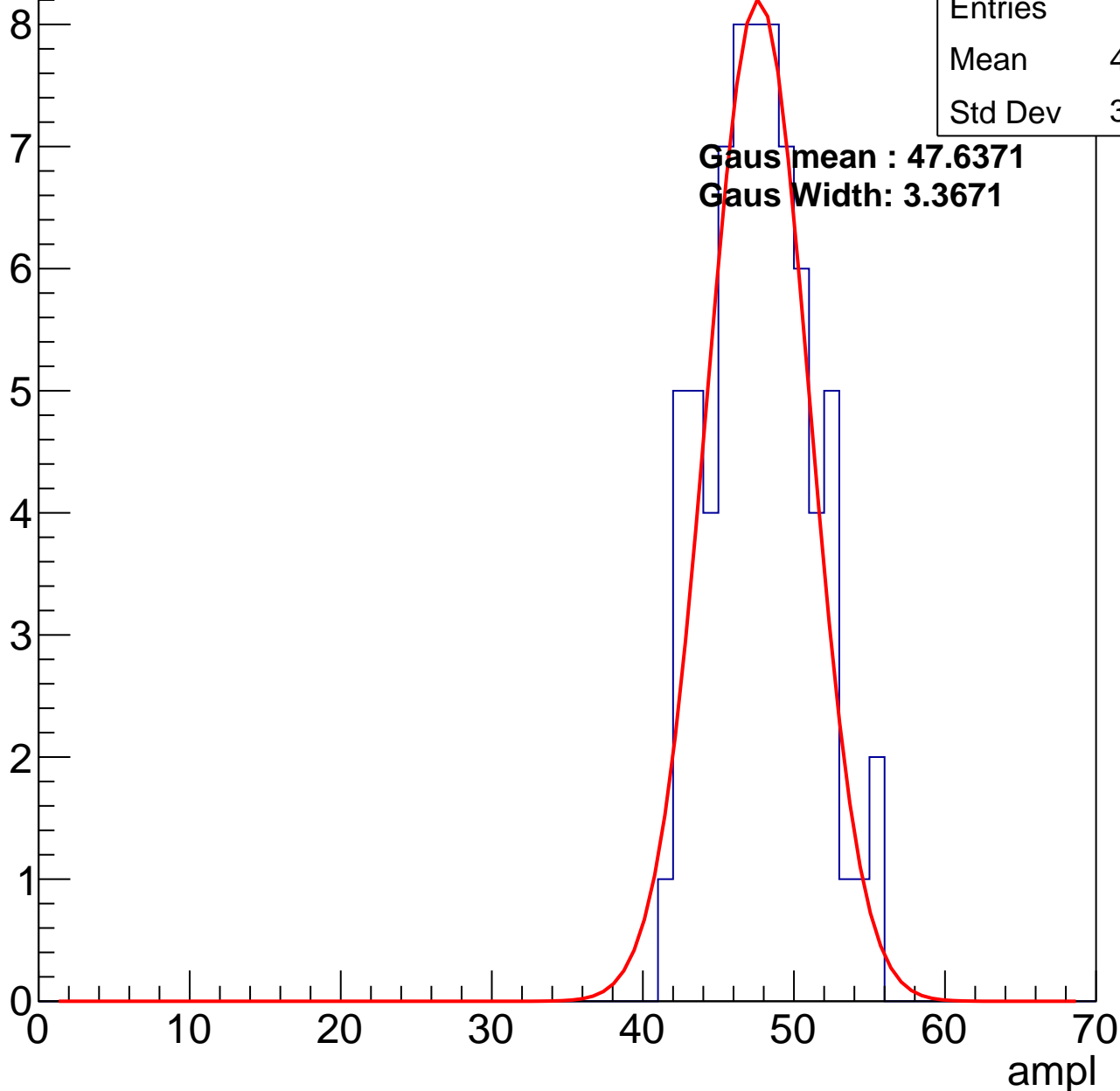
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	47.35
Std Dev	3.326

Gaus mean : 47.6371

Gaus Width: 3.3671

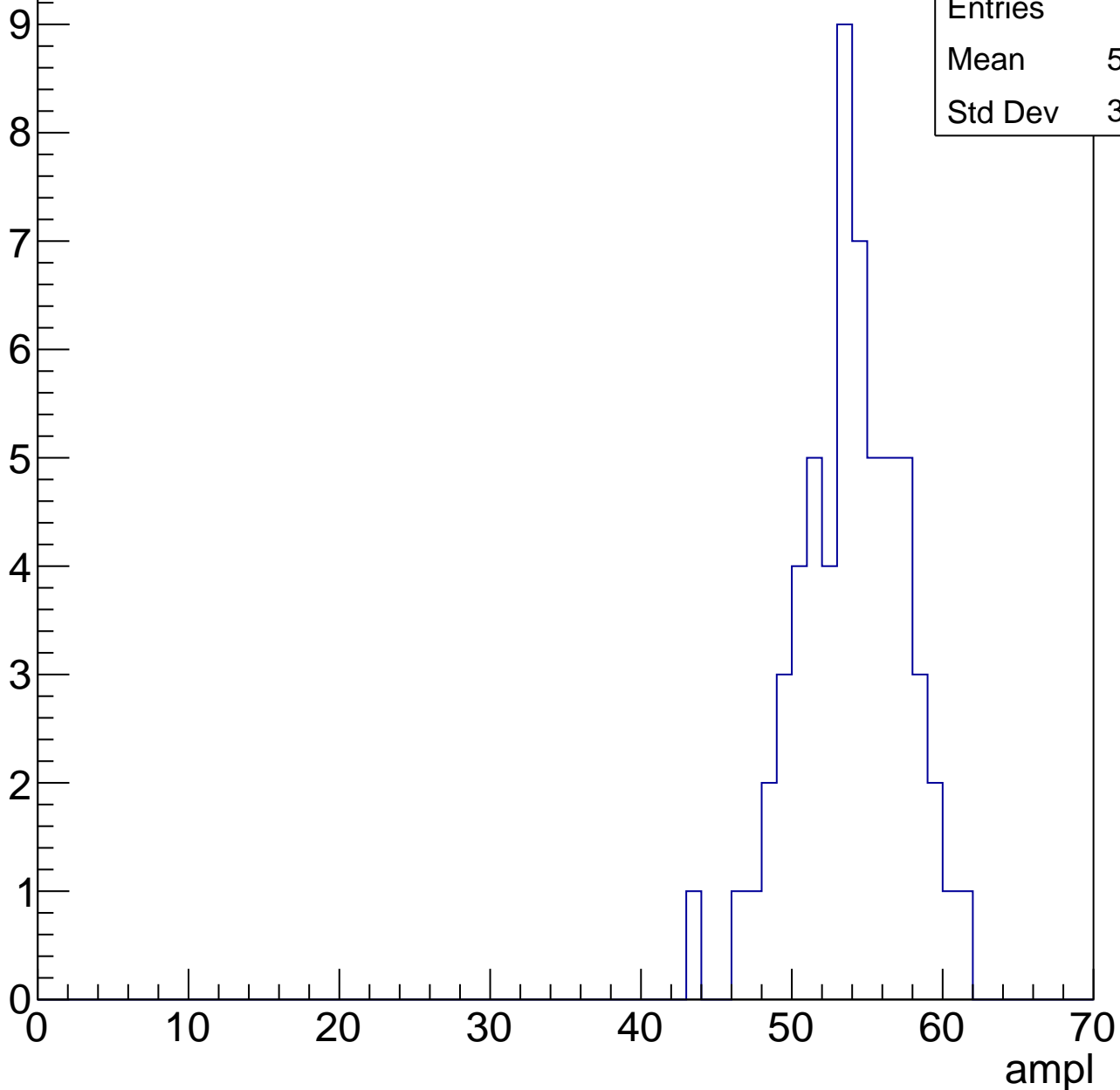


B0L001S, U18-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	53.39
Std Dev	3.556

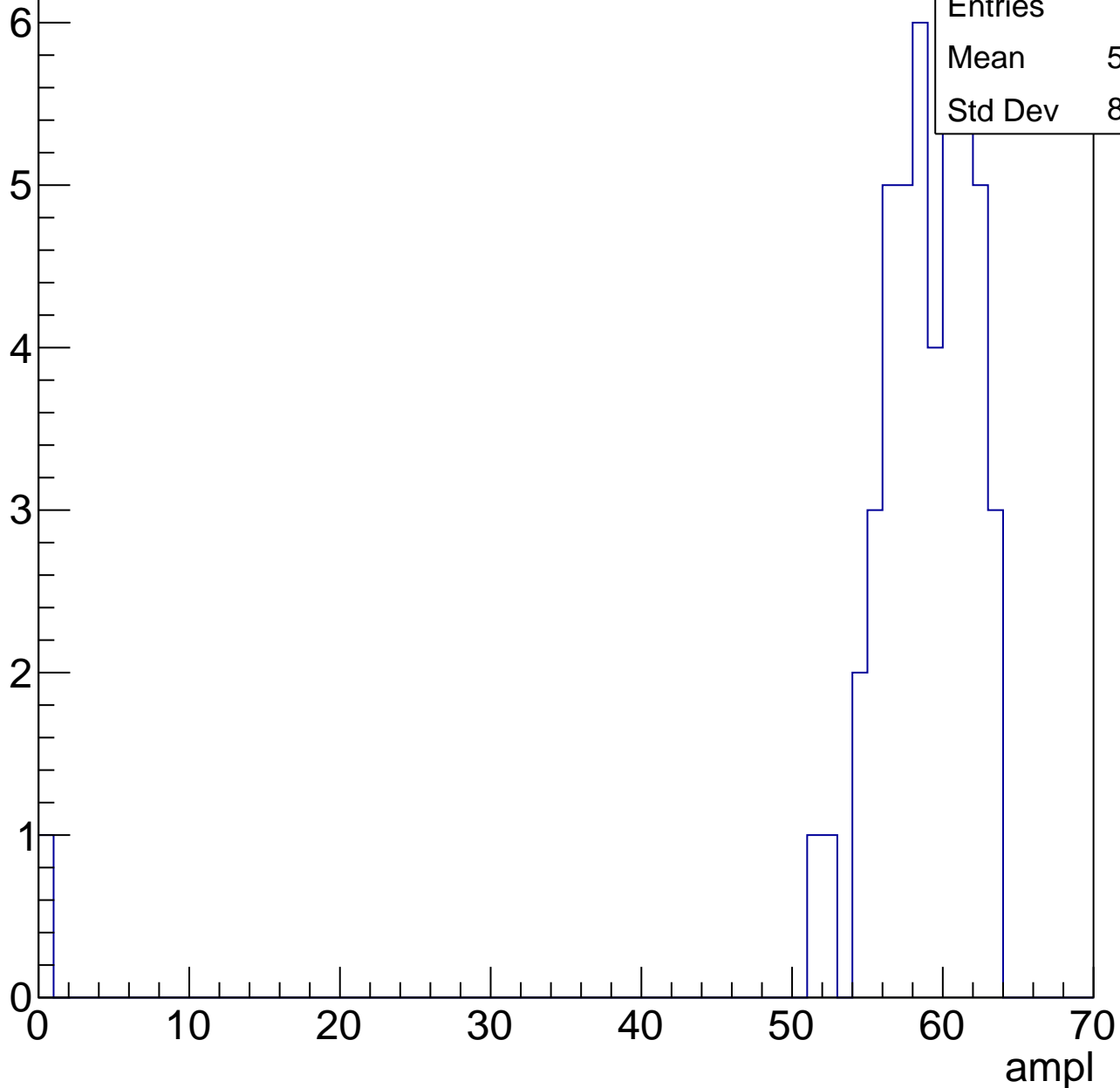


B0L001S, U18-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57.29
Std Dev	8.834

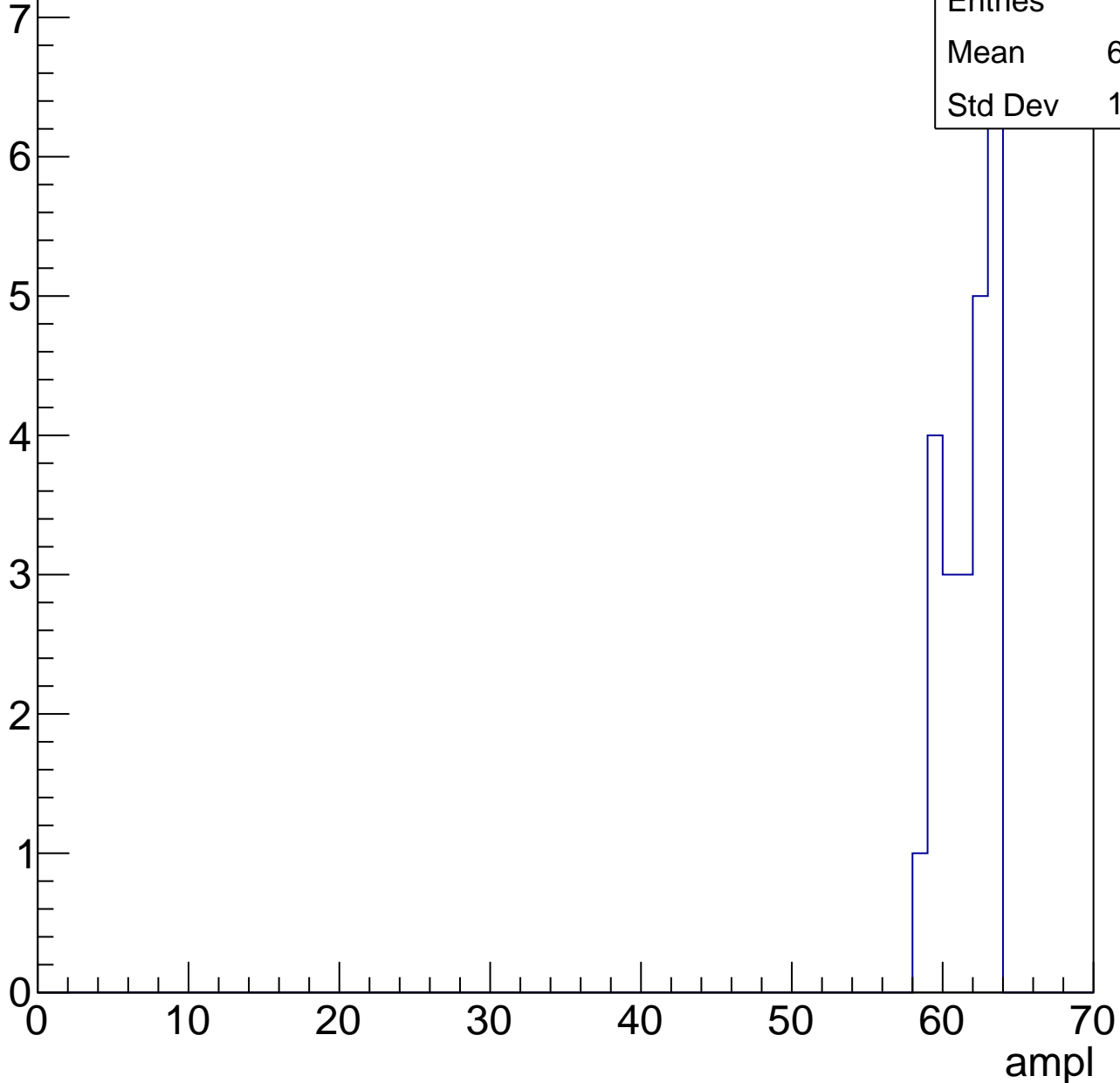


B0L001S, U18-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	61.22
Std Dev	1.614



B0L001S, U18-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch88, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	30.25
Std Dev	6.24

Gaus mean : 30.7567

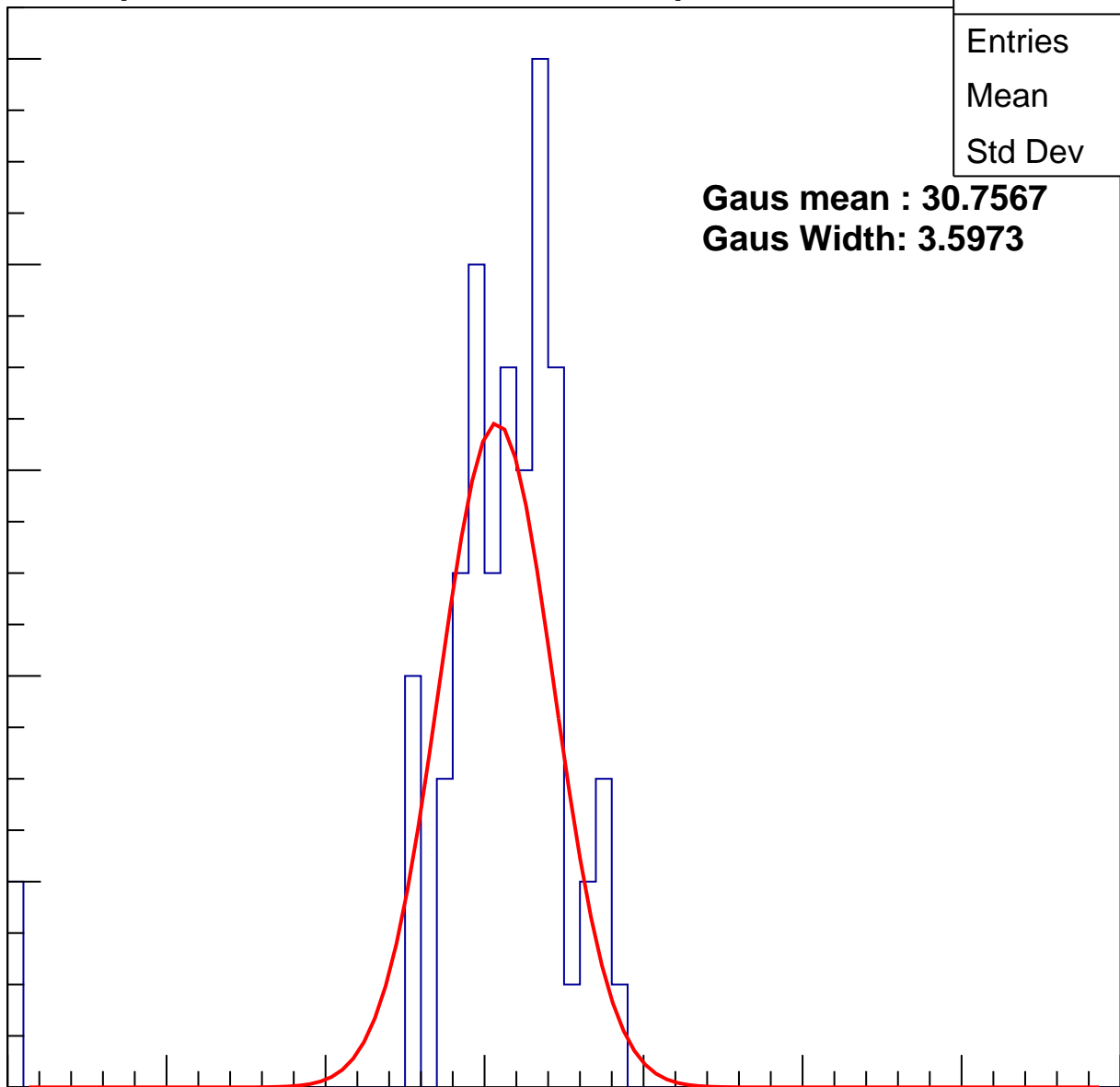
Gaus Width: 3.5973

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch88, adc1

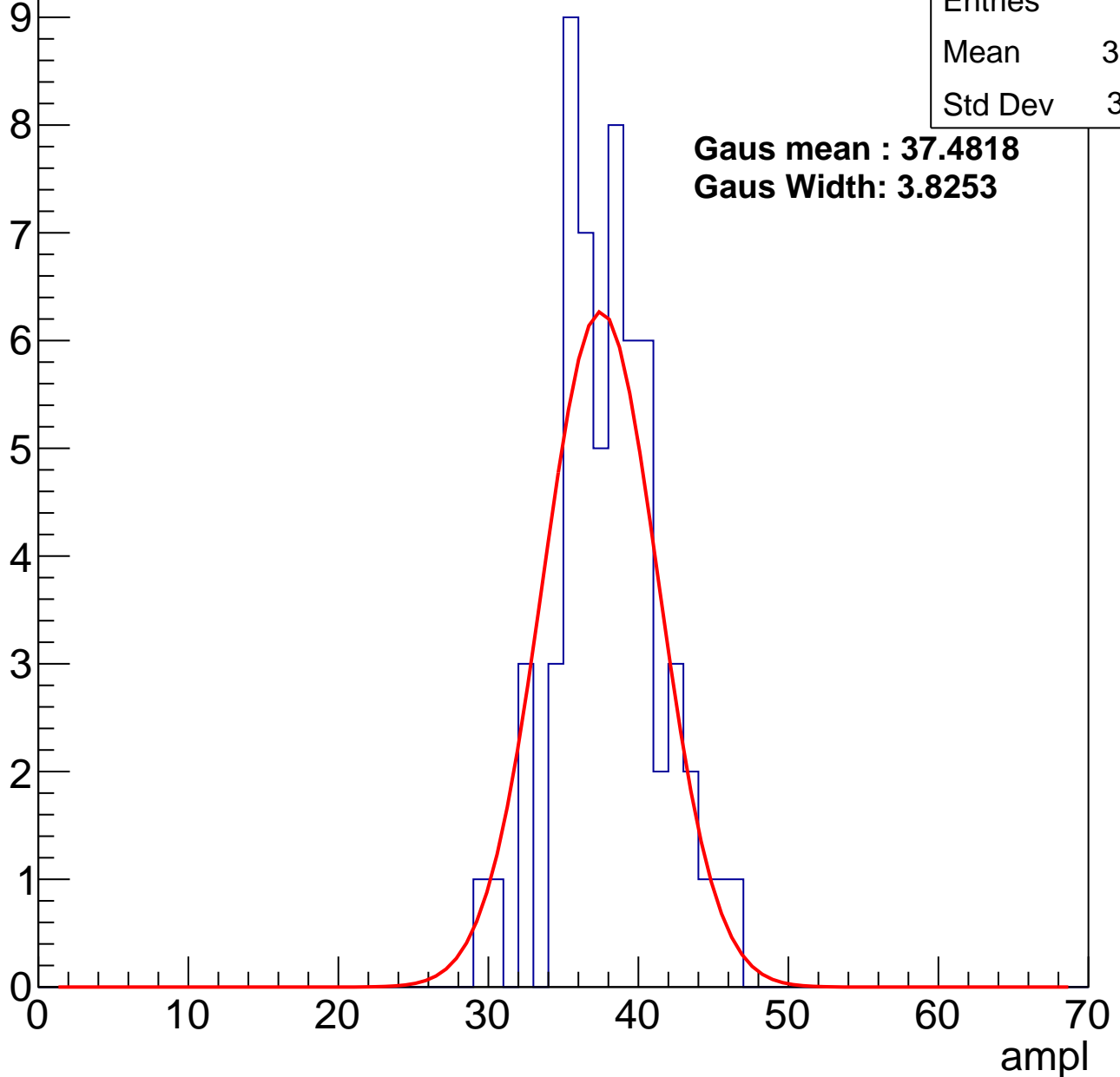
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.56
Std Dev	3.441

Gaus mean : 37.4818

Gaus Width: 3.8253



B0L001S, U18-ch88, adc2

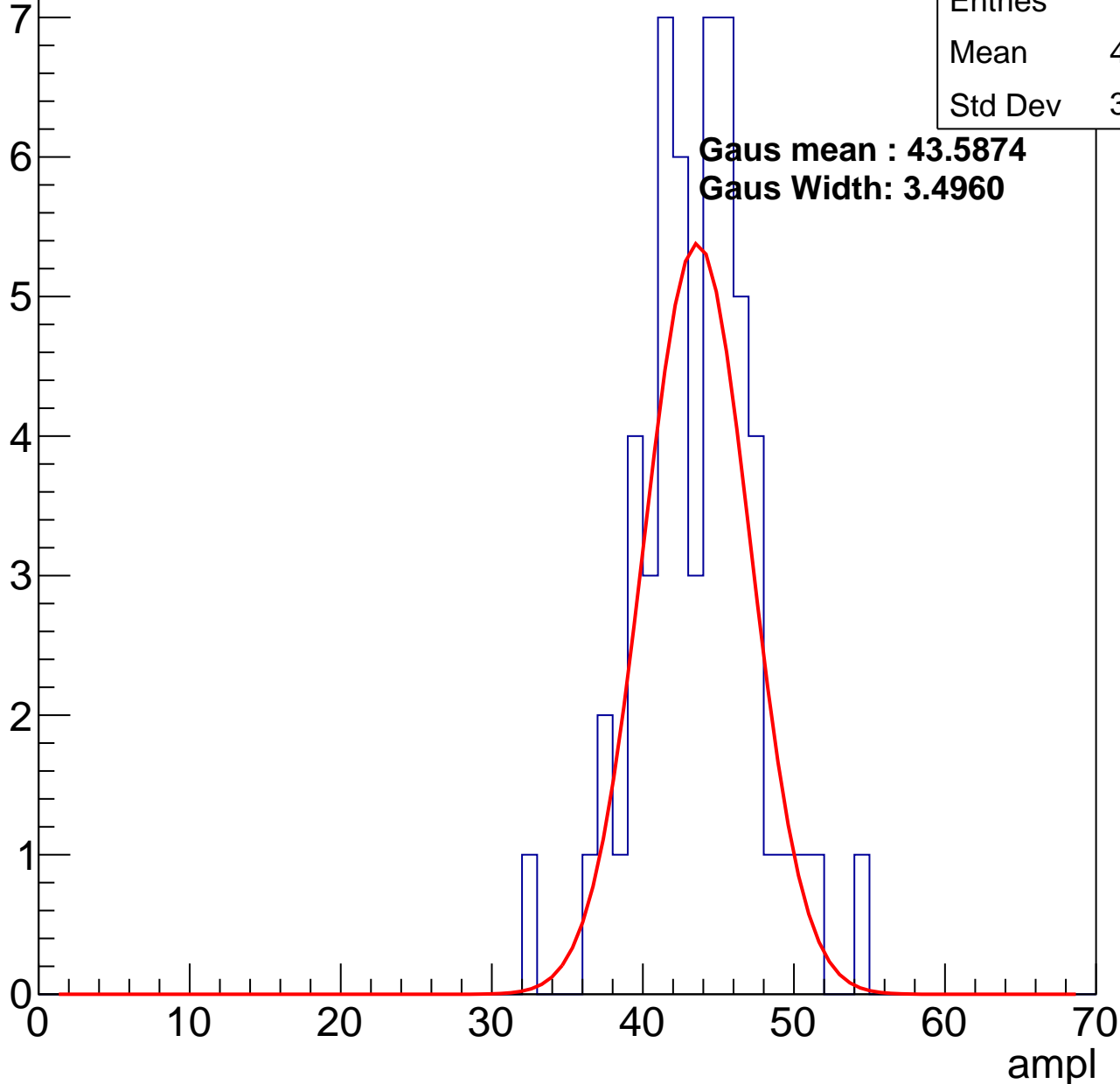
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.16
Std Dev	3.849

Gaus mean : 43.5874

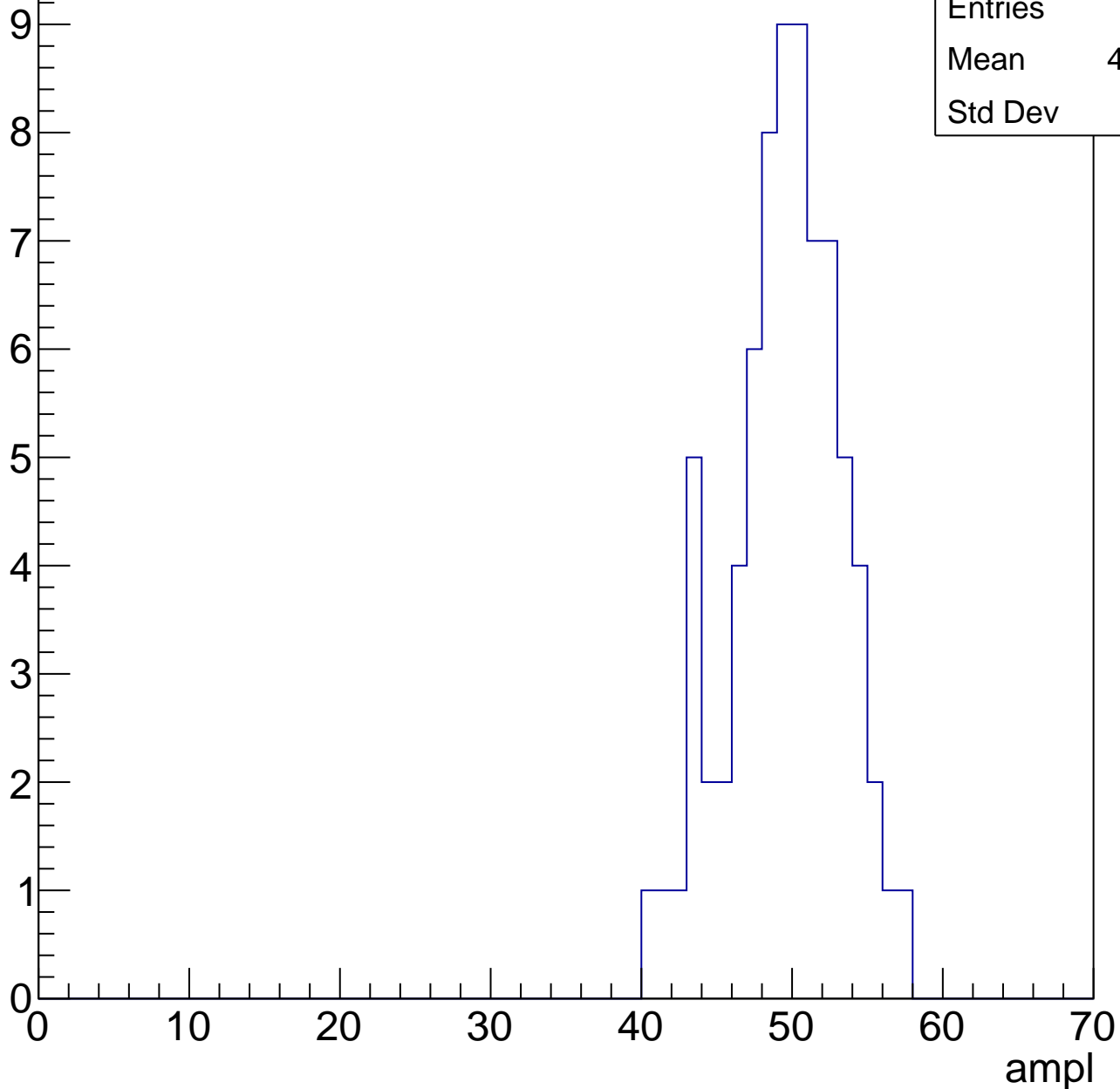
Gaus Width: 3.4960



B0L001S, U18-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

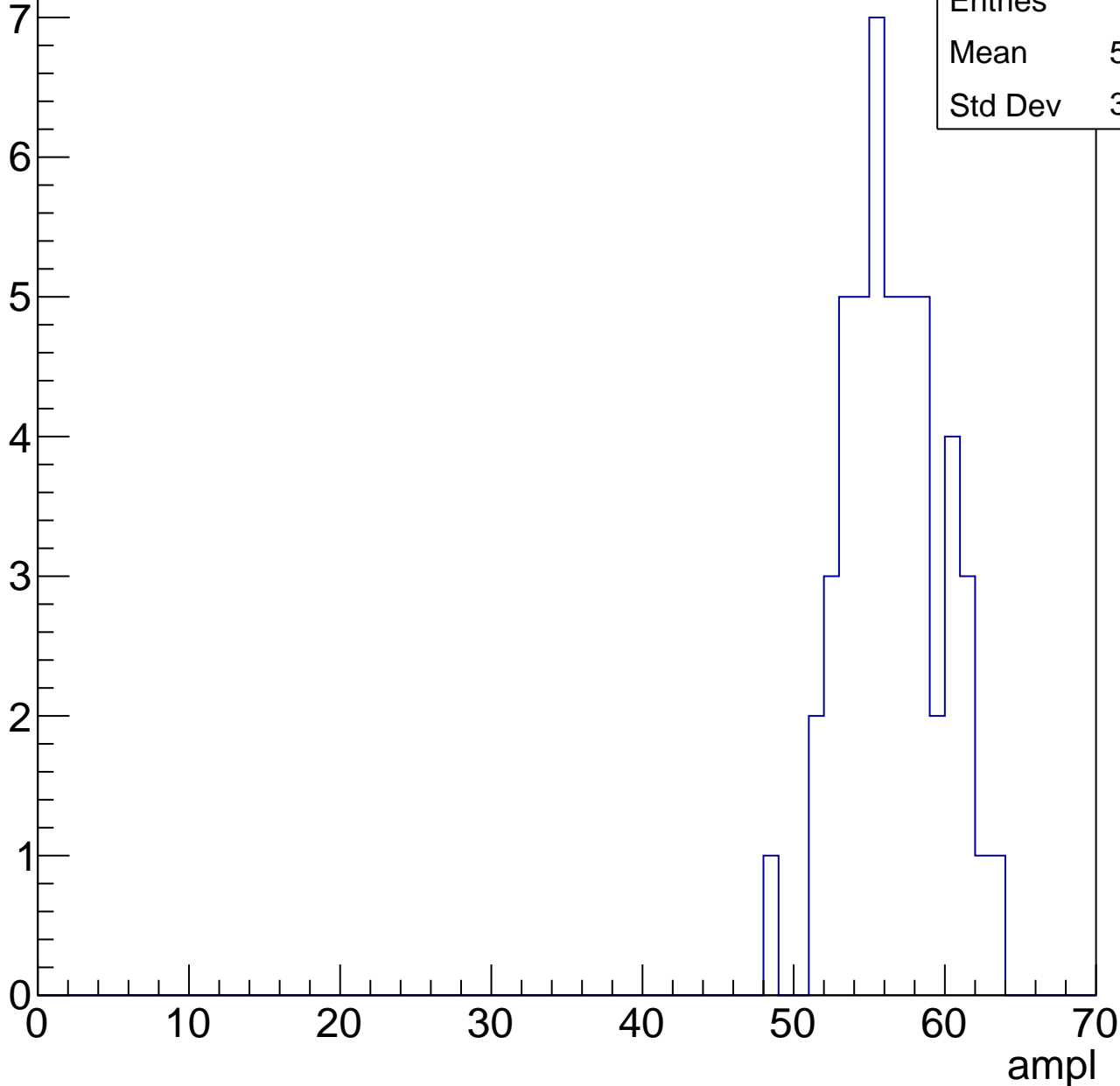


B0L001S, U18-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

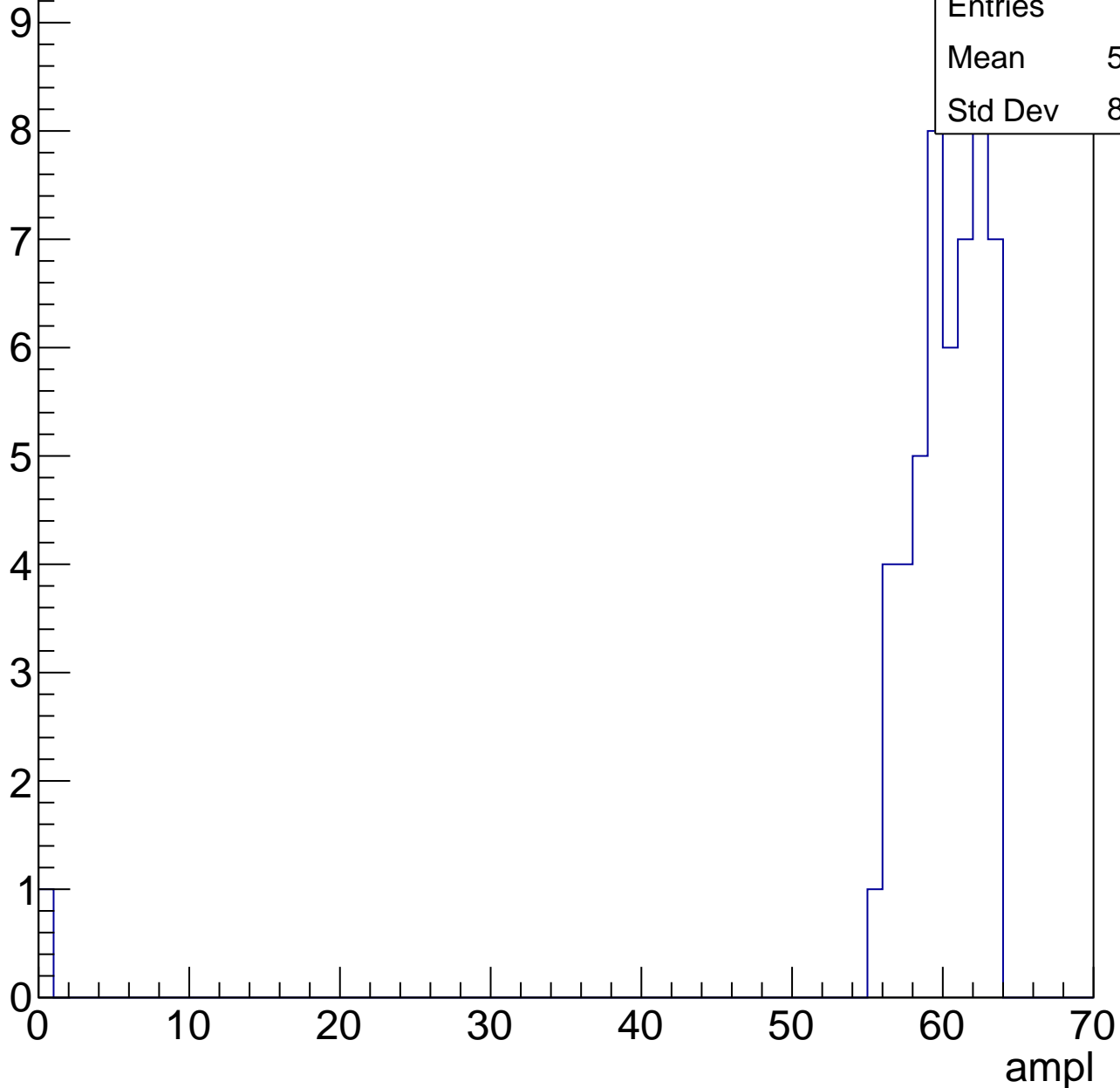
Entries	49
Mean	56.06
Std Dev	3.197



B0L001S, U18-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U18-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch89, adc0

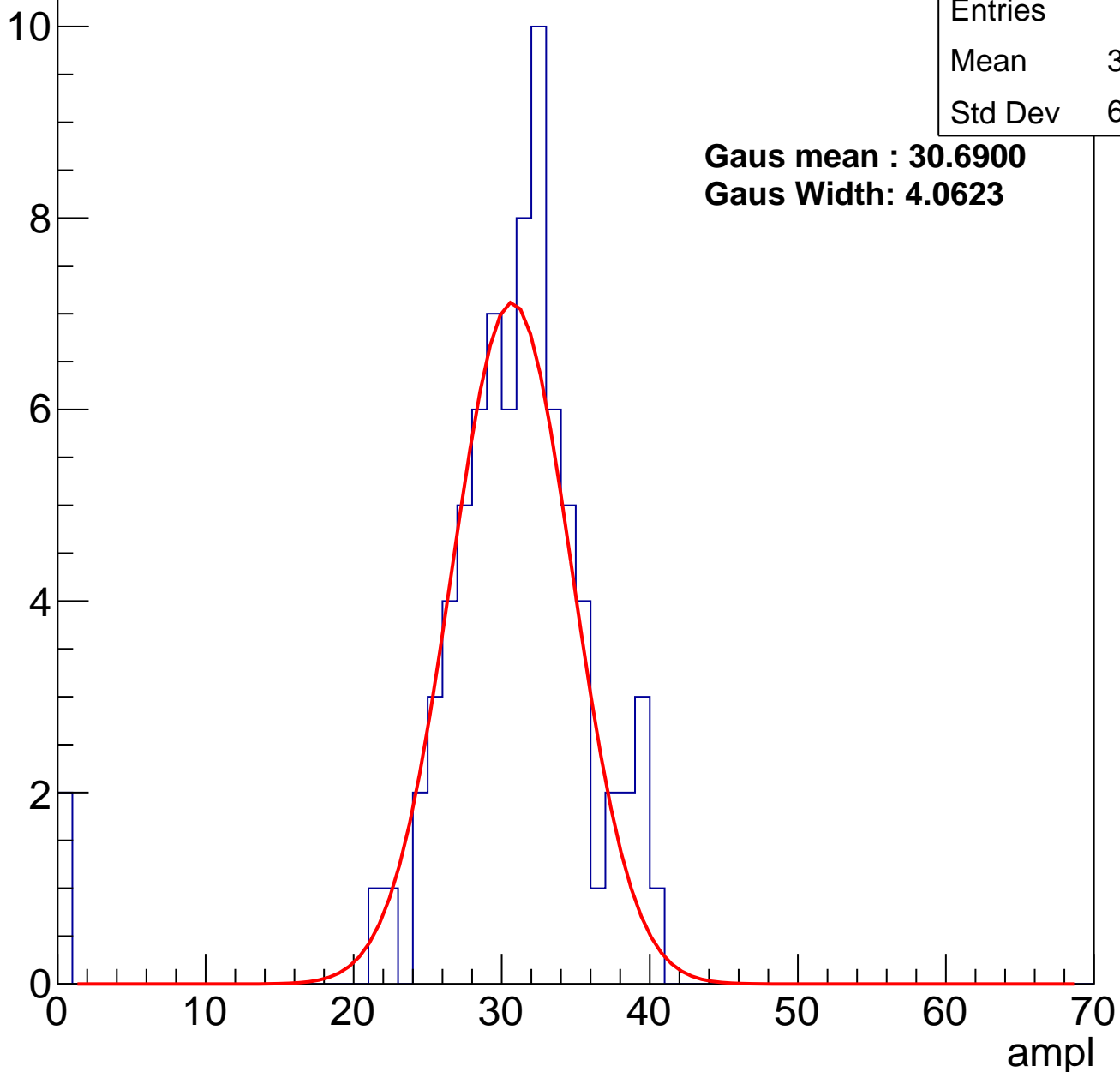
calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	30.06
Std Dev	6.283

Gaus mean : 30.6900

Gaus Width: 4.0623

Entry



B0L001S, U18-ch89, adc1

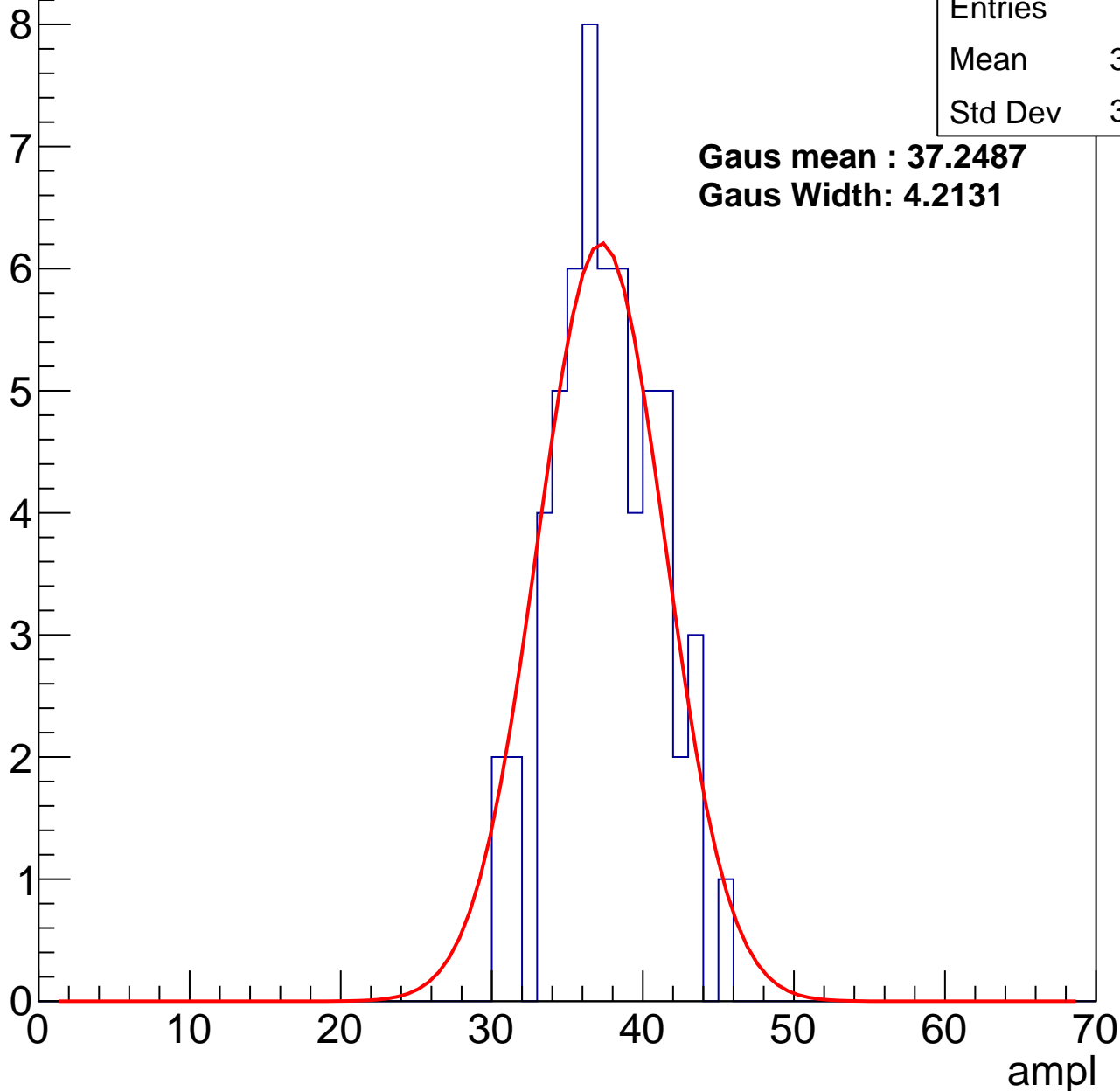
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.14
Std Dev	3.392

Gaus mean : 37.2487

Gaus Width: 4.2131



B0L001S, U18-ch89, adc2

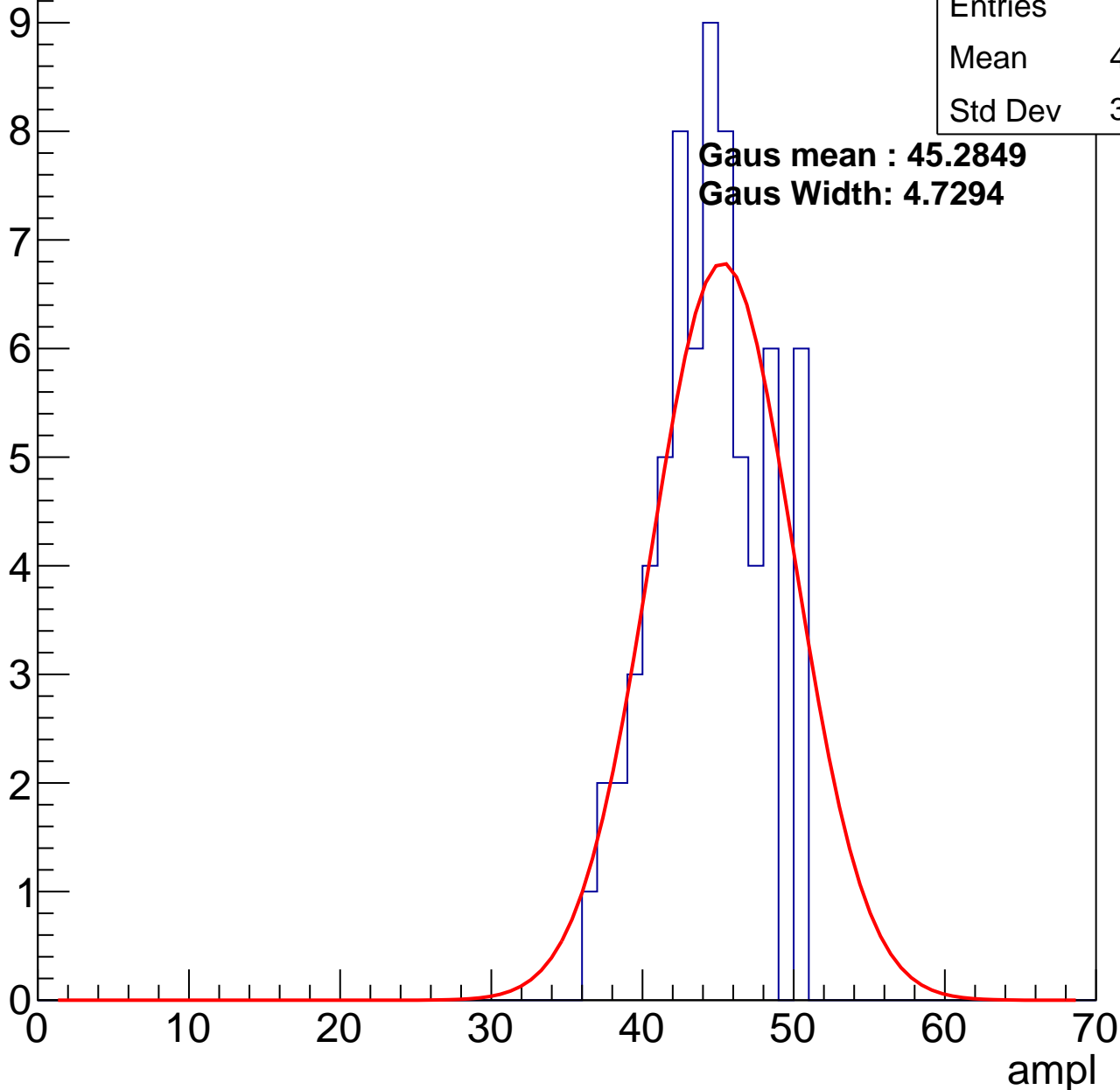
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.83
Std Dev	3.468

Gaus mean : 45.2849

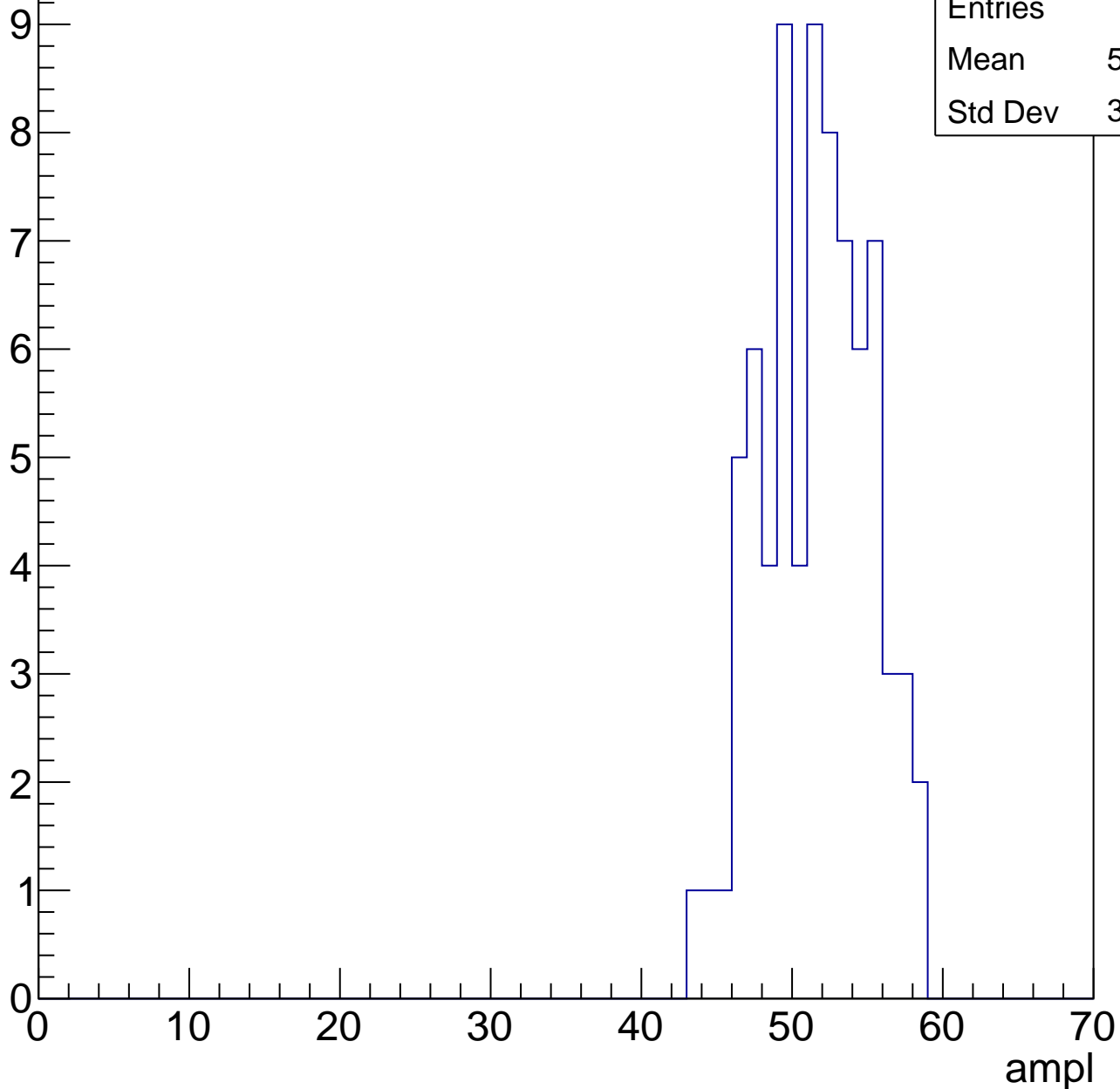
Gaus Width: 4.7294



B0L001S, U18-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

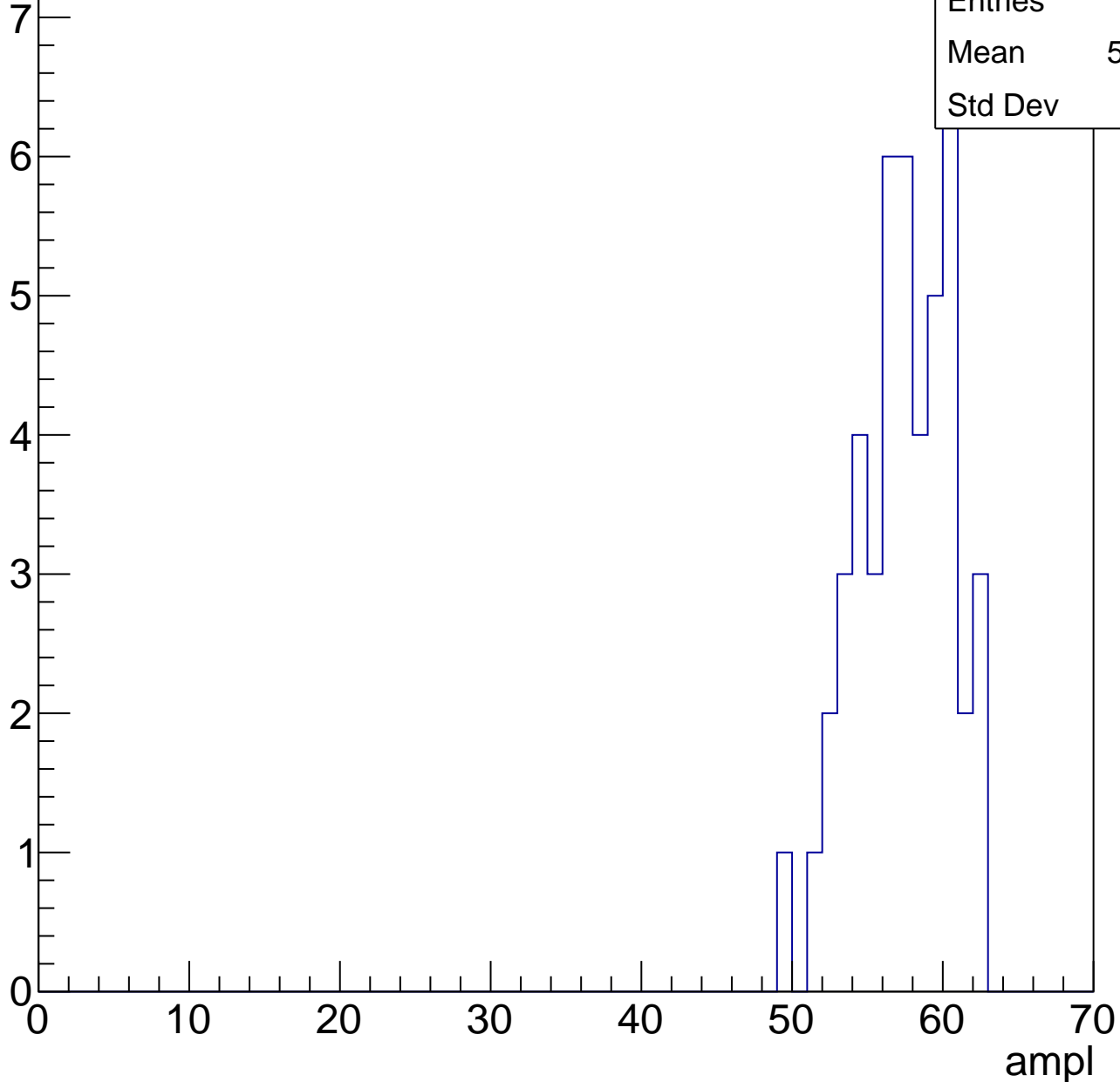
Entry



B0L001S, U18-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	38
Mean	59.16
Std Dev	9.917

Entry

10

8

6

4

2

0

0

10

20

30

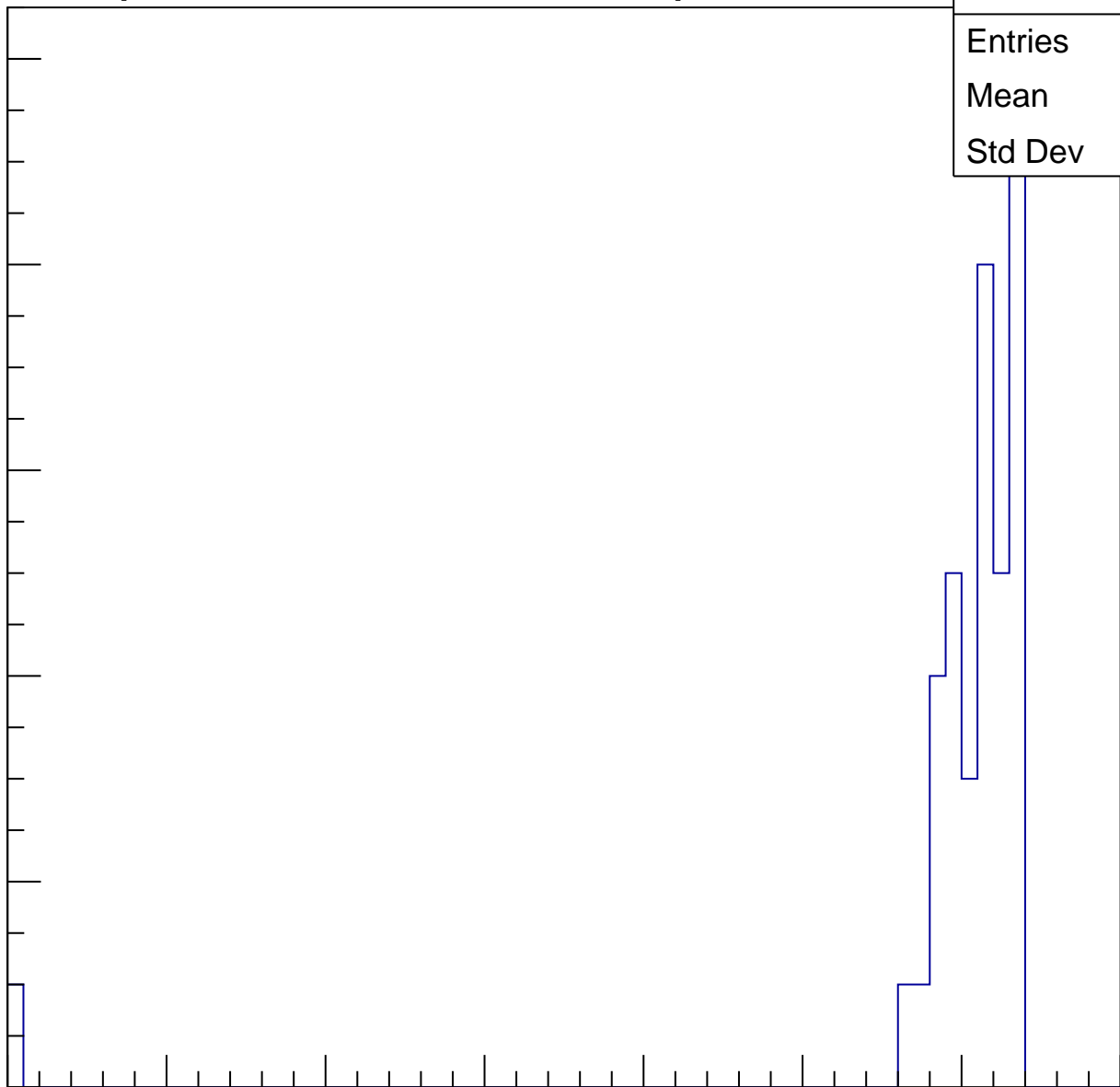
40

50

60

70

ampl



B0L001S, U18-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch90, adc0

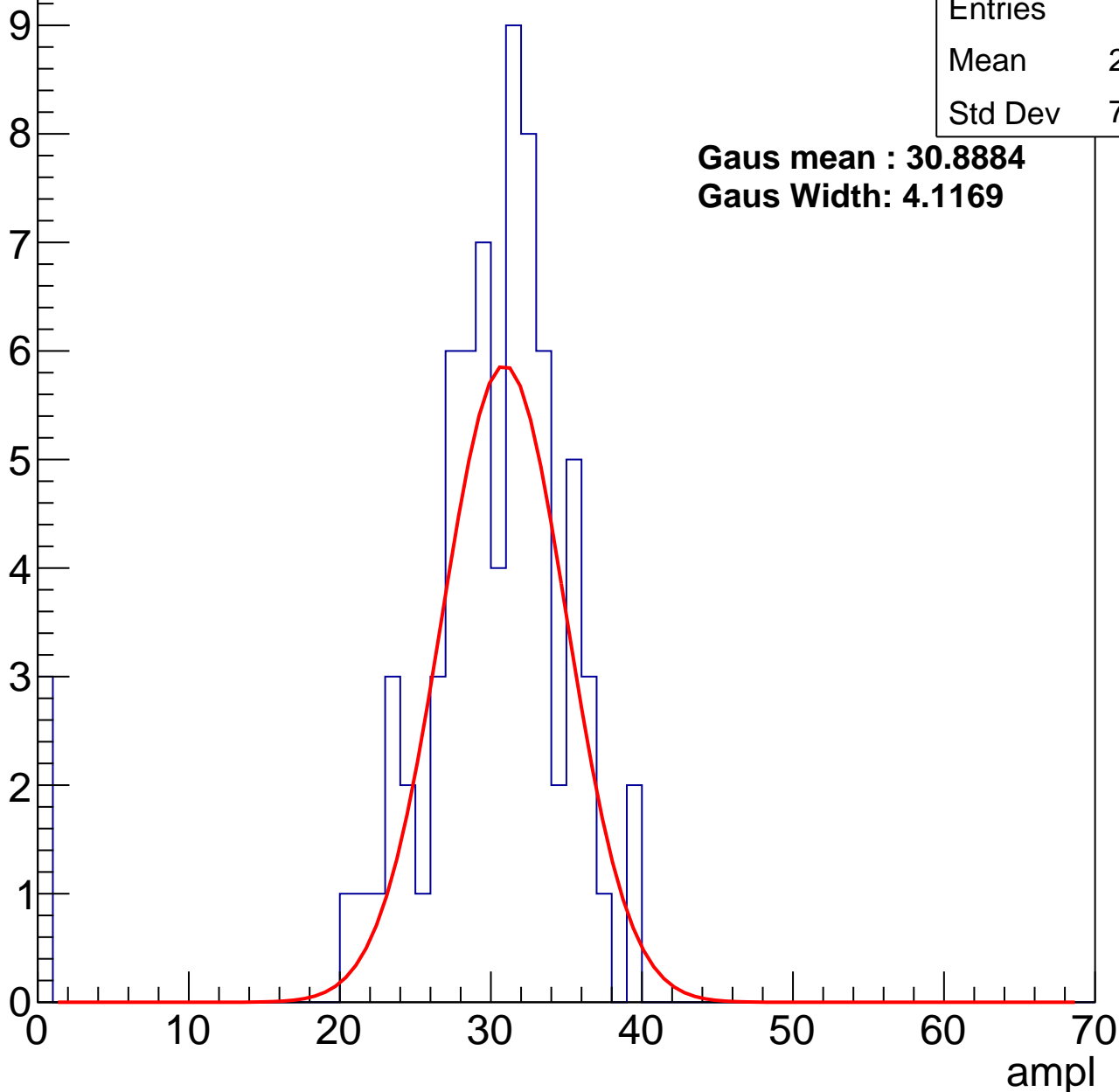
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	28.85
Std Dev	7.162

Gaus mean : 30.8884

Gaus Width: 4.1169



B0L001S, U18-ch90, adc1

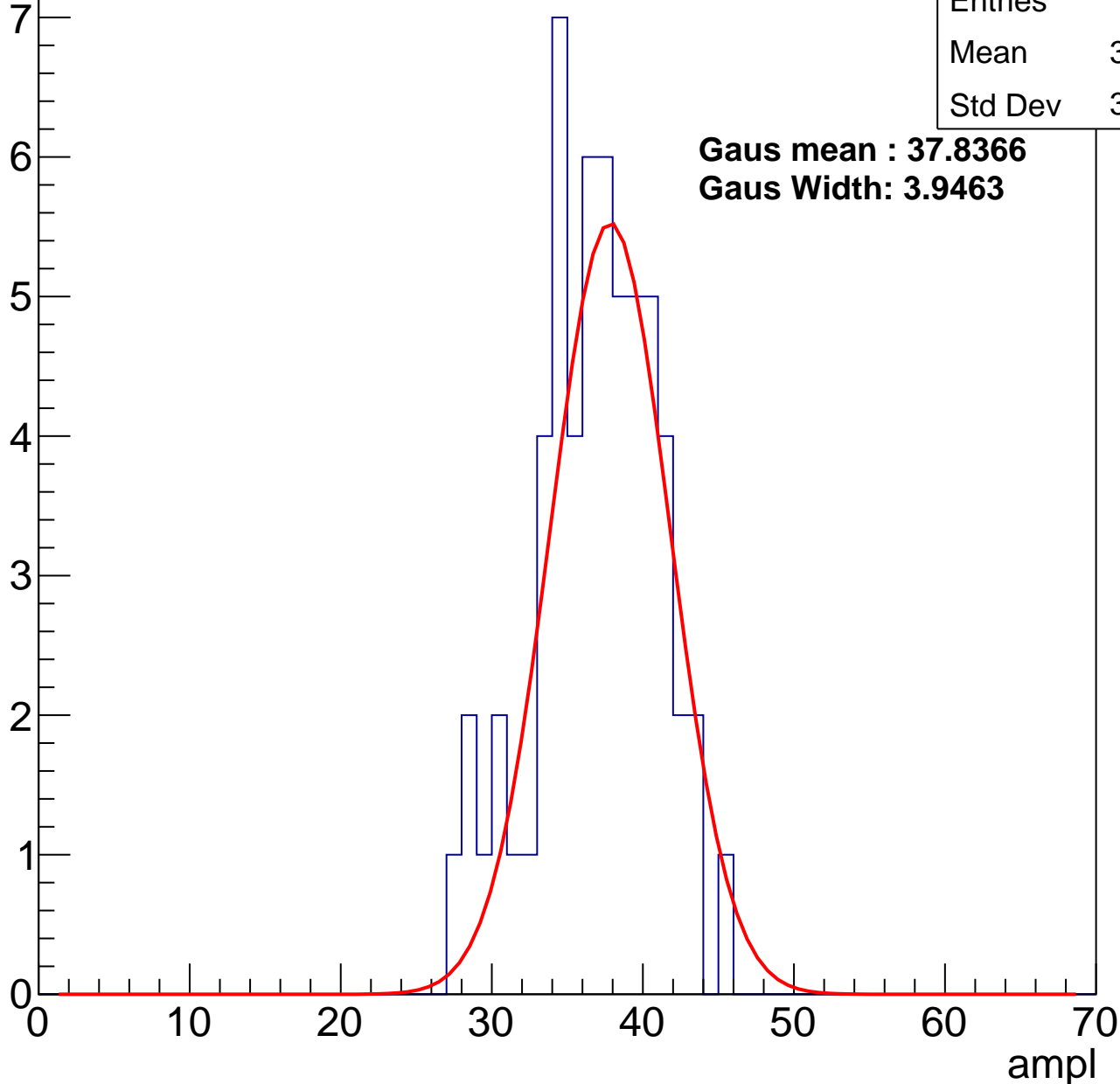
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	36.39
Std Dev	3.975

Gaus mean : 37.8366

Gaus Width: 3.9463



B0L001S, U18-ch90, adc2

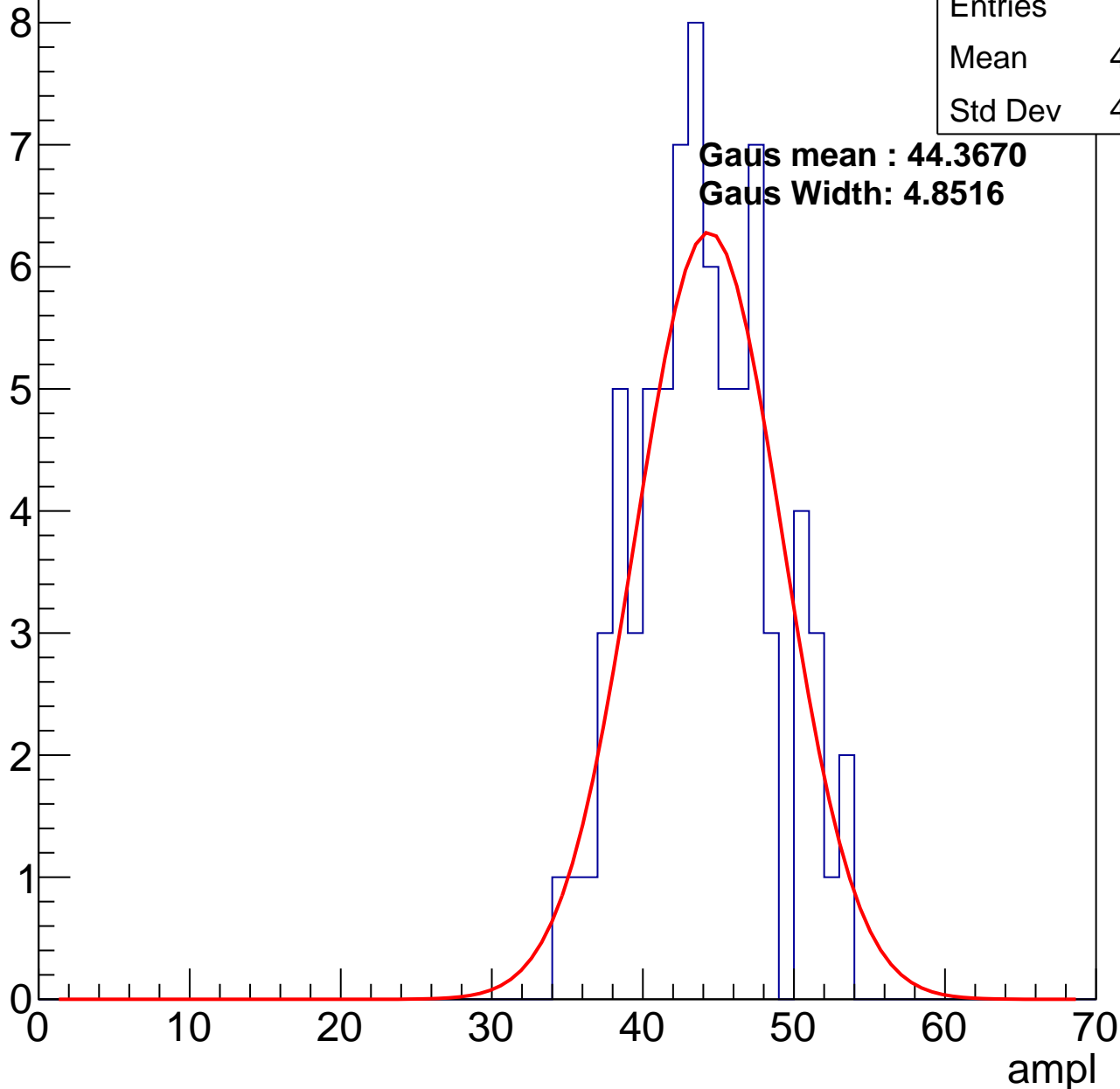
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.59
Std Dev	4.394

Gaus mean : 44.3670

Gaus Width: 4.8516

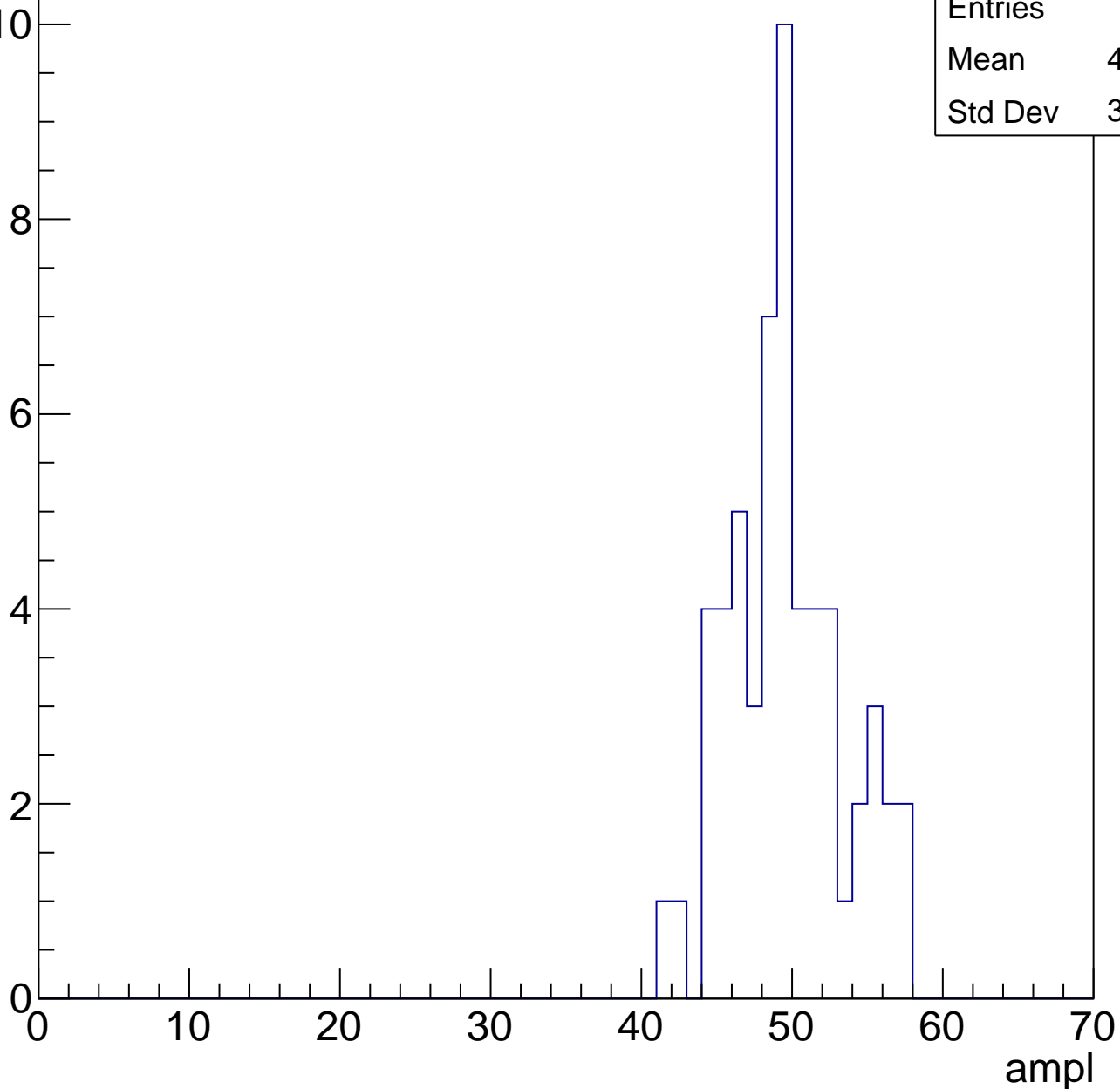


B0L001S, U18-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	49.12
Std Dev	3.737



B0L001S, U18-ch90, adc4

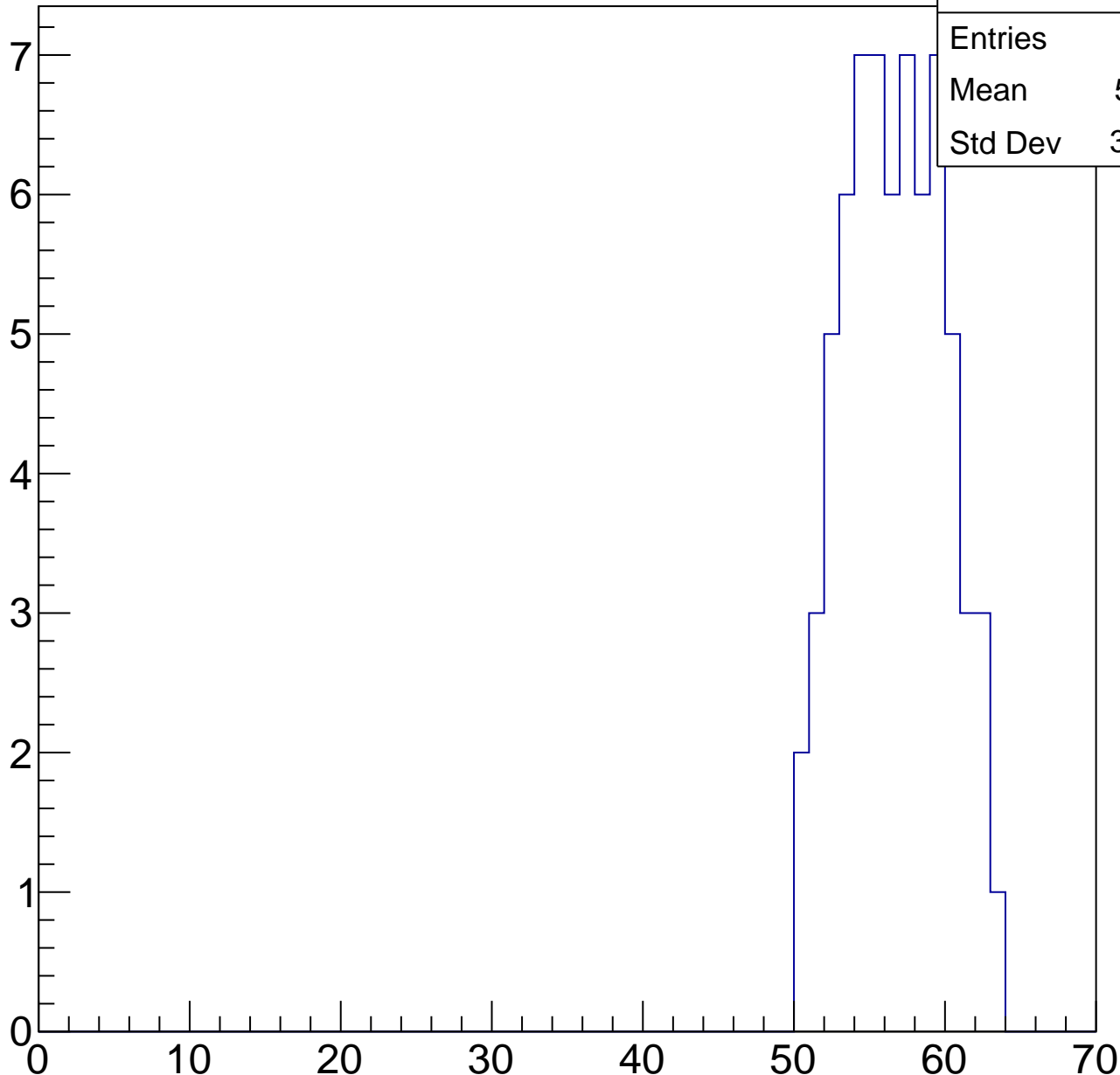
calib_packv5_042523_0143.root, FC#9, port A1

Entry

7
6
5
4
3
2
1
0

Entries	68
Mean	56.21
Std Dev	3.252

ampl

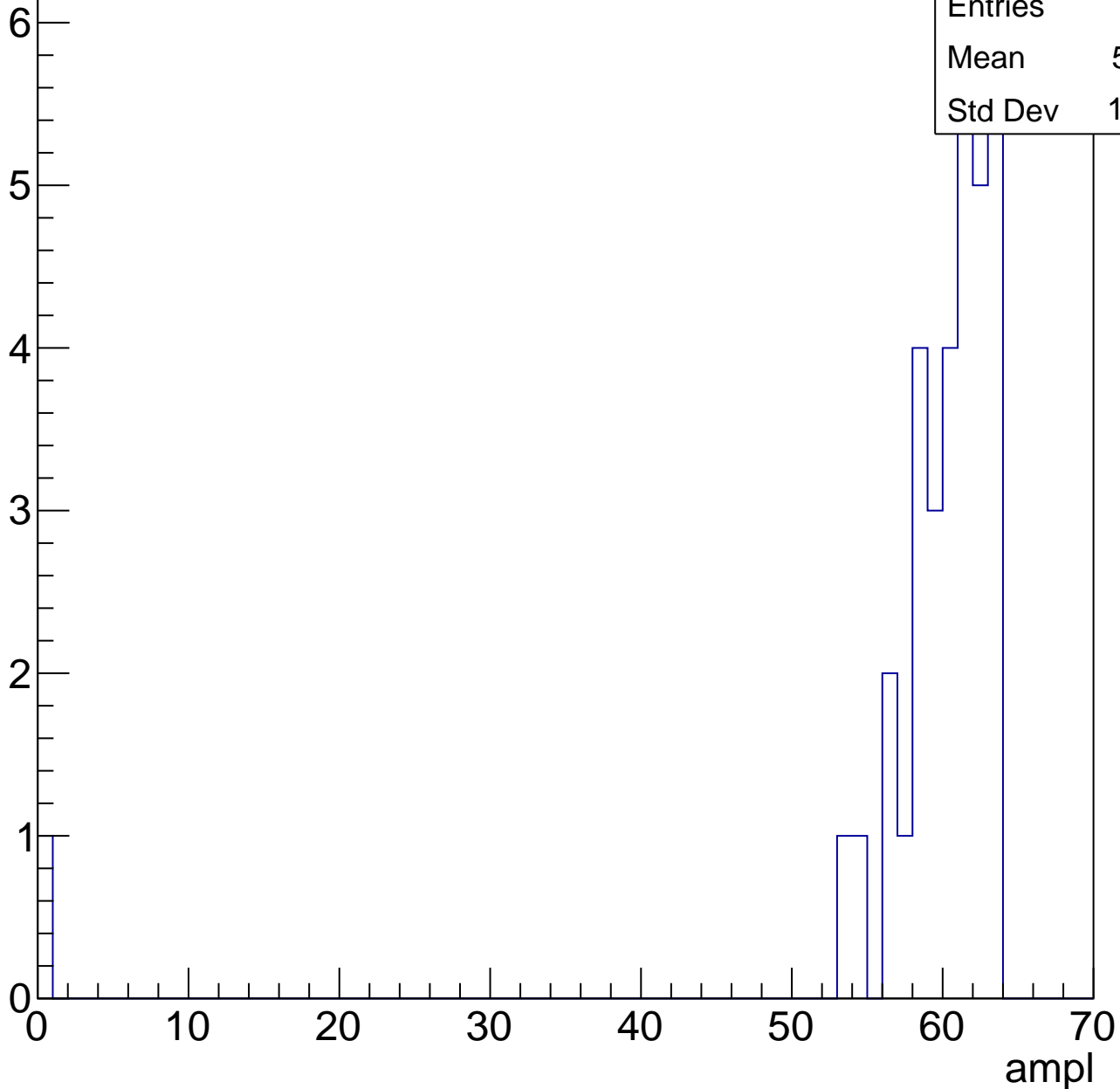


B0L001S, U18-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.21
Std Dev	10.45



B0L001S, U18-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch91, adc0

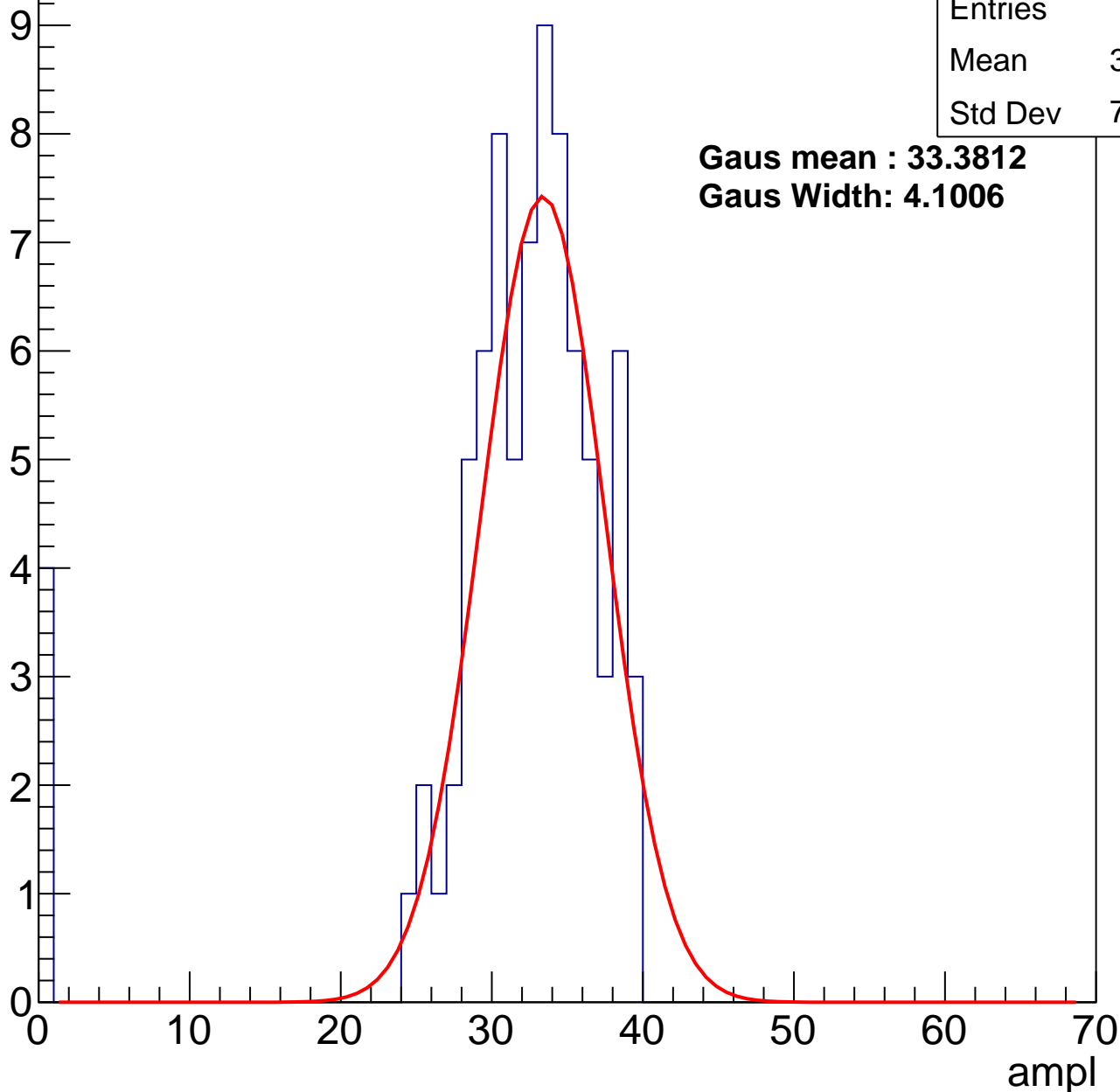
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.89
Std Dev	7.879

Gaus mean : 33.3812

Gaus Width: 4.1006



B0L001S, U18-ch91, adc1

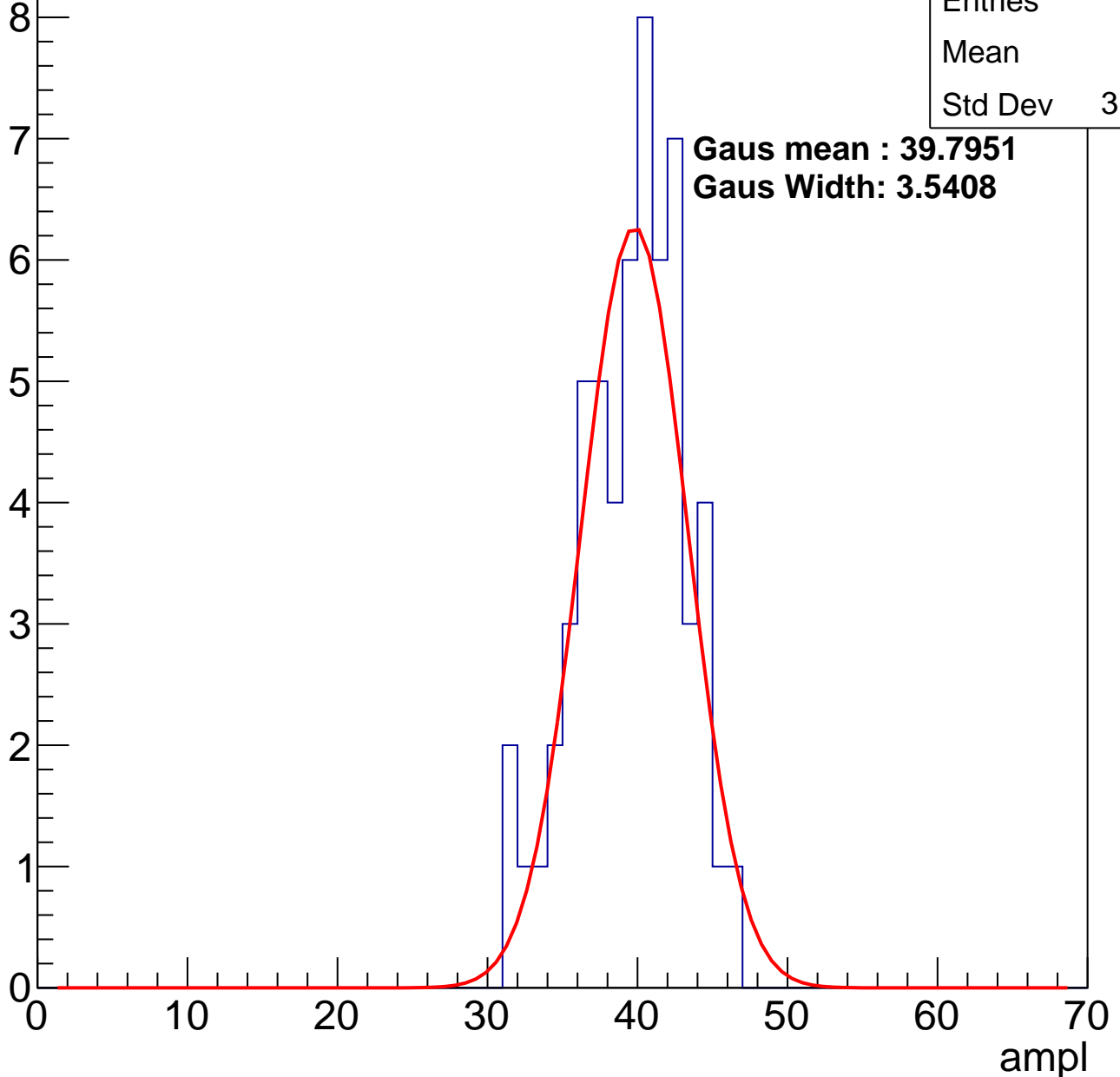
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	39.1
Std Dev	3.463

Gaus mean : 39.7951

Gaus Width: 3.5408



B0L001S, U18-ch91, adc2

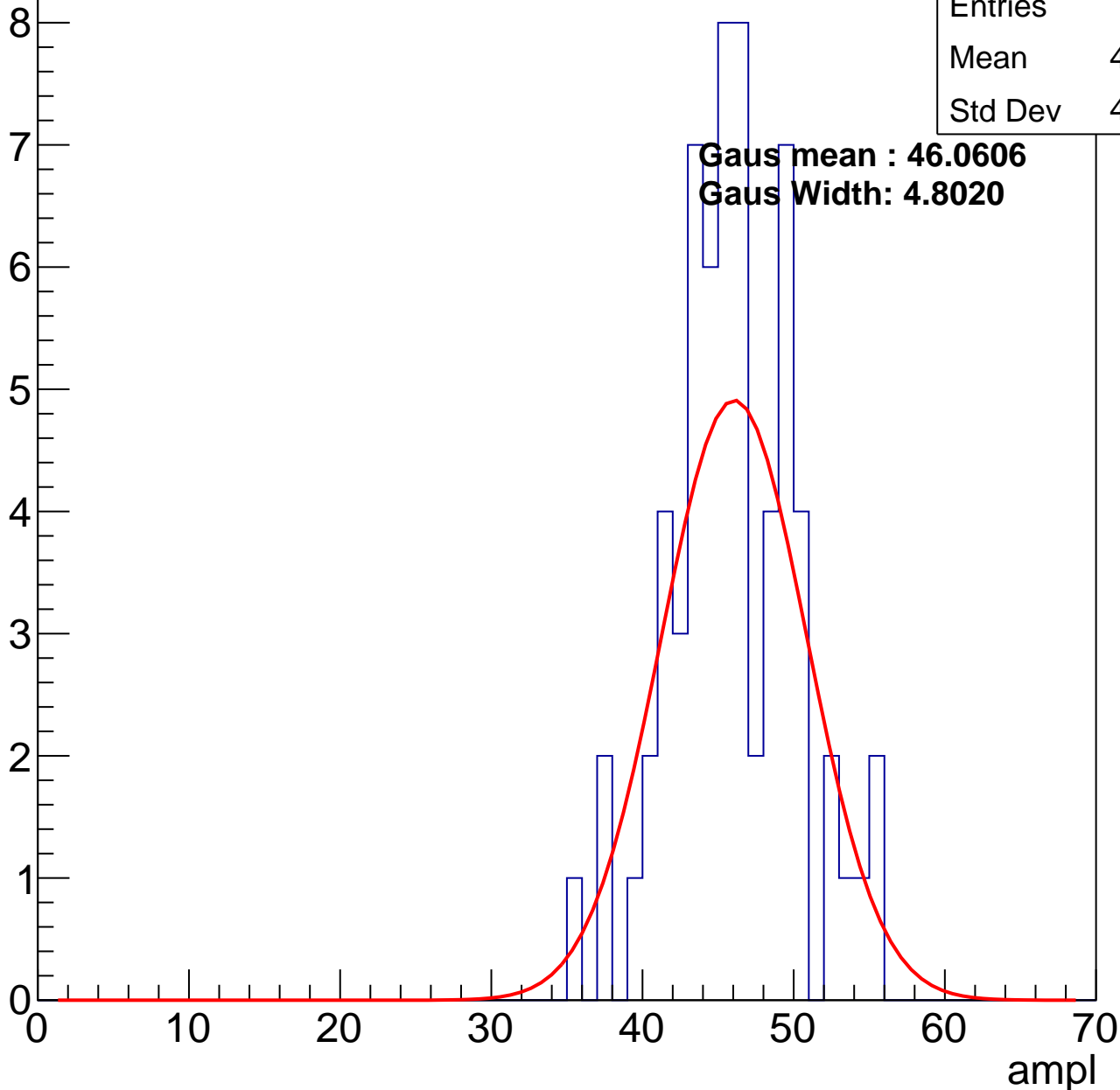
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.55
Std Dev	4.162

Gaus mean : 46.0606

Gaus Width: 4.8020

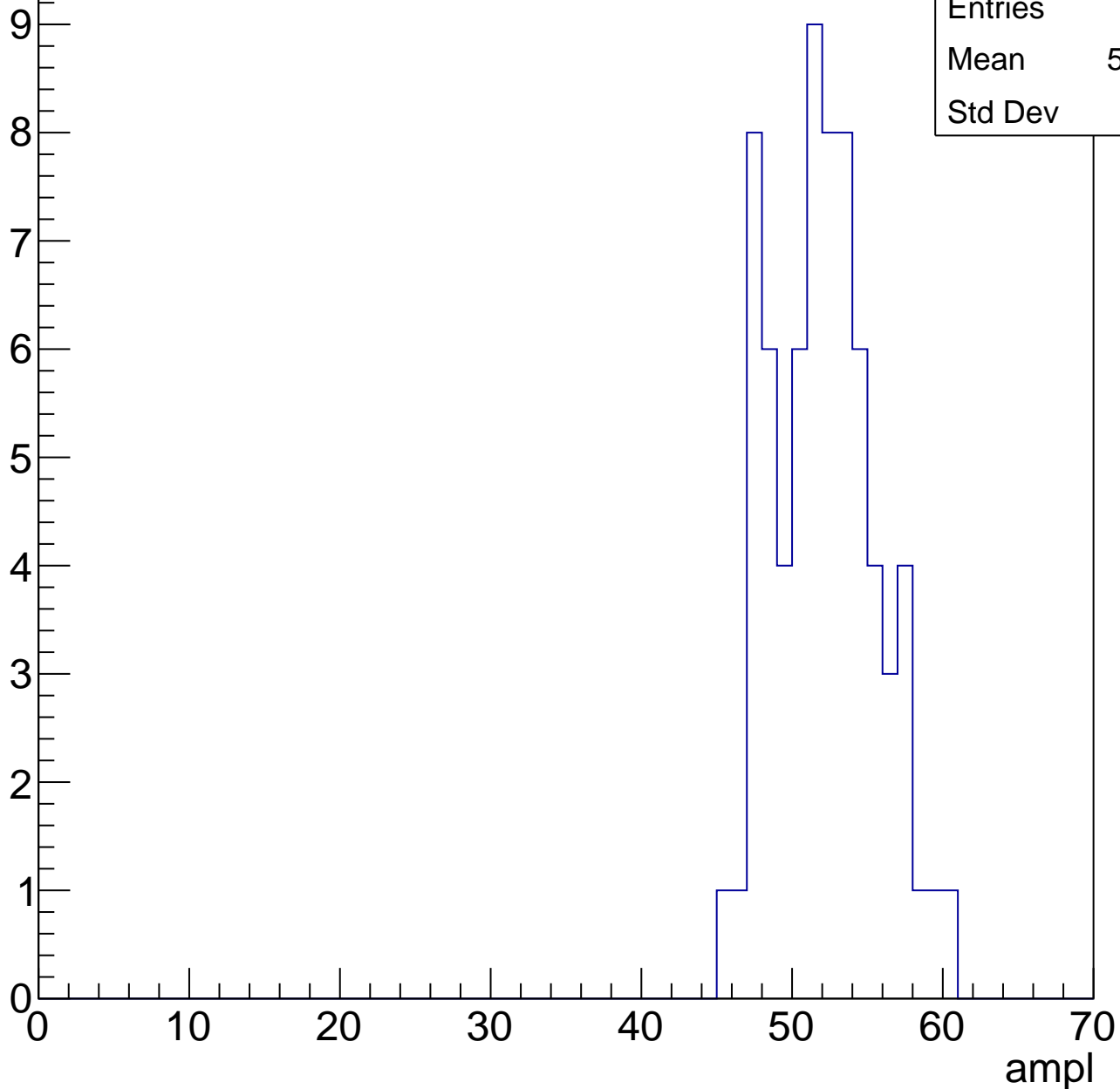


B0L001S, U18-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.65
Std Dev	3.37

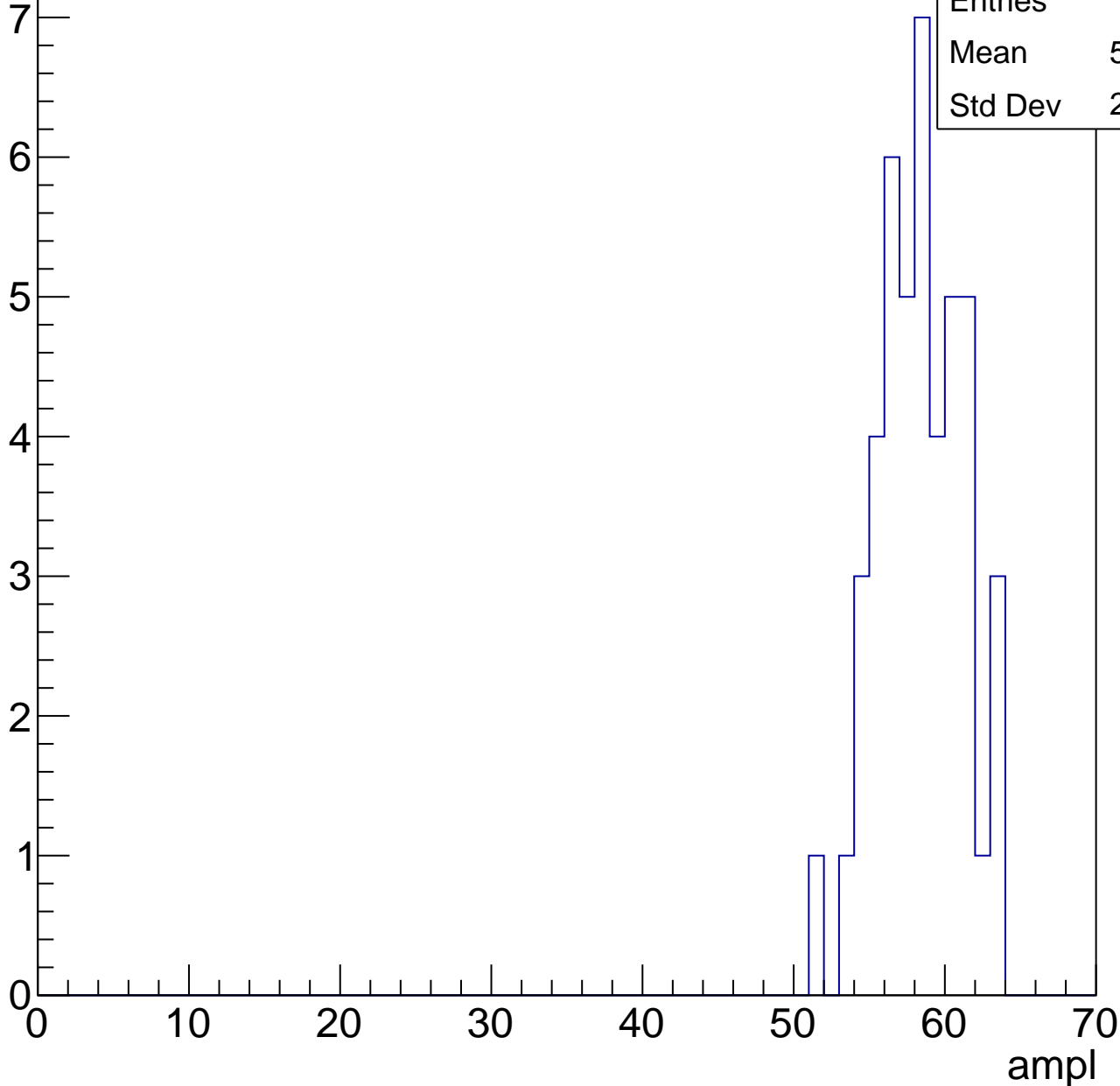


B0L001S, U18-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.89
Std Dev	2.775

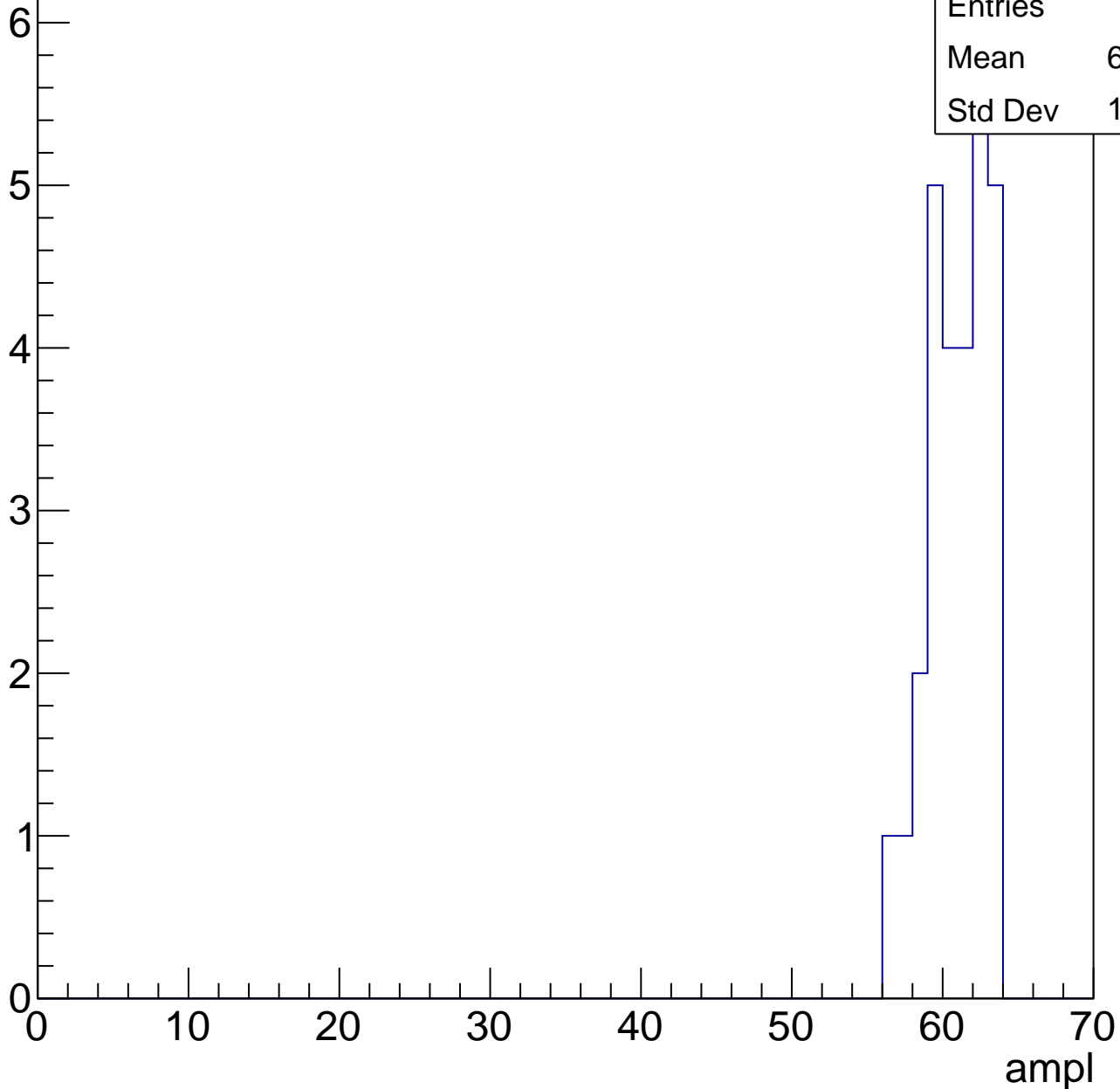


B0L001S, U18-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

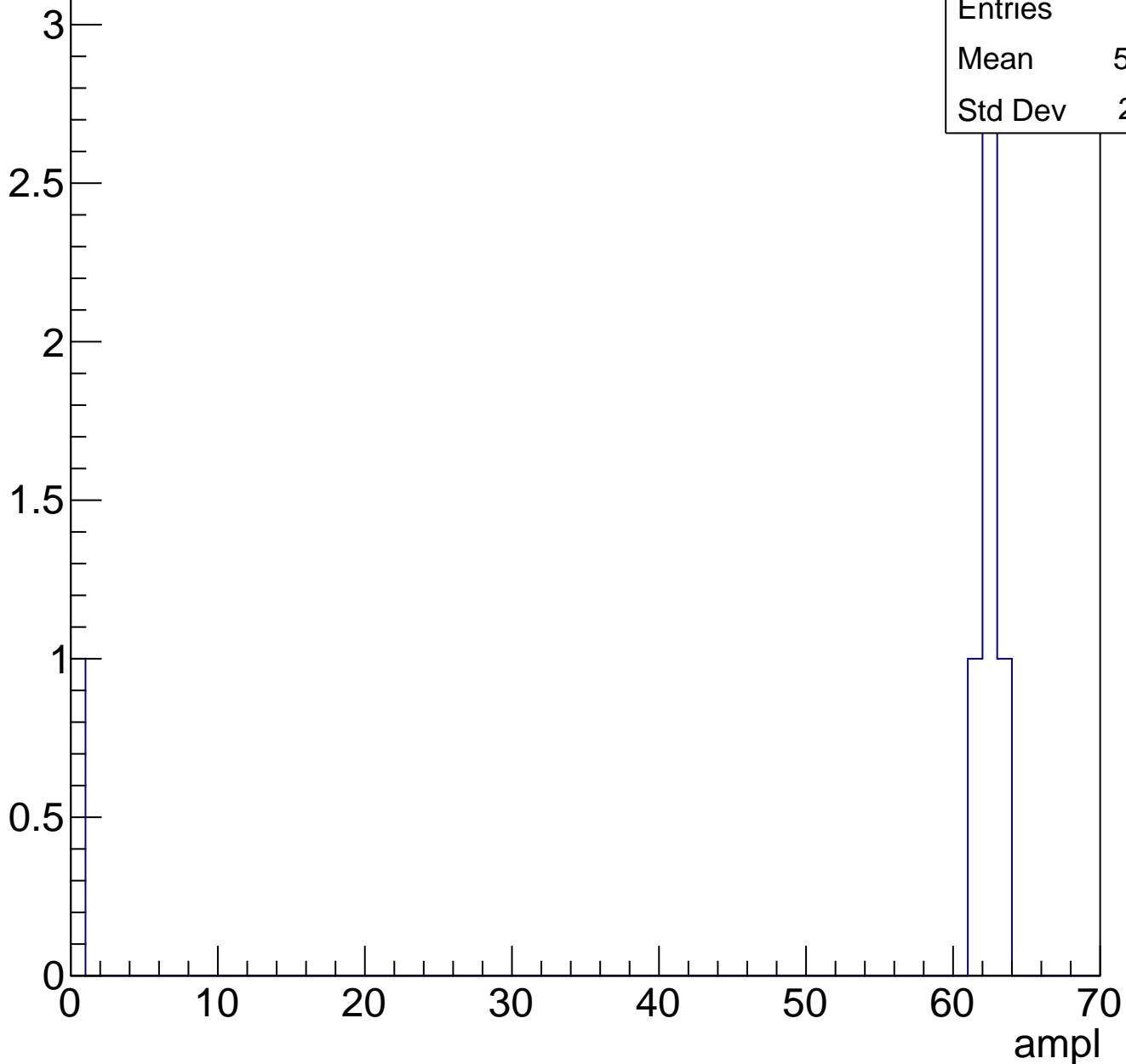
Entries	28
Mean	60.54
Std Dev	1.918



B0L001S, U18-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch92, adc0

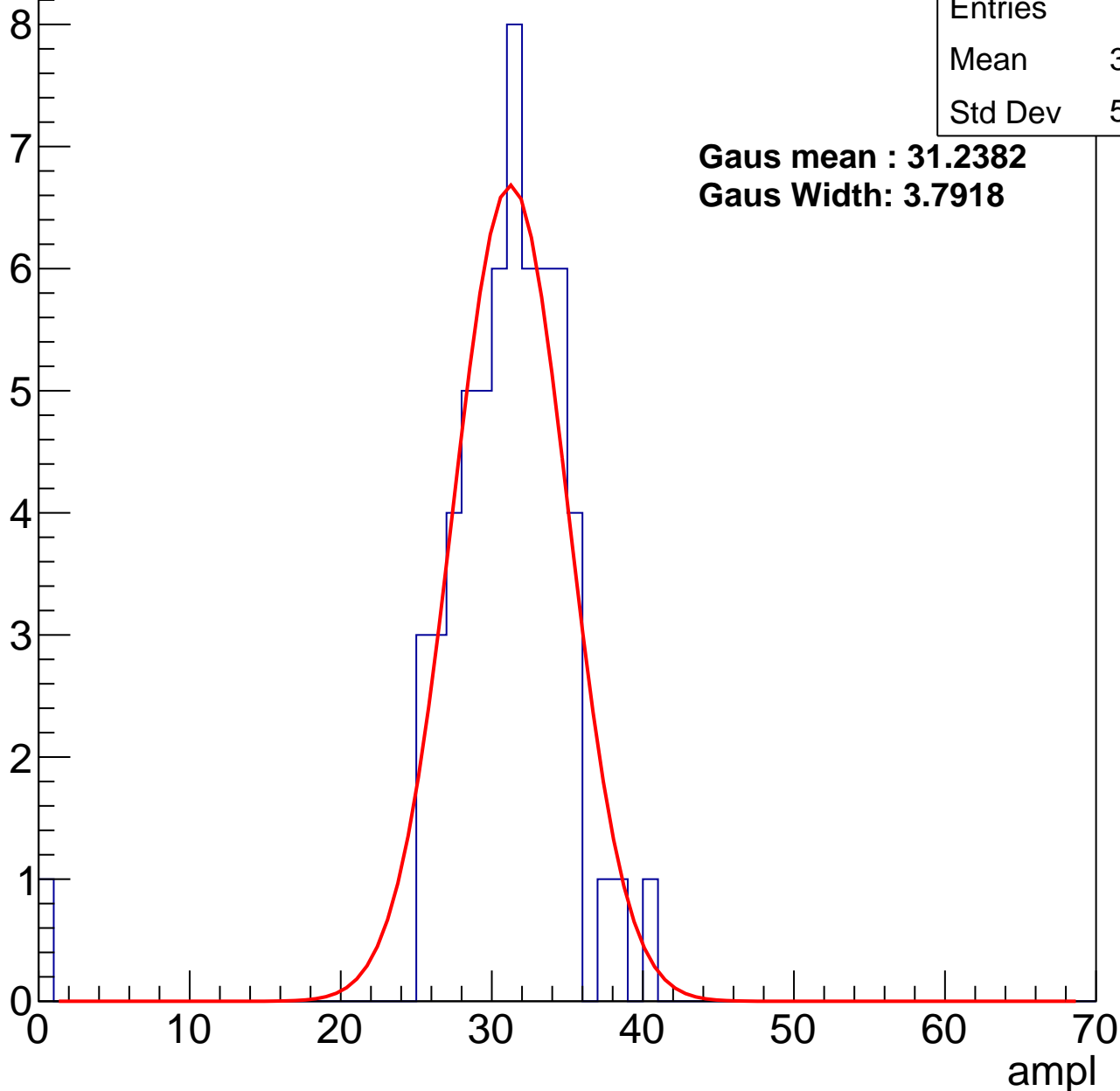
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.38
Std Dev	5.116

Gaus mean : 31.2382

Gaus Width: 3.7918



B0L001S, U18-ch92, adc1

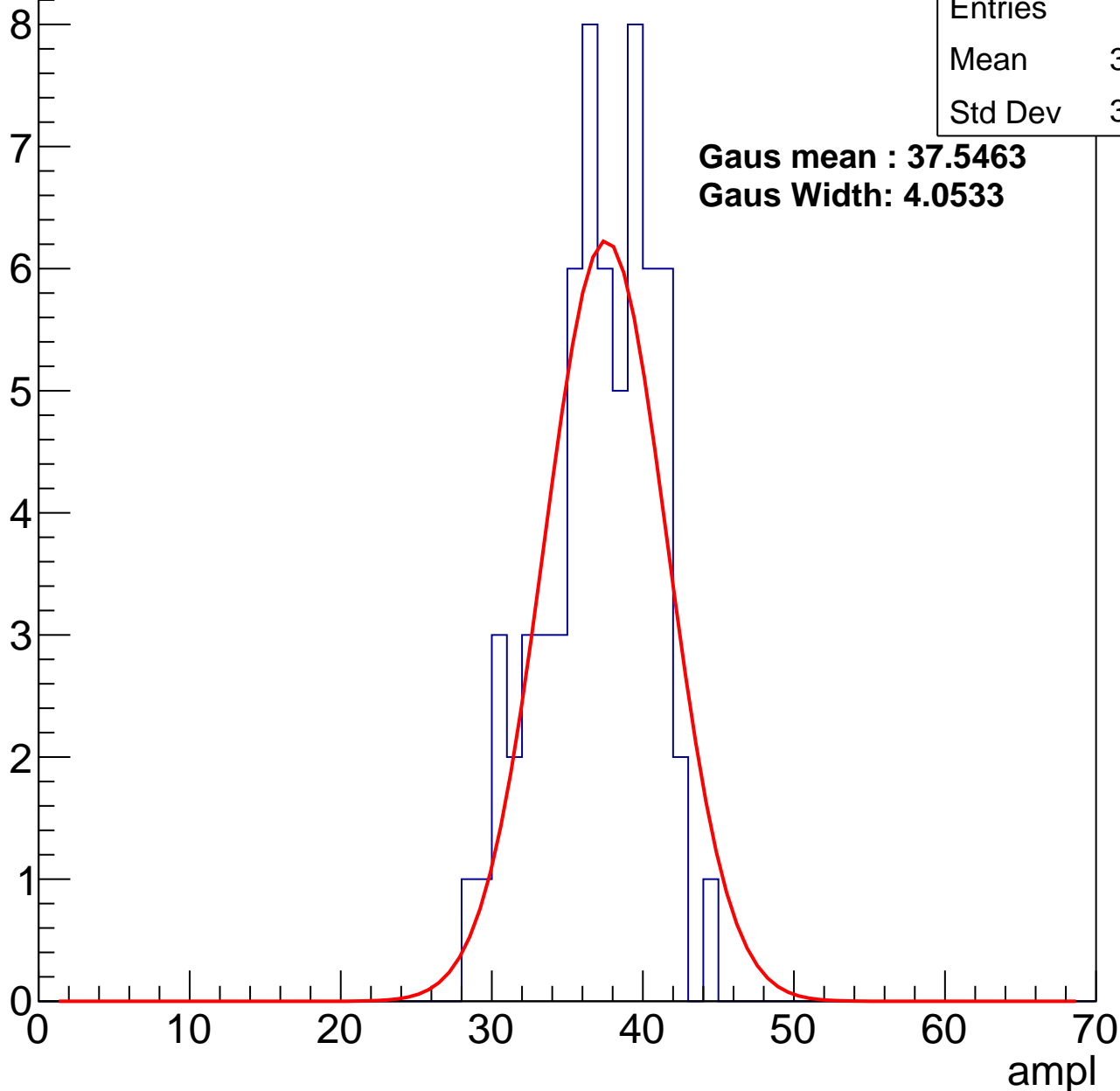
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	36.59
Std Dev	3.583

Gaus mean : 37.5463

Gaus Width: 4.0533



B0L001S, U18-ch92, adc2

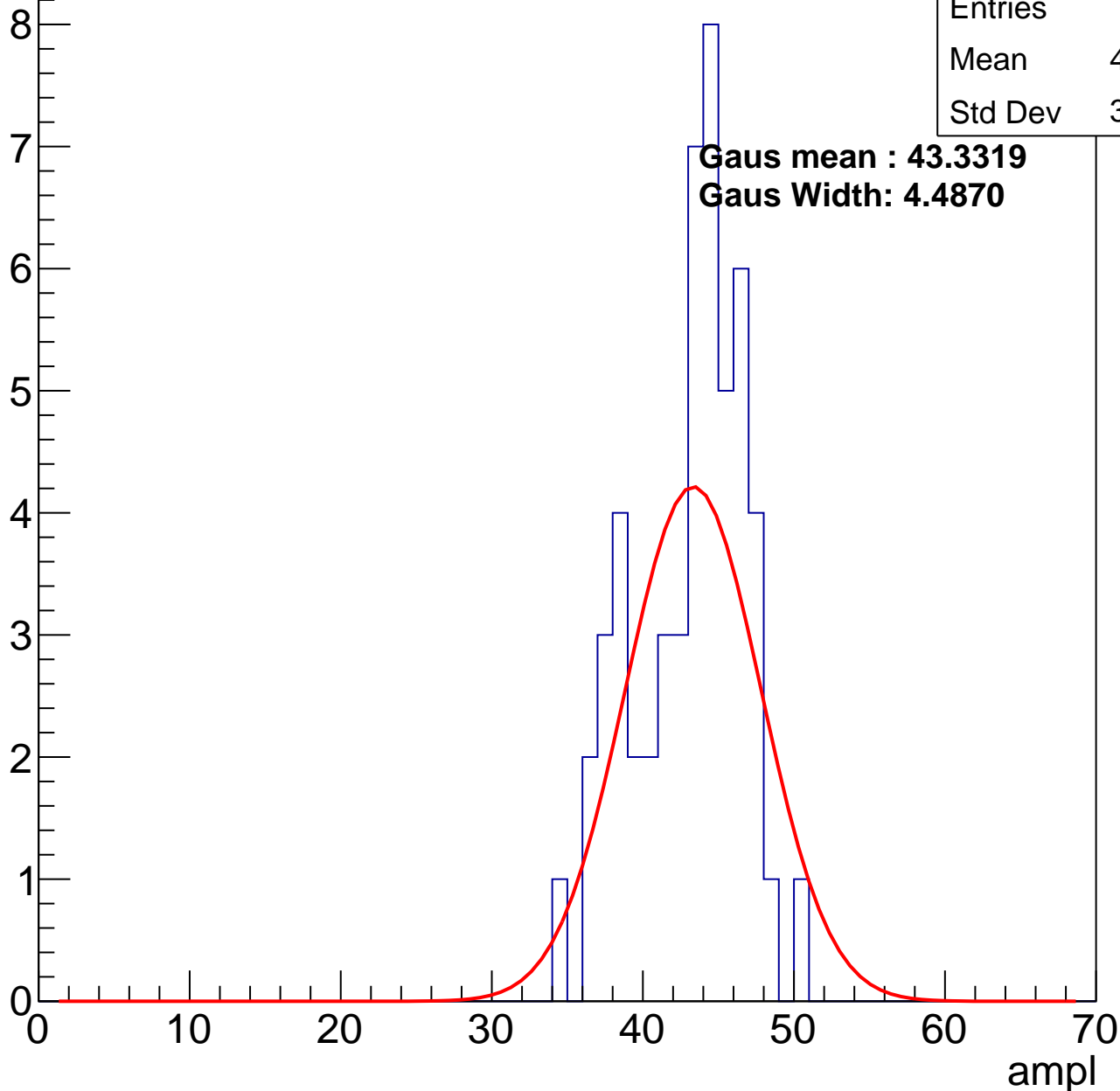
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	42.62
Std Dev	3.574

Gaus mean : 43.3319

Gaus Width: 4.4870

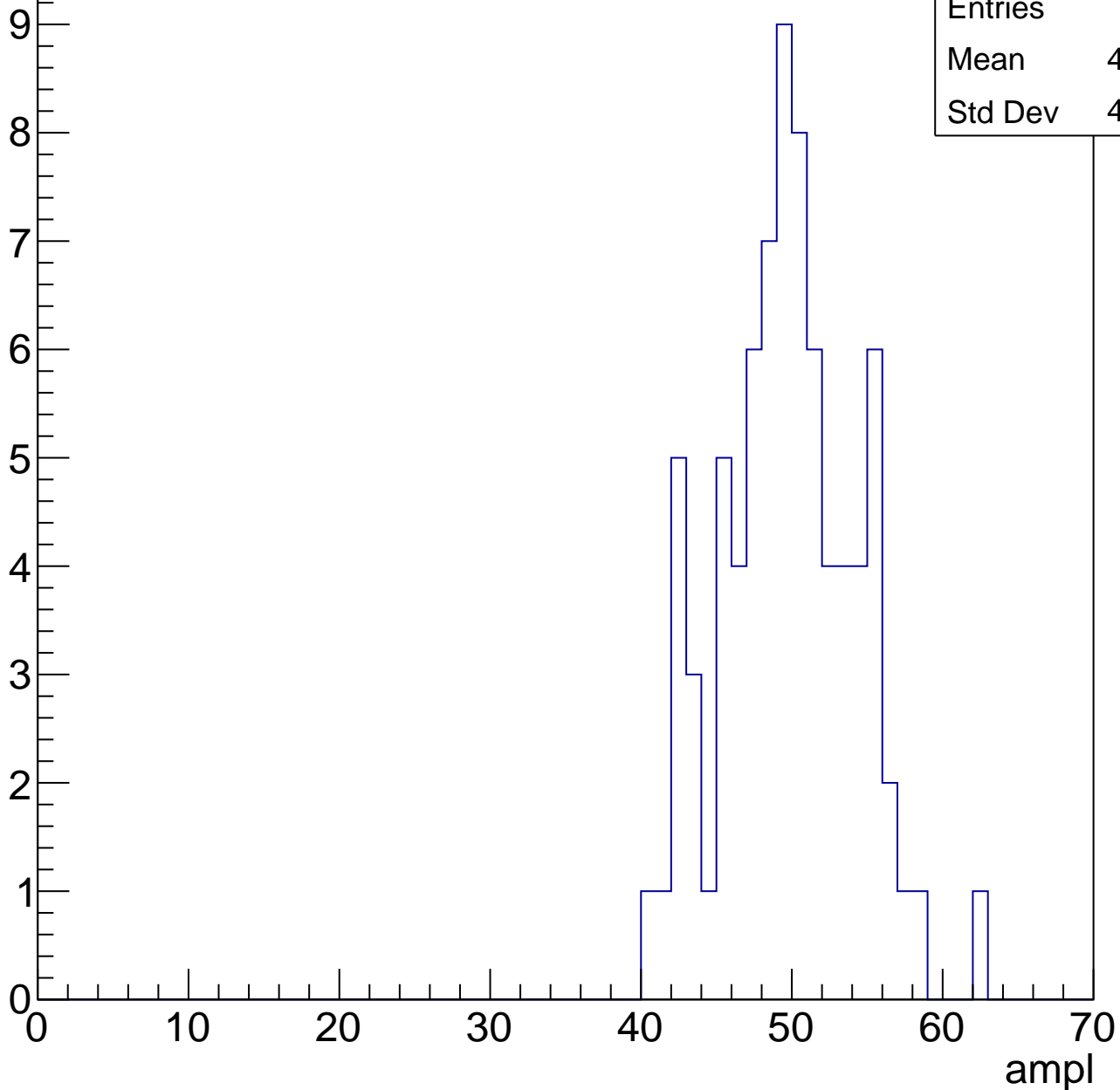


B0L001S, U18-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	49.28
Std Dev	4.392

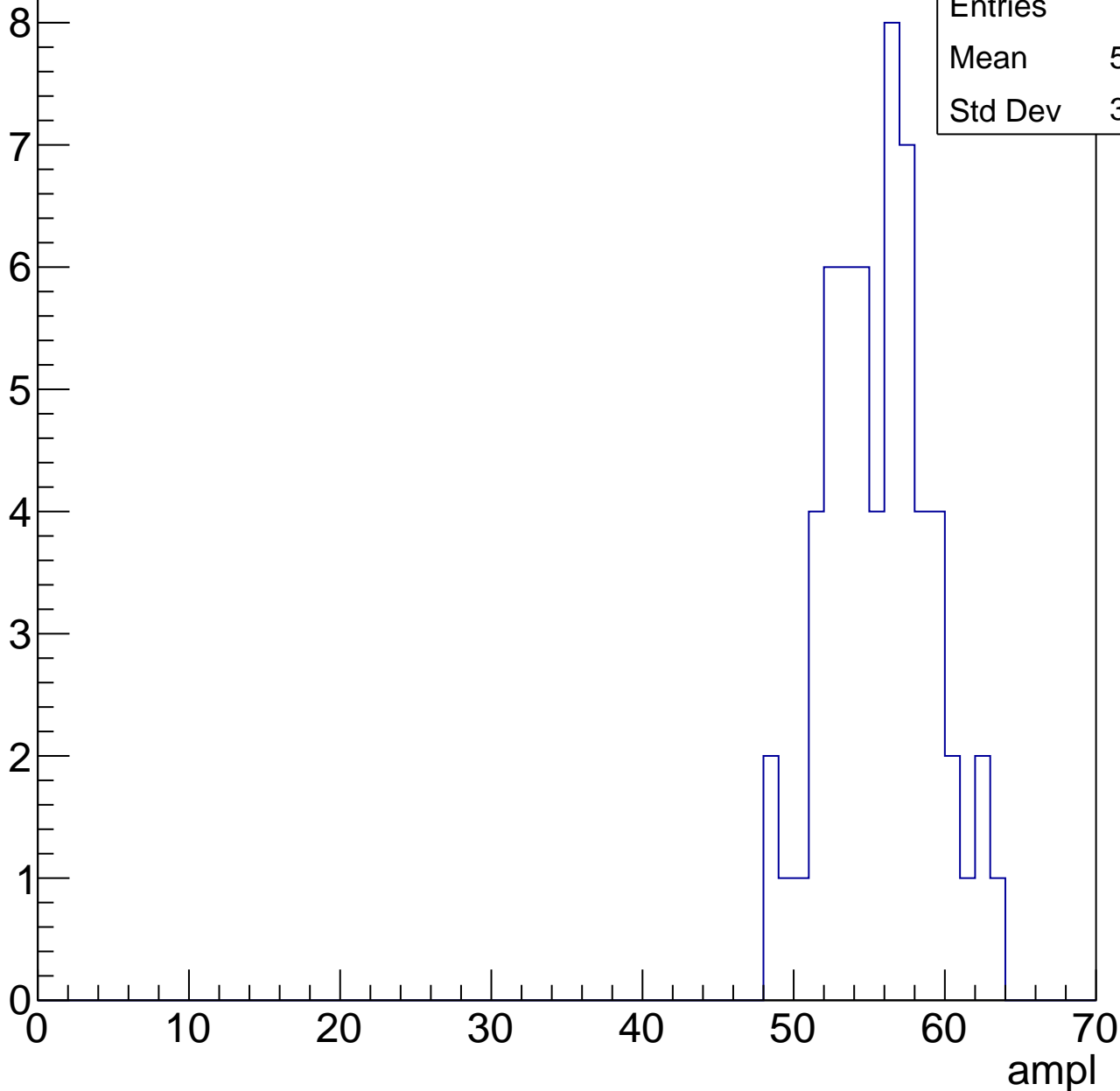


B0L001S, U18-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.19
Std Dev	3.417

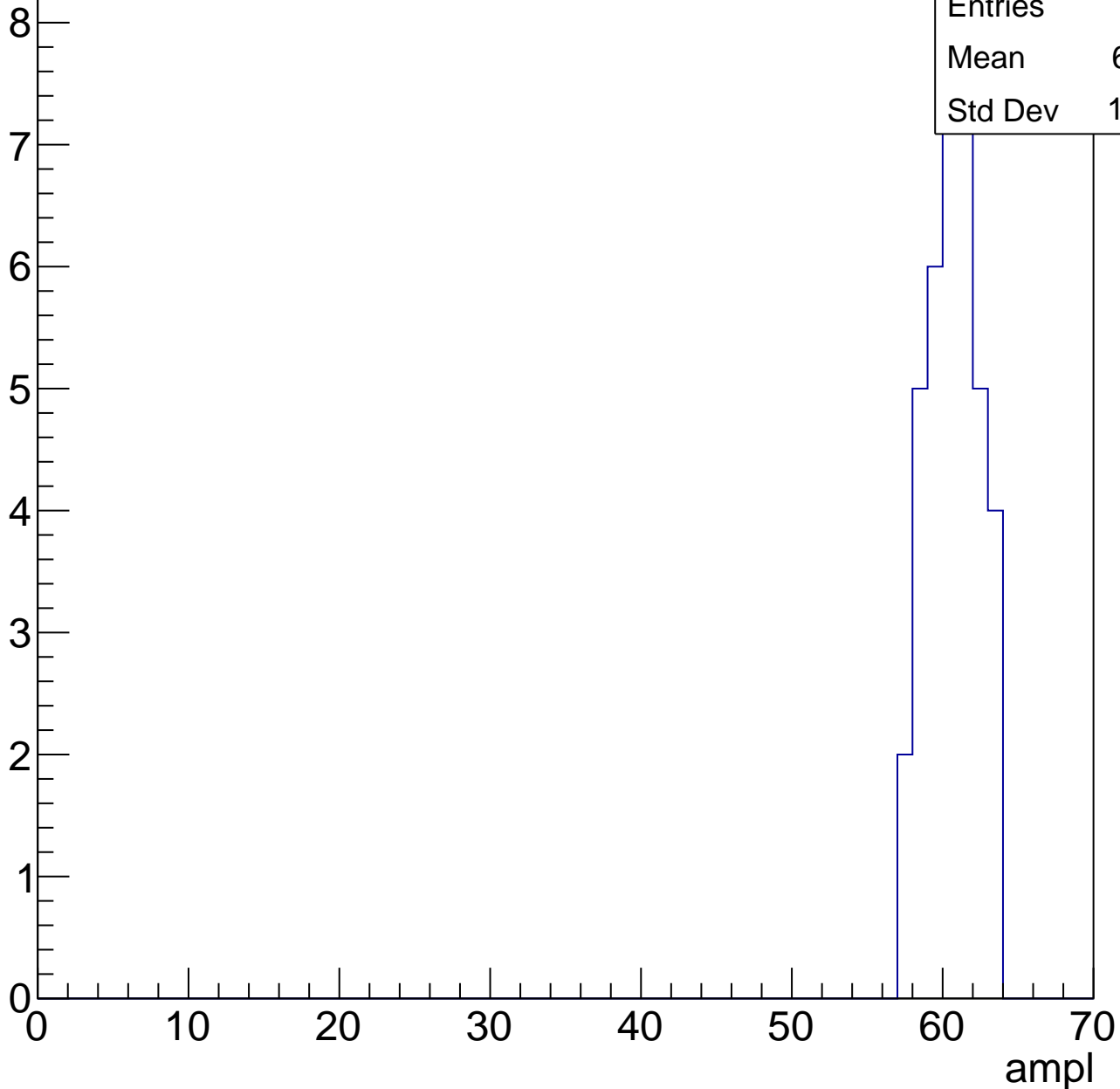


B0L001S, U18-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	60.21
Std Dev	1.673

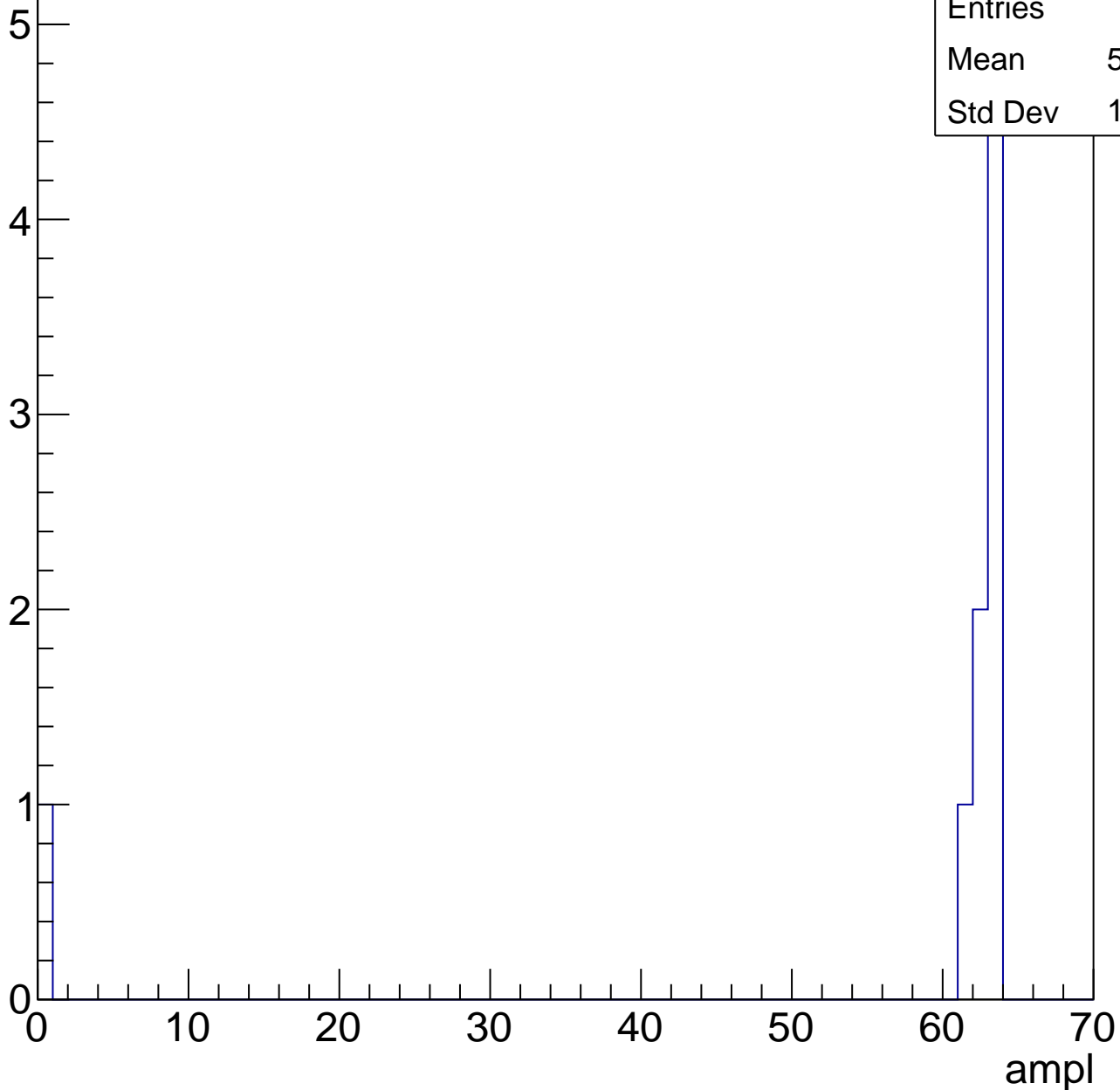


B0L001S, U18-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

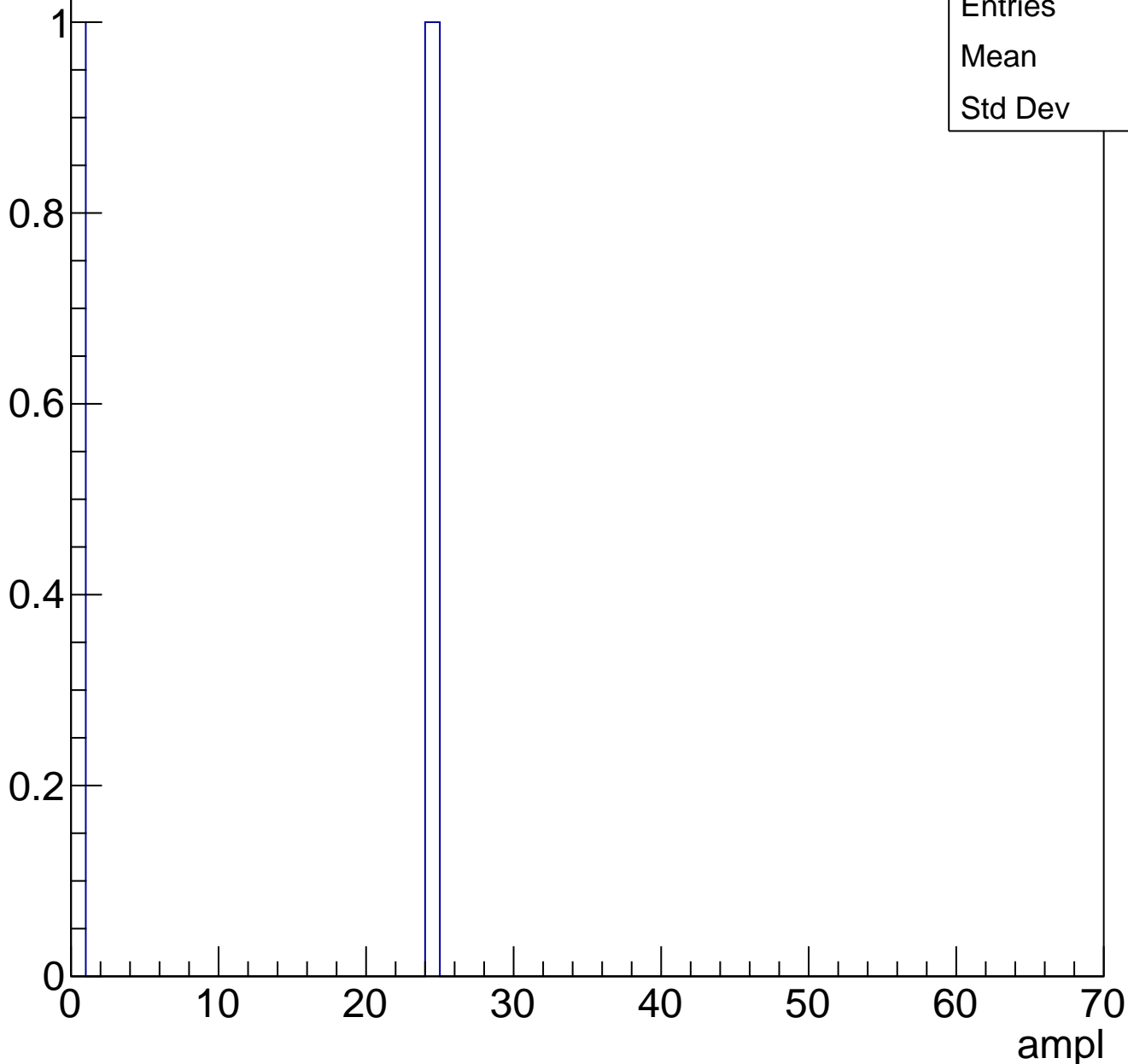
Entries	9
Mean	55.56
Std Dev	19.65



B0L001S, U18-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch93, adc0

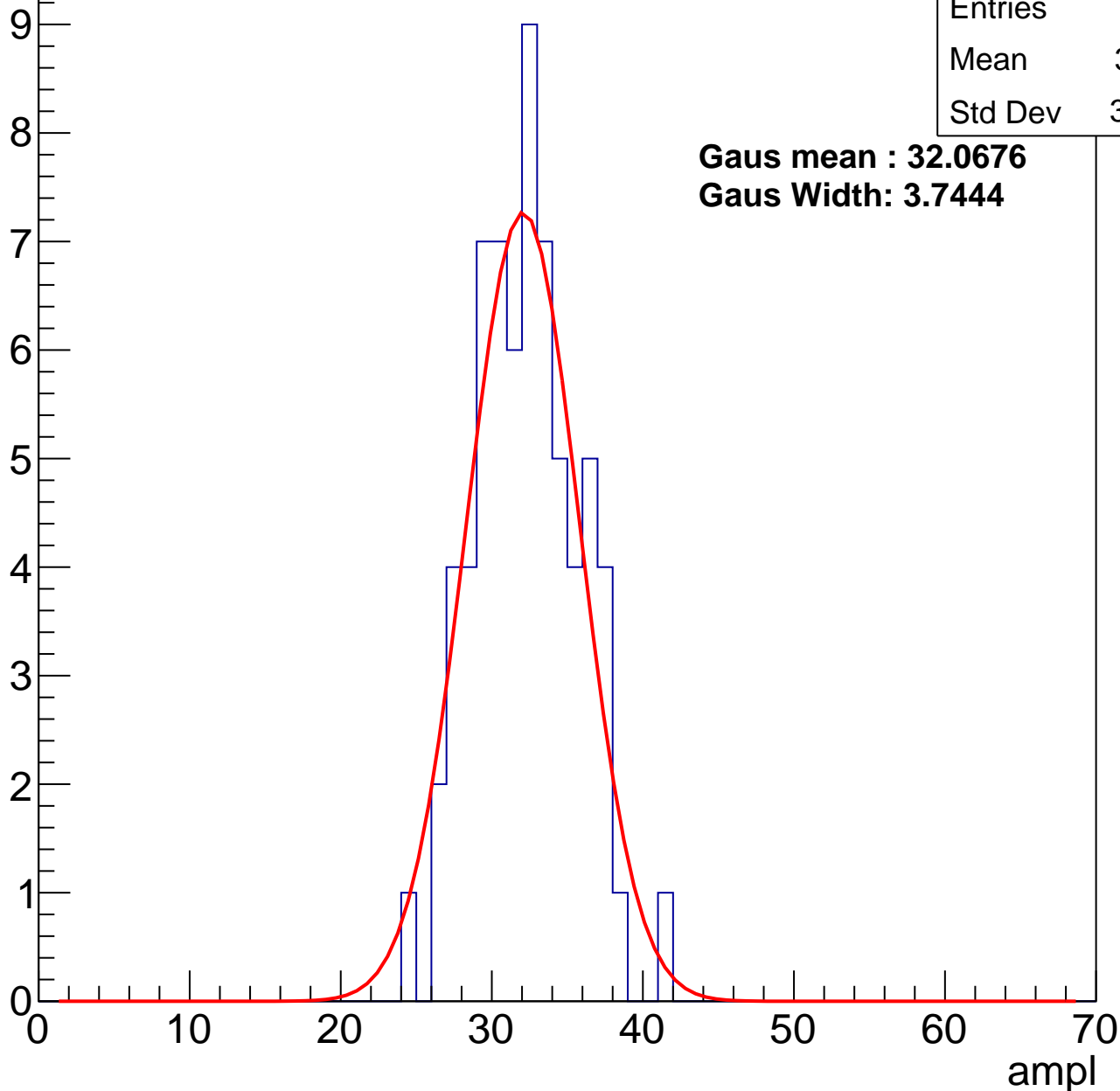
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	31.81
Std Dev	3.356

Gaus mean : 32.0676

Gaus Width: 3.7444

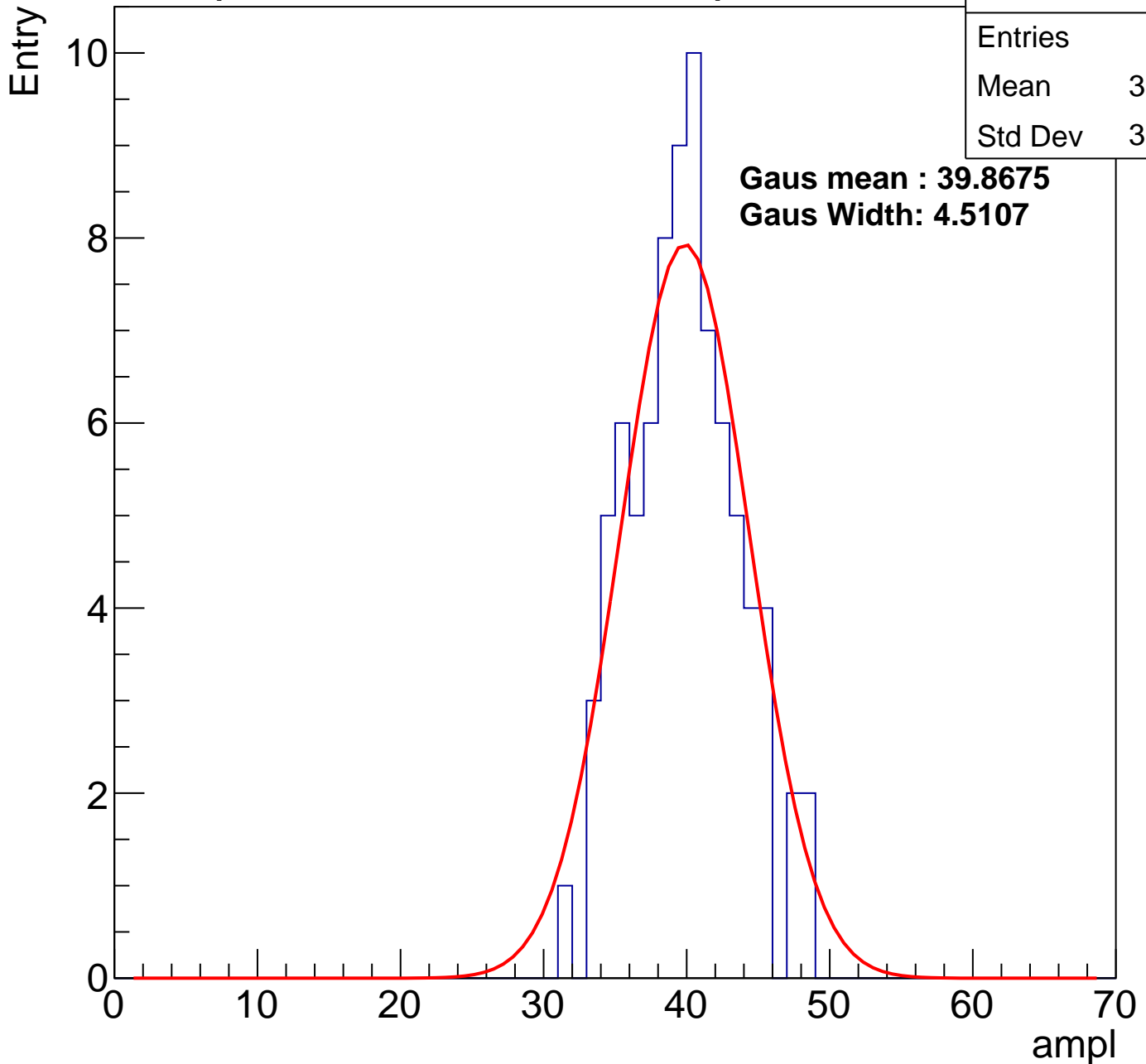


B0L001S, U18-ch93, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	39.36
Std Dev	3.747

Gaus mean : 39.8675
Gaus Width: 4.5107



B0L001S, U18-ch93, adc2

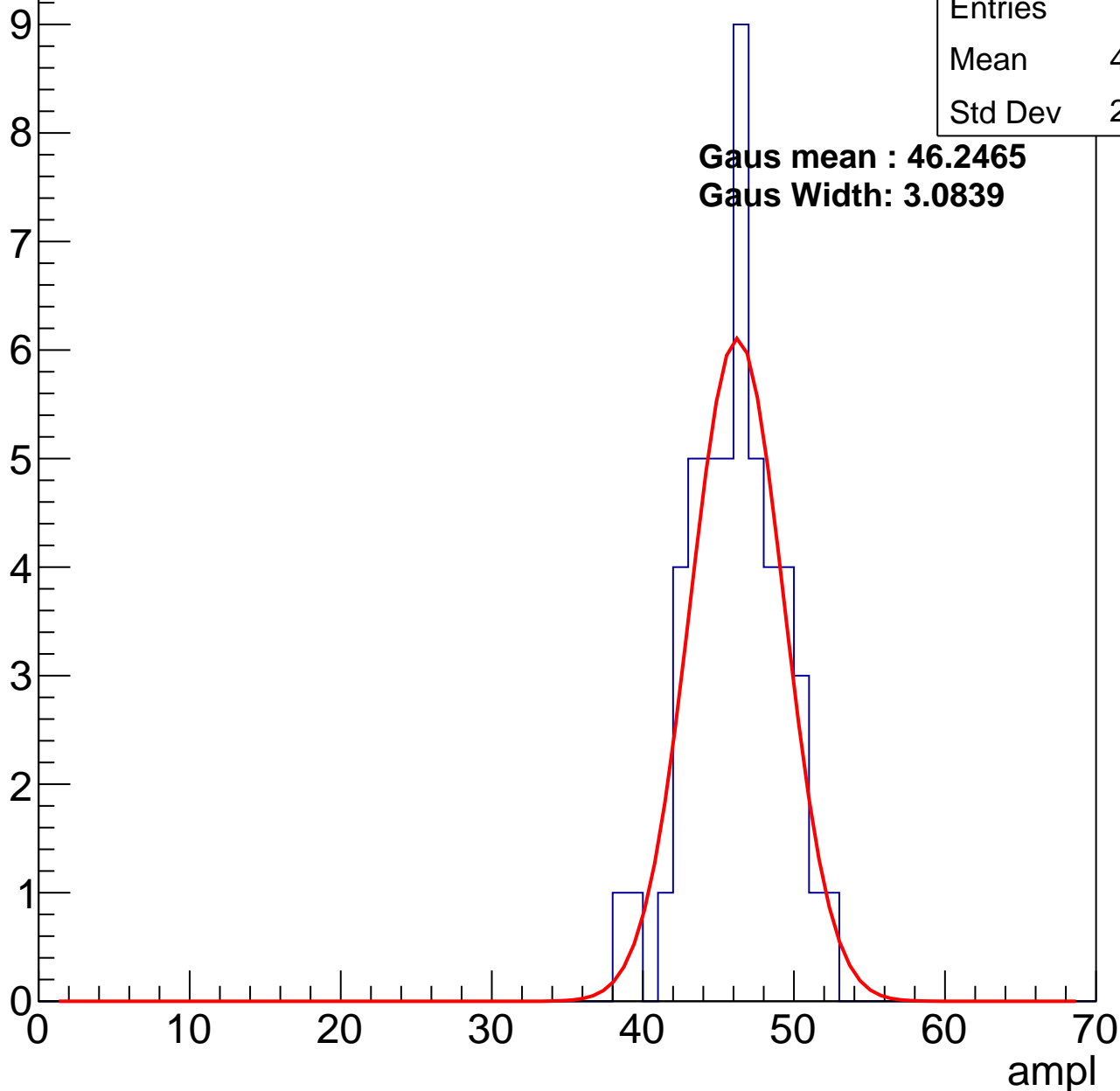
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	45.63
Std Dev	2.967

Gaus mean : 46.2465

Gaus Width: 3.0839

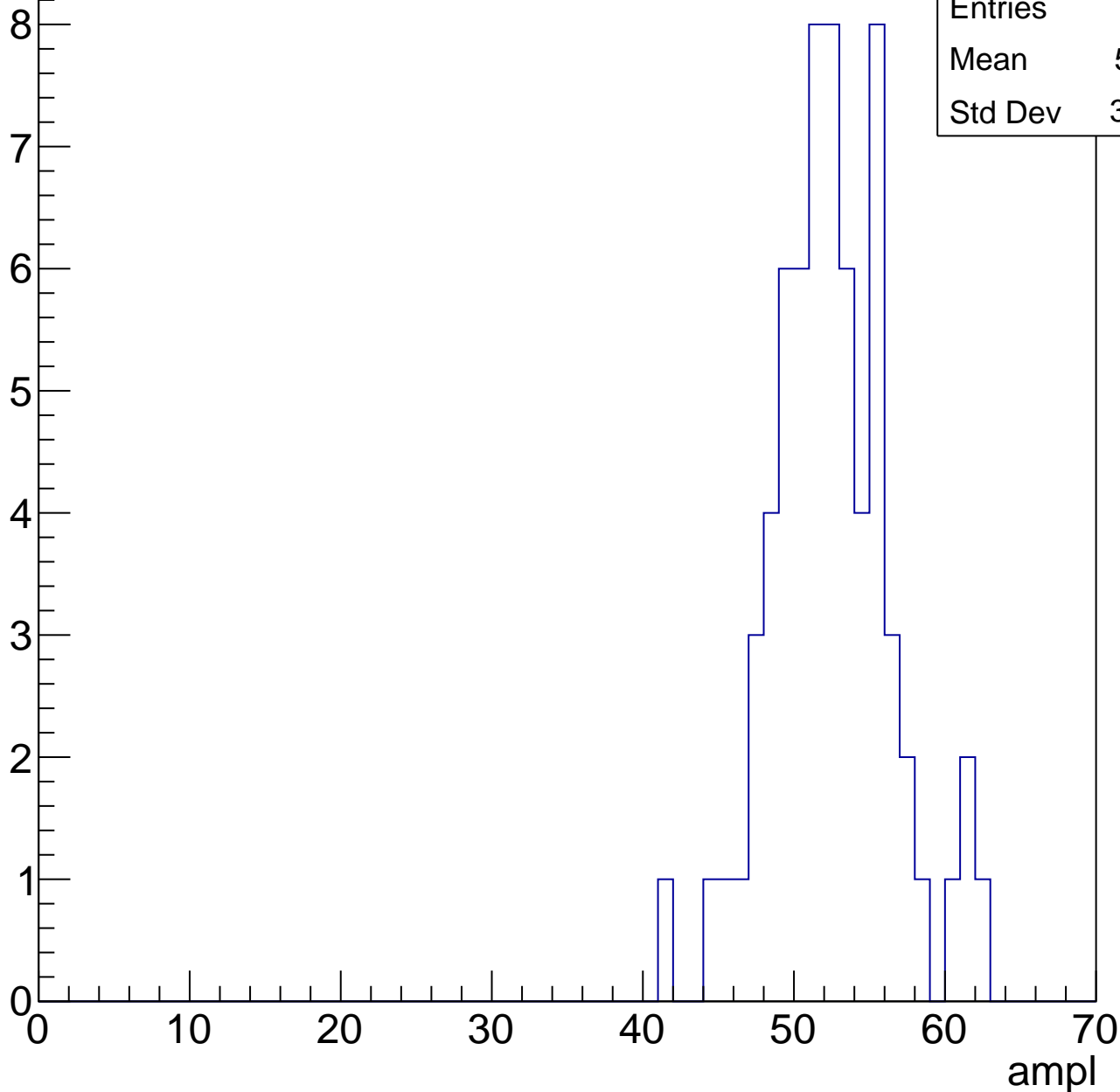


B0L001S, U18-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	52.01
Std Dev	3.957

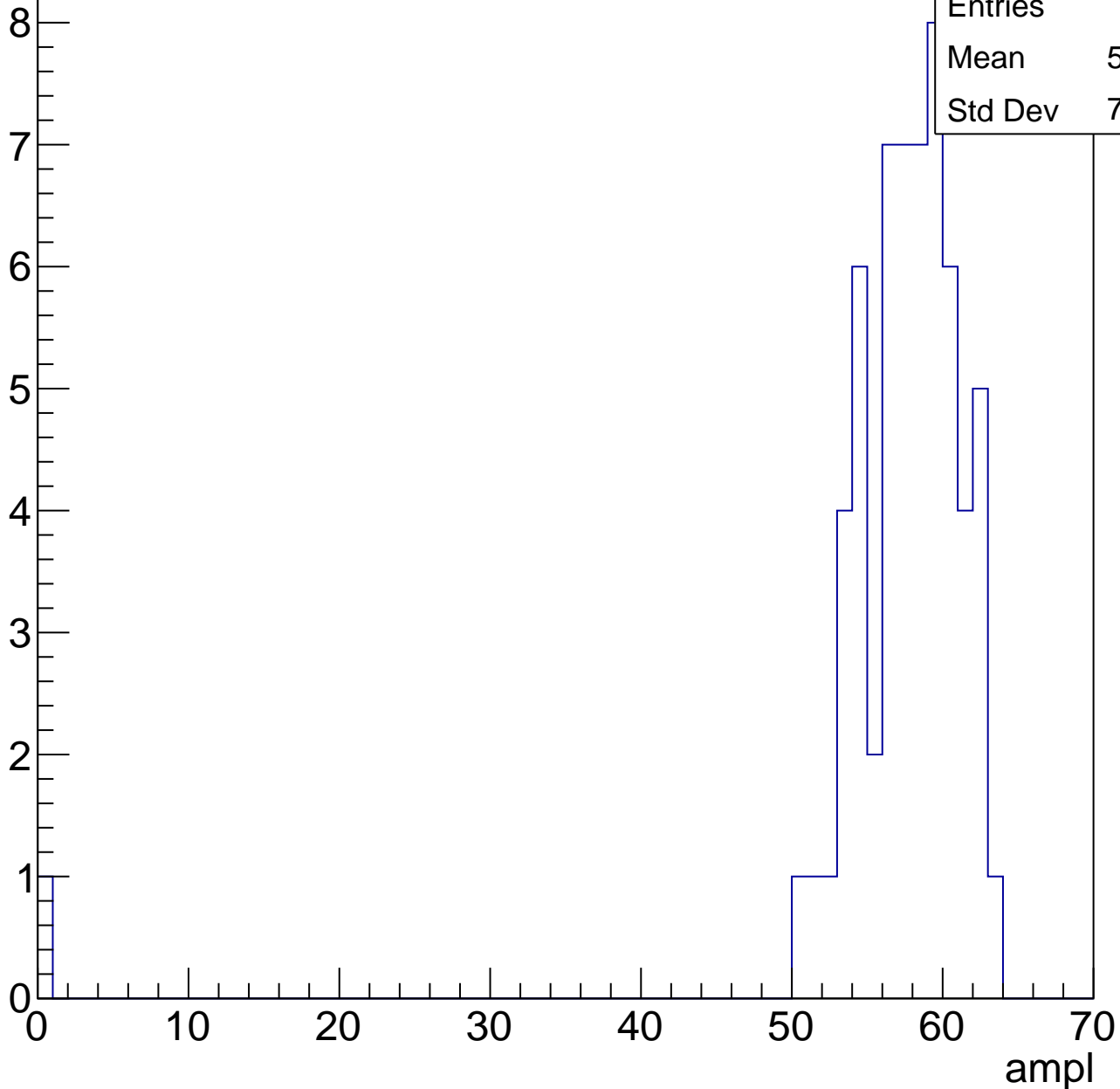


B0L001S, U18-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.48
Std Dev	7.886

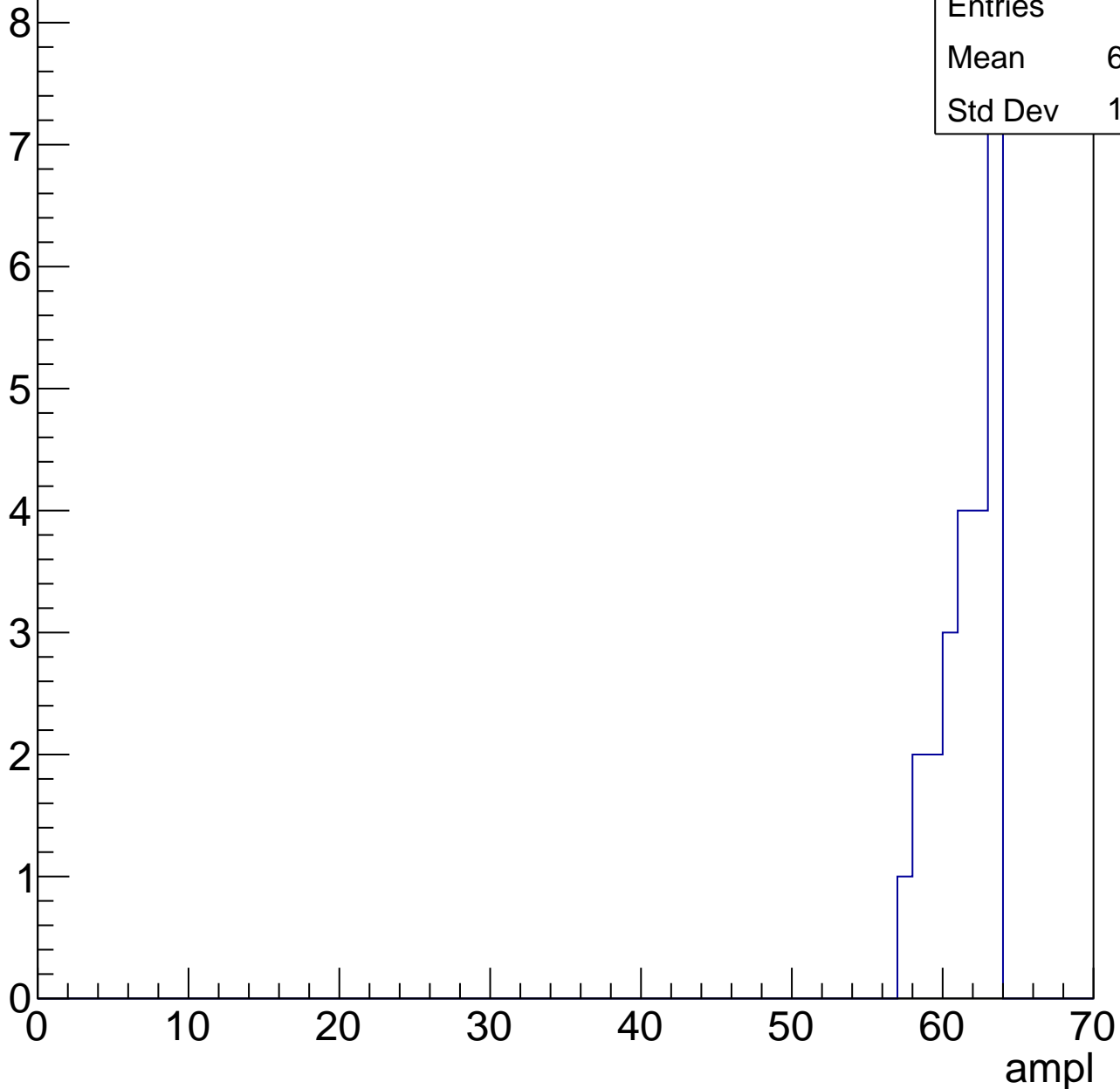


B0L001S, U18-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	61.12
Std Dev	1.833



B0L001S, U18-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U18-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

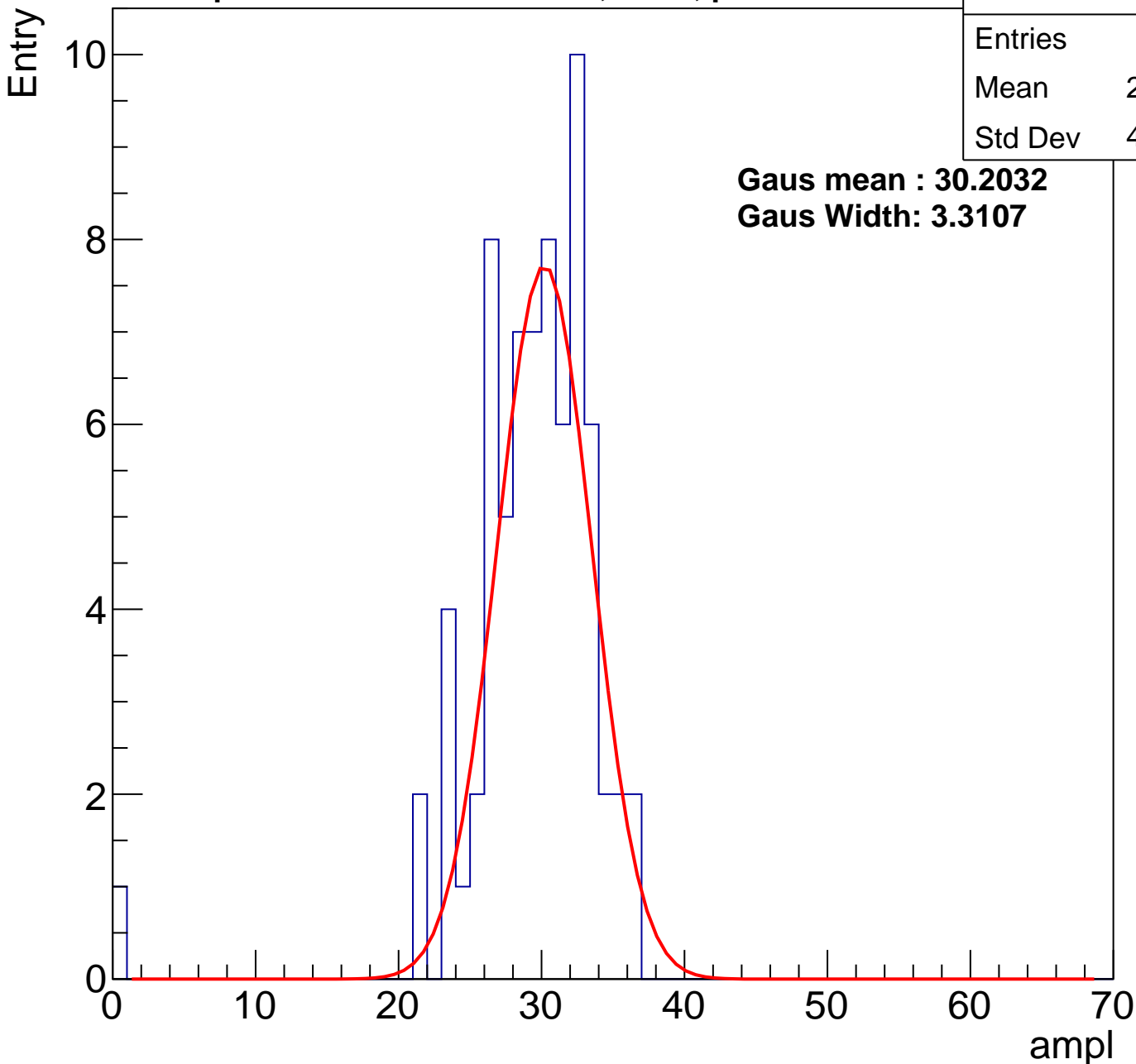
B0L001S, U18-ch94, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	28.82
Std Dev	4.832

Gaus mean : 30.2032

Gaus Width: 3.3107



B0L001S, U18-ch94, adc1

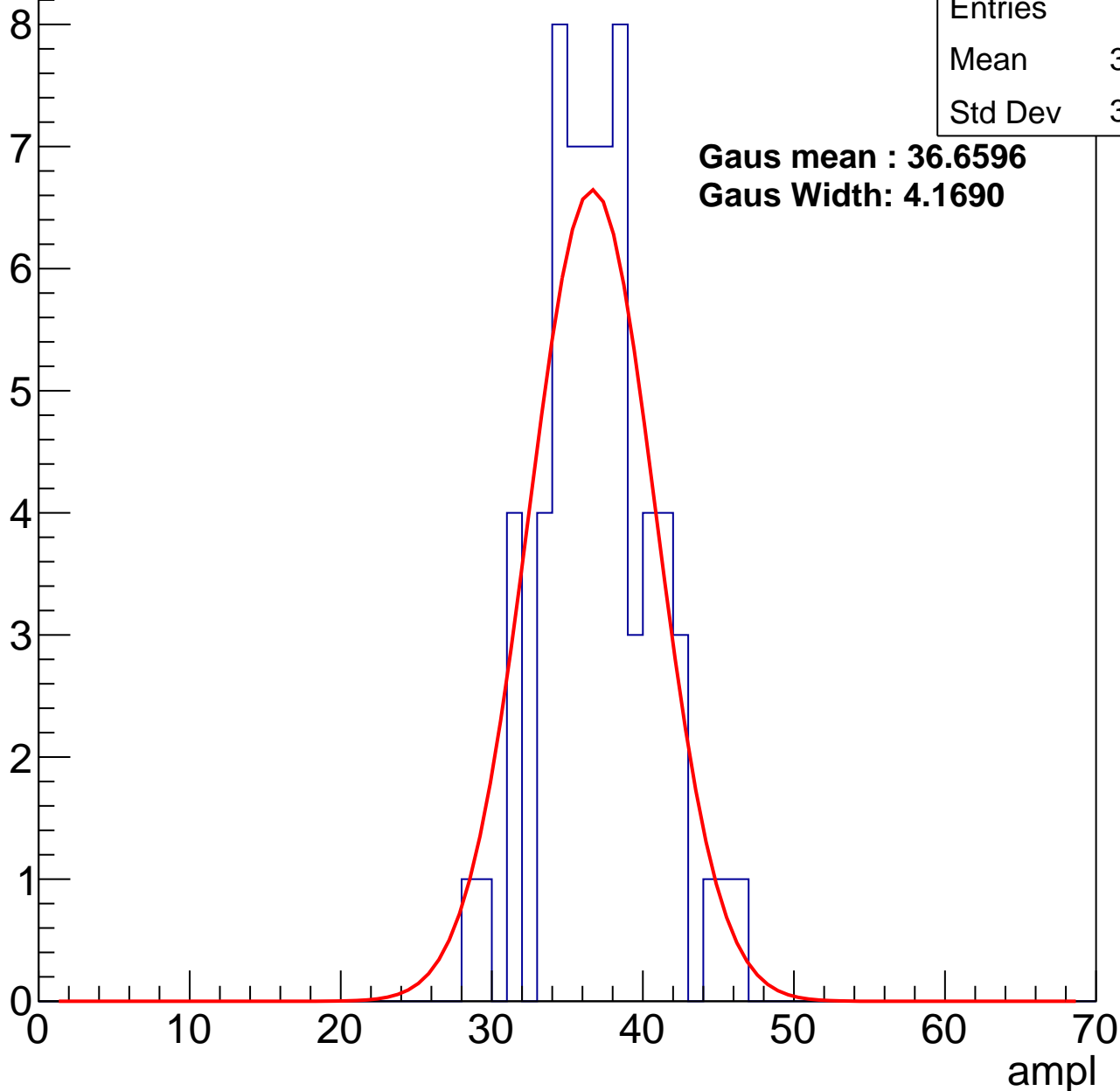
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	36.67
Std Dev	3.627

Gaus mean : 36.6596

Gaus Width: 4.1690



B0L001S, U18-ch94, adc2

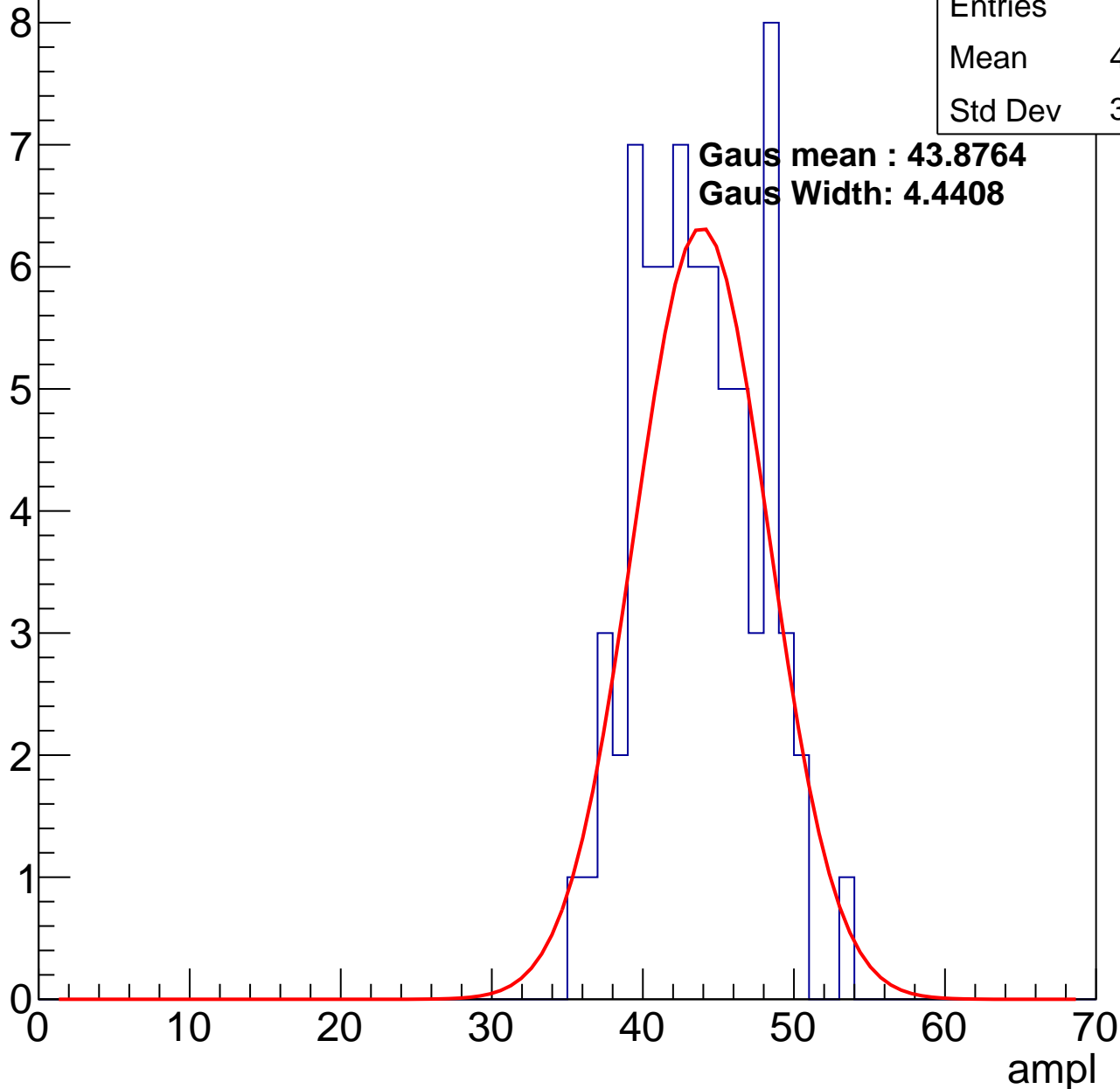
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.24
Std Dev	3.889

Gaus mean : 43.8764

Gaus Width: 4.4408

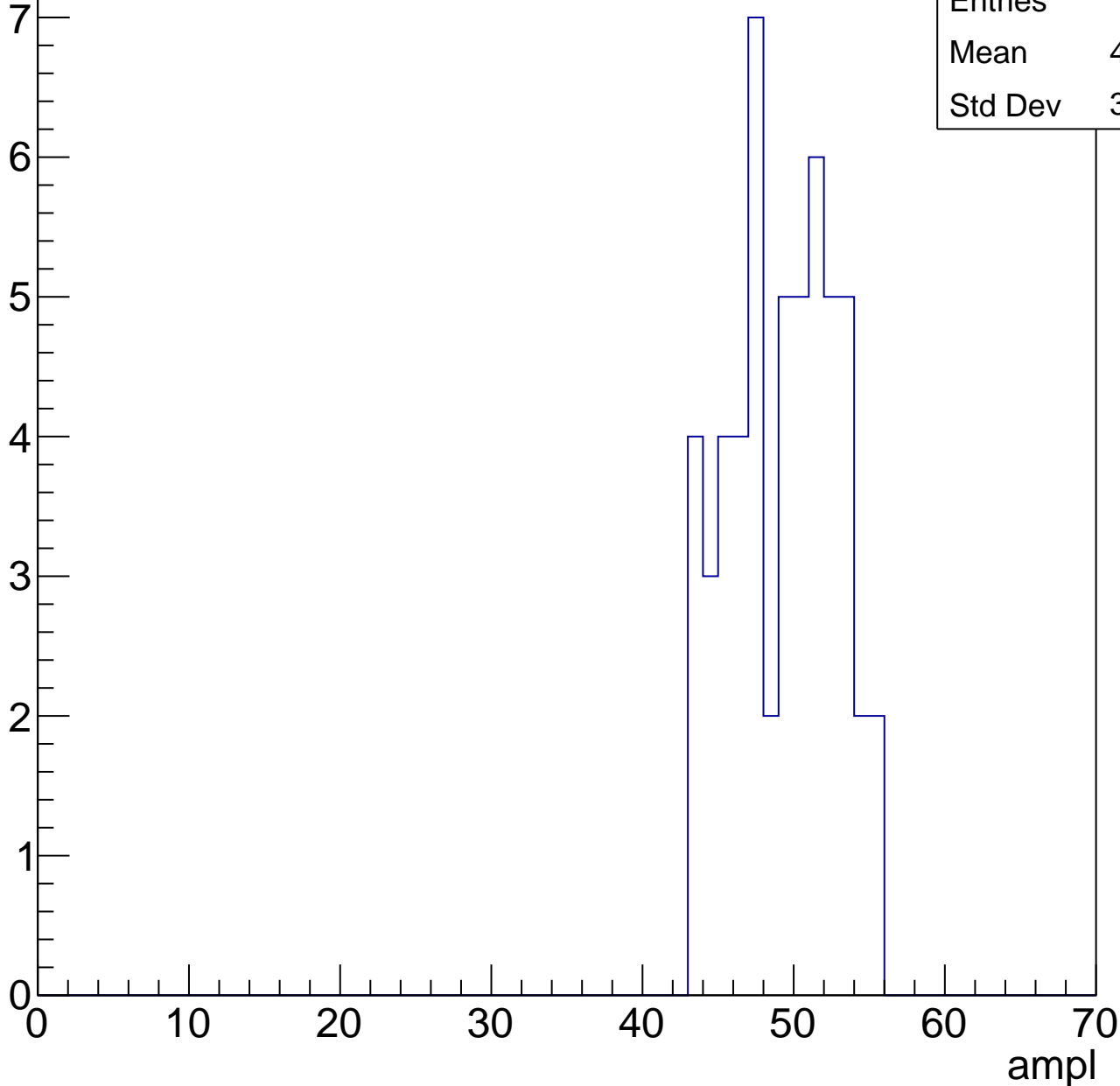


B0L001S, U18-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

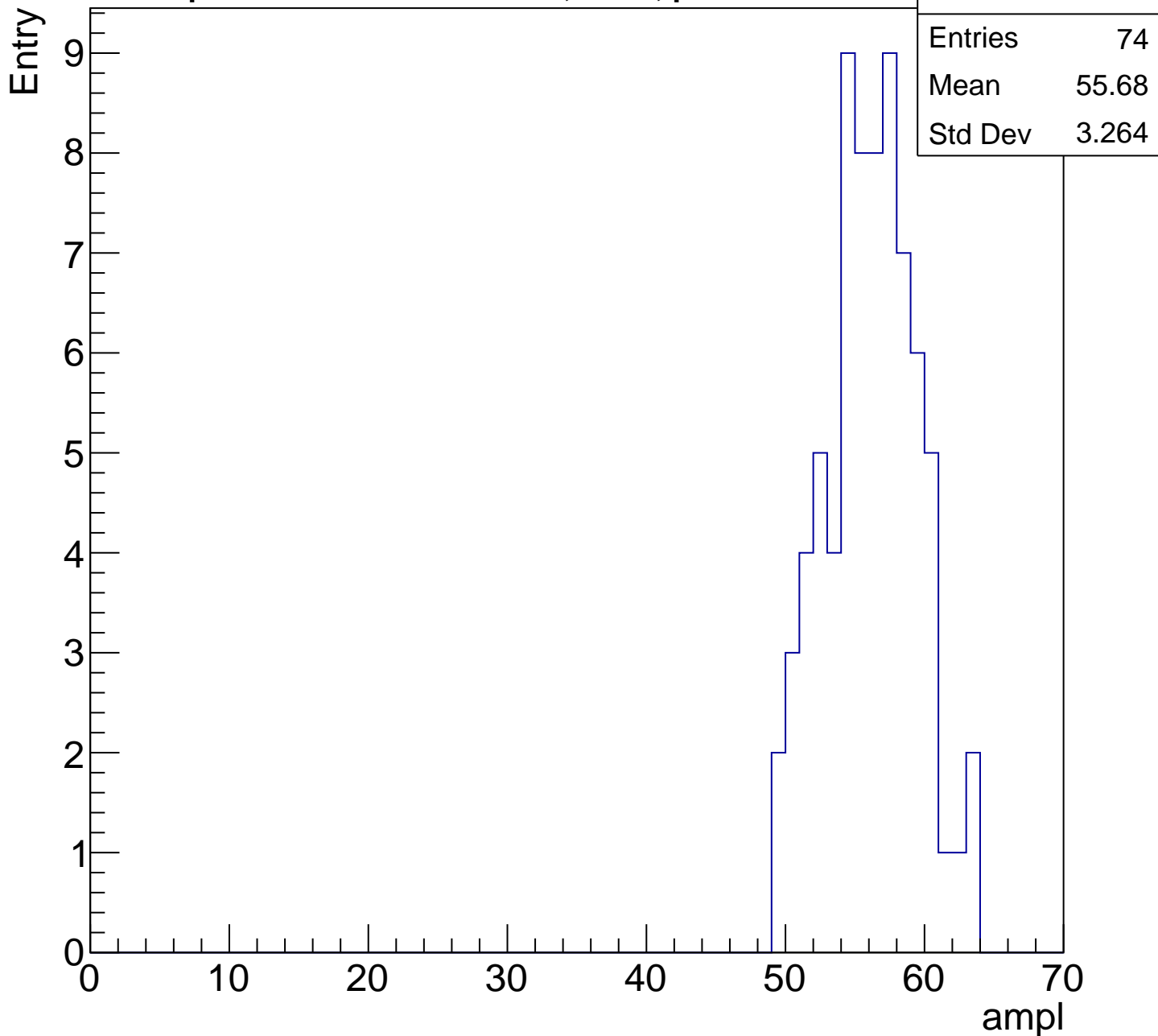
Entry

Entries	54
Mean	48.83
Std Dev	3.398



B0L001S, U18-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

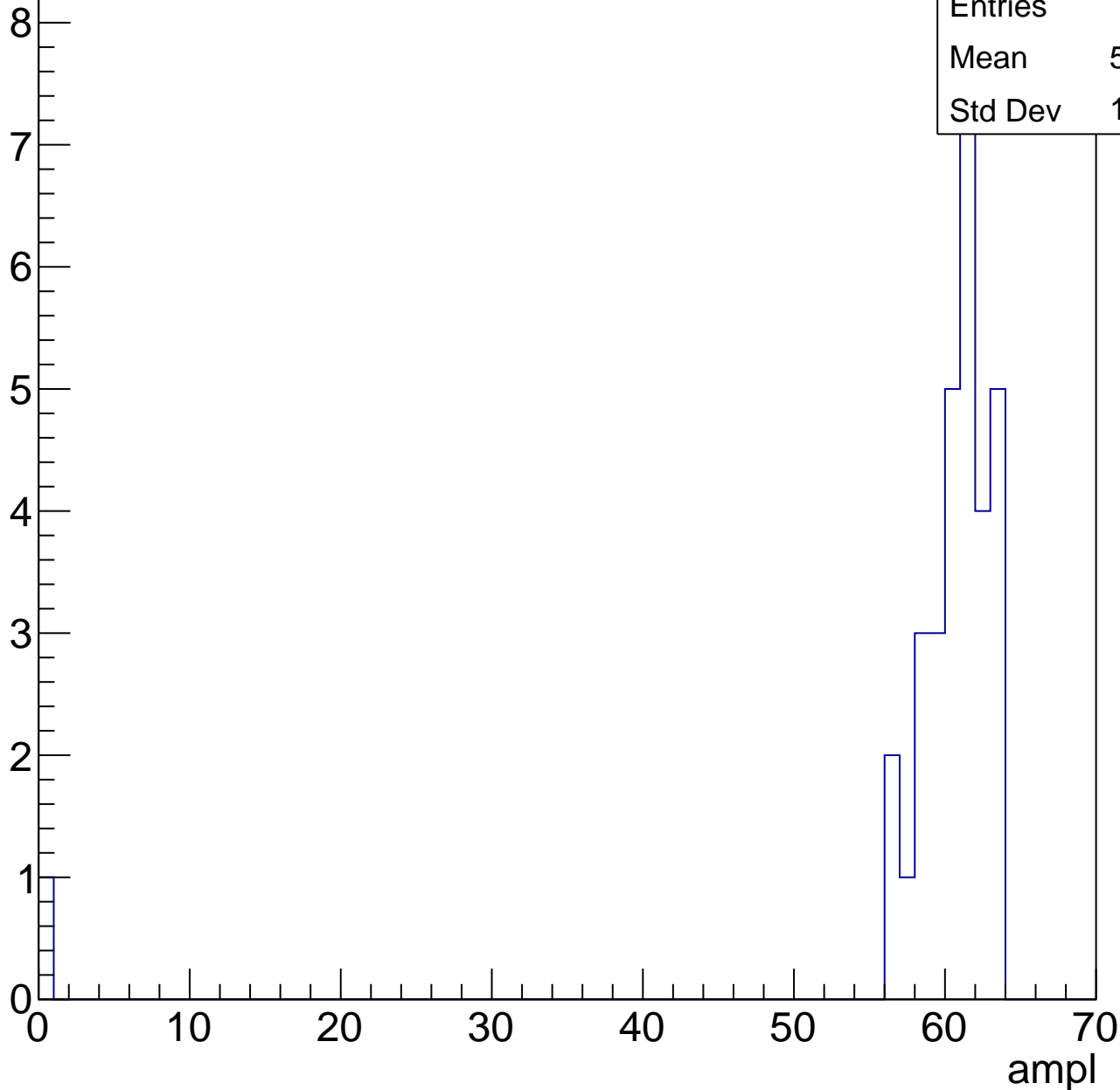


B0L001S, U18-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.47
Std Dev	10.68

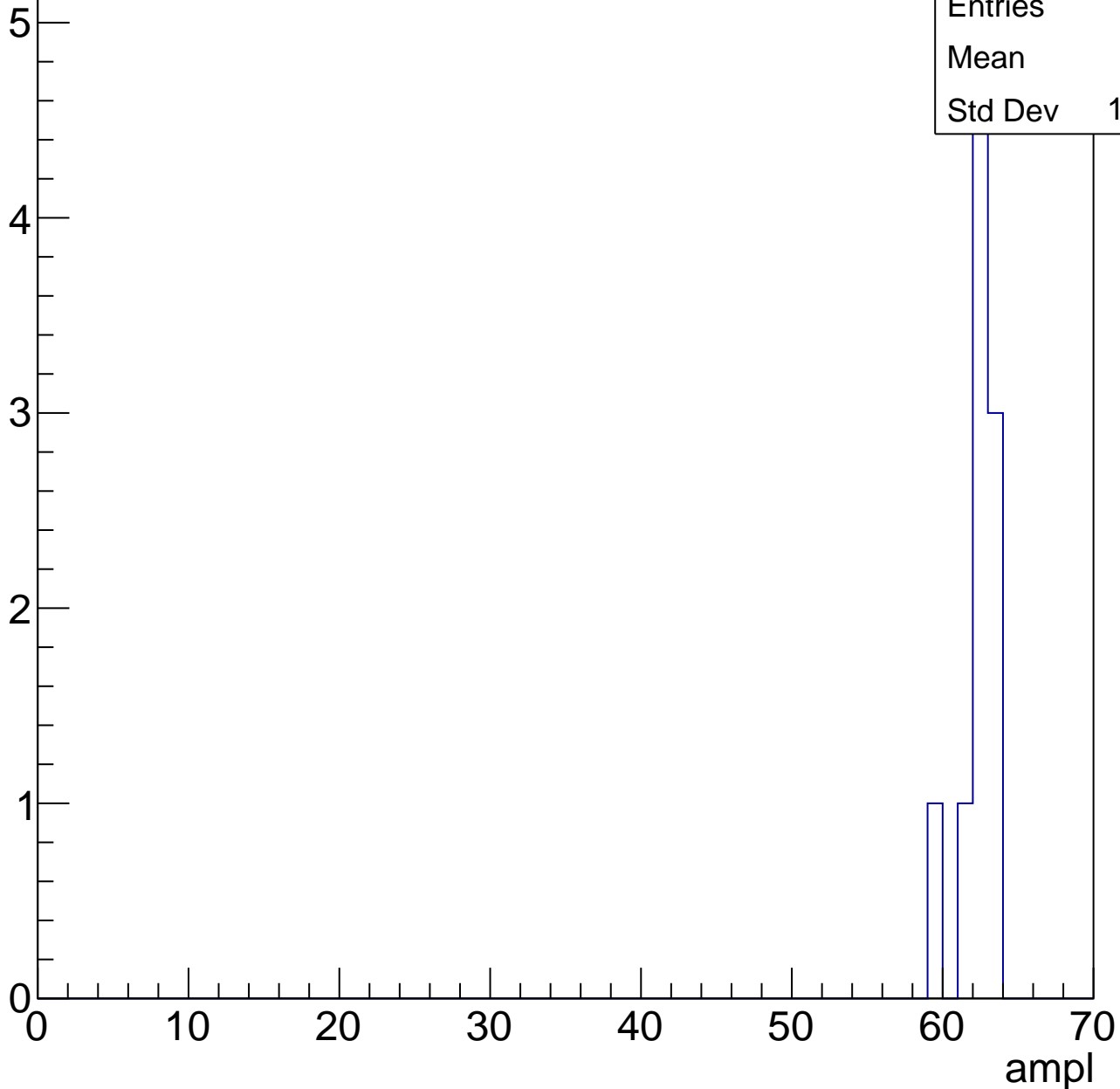


B0L001S, U18-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	10
Mean	61.9
Std Dev	1.136



B0L001S, U18-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch95, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	55
Mean	28.6
Std Dev	7.714

Gaus mean : 31.0022

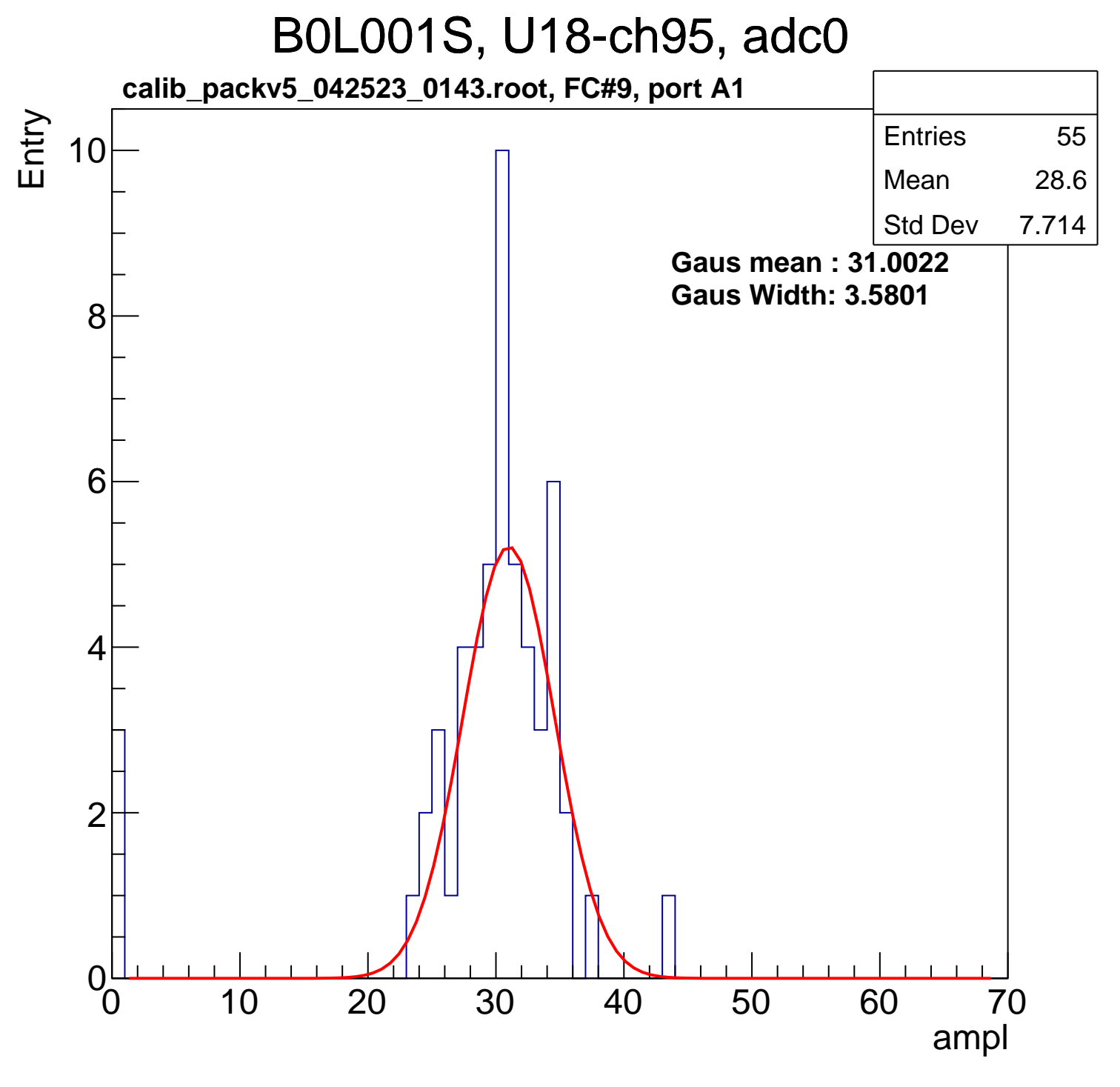
Gaus Width: 3.5801

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



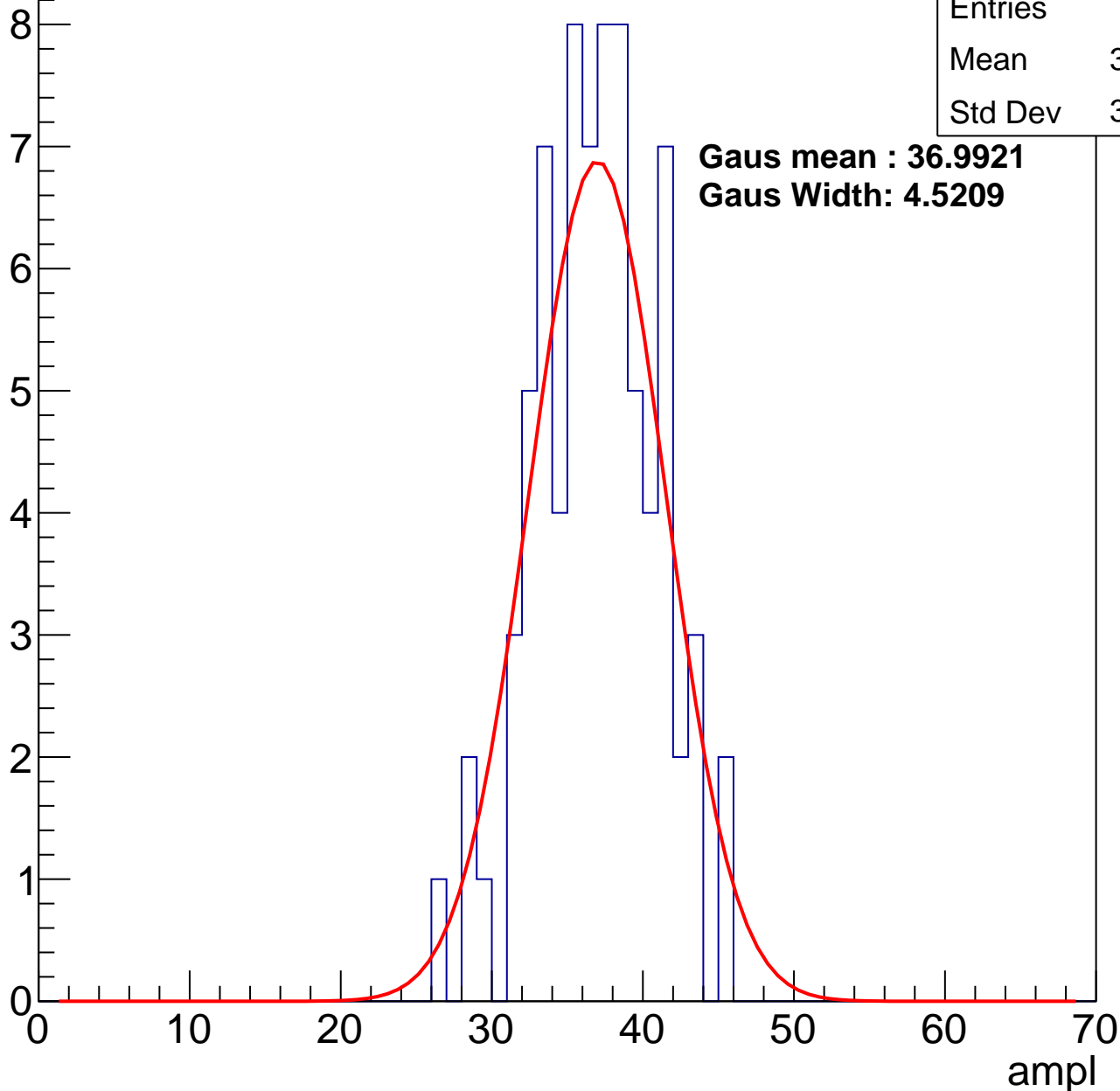
B0L001S, U18-ch95, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.47
Std Dev	3.959

Gaus mean : 36.9921
Gaus Width: 4.5209



B0L001S, U18-ch95, adc2

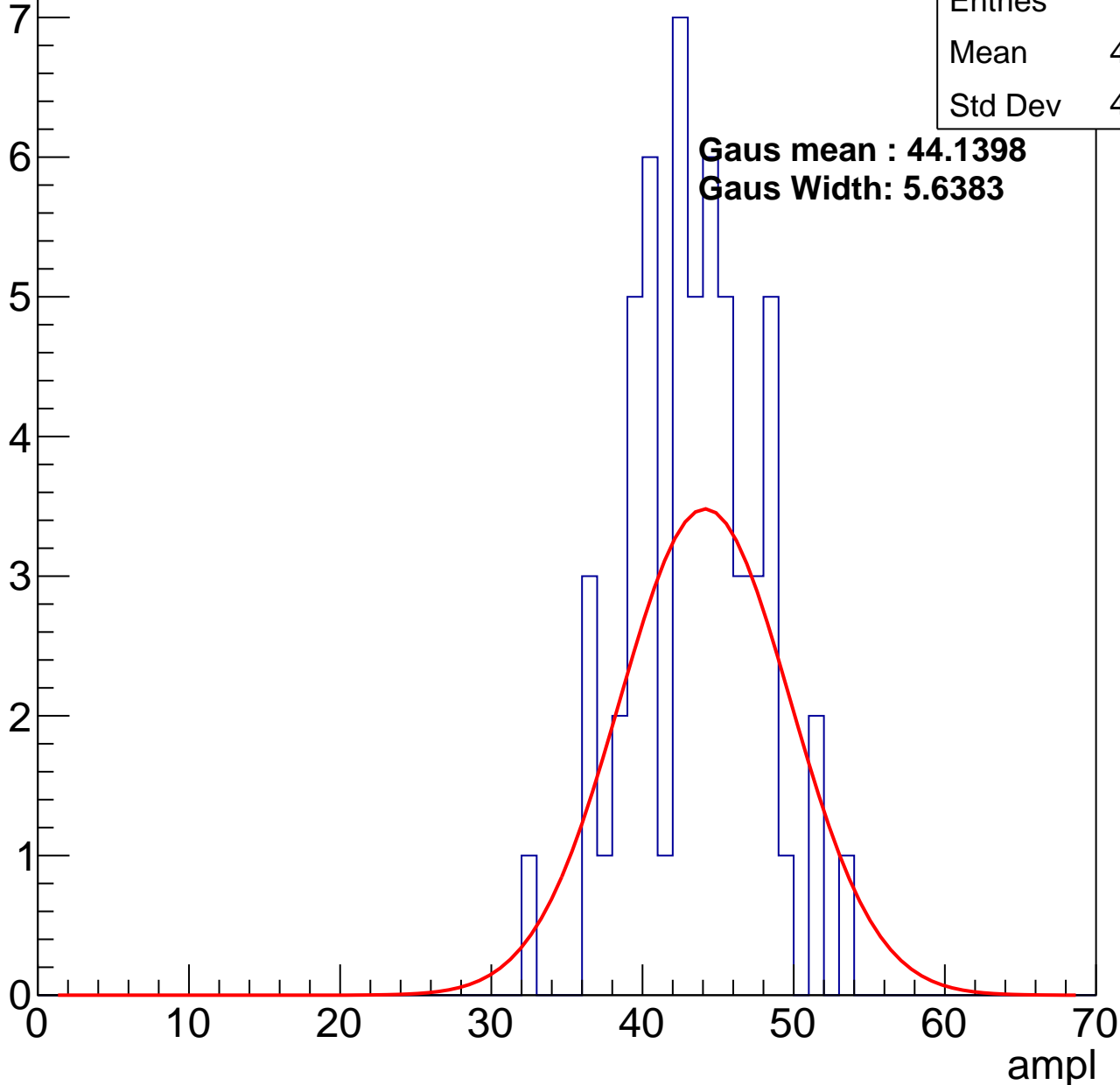
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	42.98
Std Dev	4.165

Gaus mean : 44.1398

Gaus Width: 5.6383

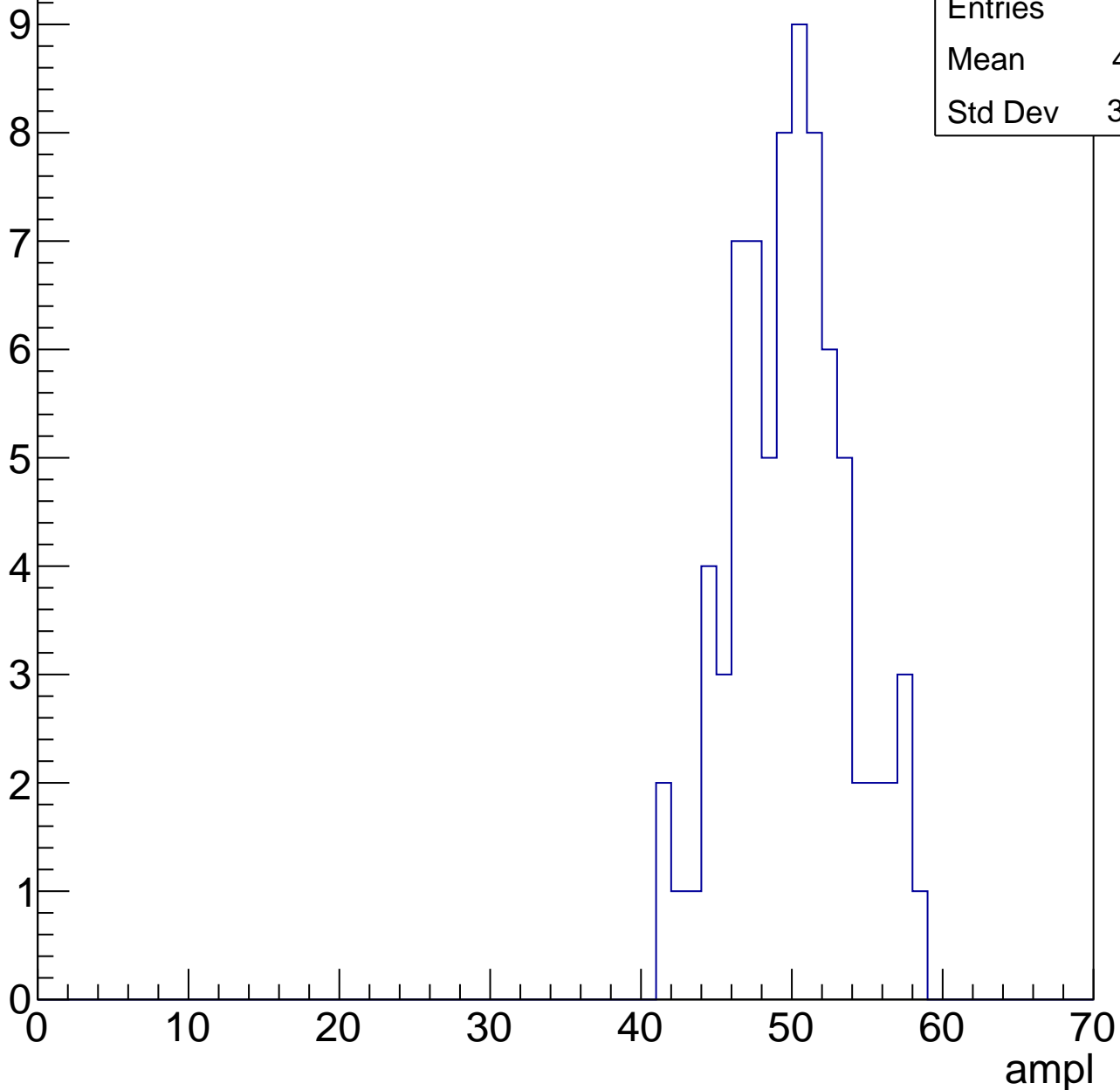


B0L001S, U18-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	49.41
Std Dev	3.826

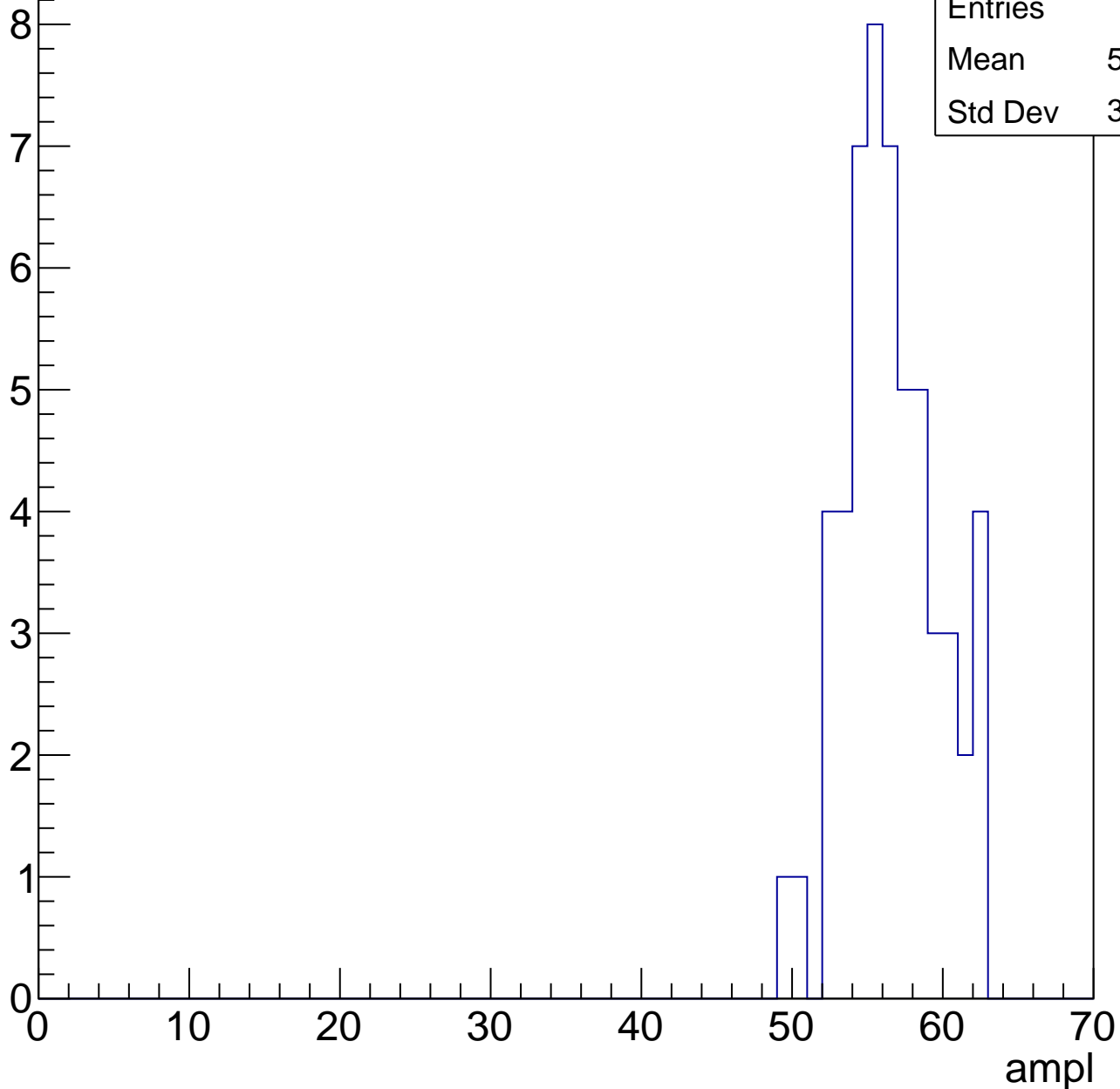


B0L001S, U18-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

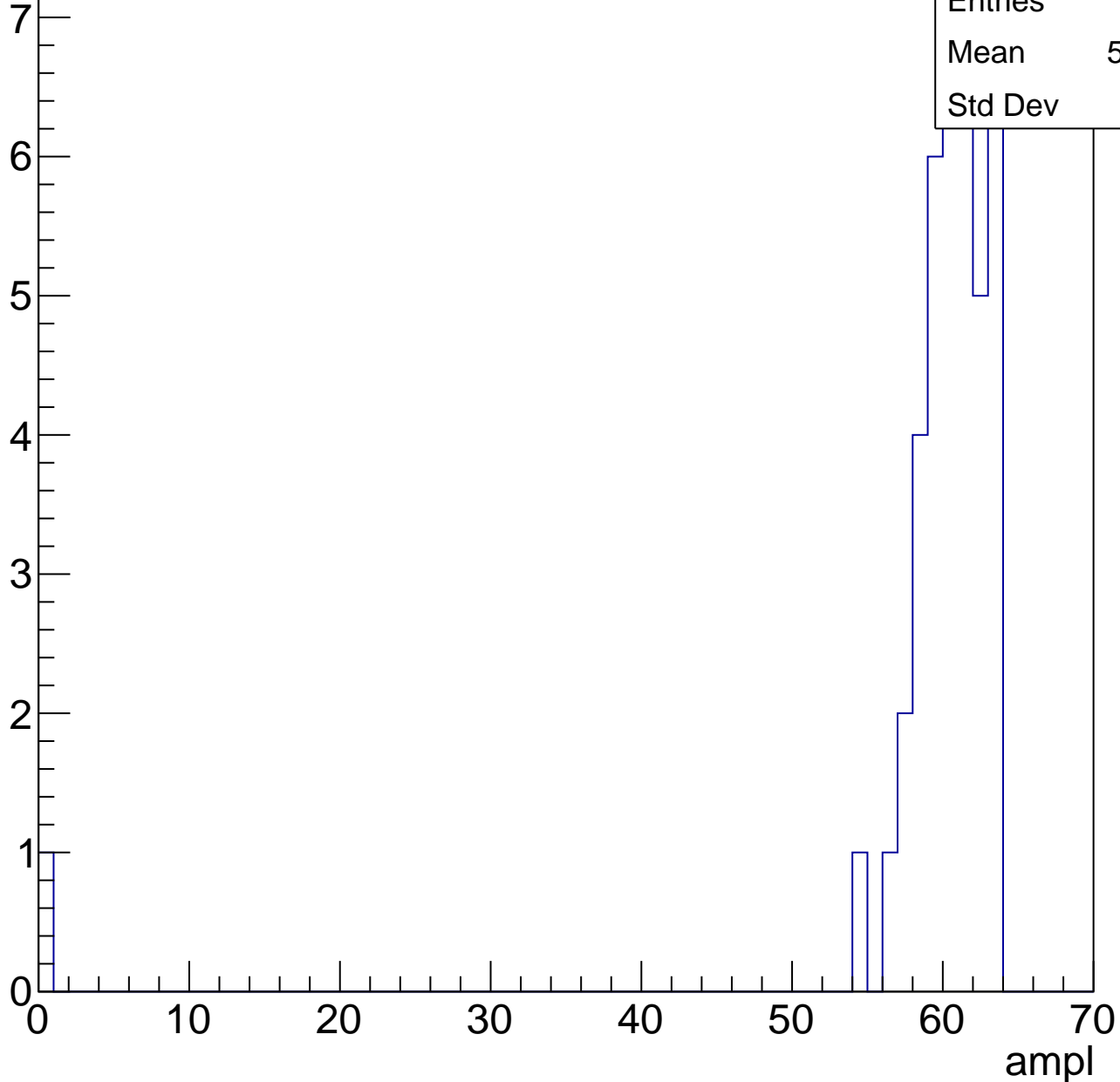
Entries	54
Mean	56.13
Std Dev	3.085



B0L001S, U18-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

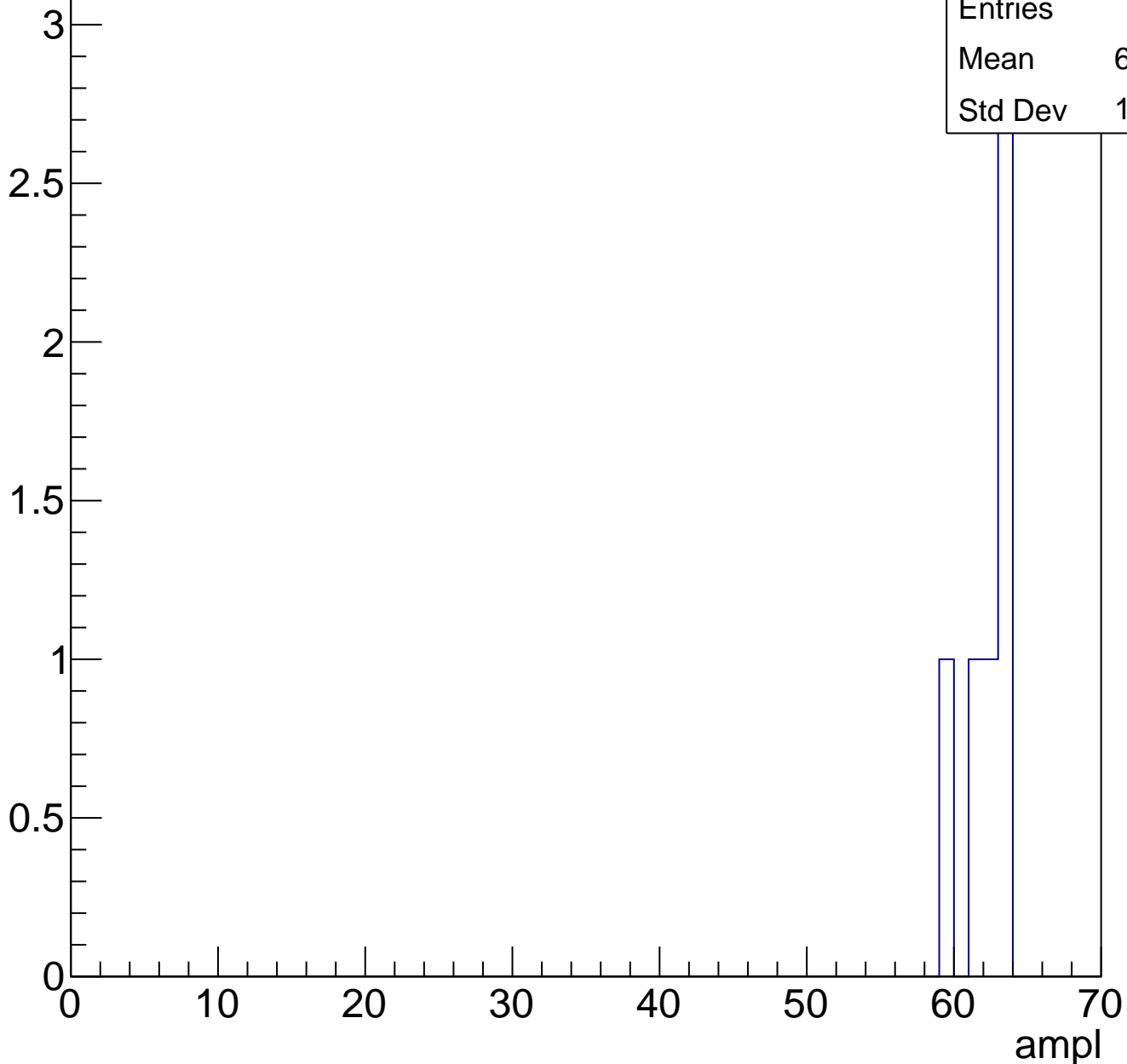
Entry



B0L001S, U18-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch96, adc0

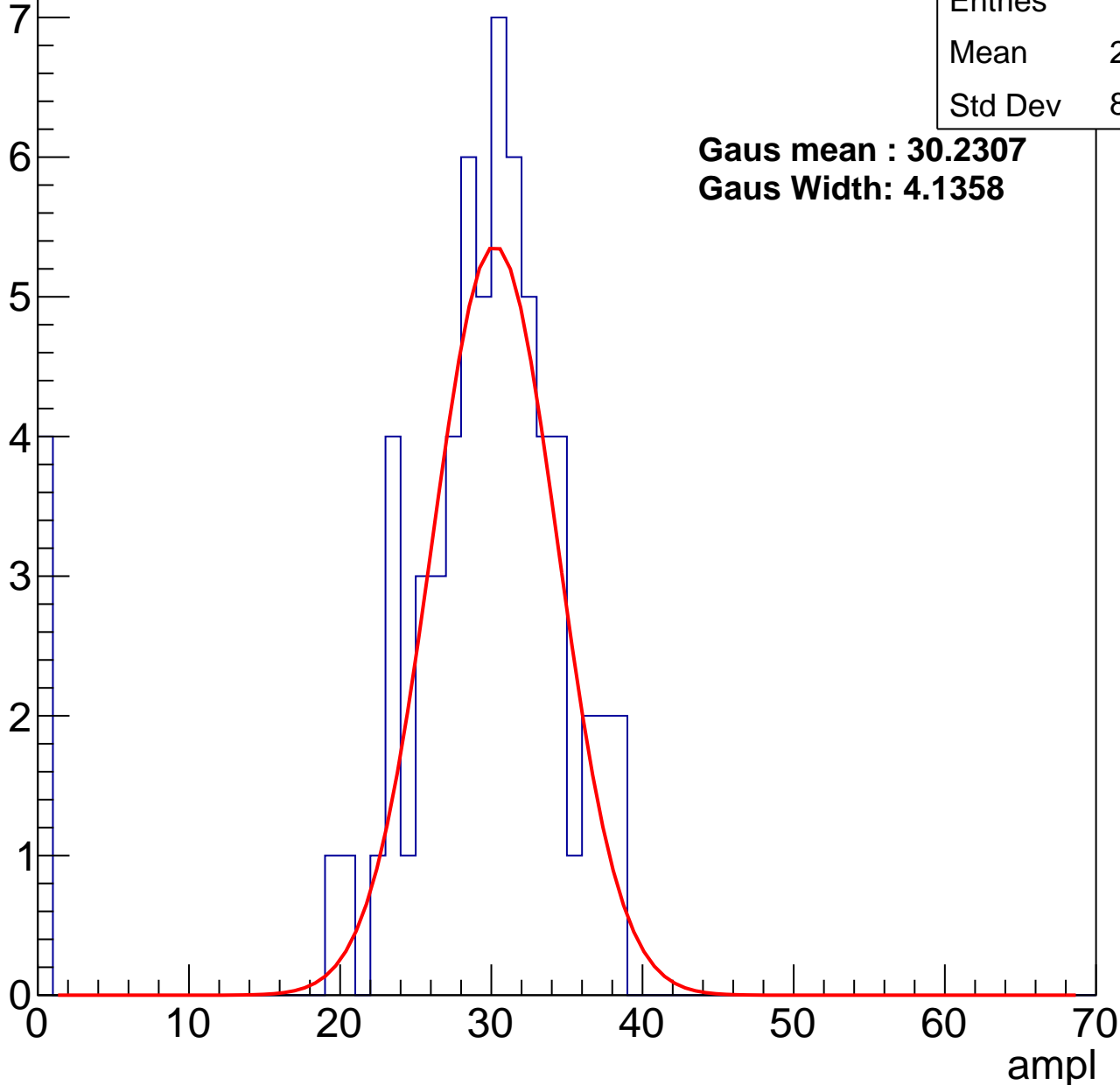
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	27.76
Std Dev	8.185

Gaus mean : 30.2307

Gaus Width: 4.1358



B0L001S, U18-ch96, adc1

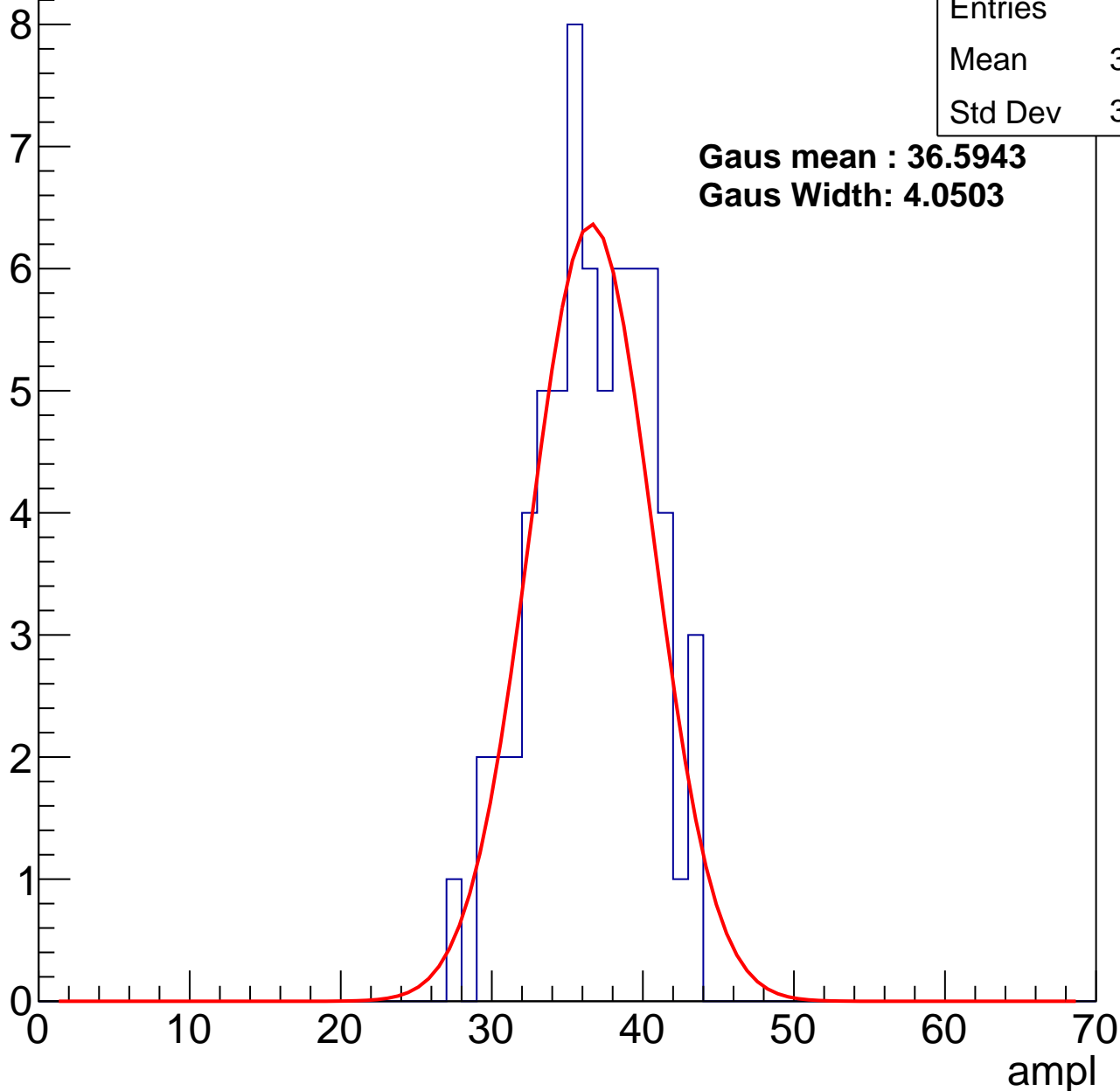
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	36.18
Std Dev	3.688

Gaus mean : 36.5943

Gaus Width: 4.0503



B0L001S, U18-ch96, adc2

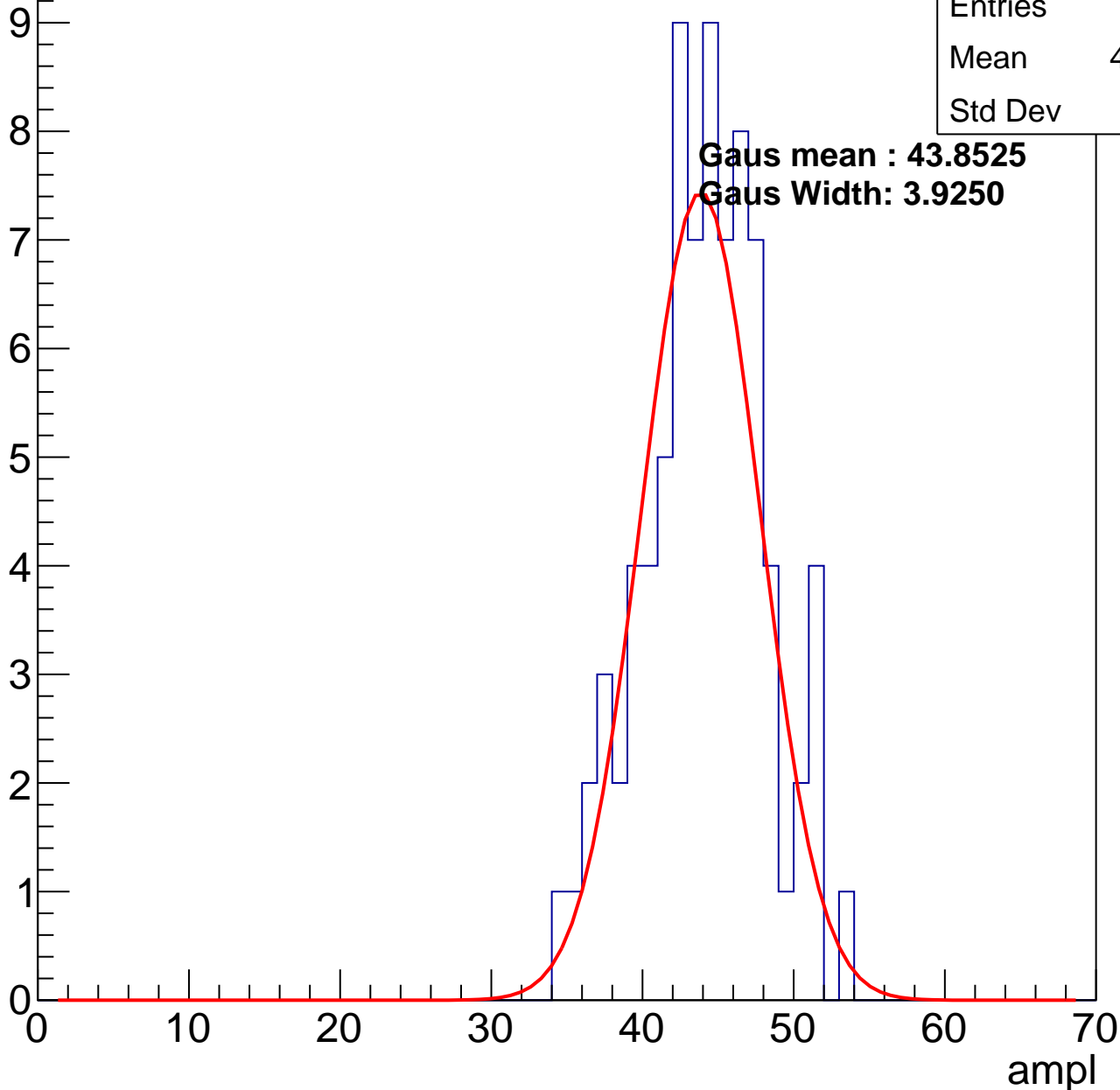
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	43.63
Std Dev	4.02

Gaus mean : 43.8525

Gaus Width: 3.9250

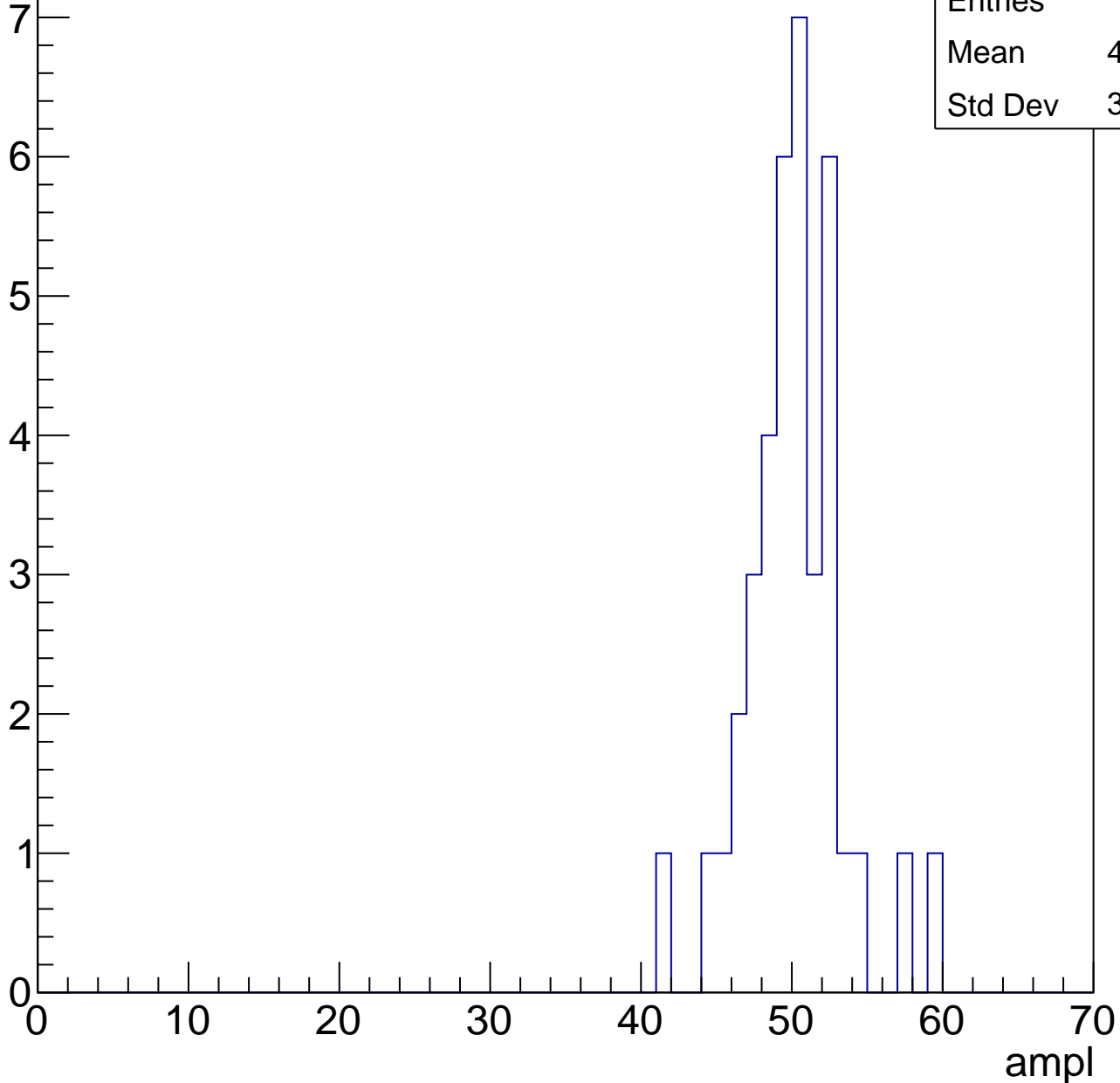


B0L001S, U18-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	49.66
Std Dev	3.247



B0L001S, U18-ch96, adc4

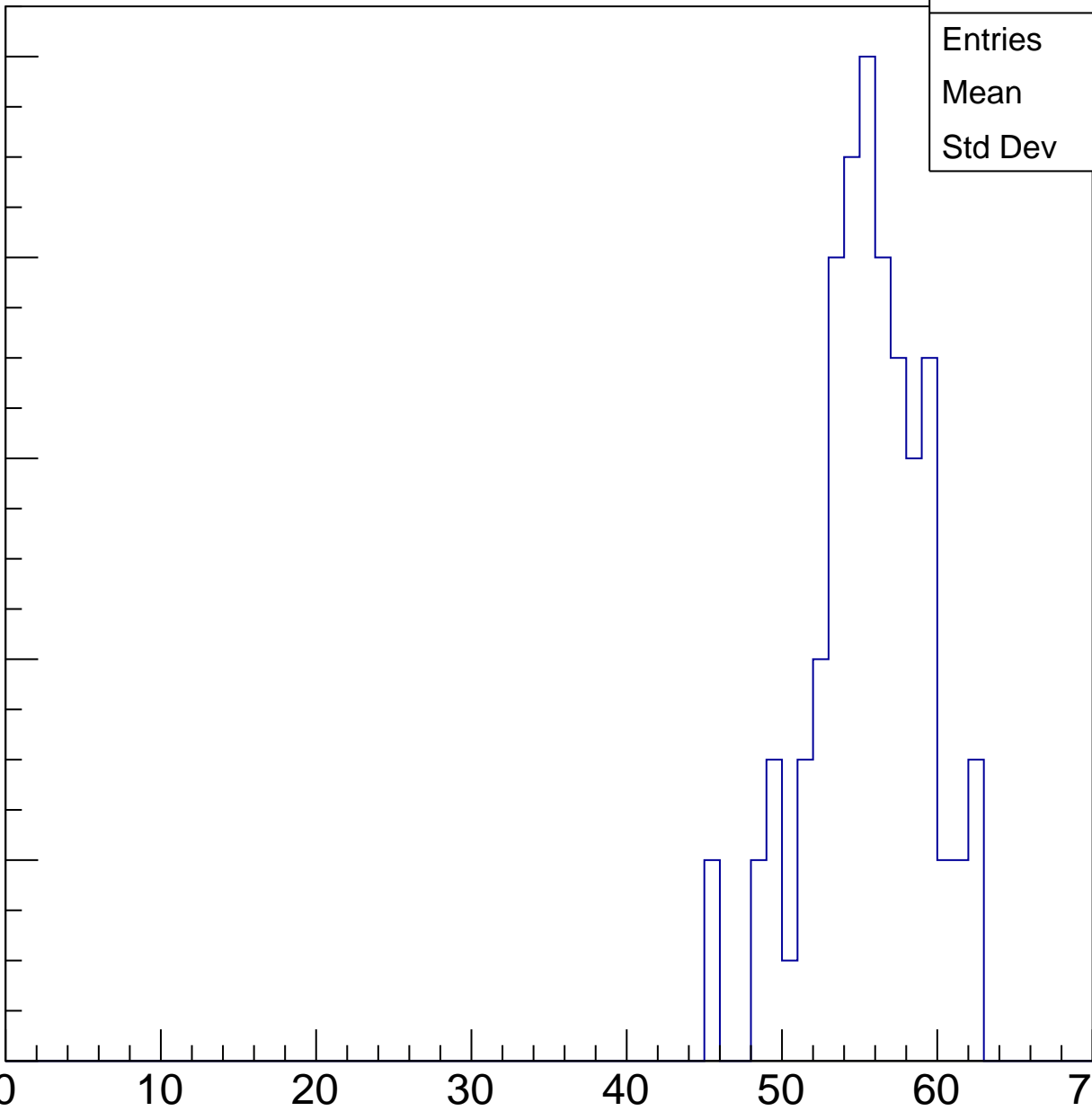
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	55.06
Std Dev	3.651

Entry

10
8
6
4
2
0

ampl

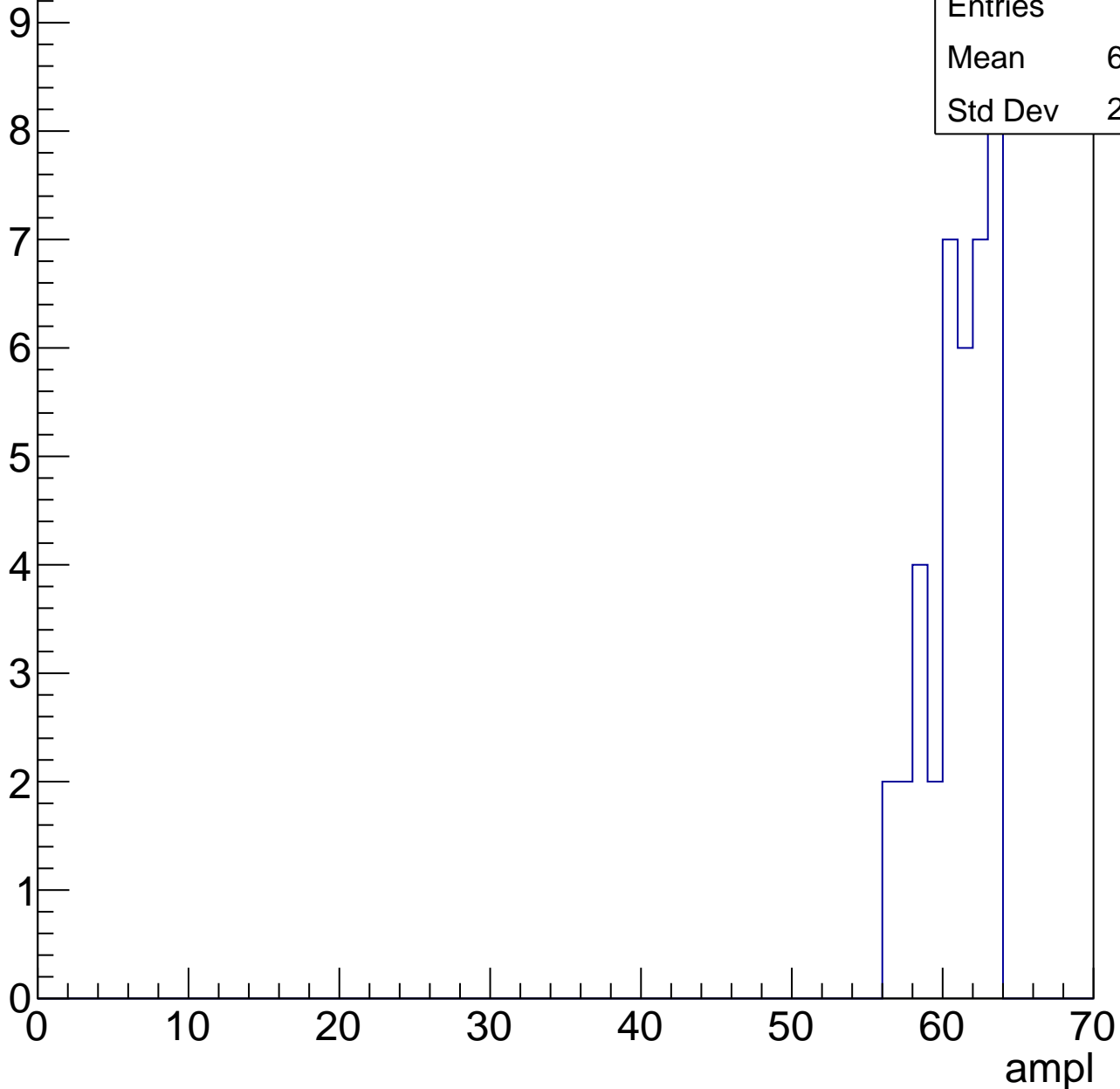


B0L001S, U18-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

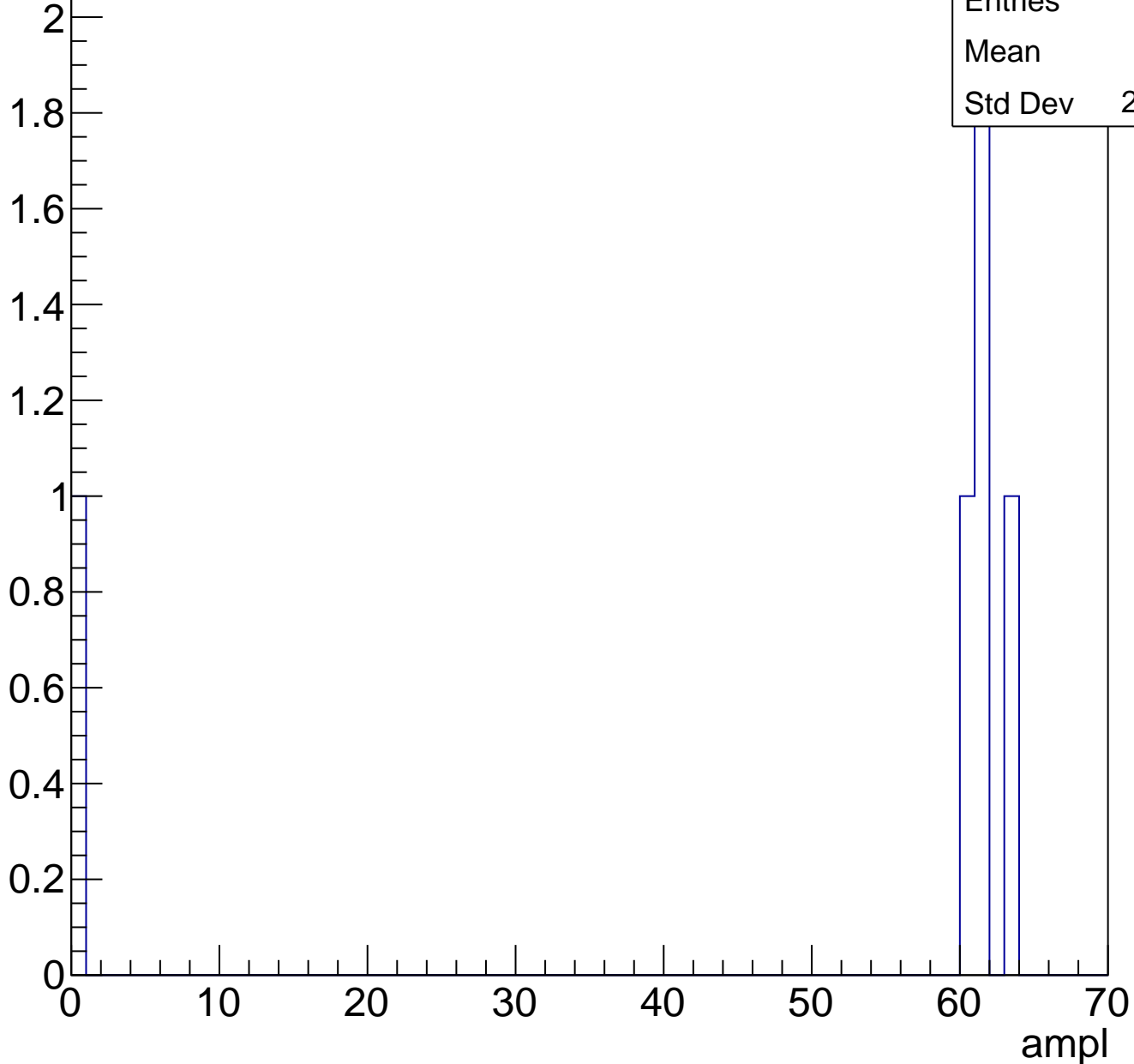
Entries	39
Mean	60.59
Std Dev	2.084



B0L001S, U18-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch97, adc0

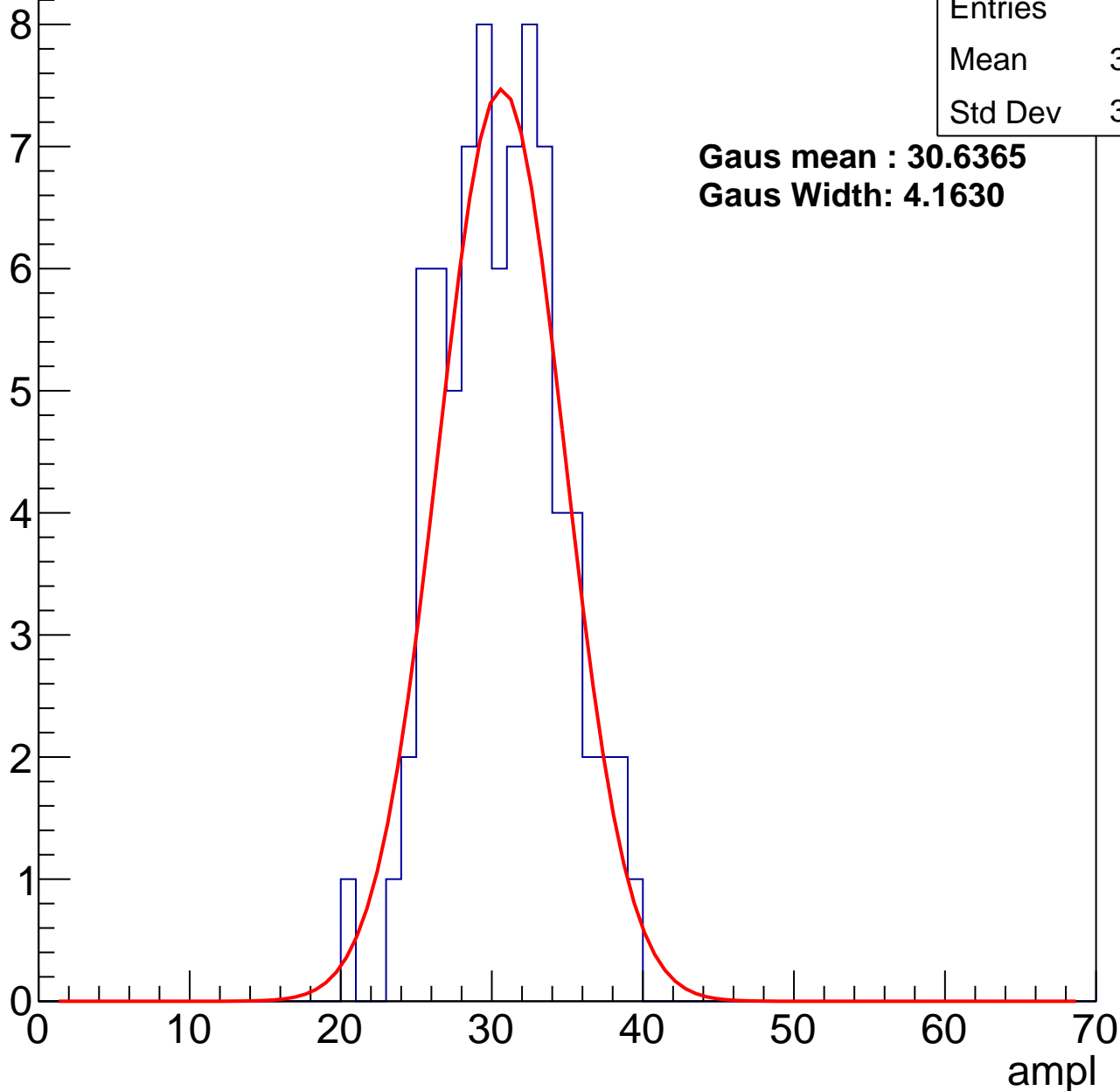
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.14
Std Dev	3.884

Gaus mean : 30.6365

Gaus Width: 4.1630



B0L001S, U18-ch97, adc1

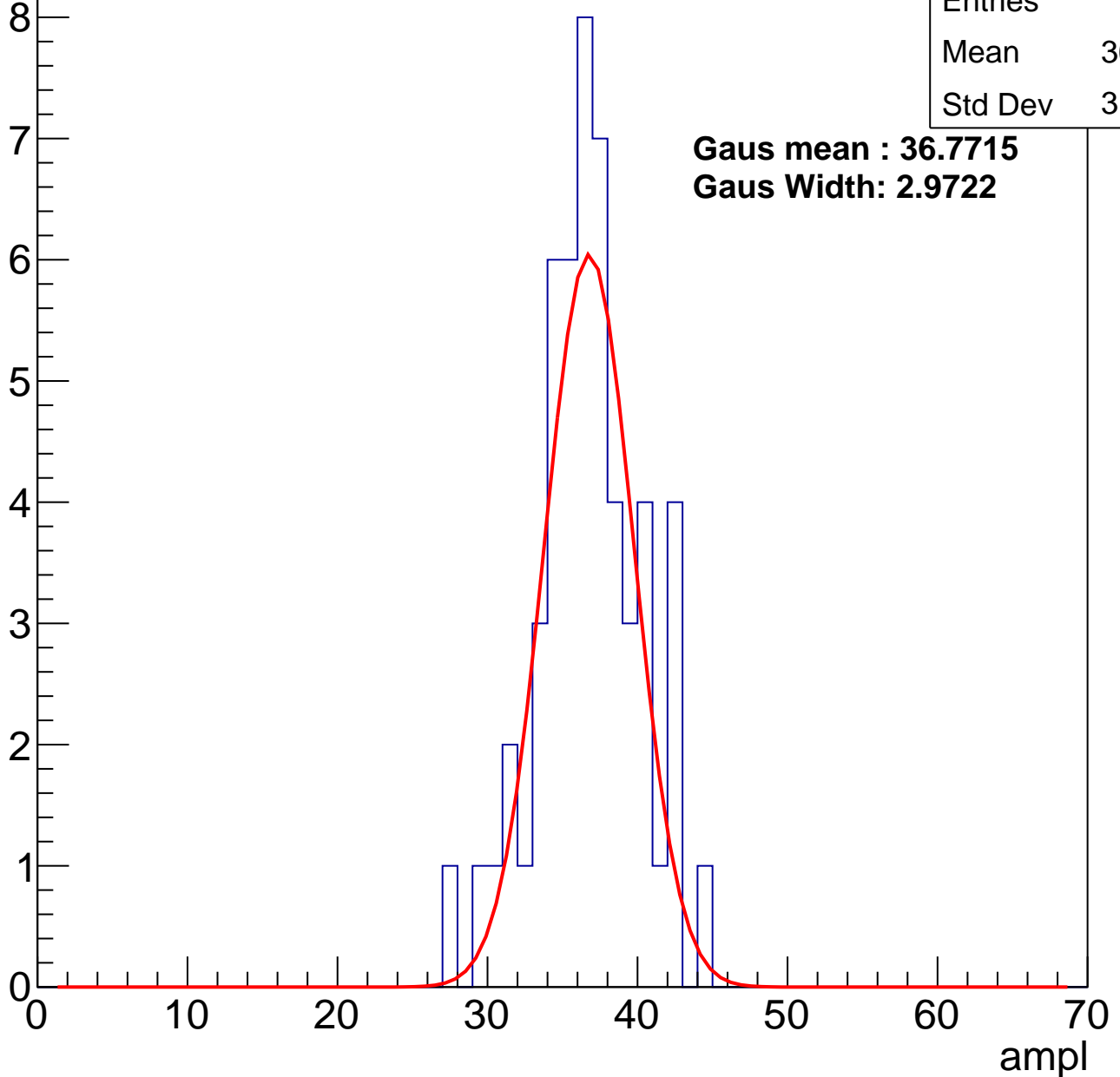
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	36.26
Std Dev	3.454

Gaus mean : 36.7715

Gaus Width: 2.9722



B0L001S, U18-ch97, adc2

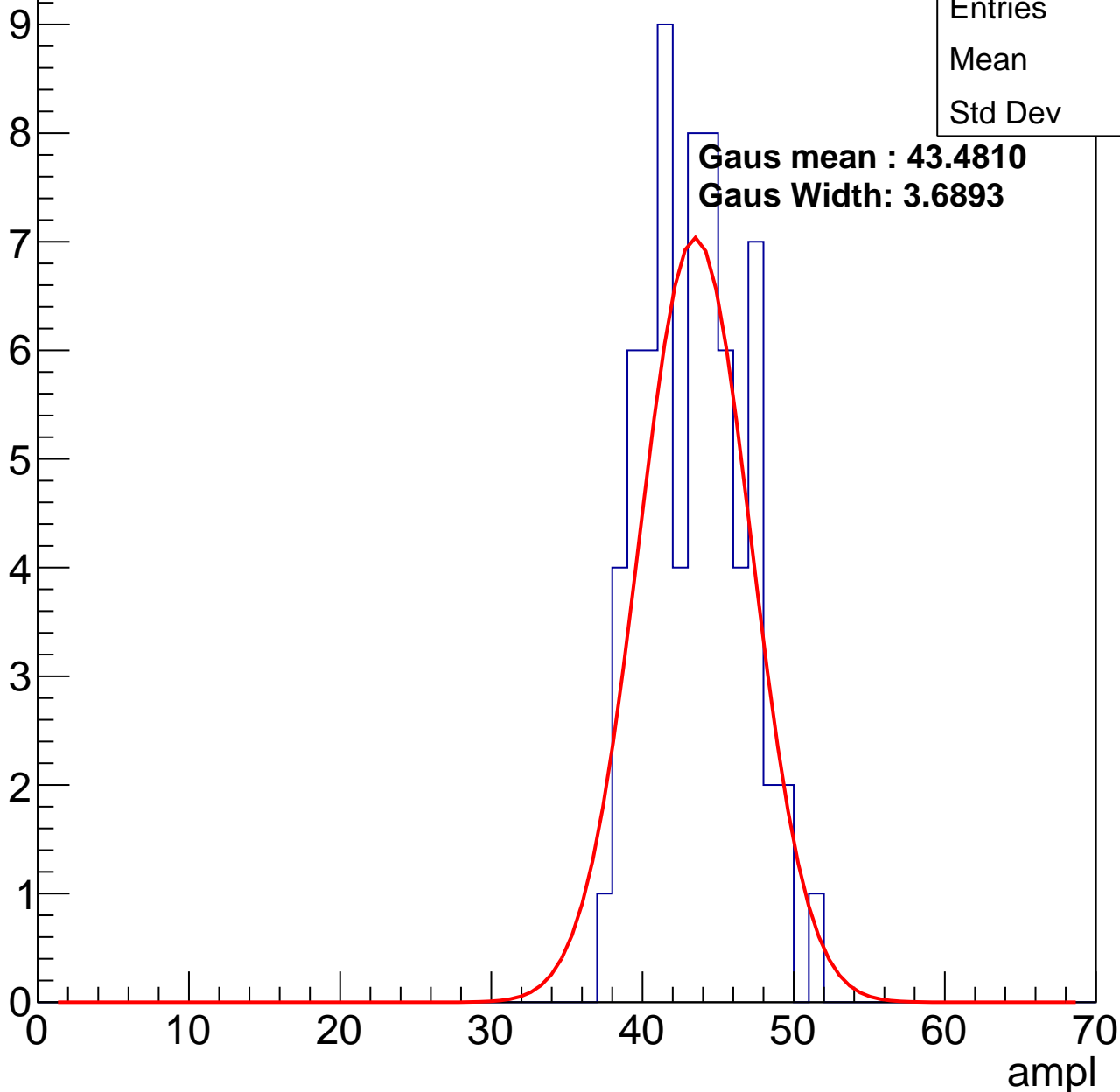
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43
Std Dev	3.19

Gaus mean : 43.4810

Gaus Width: 3.6893

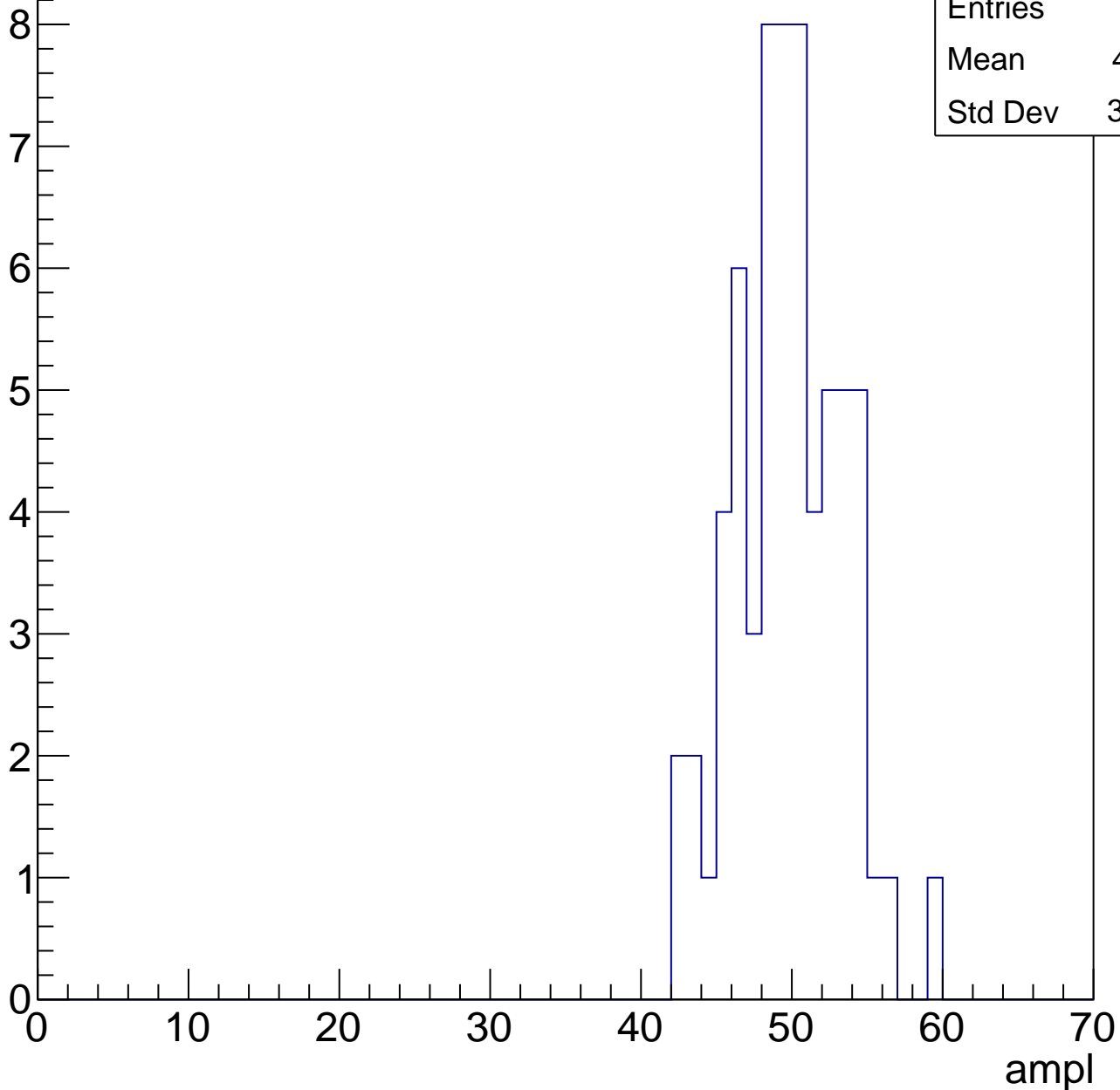


B0L001S, U18-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.31
Std Dev	3.504

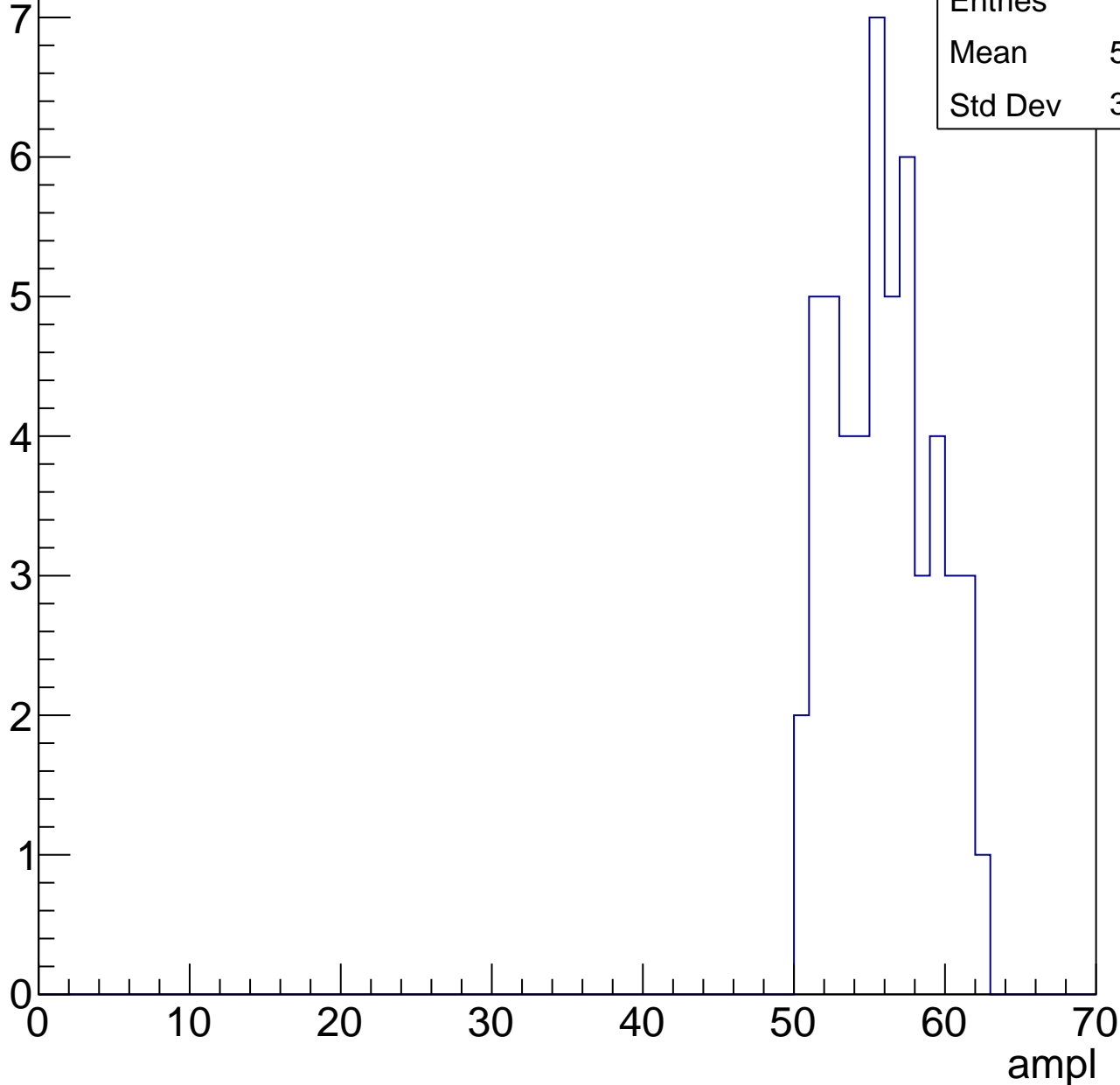


B0L001S, U18-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

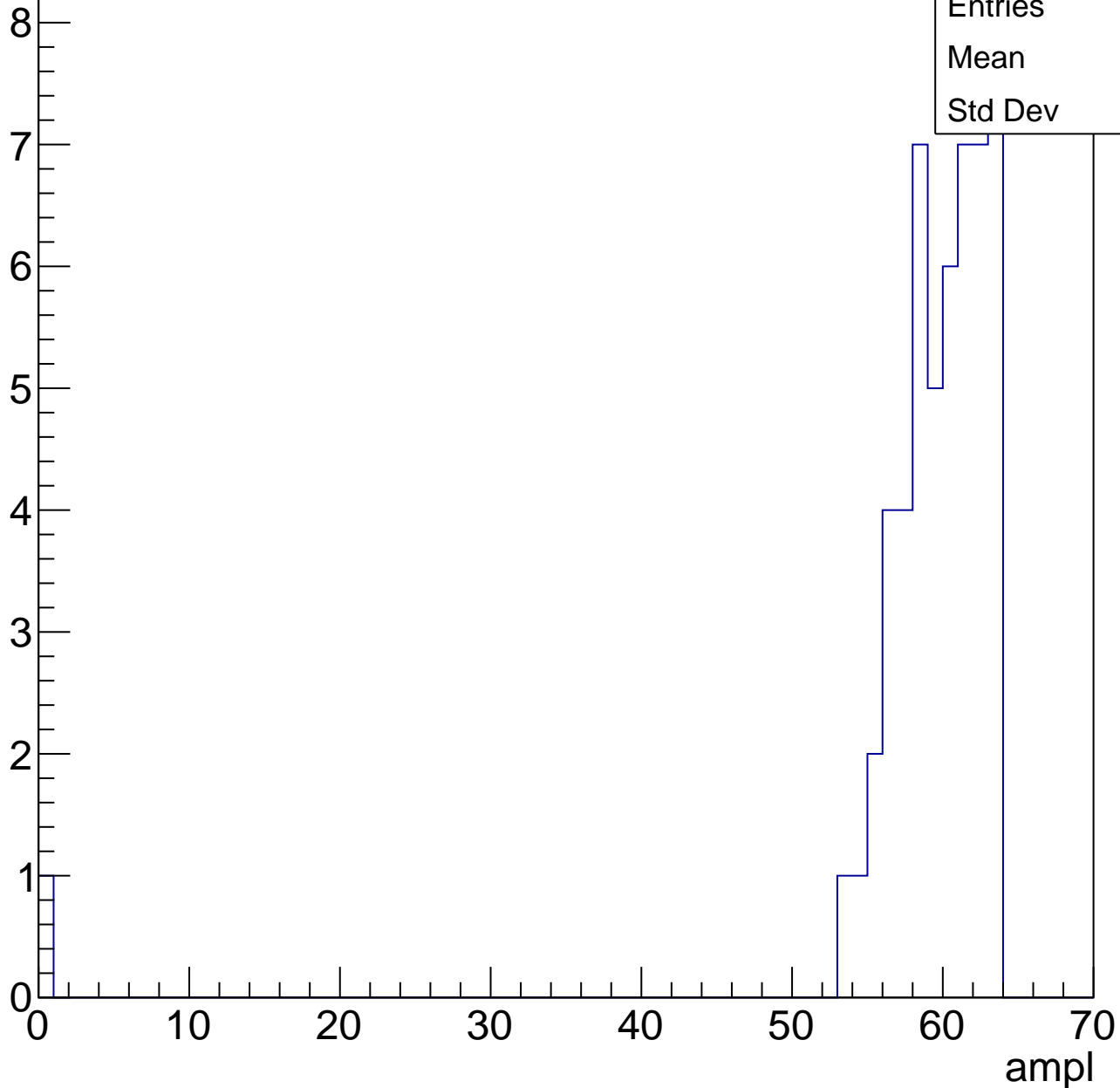
Entries	52
Mean	55.48
Std Dev	3.208



B0L001S, U18-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch98, adc0

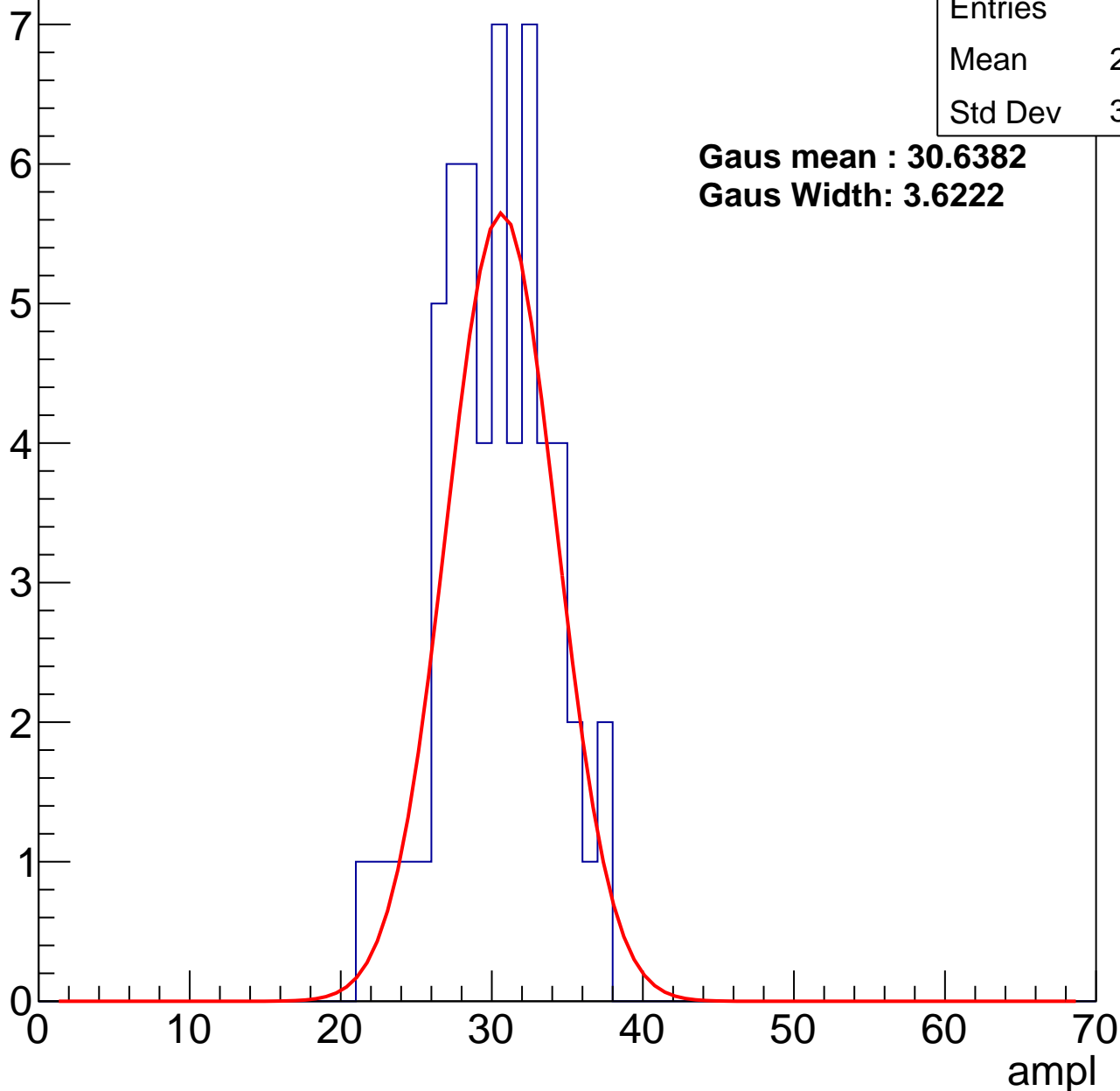
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.77
Std Dev	3.579

Gaus mean : 30.6382

Gaus Width: 3.6222



B0L001S, U18-ch98, adc1

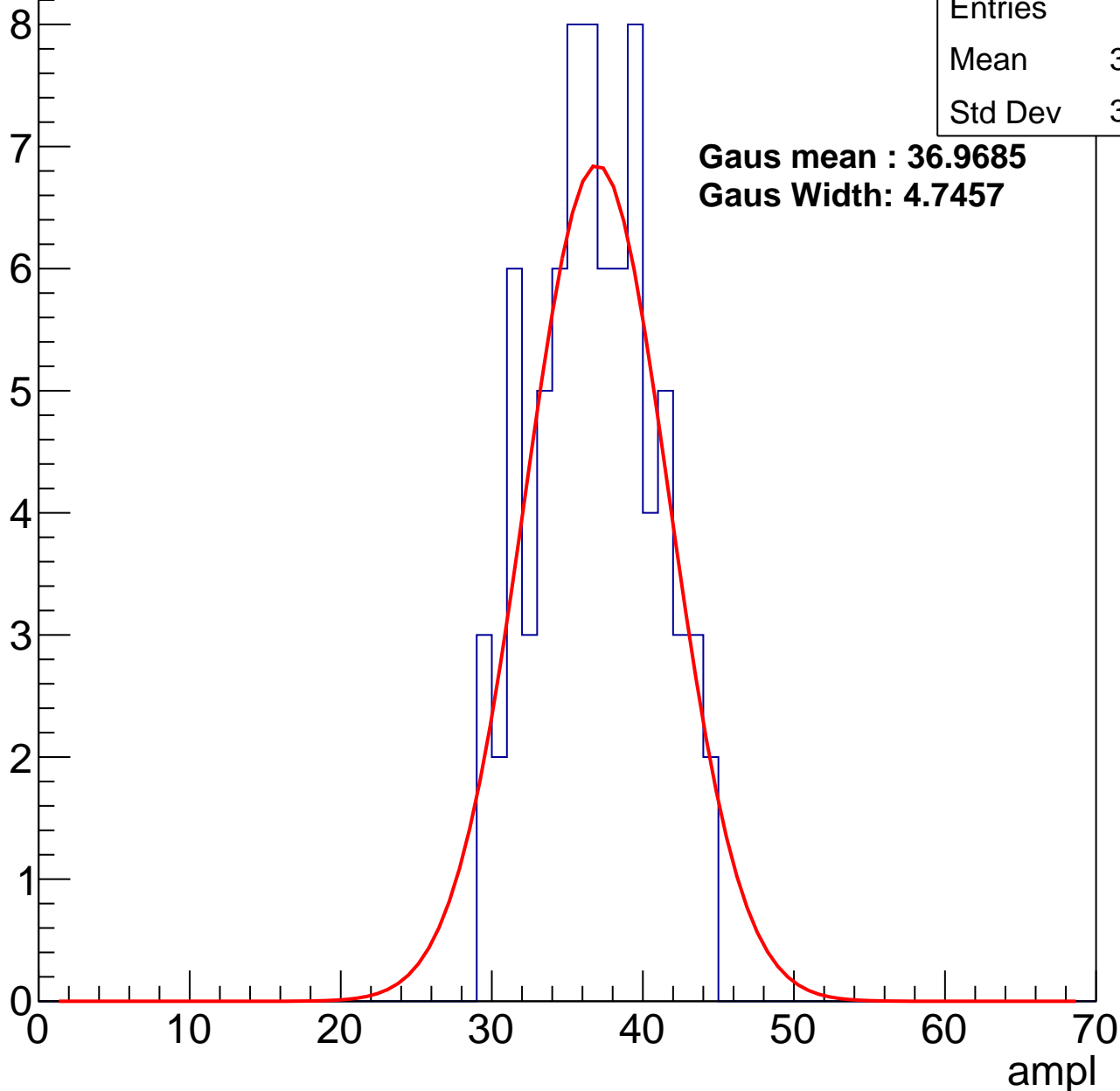
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.36
Std Dev	3.853

Gaus mean : 36.9685

Gaus Width: 4.7457



B0L001S, U18-ch98, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	43.95
Std Dev	3.72

Gaus mean : 44.2376

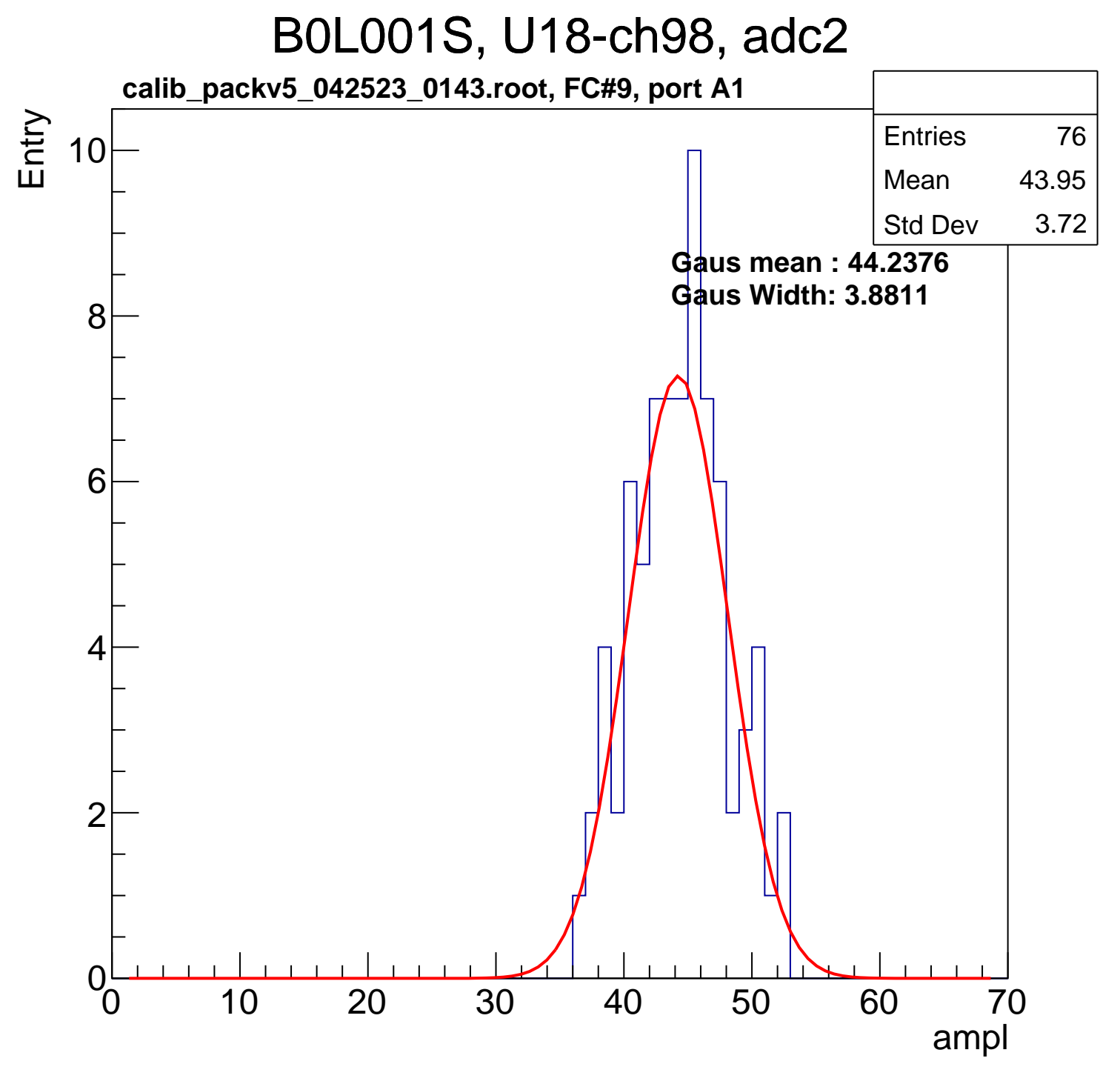
Gaus Width: 3.8811

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

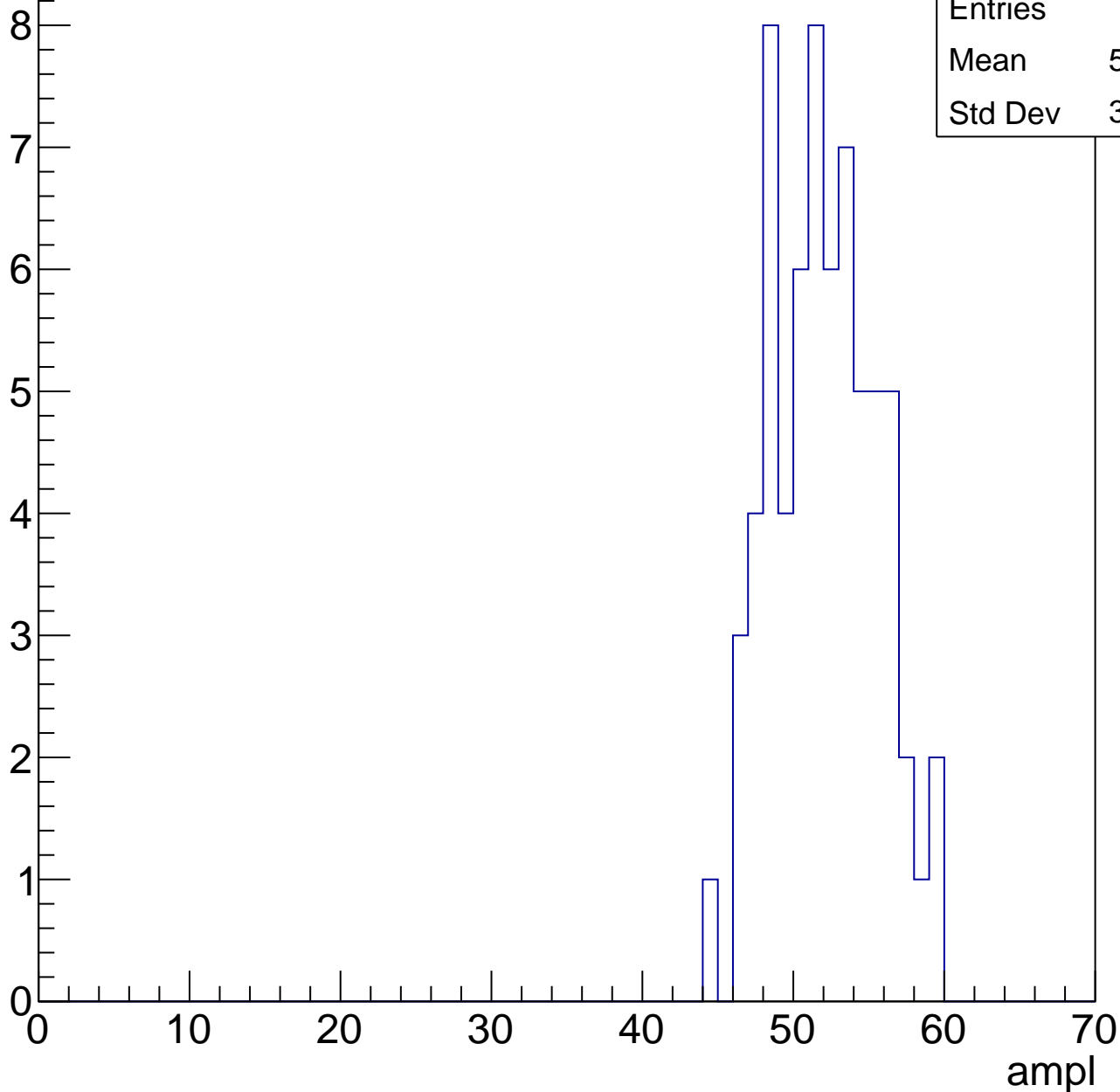


B0L001S, U18-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	51.58
Std Dev	3.439

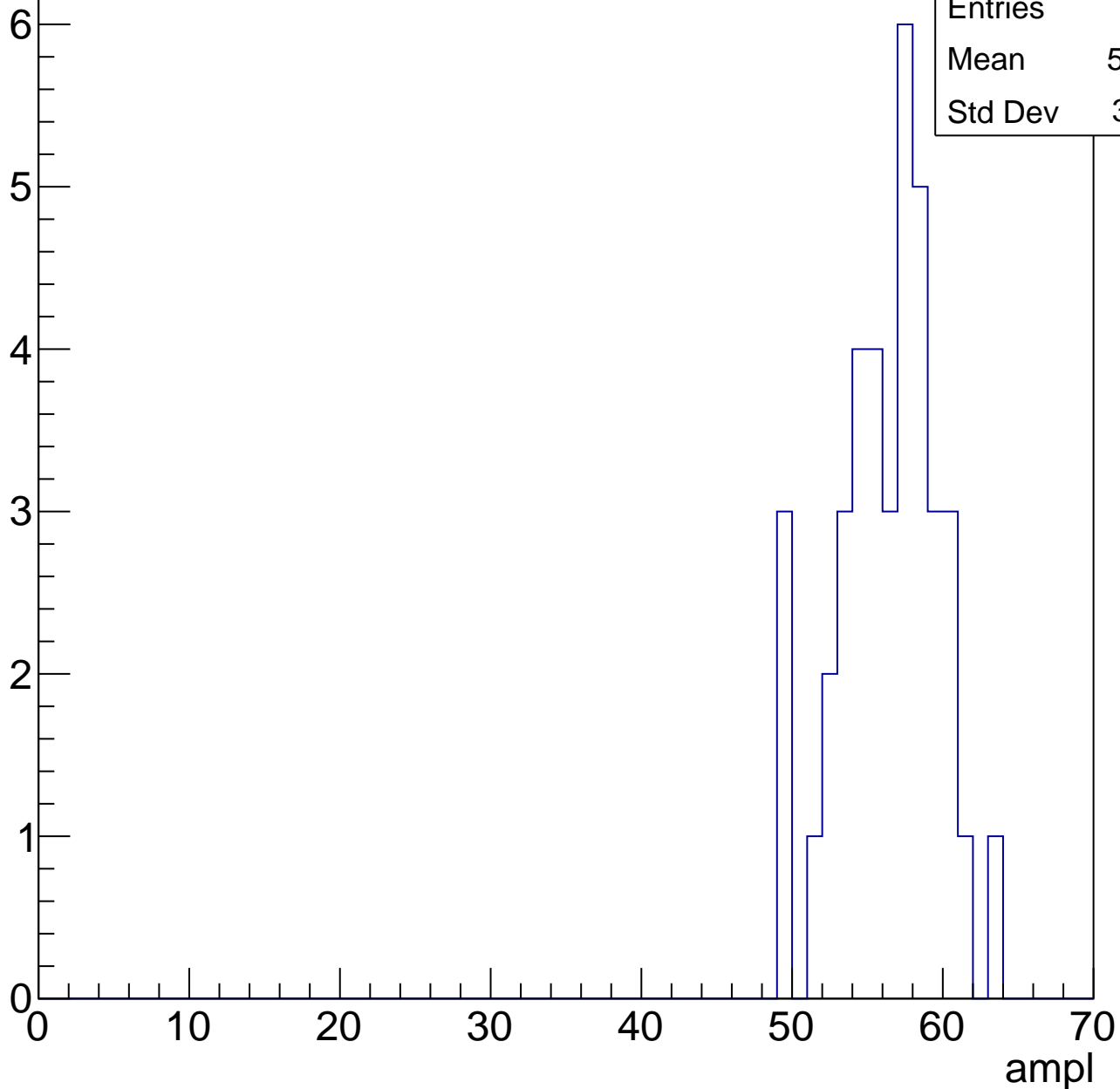


B0L001S, U18-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	55.85
Std Dev	3.301

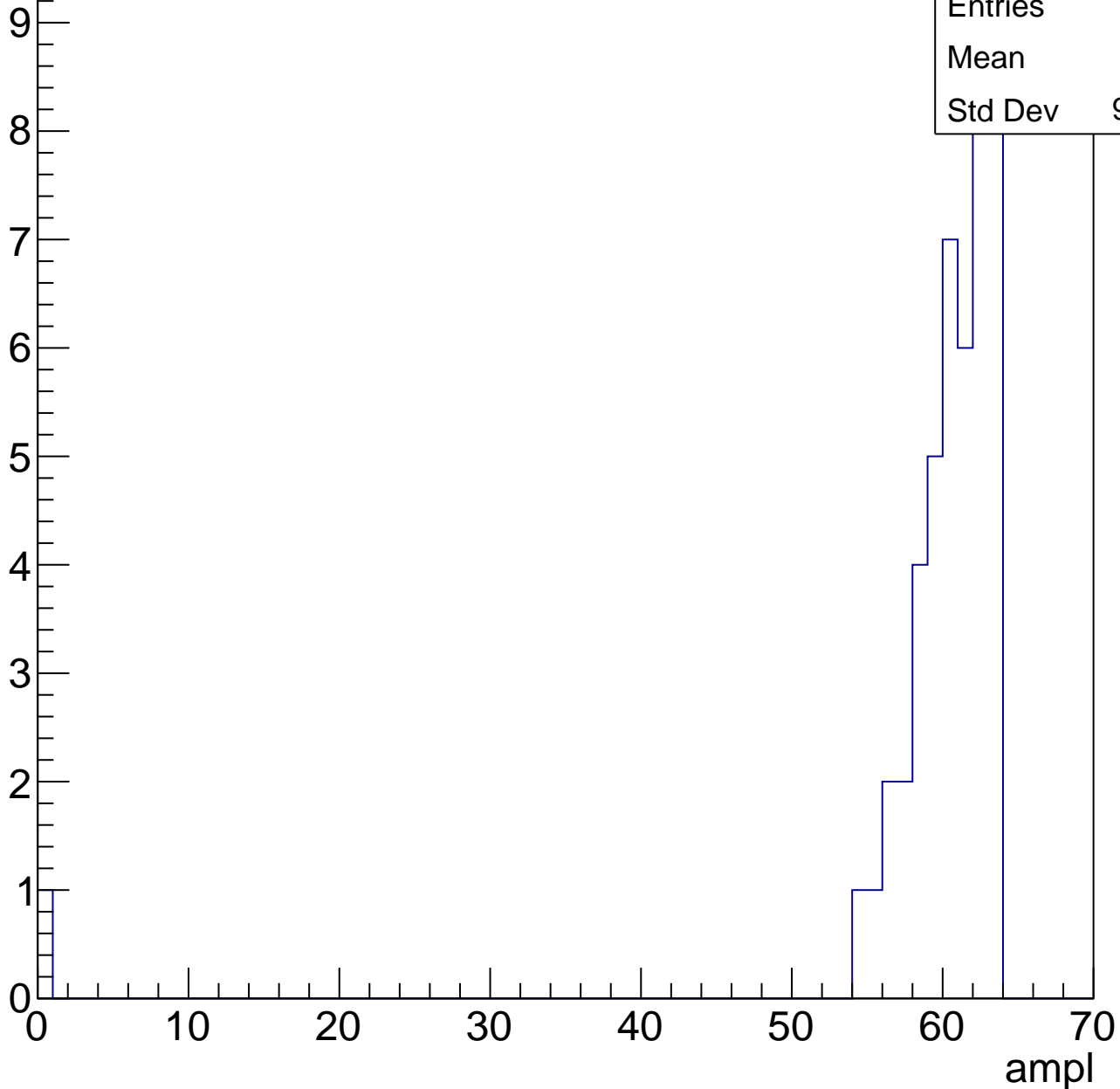


B0L001S, U18-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

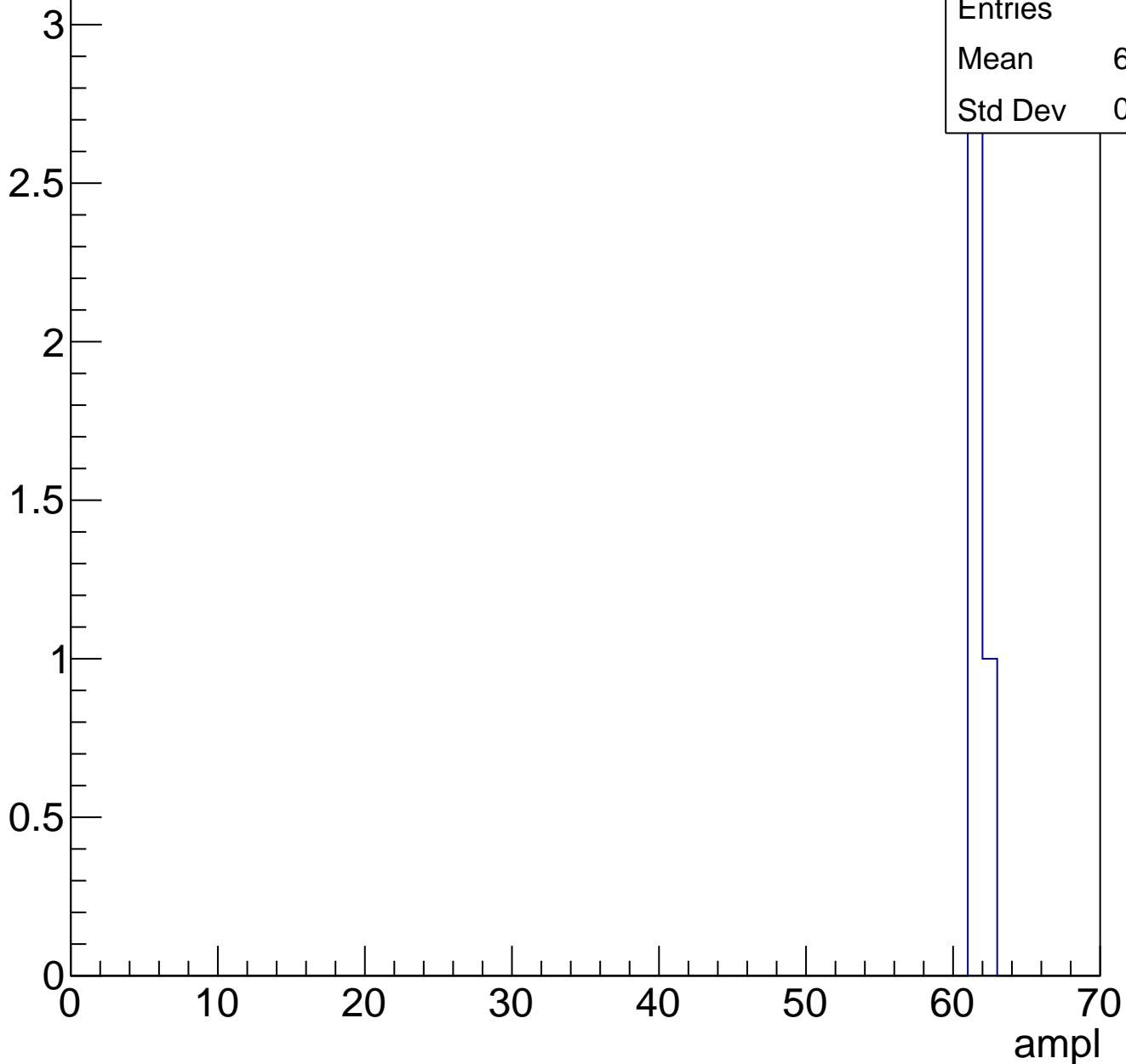
Entries	47
Mean	59
Std Dev	9.001



B0L001S, U18-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch99, adc0

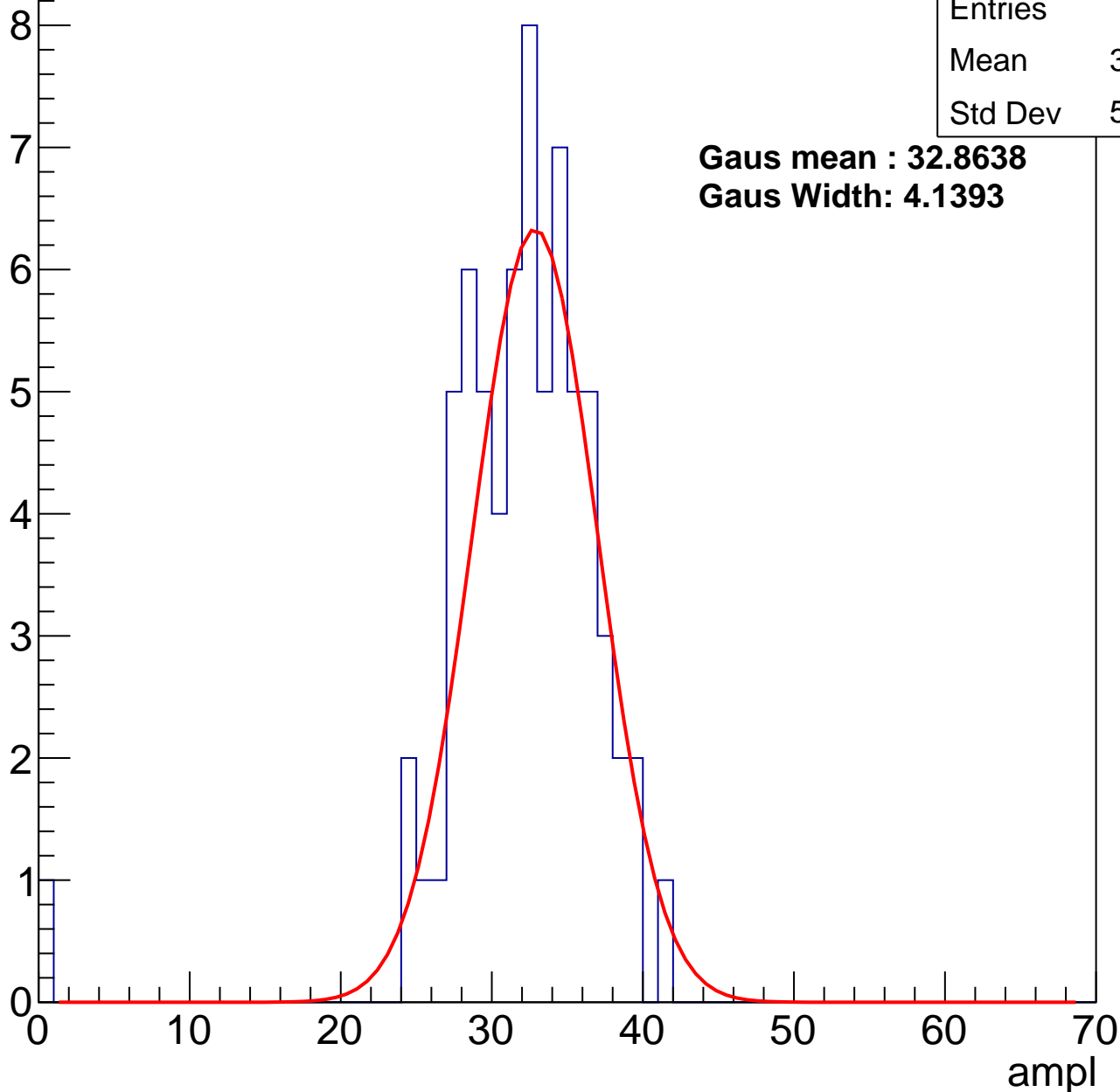
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.49
Std Dev	5.372

Gaus mean : 32.8638

Gaus Width: 4.1393



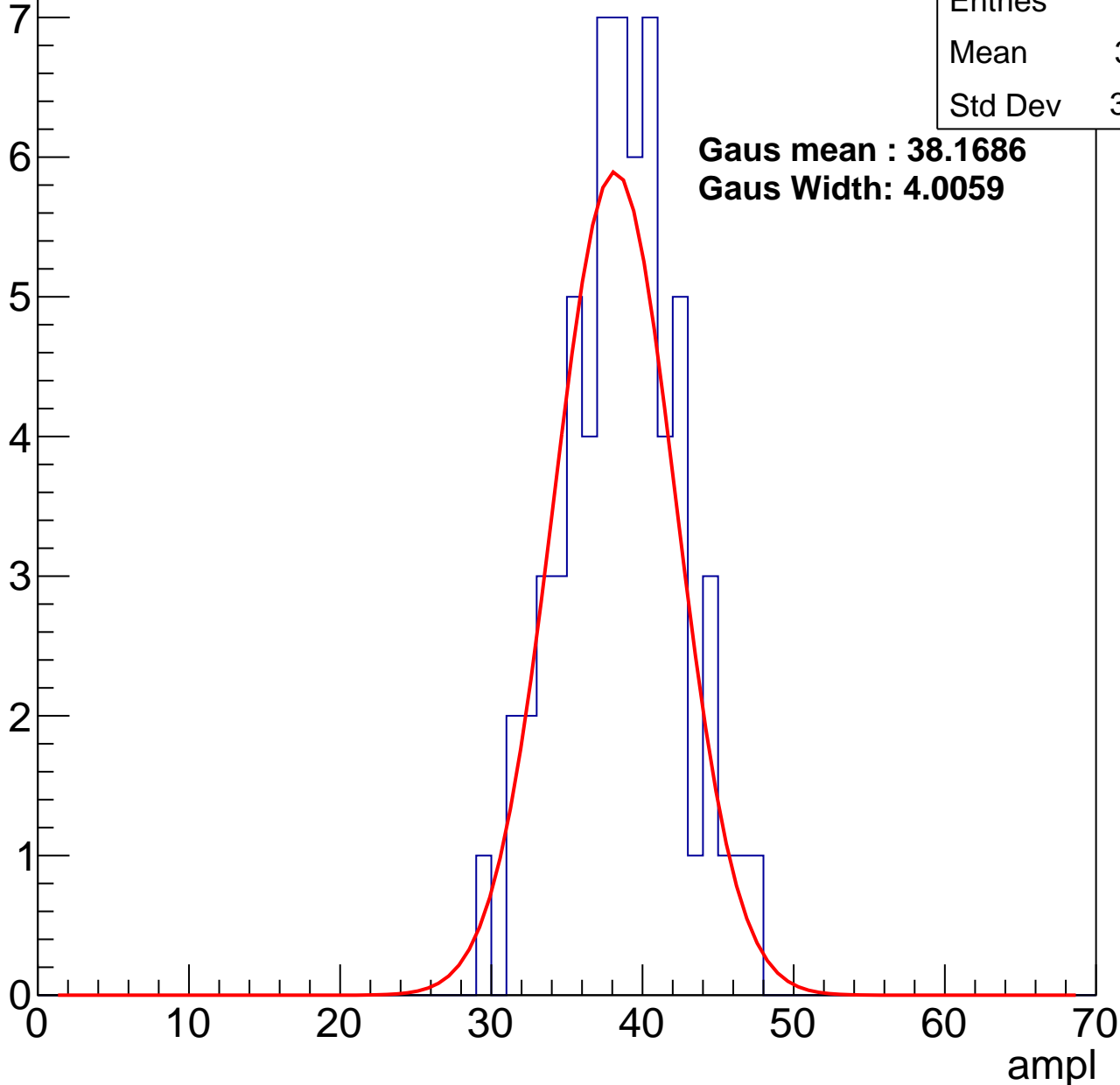
B0L001S, U18-ch99, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.11
Std Dev	3.818

Gaus mean : 38.1686
Gaus Width: 4.0059



B0L001S, U18-ch99, adc2

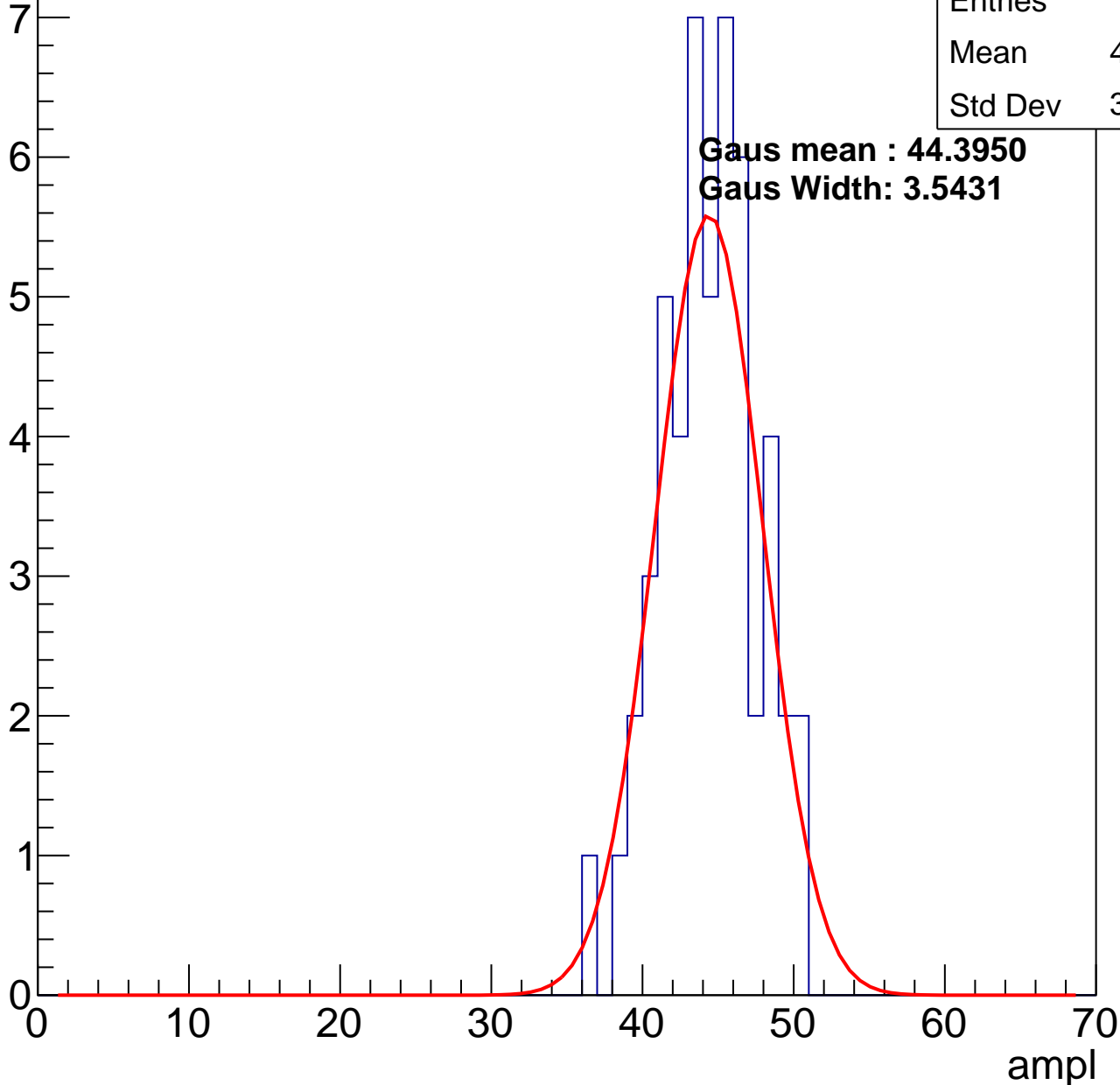
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	43.94
Std Dev	3.134

Gaus mean : 44.3950

Gaus Width: 3.5431



B0L001S, U18-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

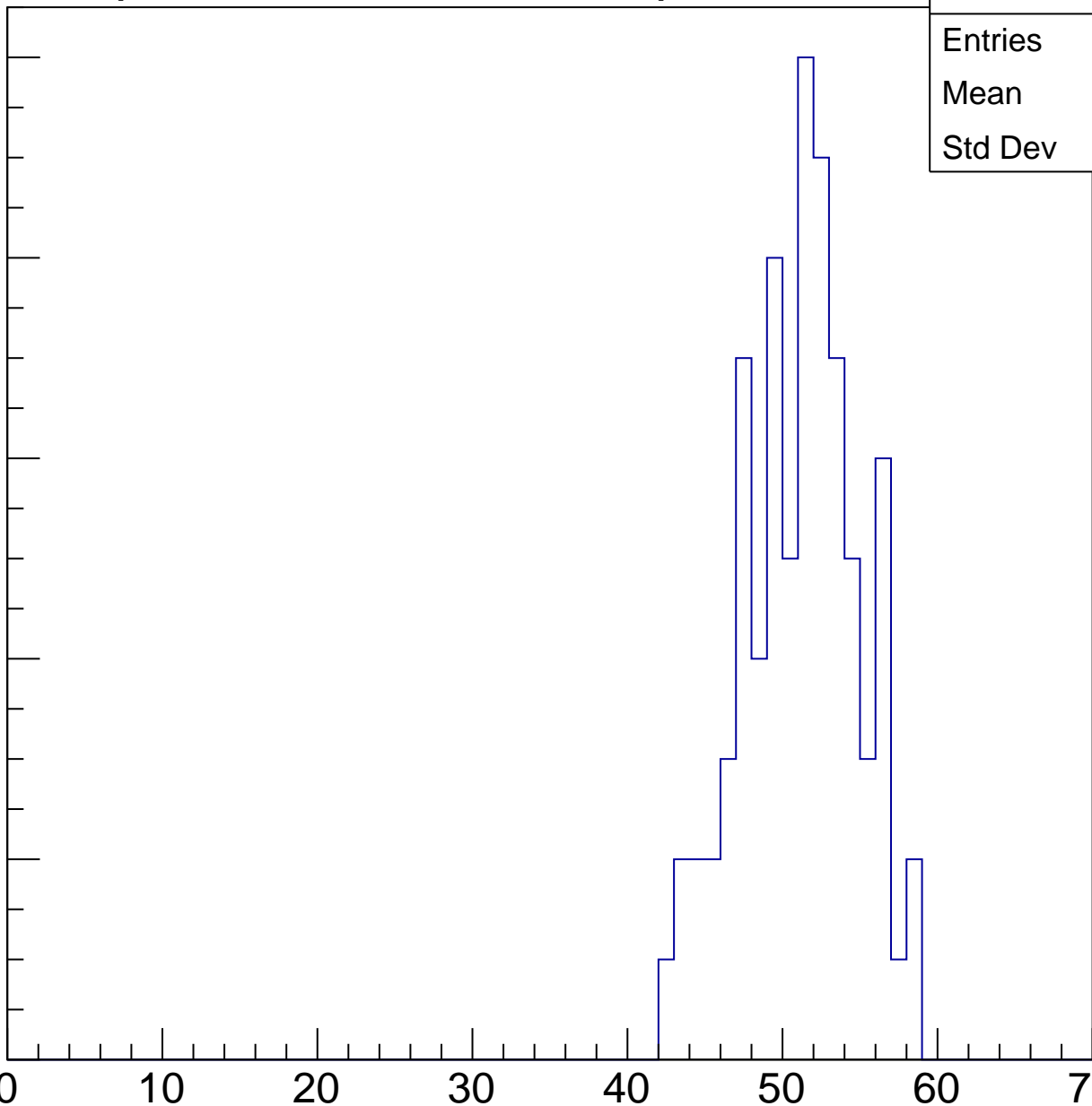
Entries	77
Mean	50.65
Std Dev	3.681

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

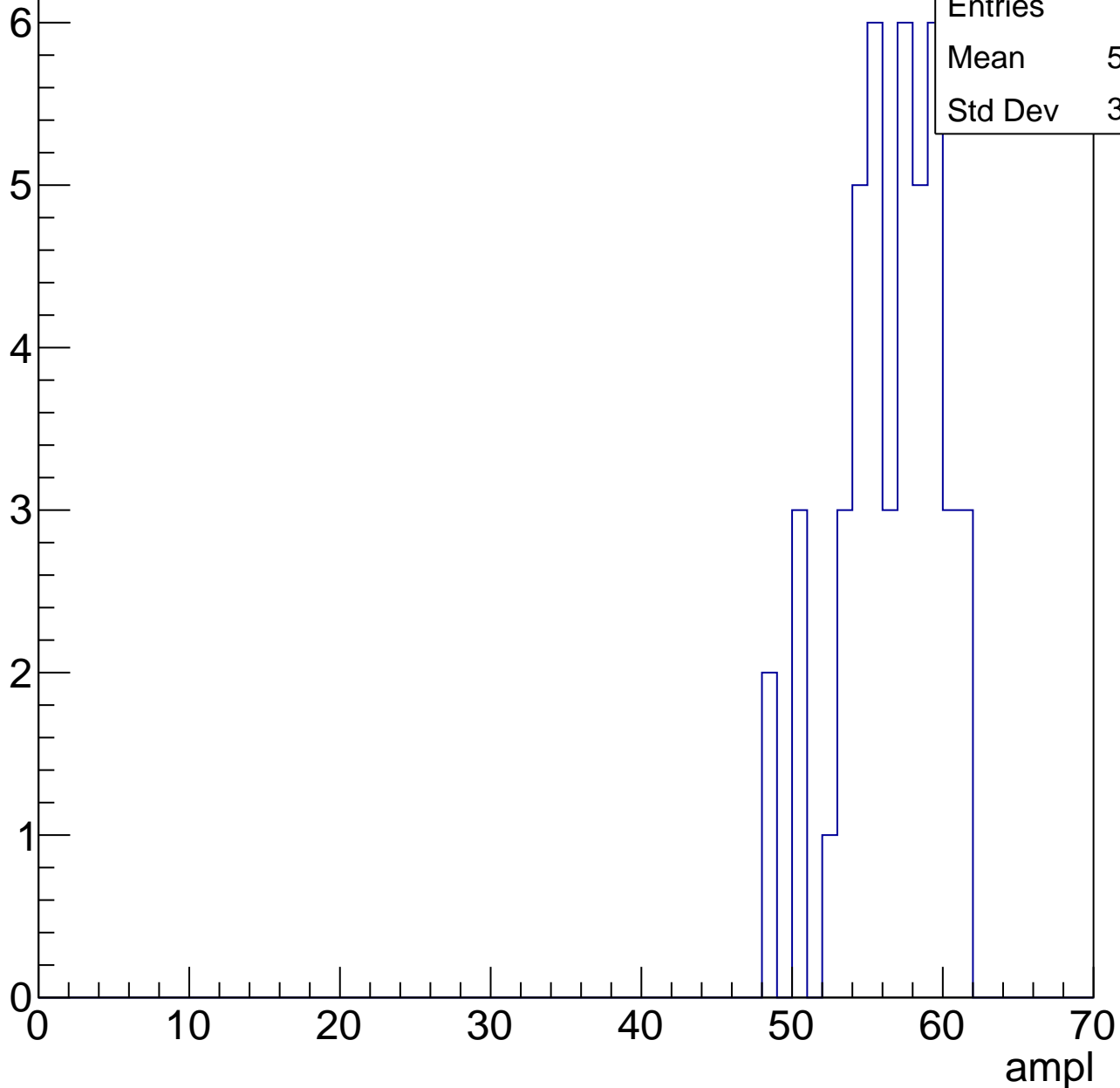
ampl



B0L001S, U18-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

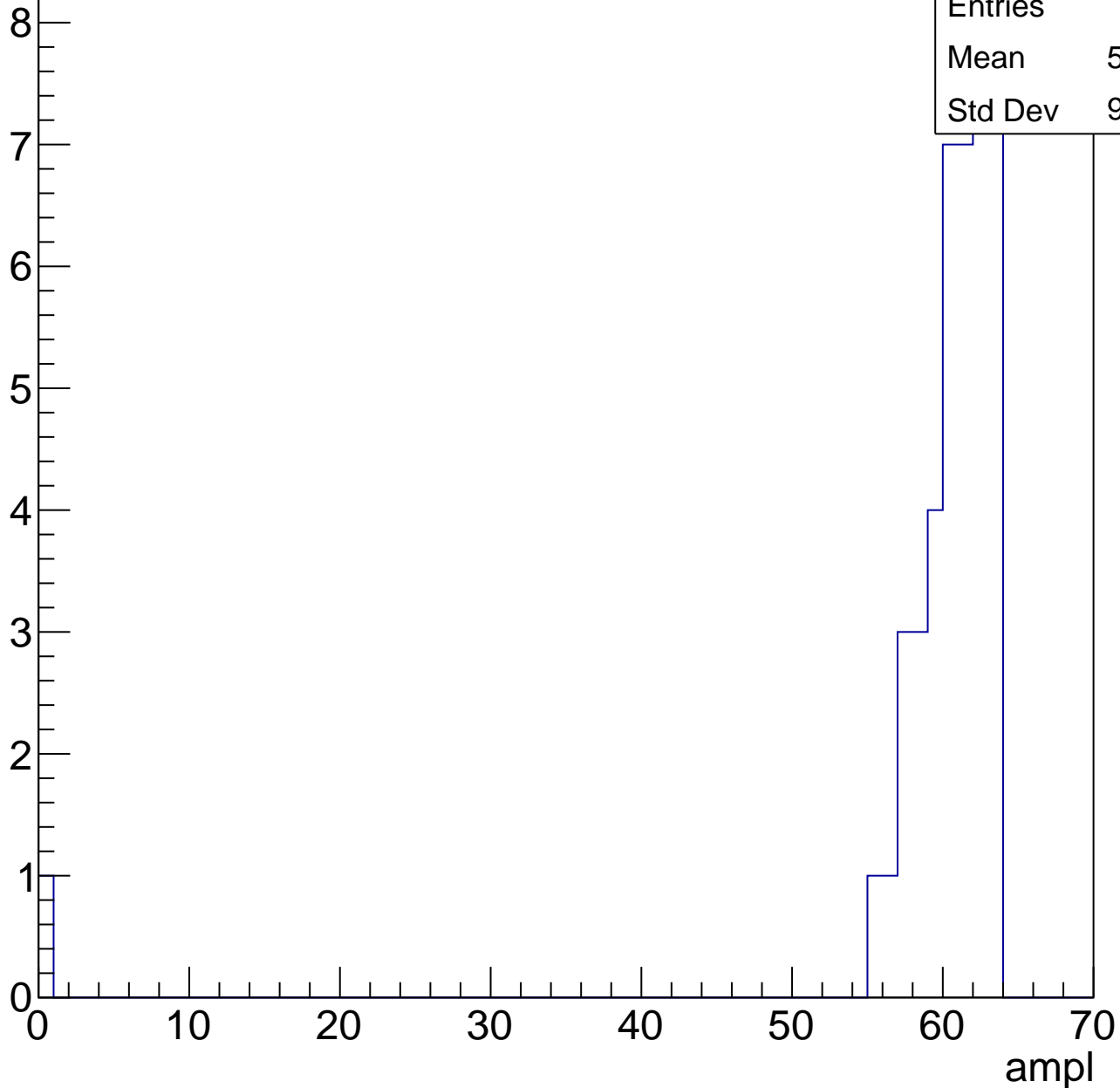


Entries	46
Mean	55.96
Std Dev	3.323

B0L001S, U18-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch100, adc0

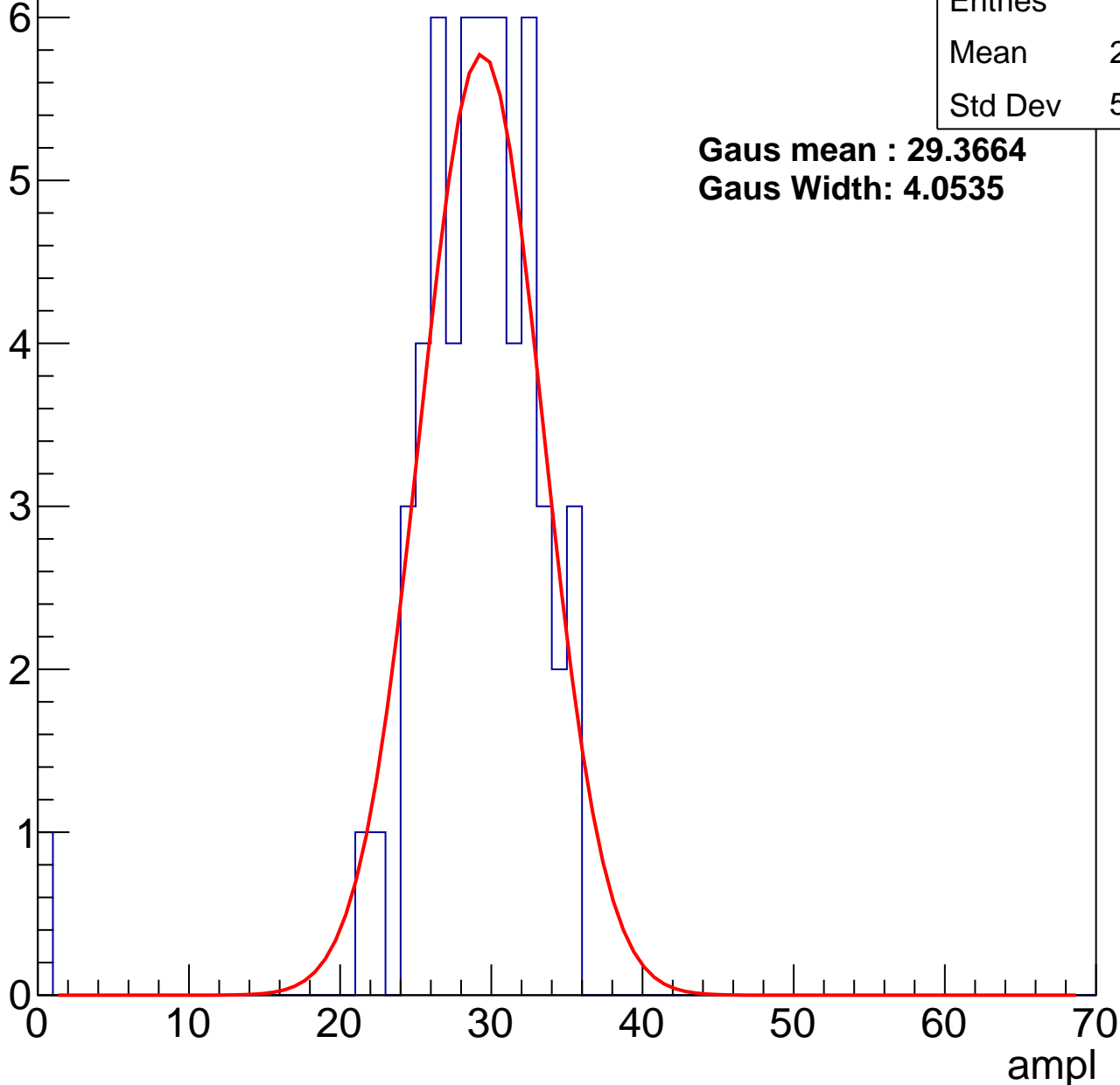
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	28.38
Std Dev	5.052

Gaus mean : 29.3664

Gaus Width: 4.0535



B0L001S, U18-ch100, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	97
Mean	36.44
Std Dev	3.943

Gaus mean : 37.3018

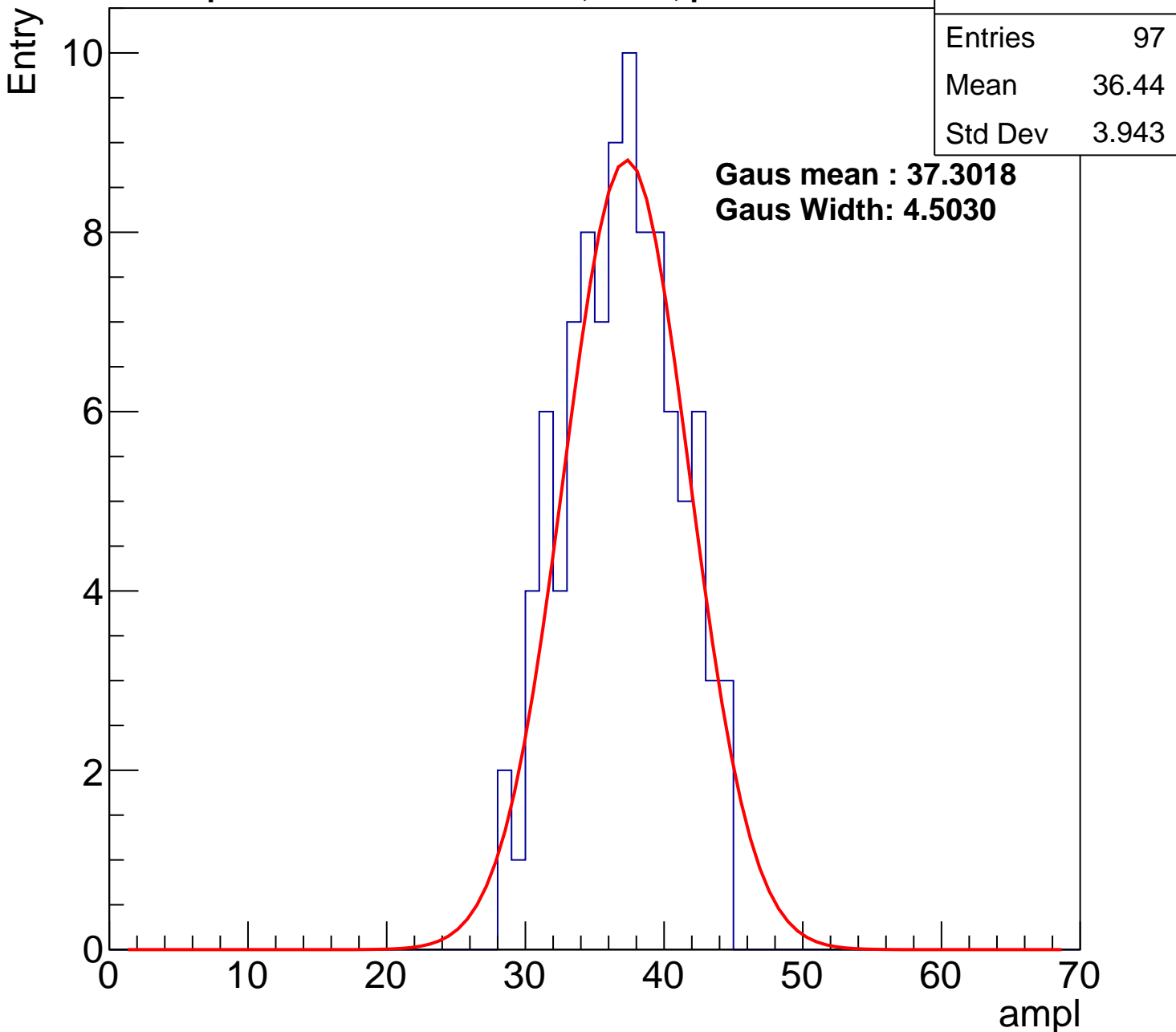
Gaus Width: 4.5030

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch100, adc2

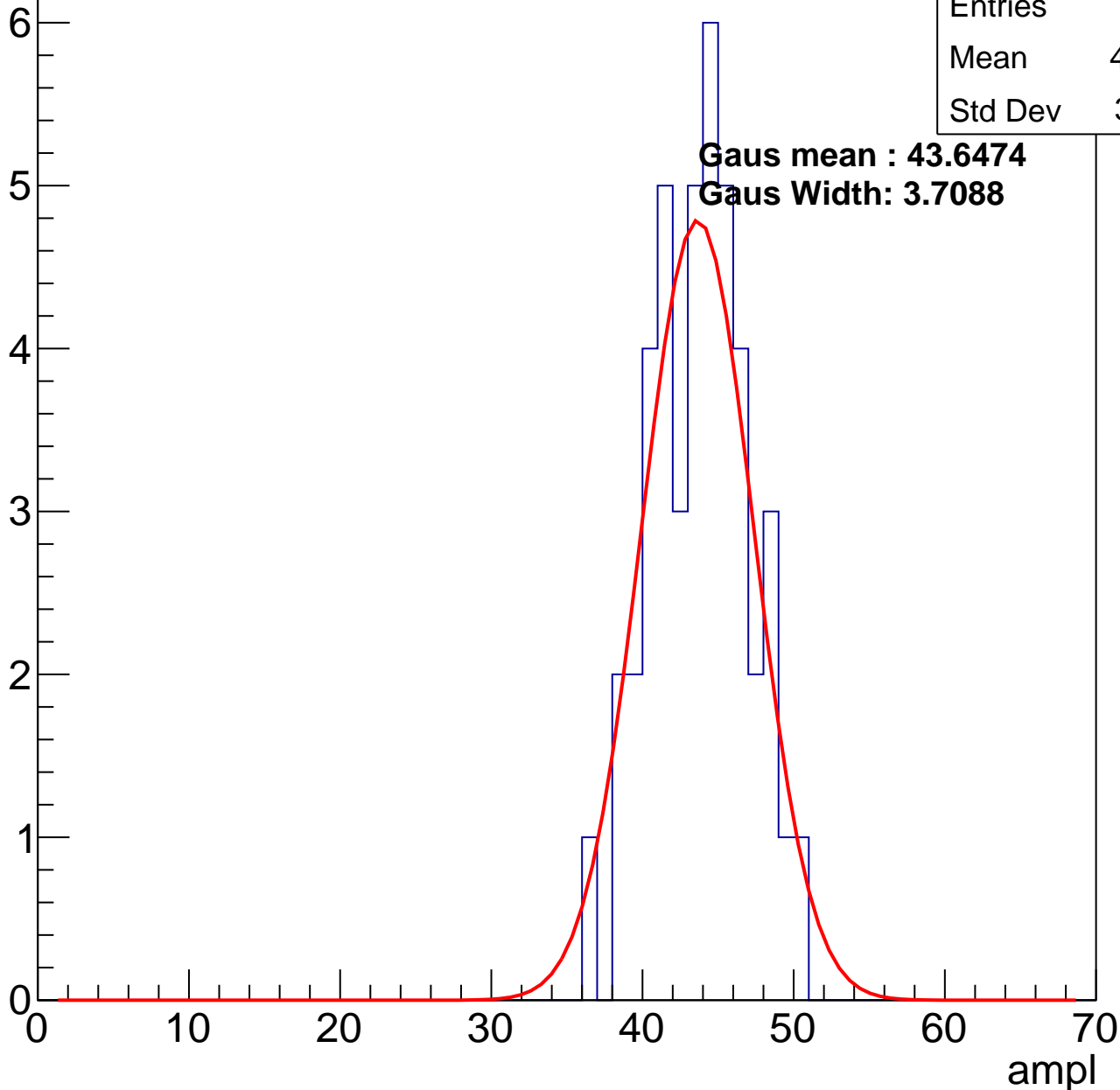
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	43.32
Std Dev	3.161

Gaus mean : 43.6474

Gaus Width: 3.7088

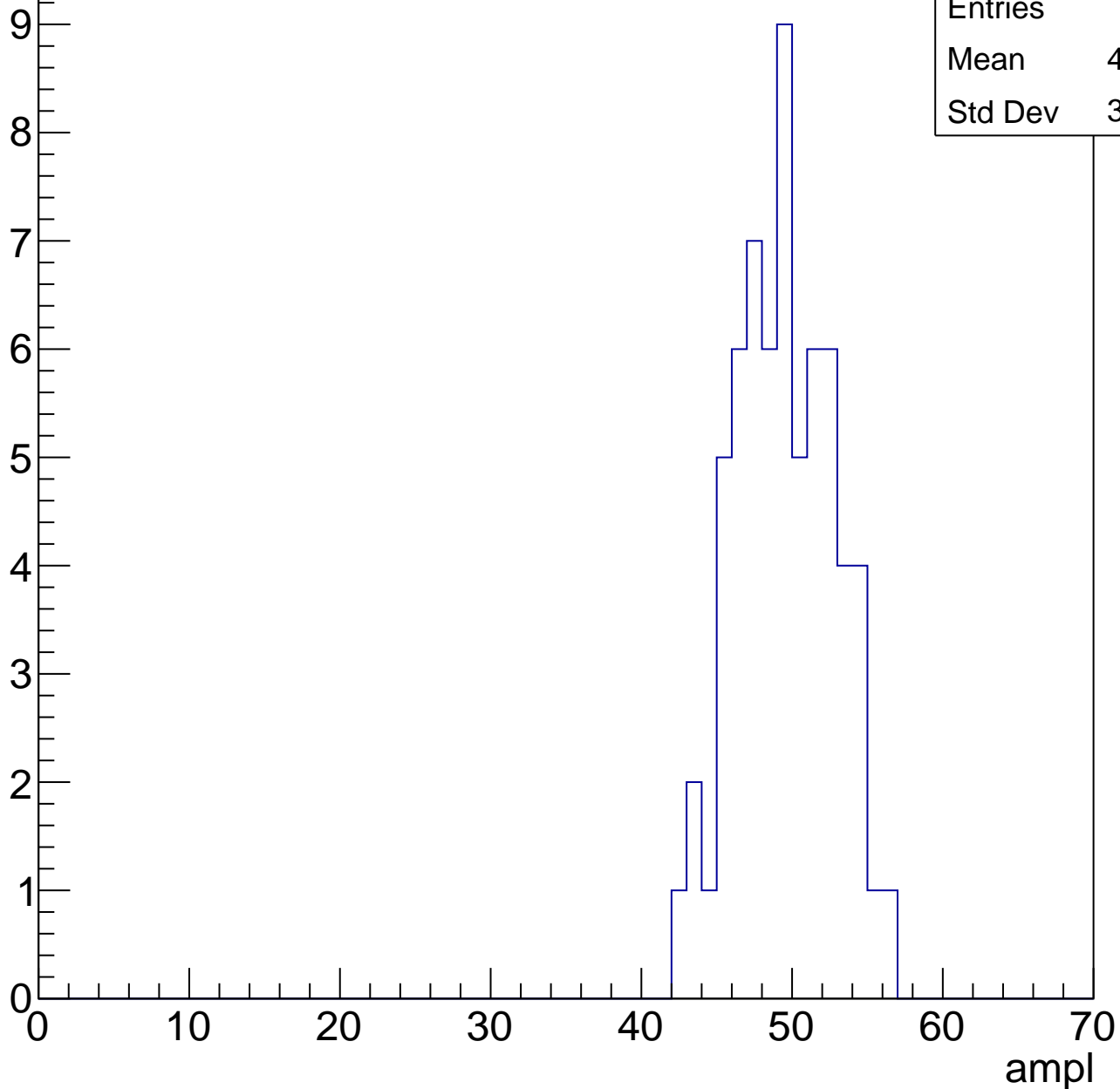


B0L001S, U18-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.03
Std Dev	3.177

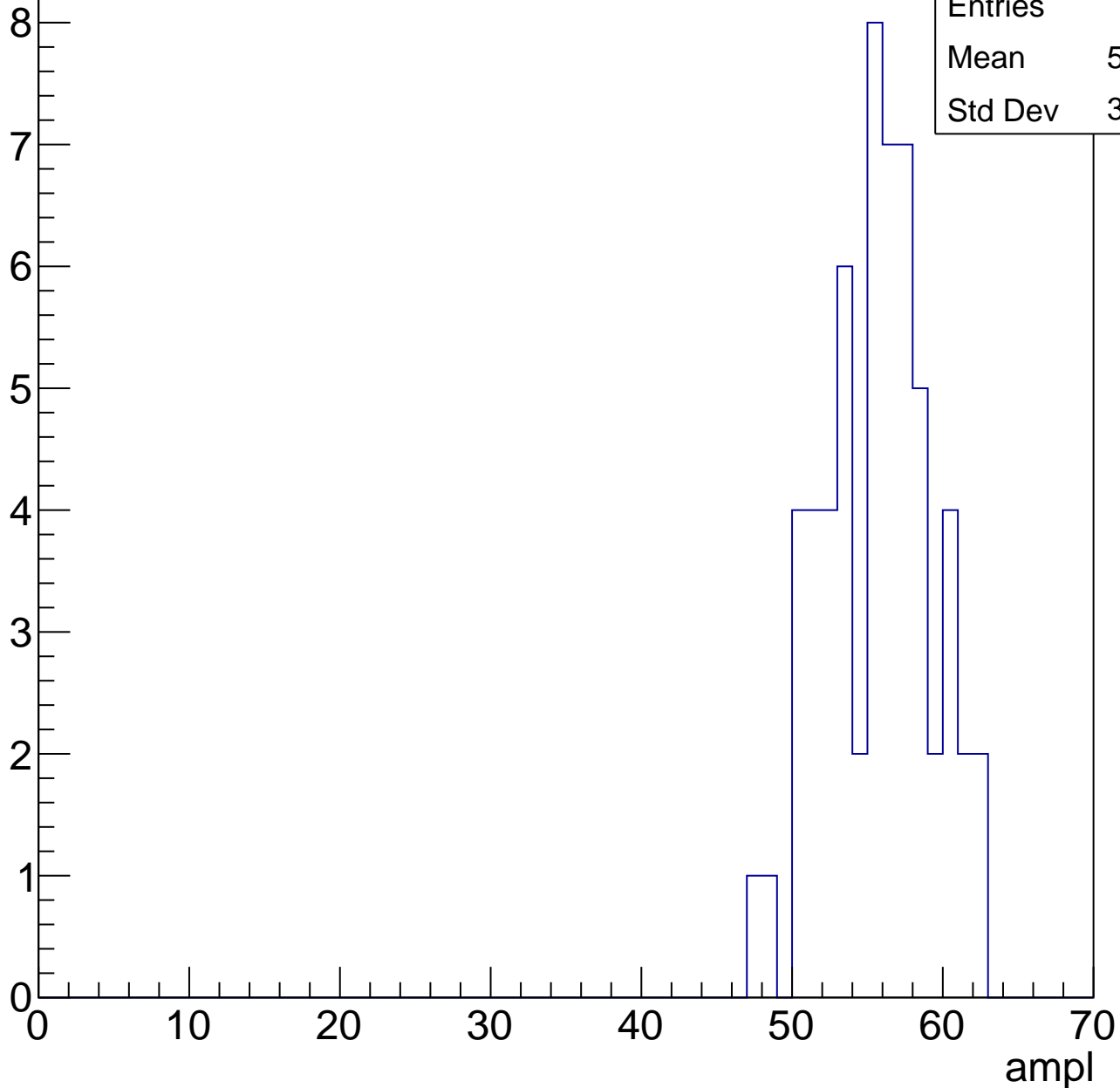


B0L001S, U18-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.22
Std Dev	3.489

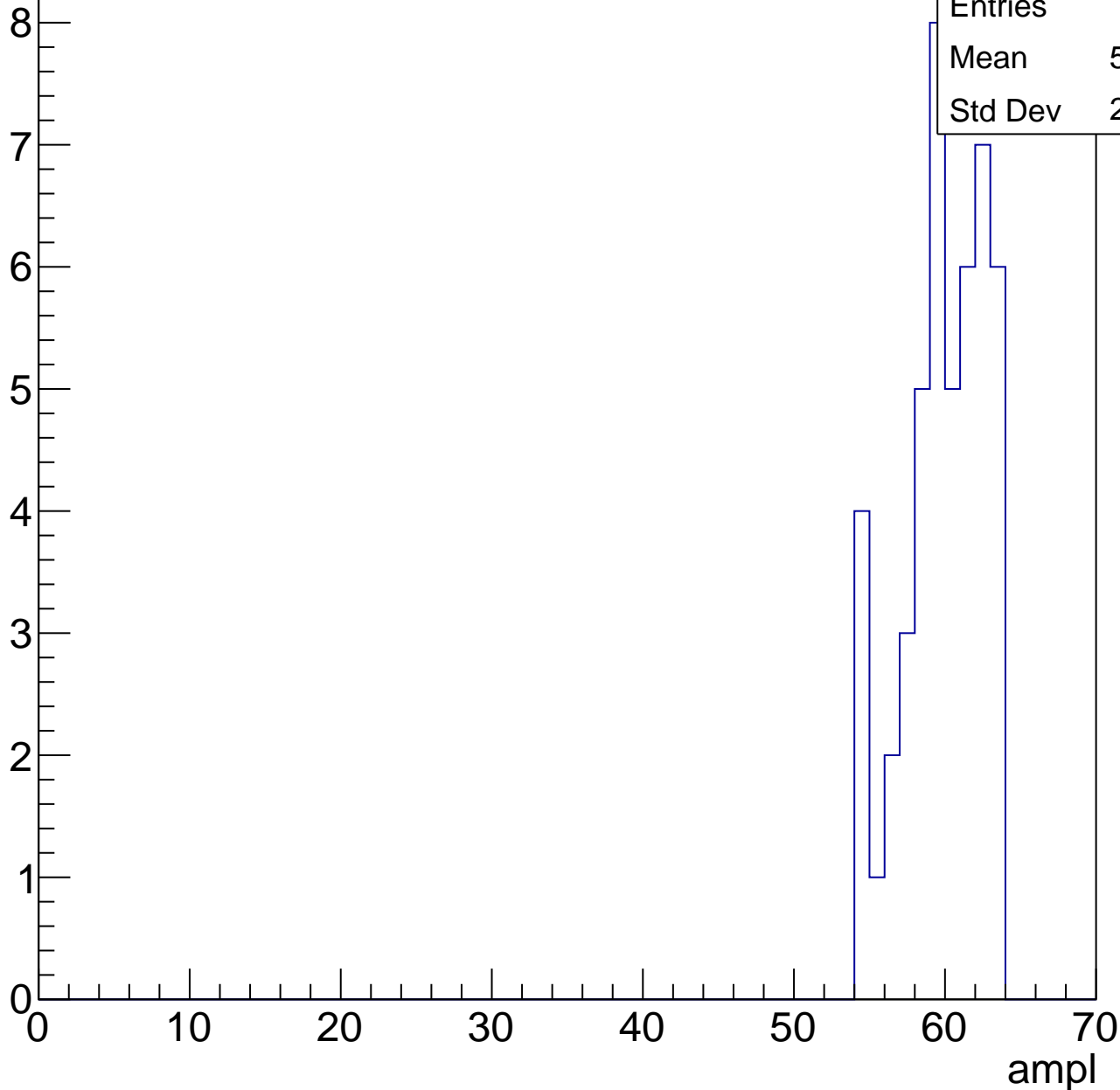


B0L001S, U18-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

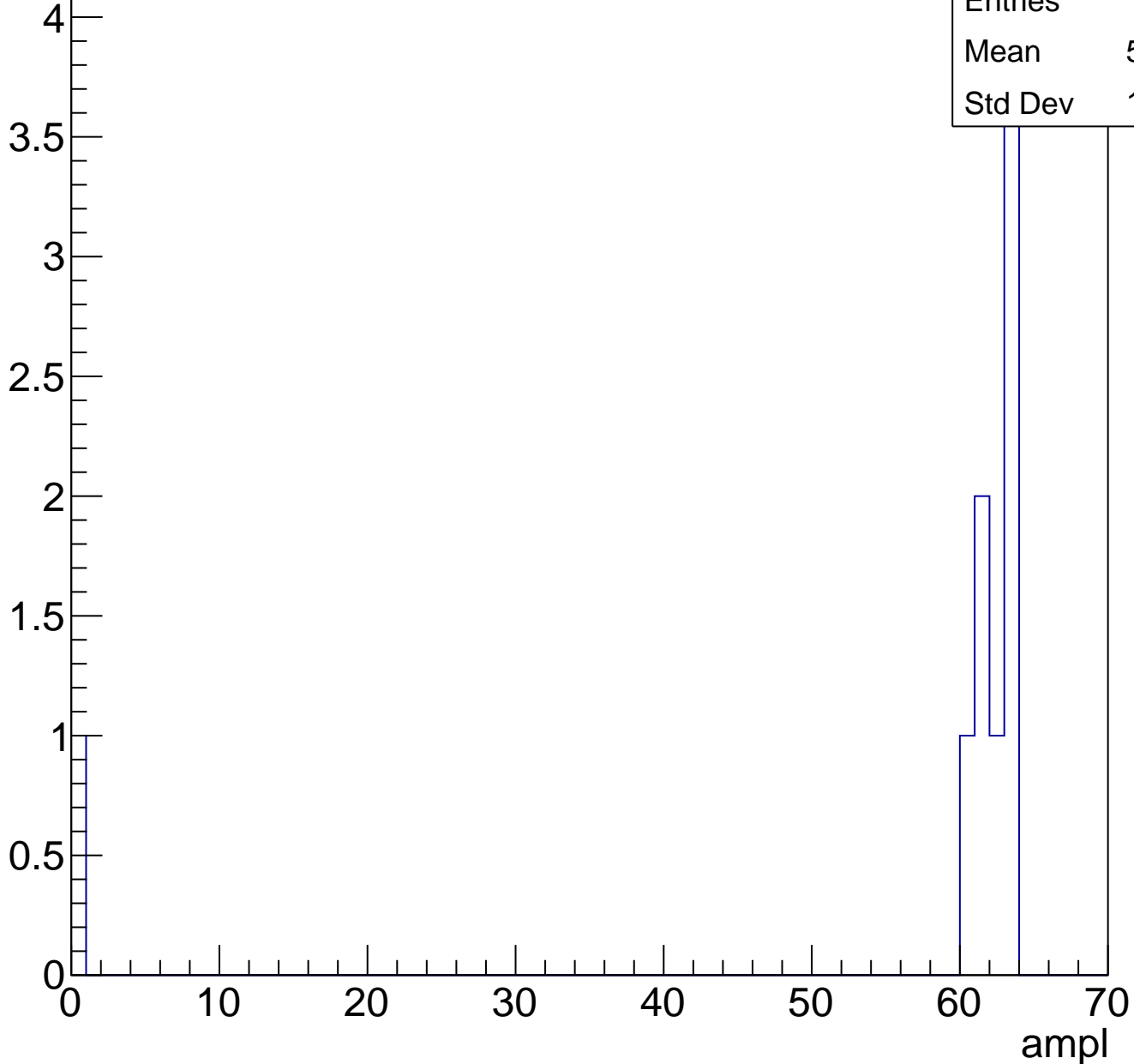
Entries	47
Mean	59.45
Std Dev	2.648



B0L001S, U18-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch101, adc0

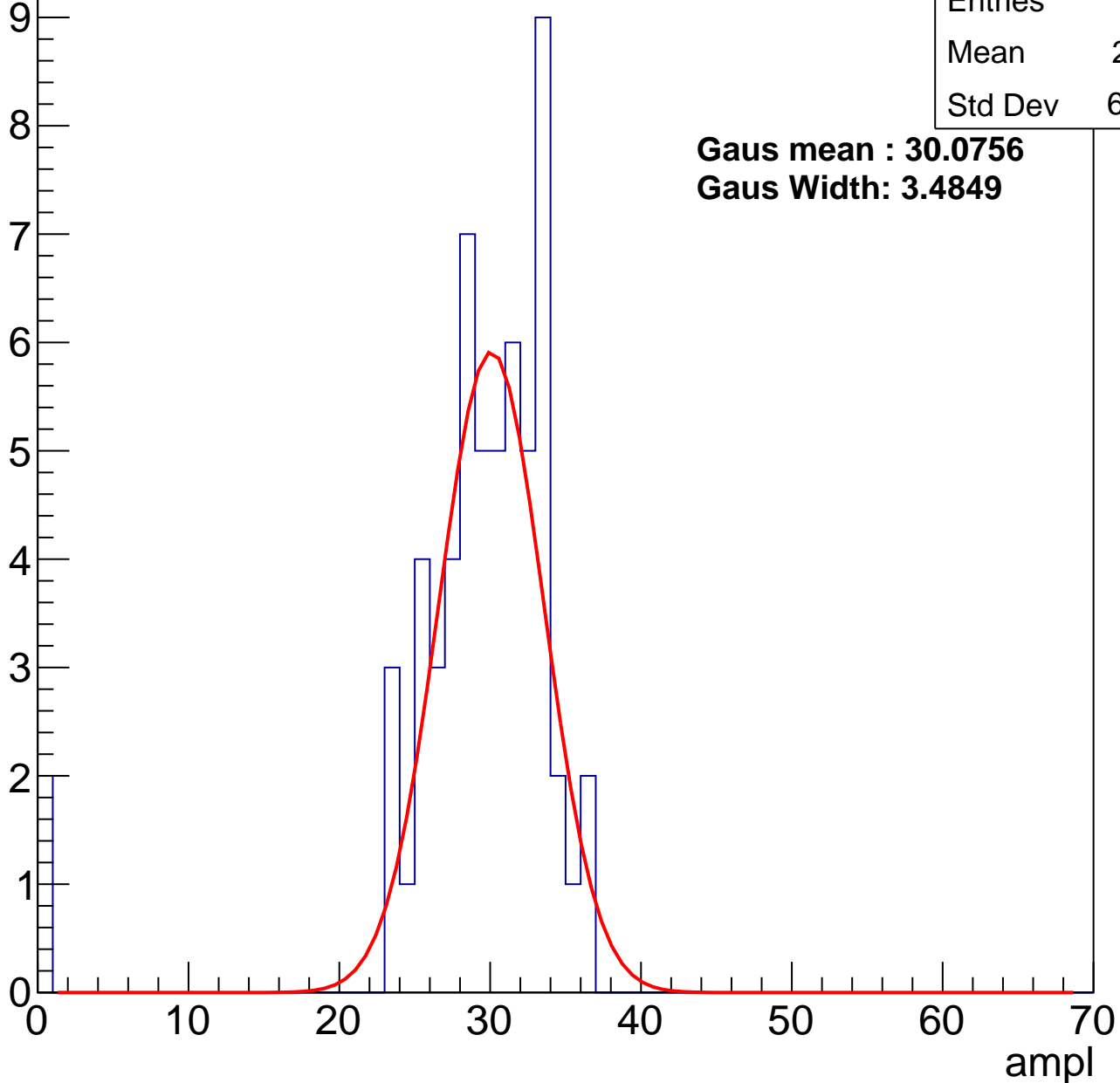
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	28.61
Std Dev	6.273

Gaus mean : 30.0756

Gaus Width: 3.4849



B0L001S, U18-ch101, adc1

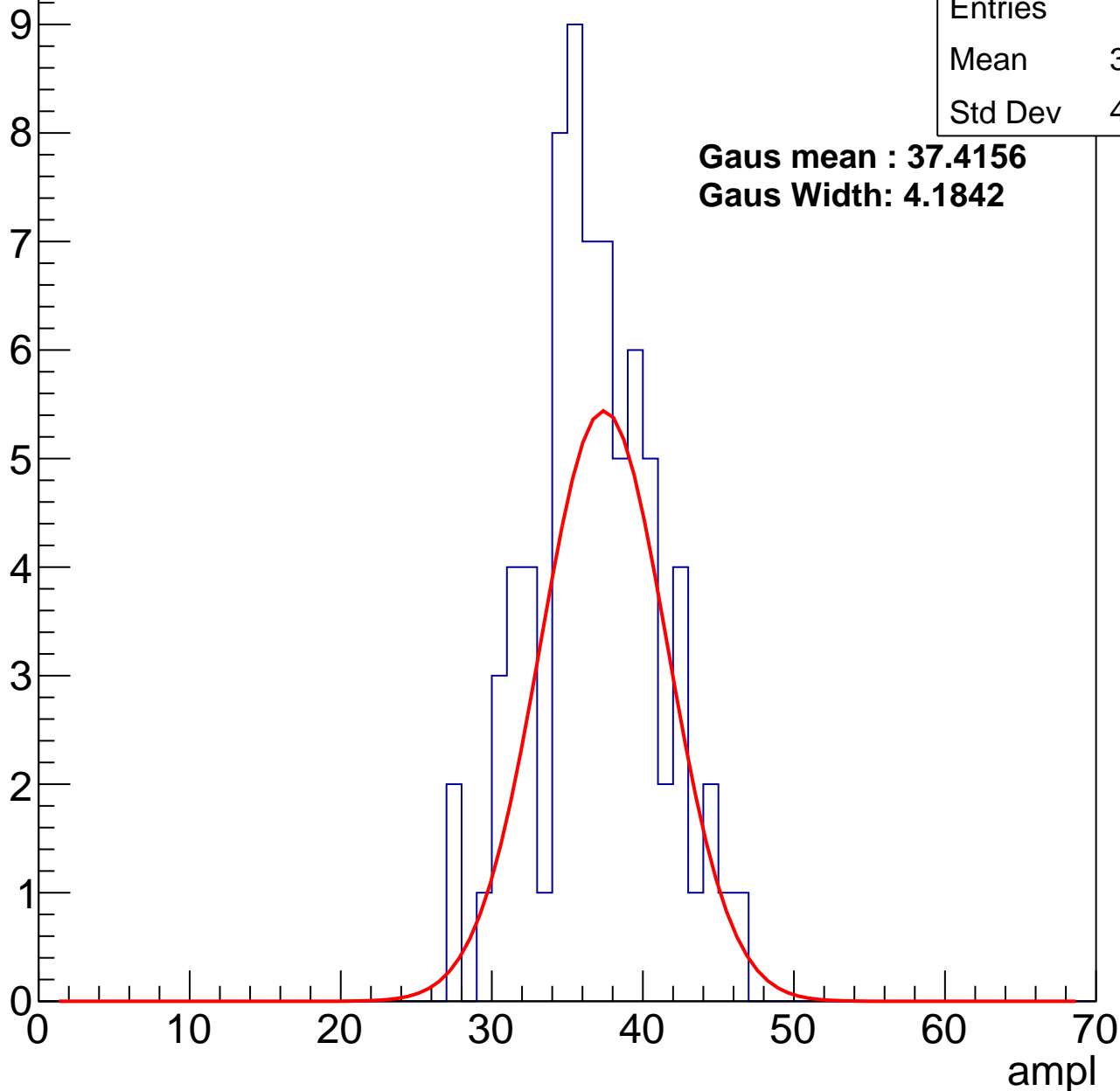
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	36.33
Std Dev	4.118

Gaus mean : 37.4156

Gaus Width: 4.1842



B0L001S, U18-ch101, adc2

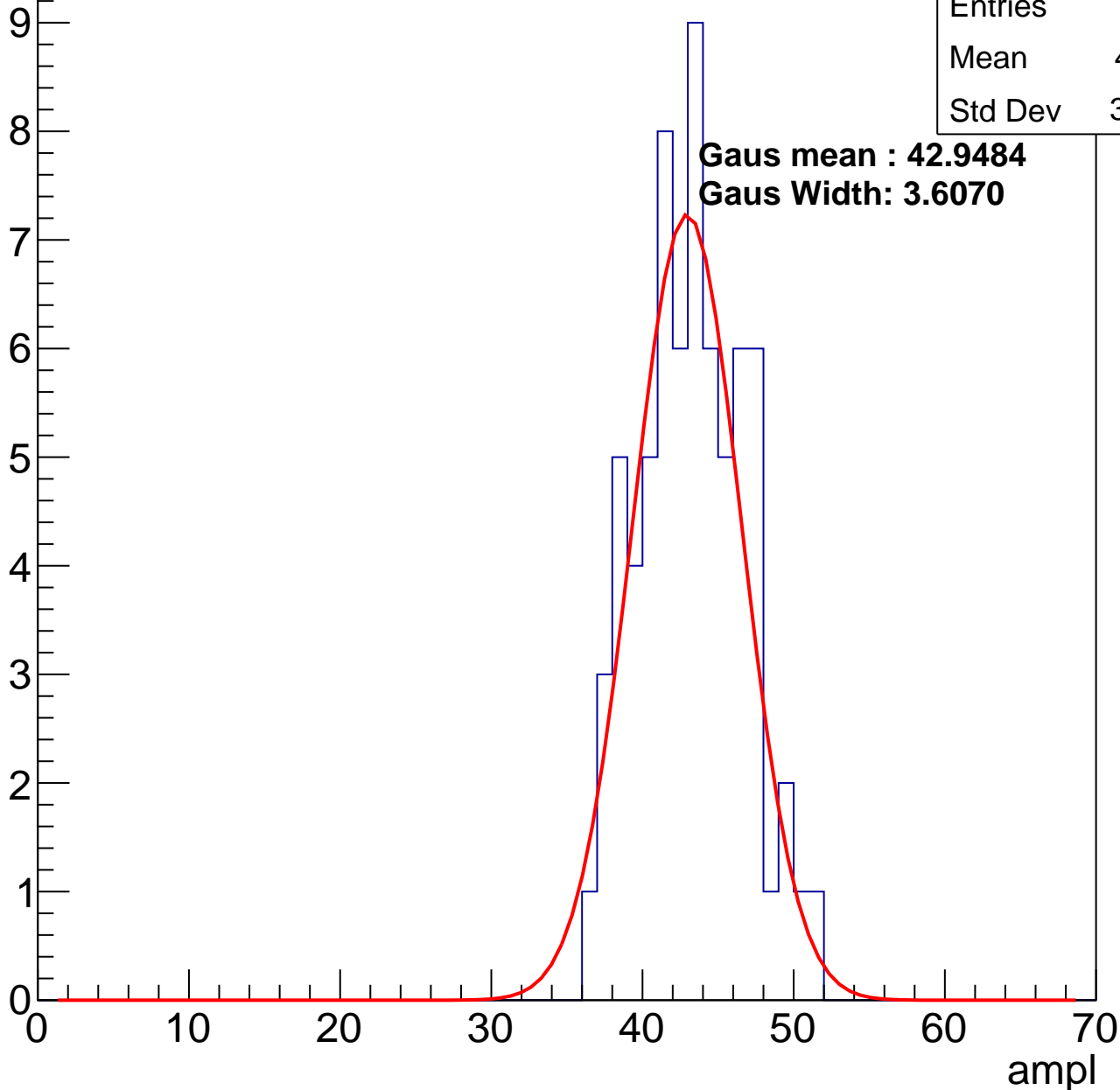
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.81
Std Dev	3.432

Gaus mean : 42.9484

Gaus Width: 3.6070

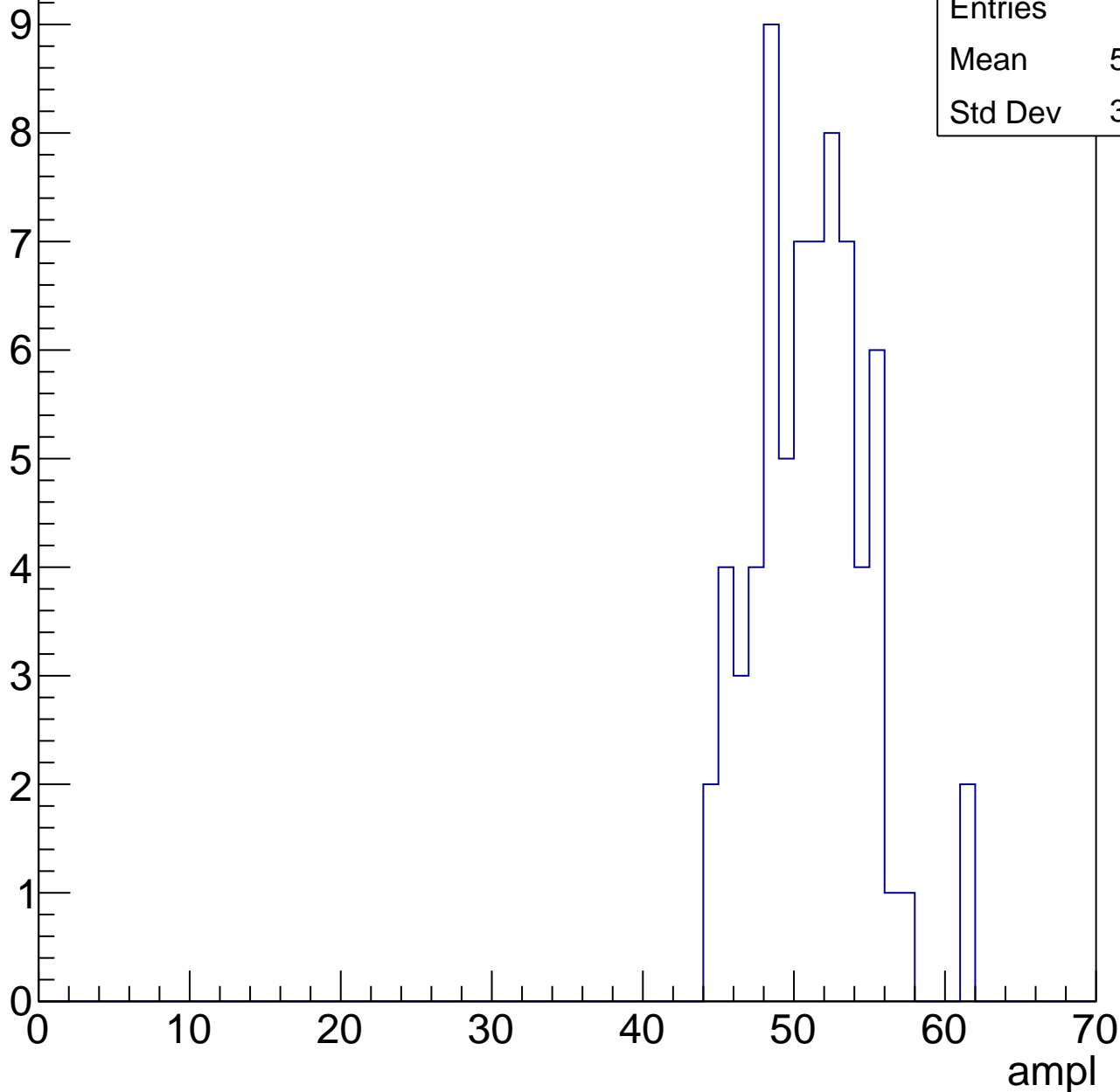


B0L001S, U18-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.66
Std Dev	3.605

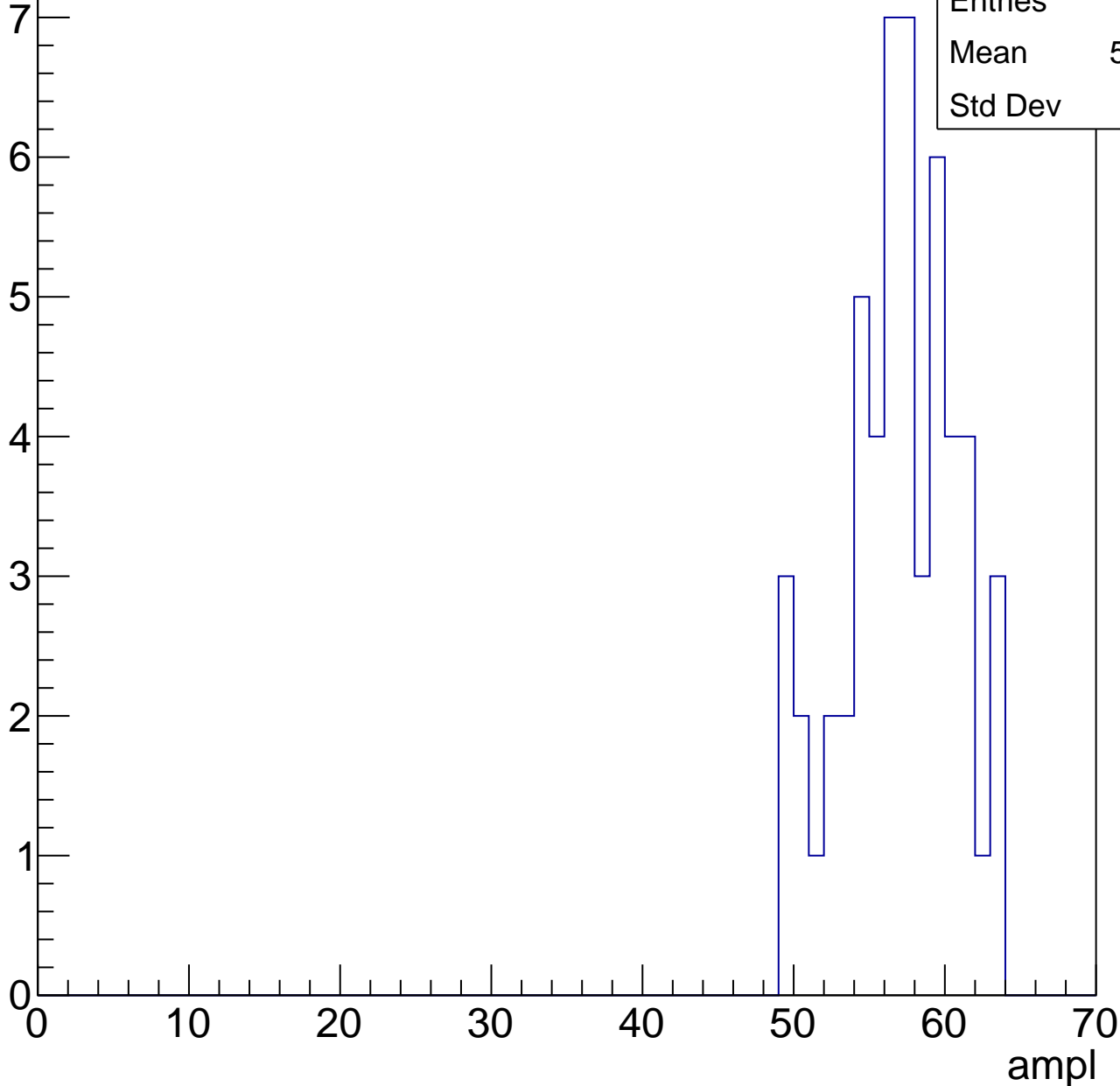


B0L001S, U18-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.52
Std Dev	3.66

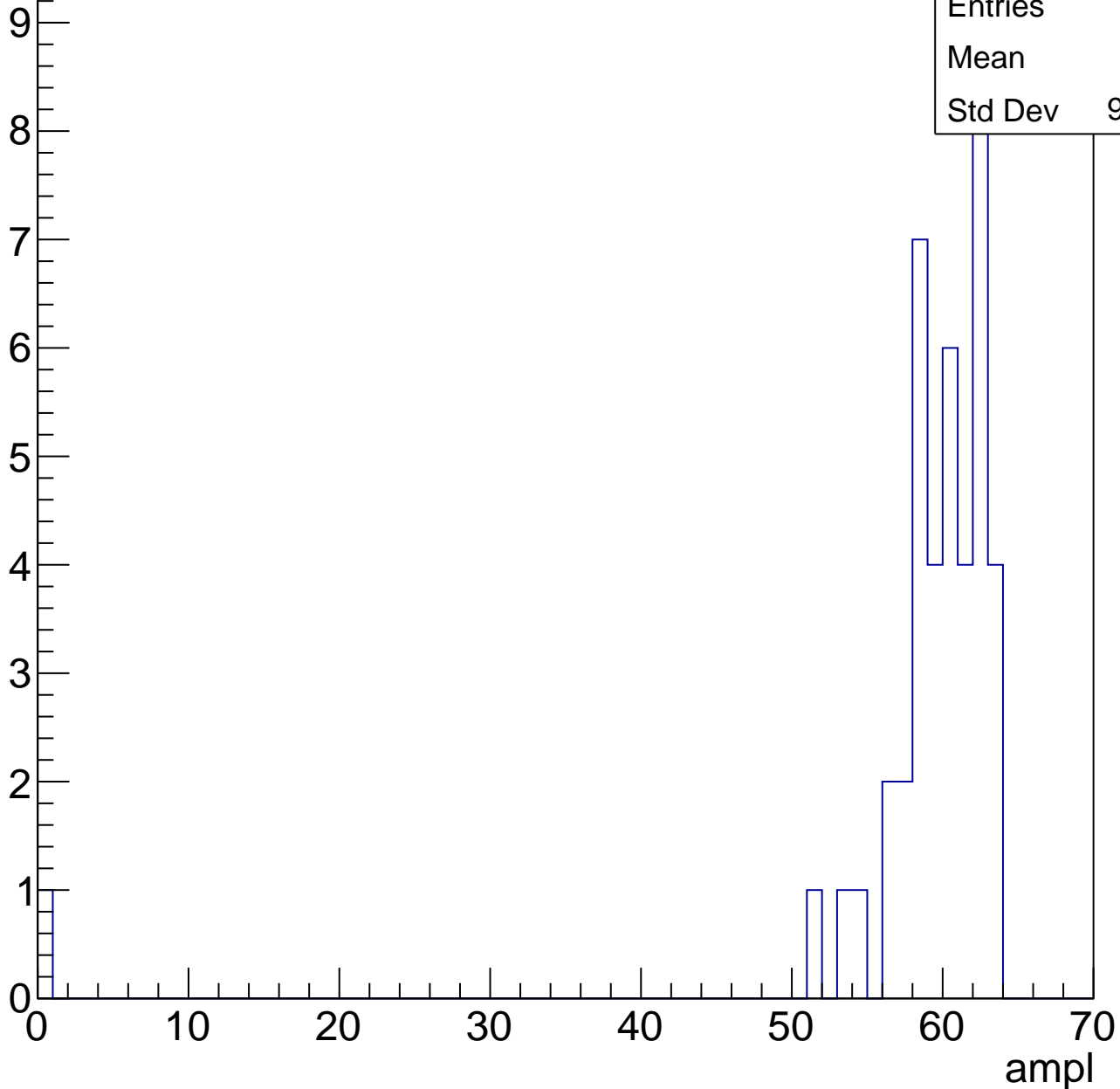


B0L001S, U18-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.1
Std Dev	9.476



B0L001S, U18-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch102, adc0

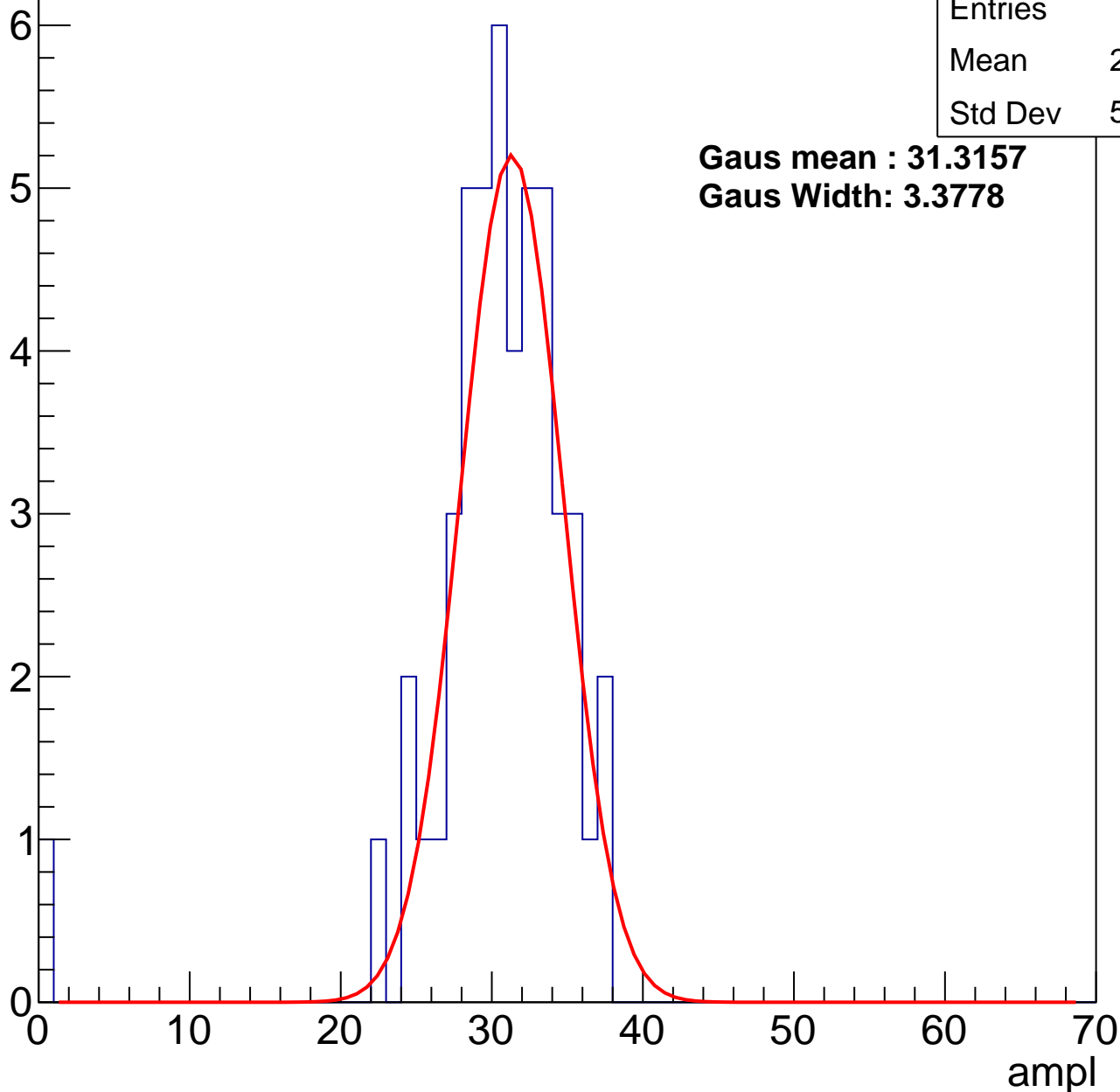
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	29.85
Std Dev	5.504

Gaus mean : 31.3157

Gaus Width: 3.3778



B0L001S, U18-ch102, adc1

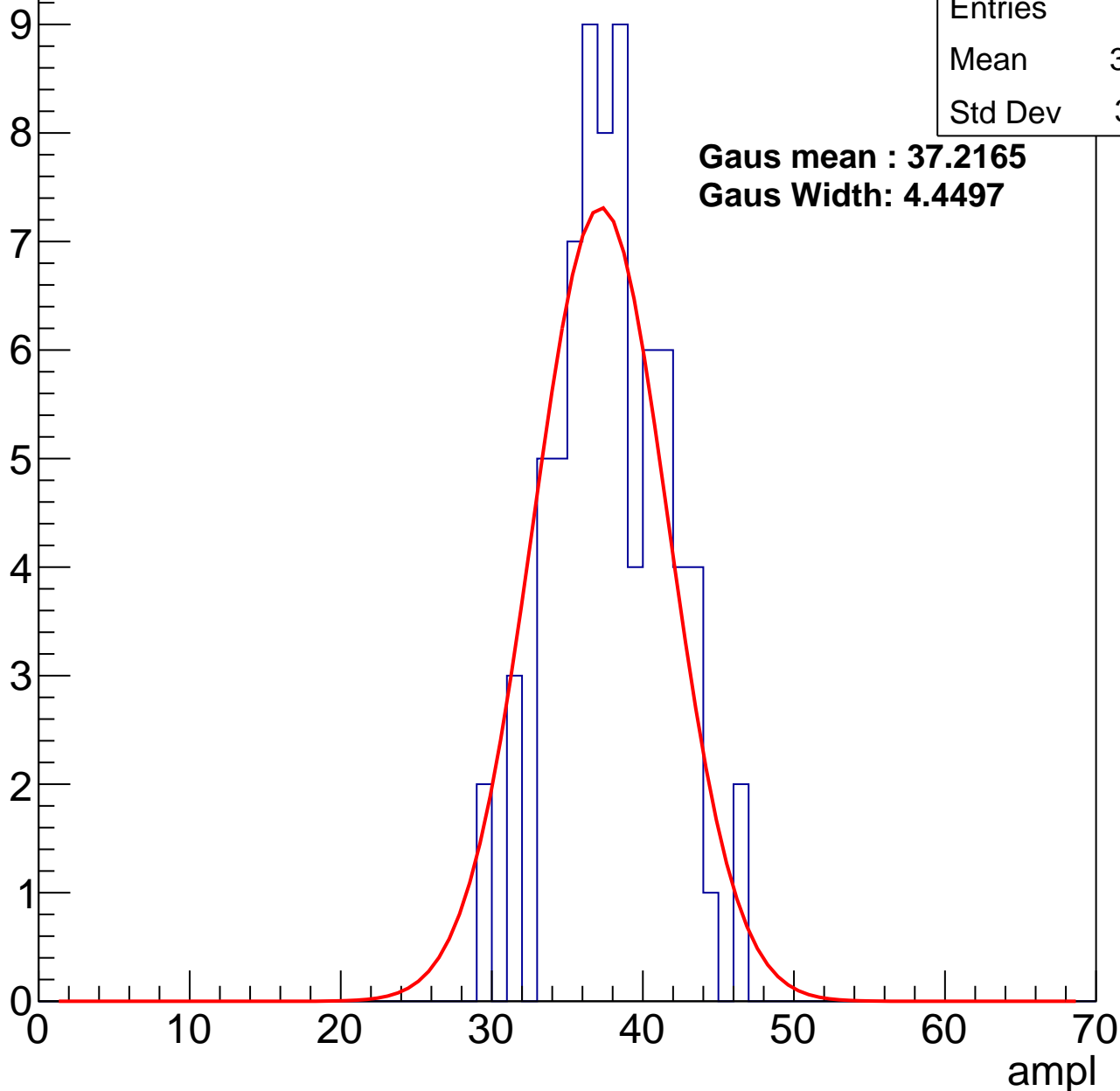
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	37.48
Std Dev	3.671

Gaus mean : 37.2165

Gaus Width: 4.4497



B0L001S, U18-ch102, adc2

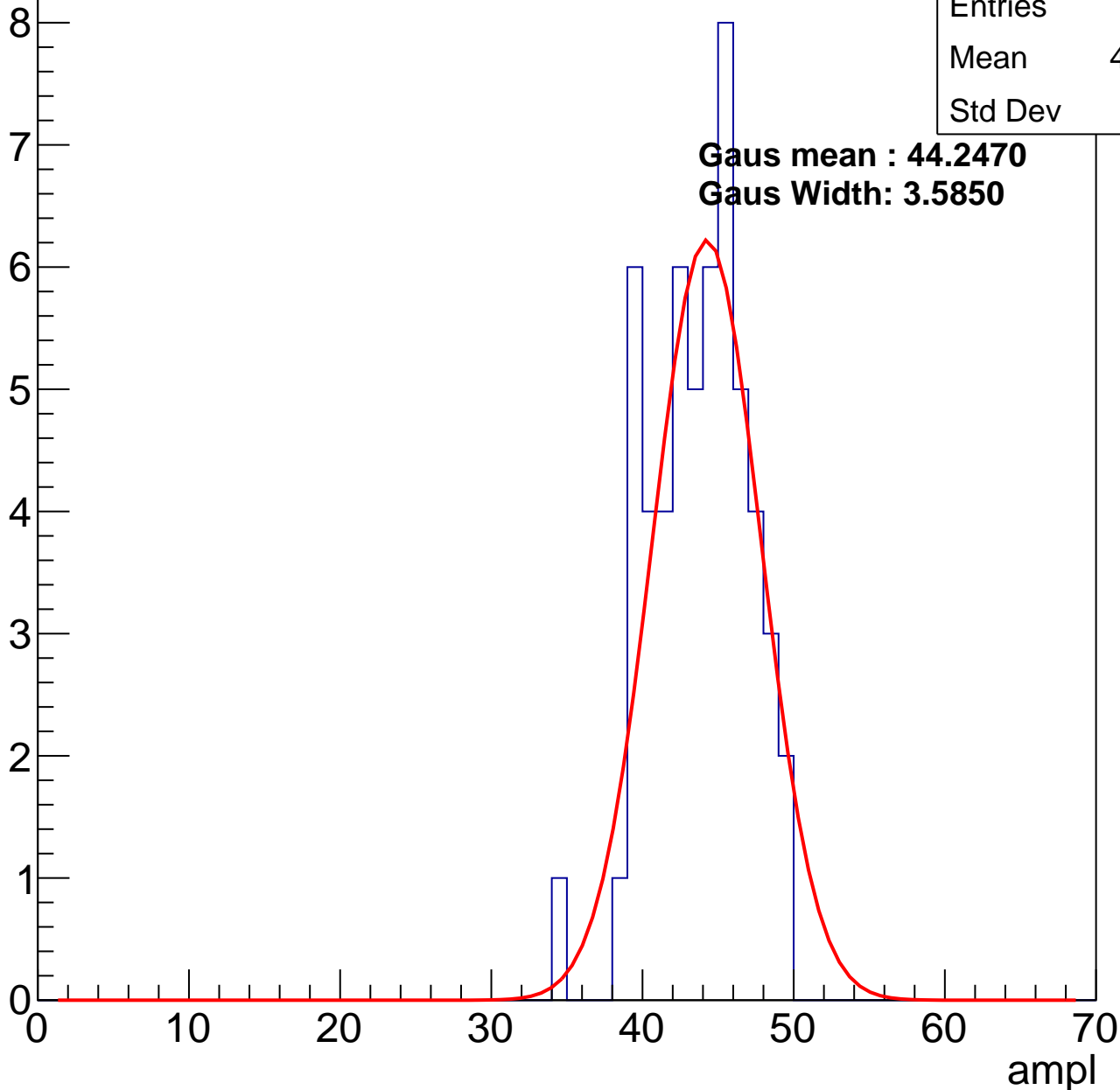
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.29
Std Dev	3.16

Gaus mean : 44.2470

Gaus Width: 3.5850

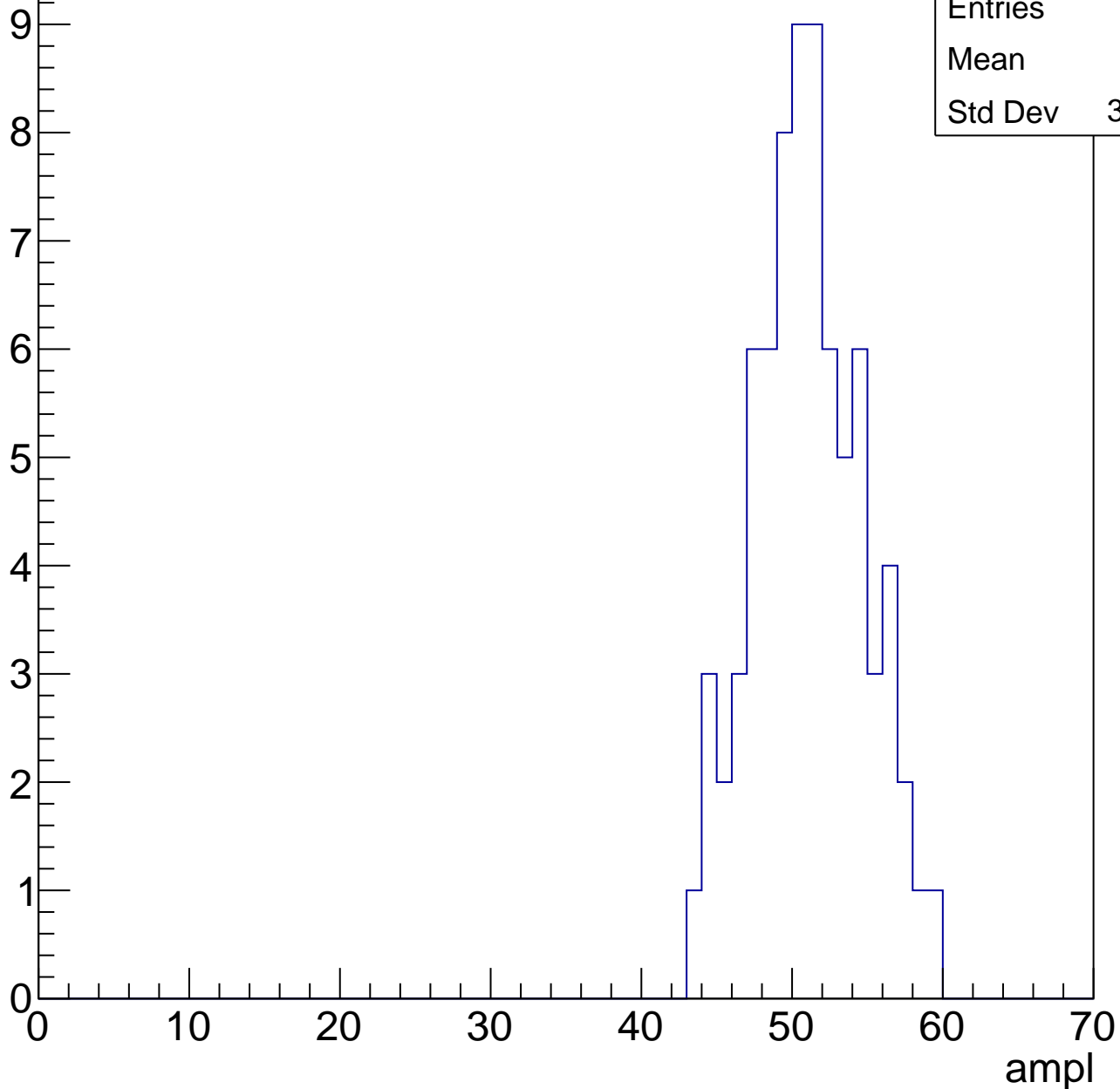


B0L001S, U18-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.6
Std Dev	3.559

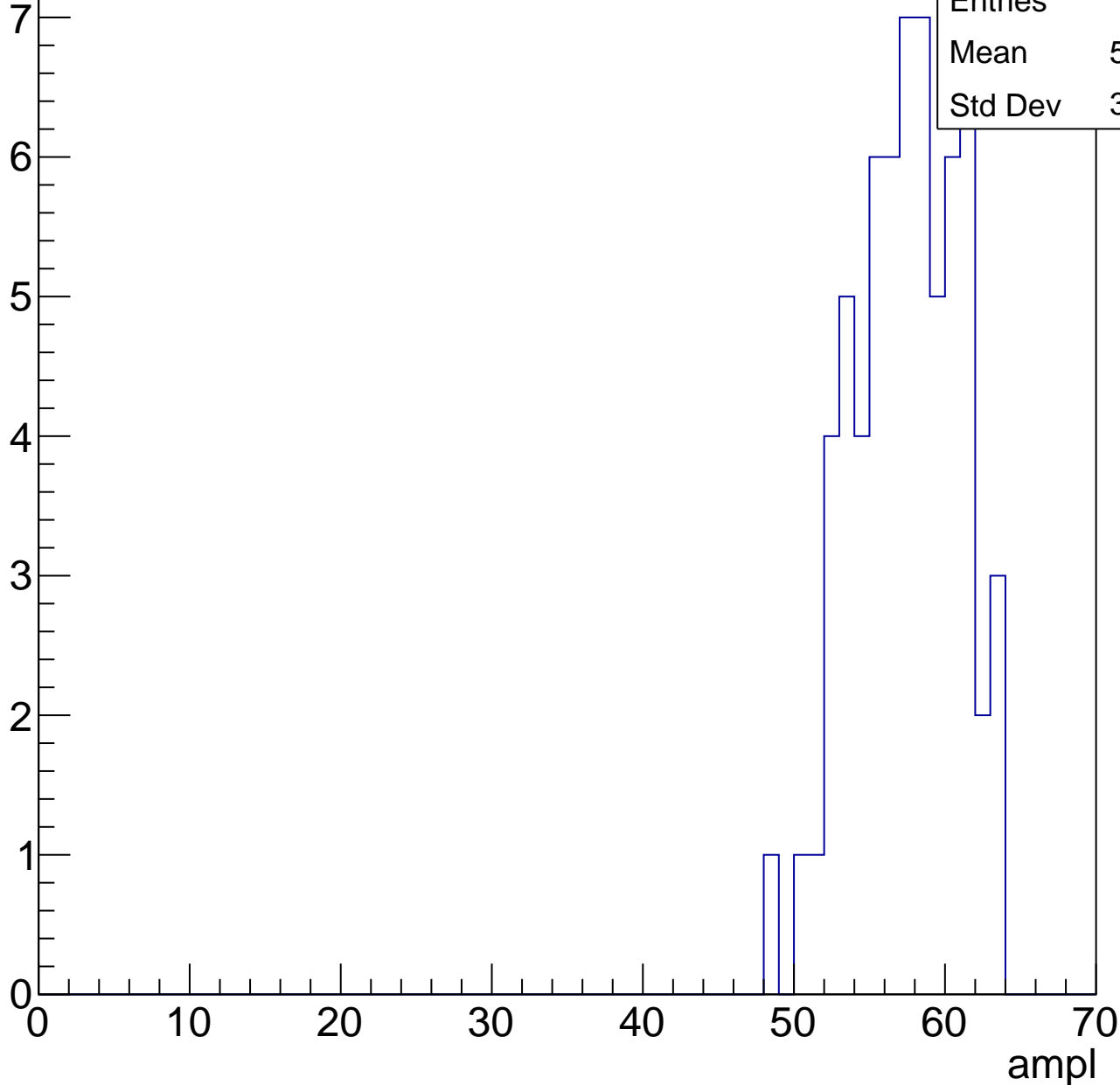


B0L001S, U18-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.98
Std Dev	3.426

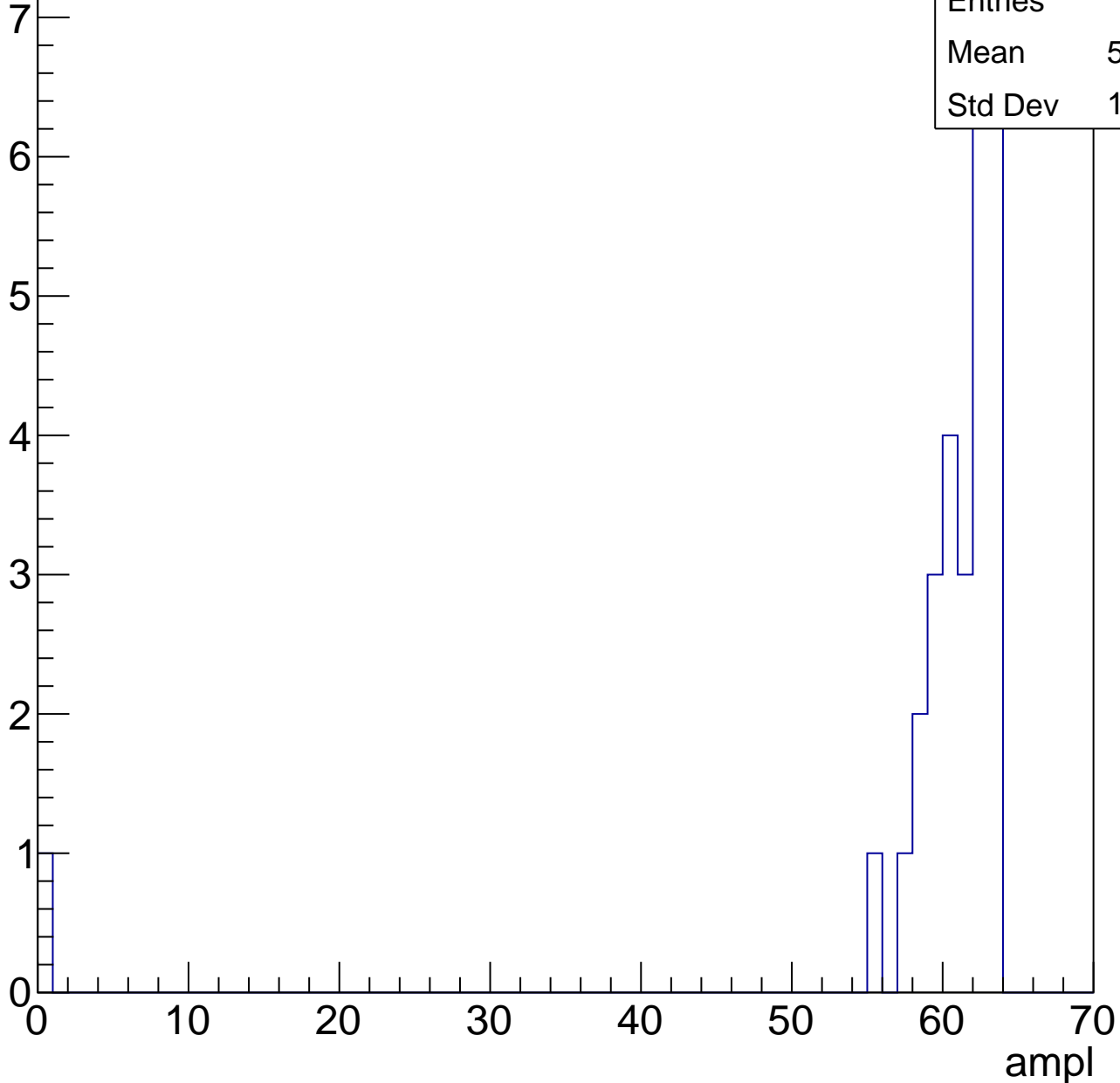


B0L001S, U18-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.72
Std Dev	11.28



B0L001S, U18-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	60.5
Std Dev	1.5

ampl

B0L001S, U18-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch103, adc0

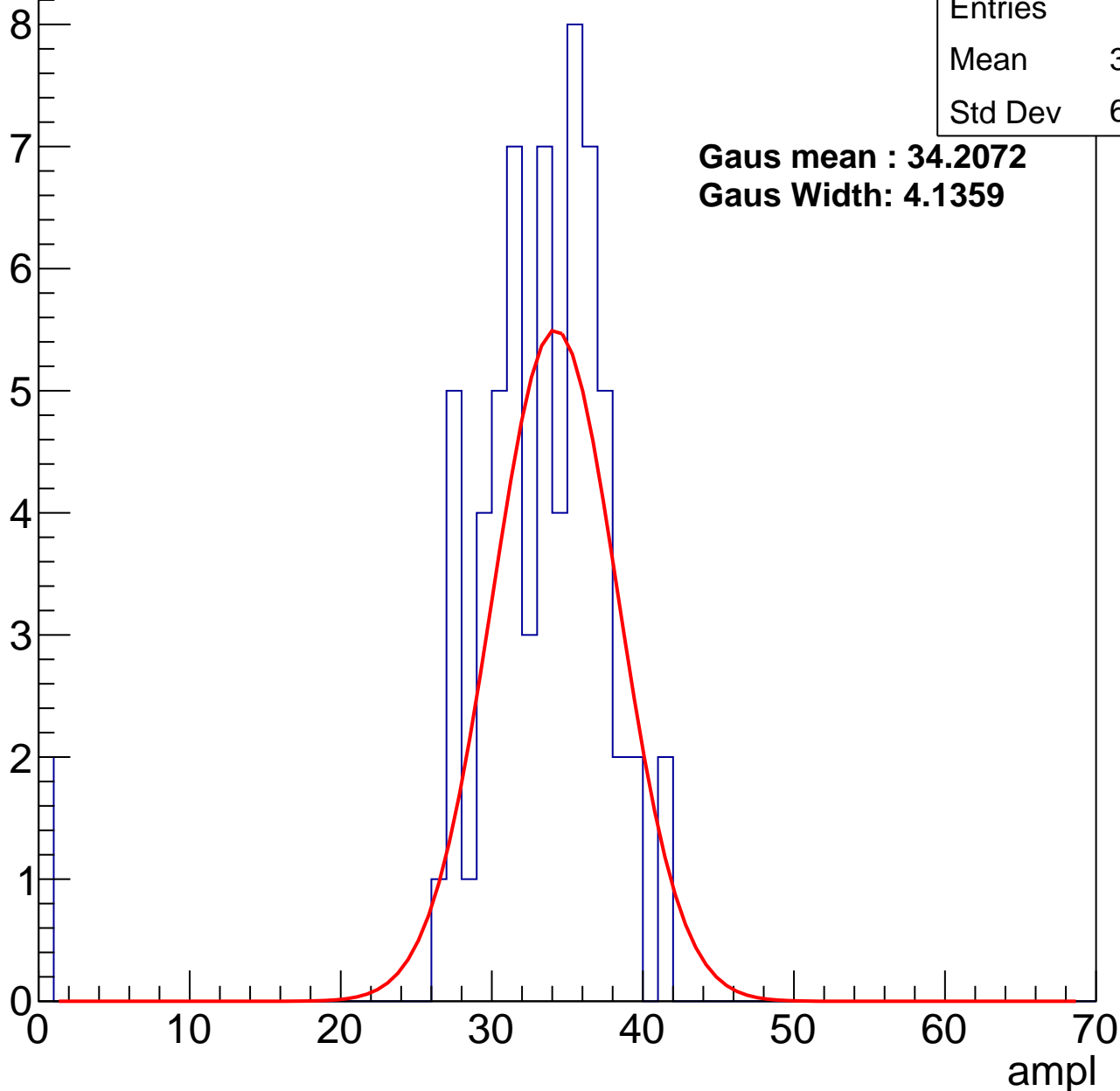
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.12
Std Dev	6.738

Gaus mean : 34.2072

Gaus Width: 4.1359



B0L001S, U18-ch103, adc1

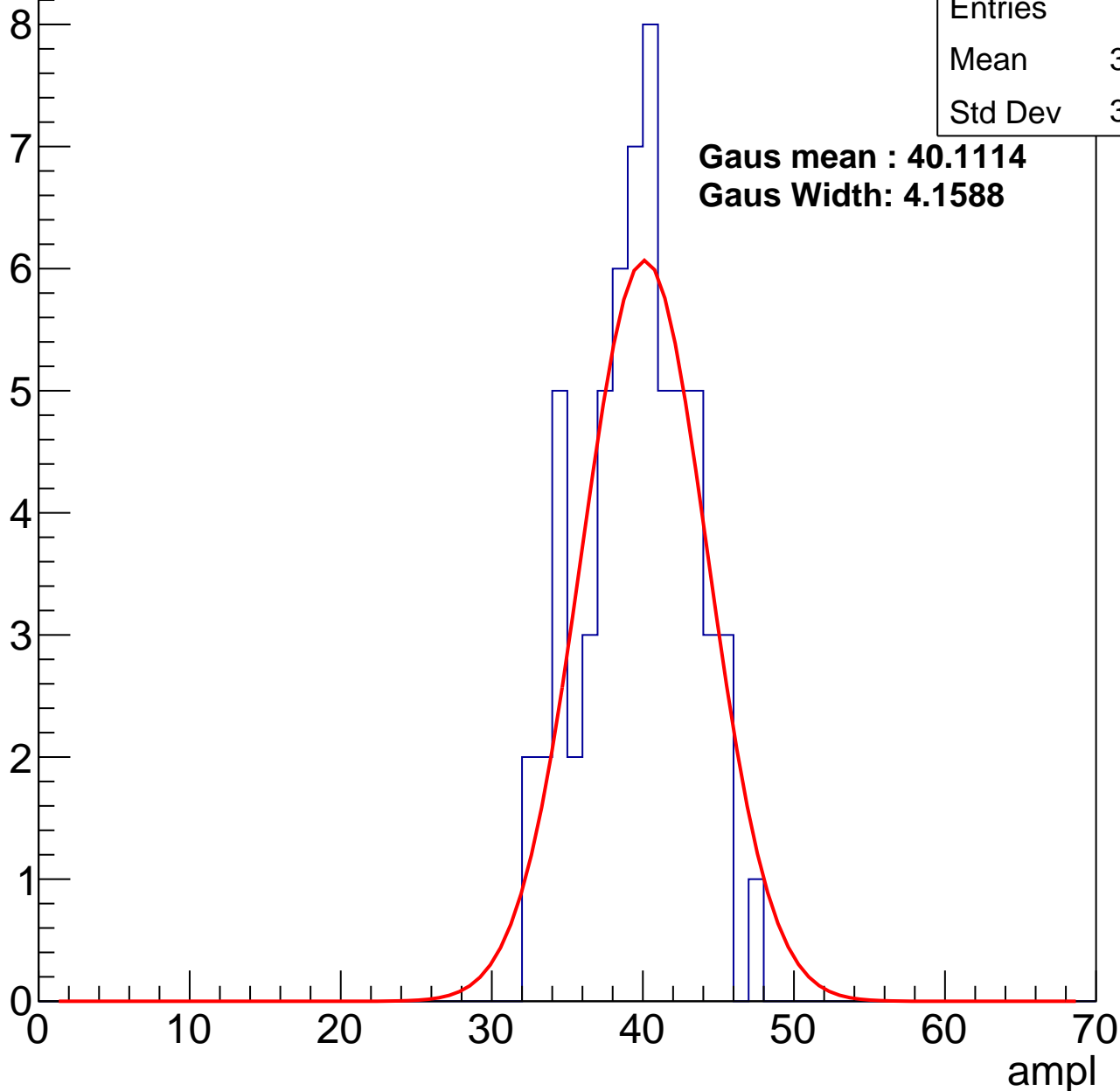
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	39.16
Std Dev	3.543

Gaus mean : 40.1114

Gaus Width: 4.1588



B0L001S, U18-ch103, adc2

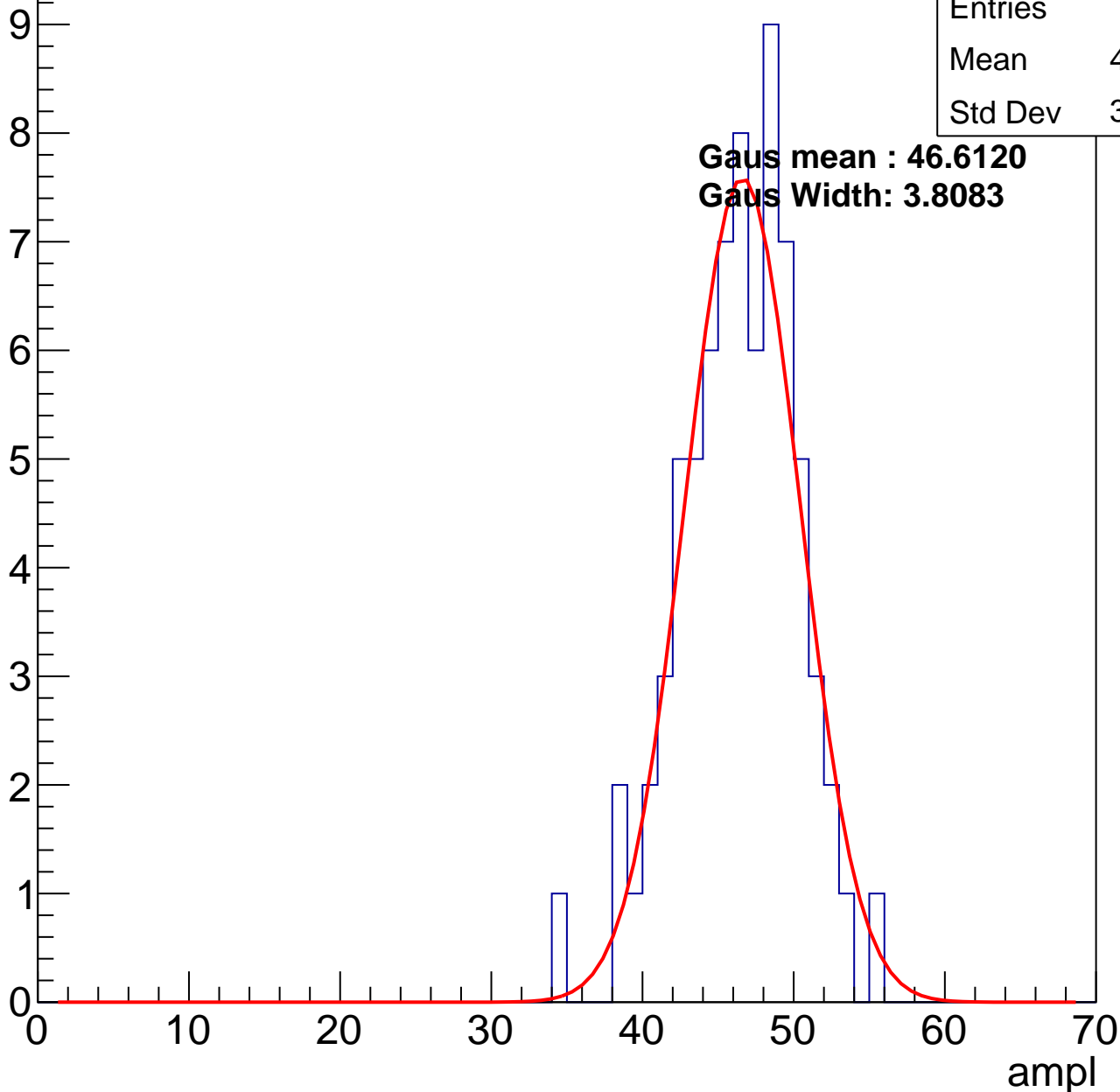
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	45.89
Std Dev	3.829

Gaus mean : 46.6120

Gaus Width: 3.8083

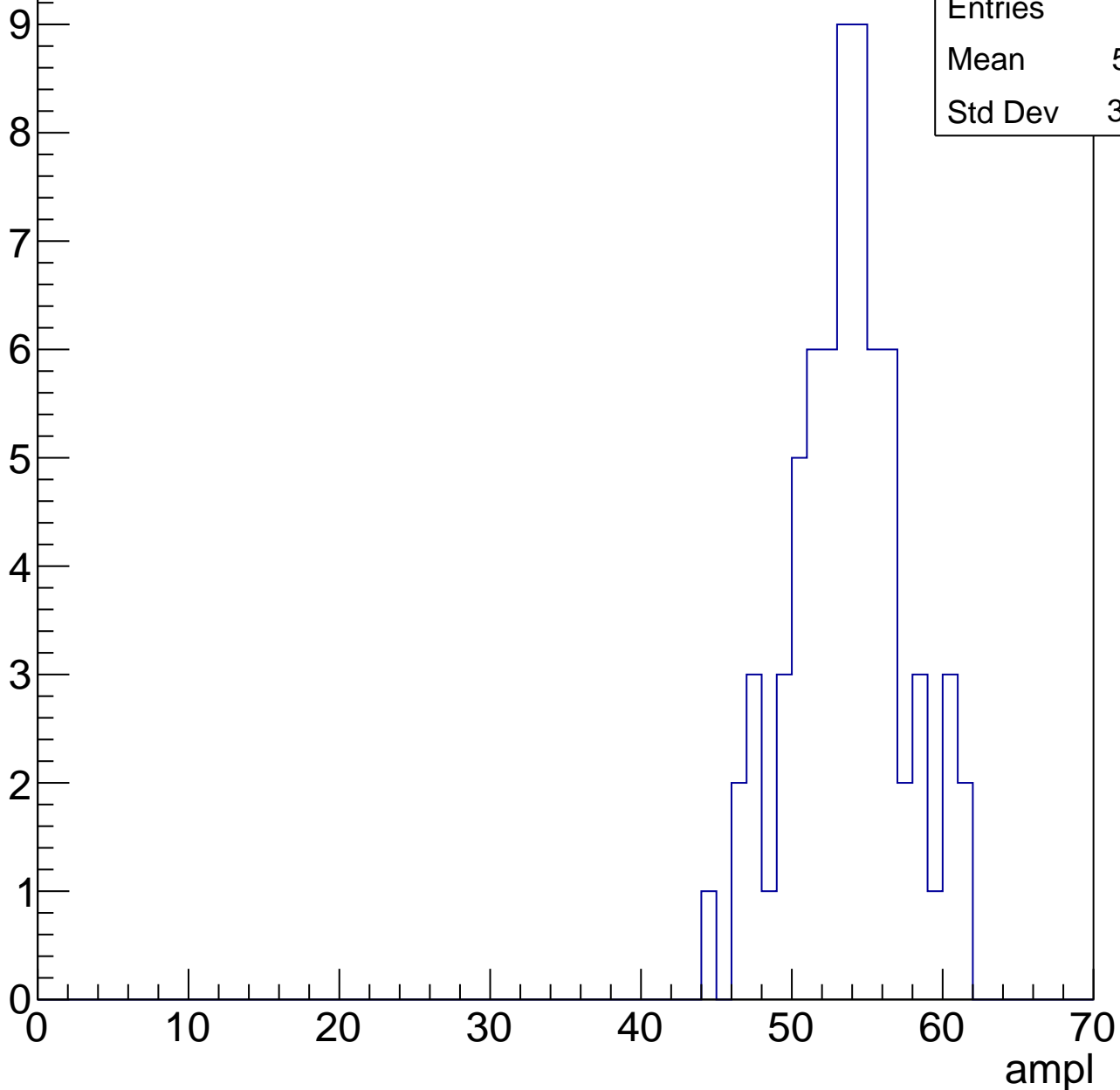


B0L001S, U18-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	53.21
Std Dev	3.712

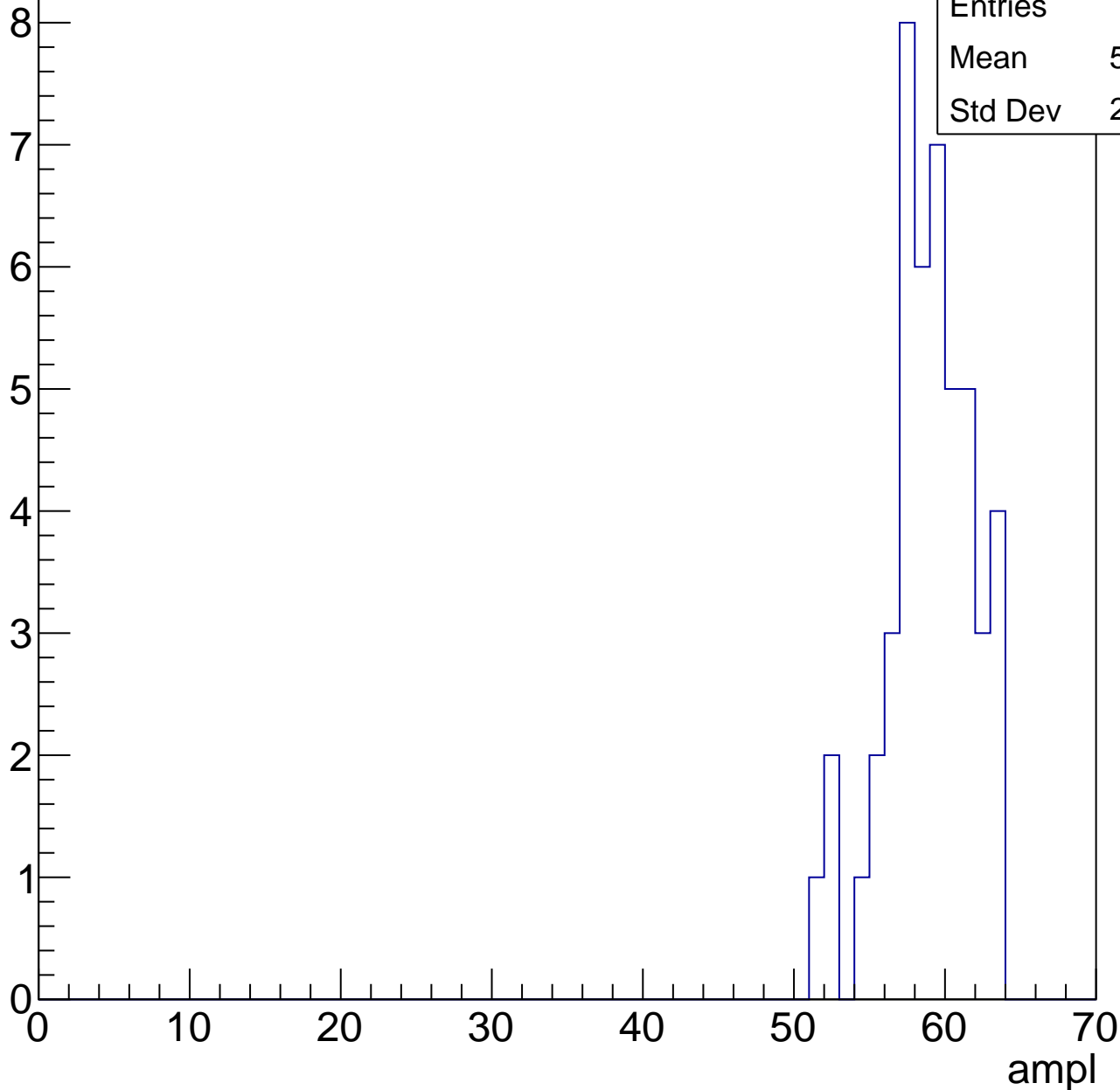


B0L001S, U18-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	58.45
Std Dev	2.864

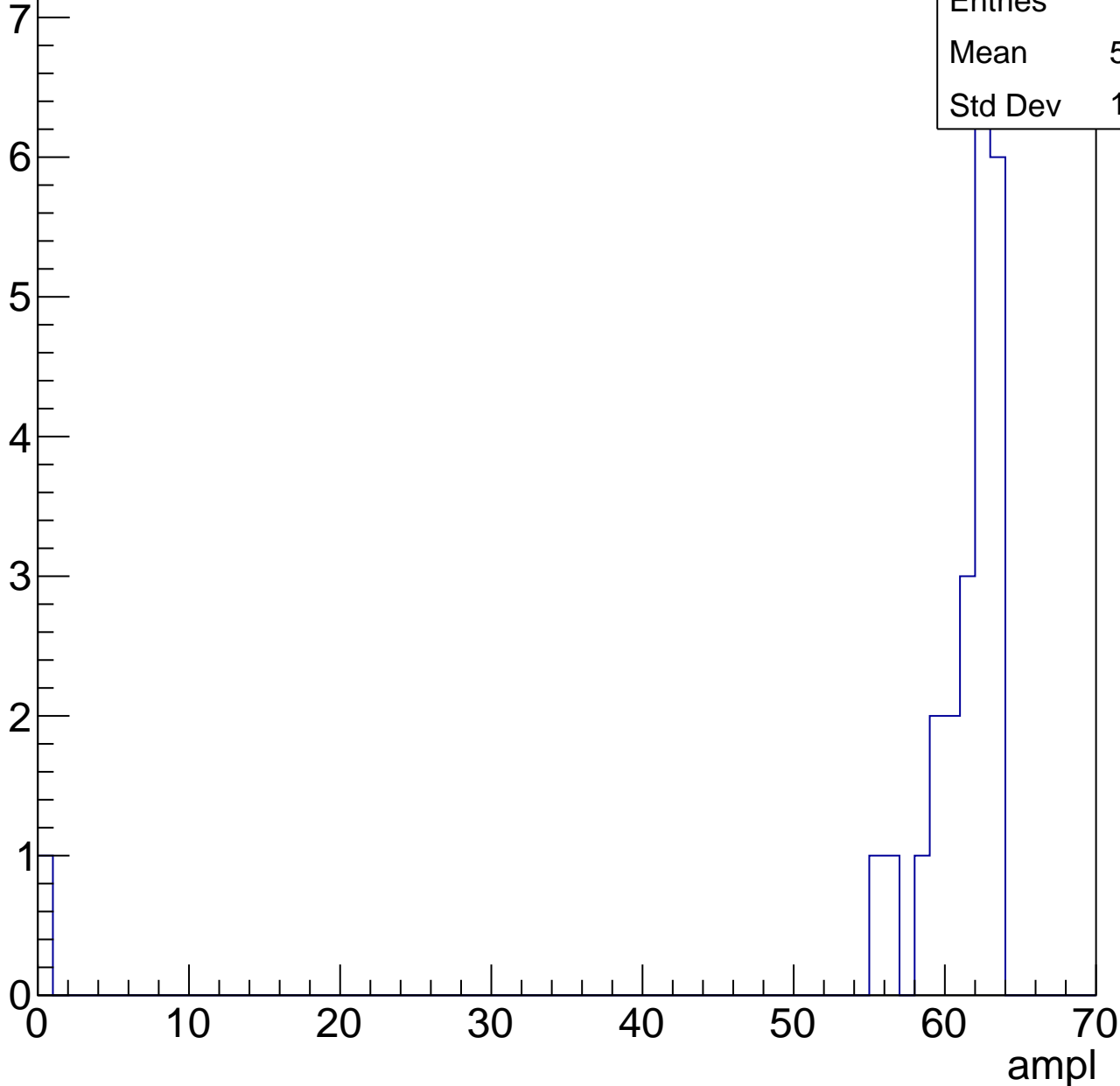


B0L001S, U18-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.42
Std Dev	12.37



B0L001S, U18-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch104, adc0

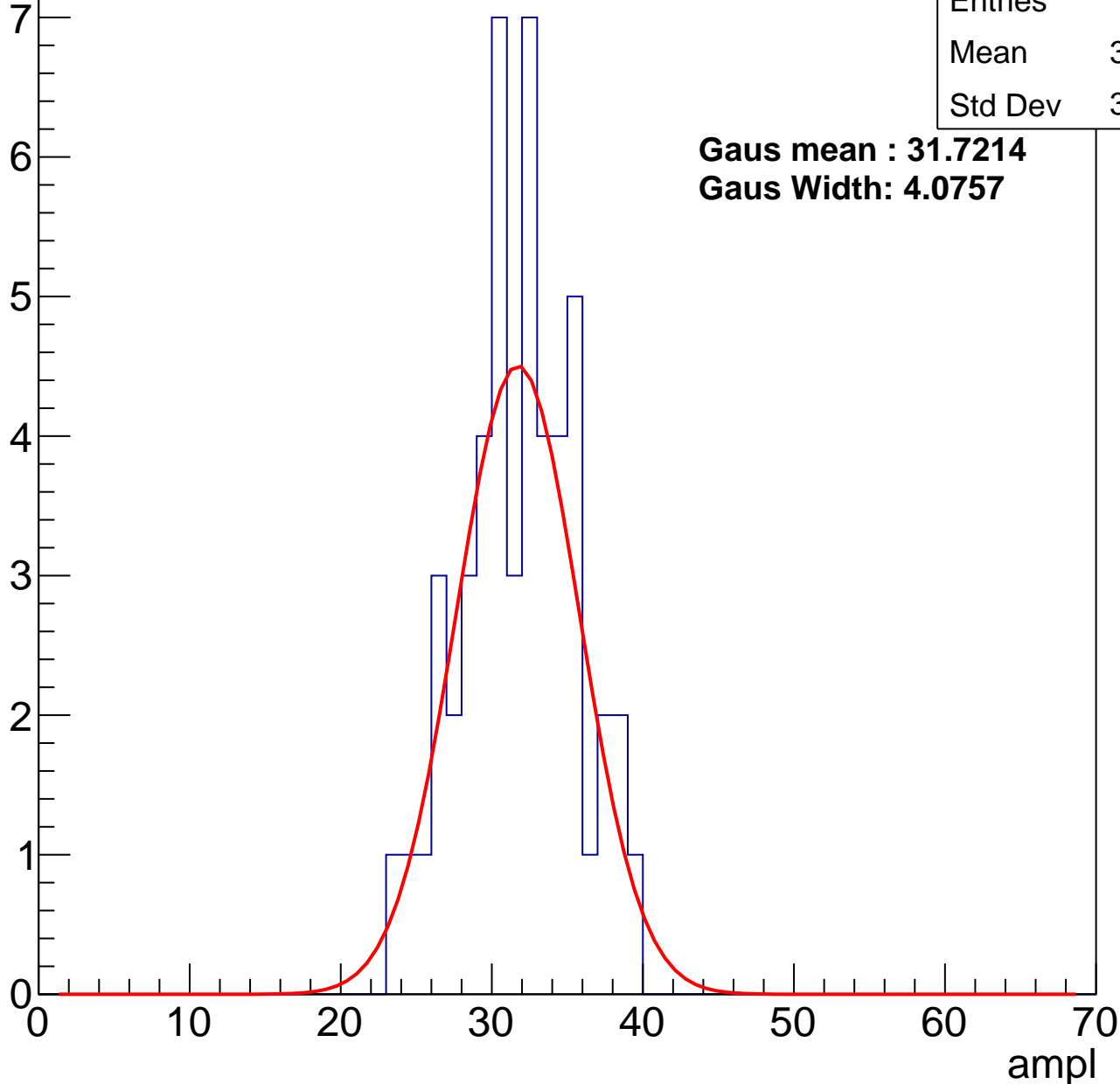
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	31.35
Std Dev	3.699

Gaus mean : 31.7214

Gaus Width: 4.0757



B0L001S, U18-ch104, adc1

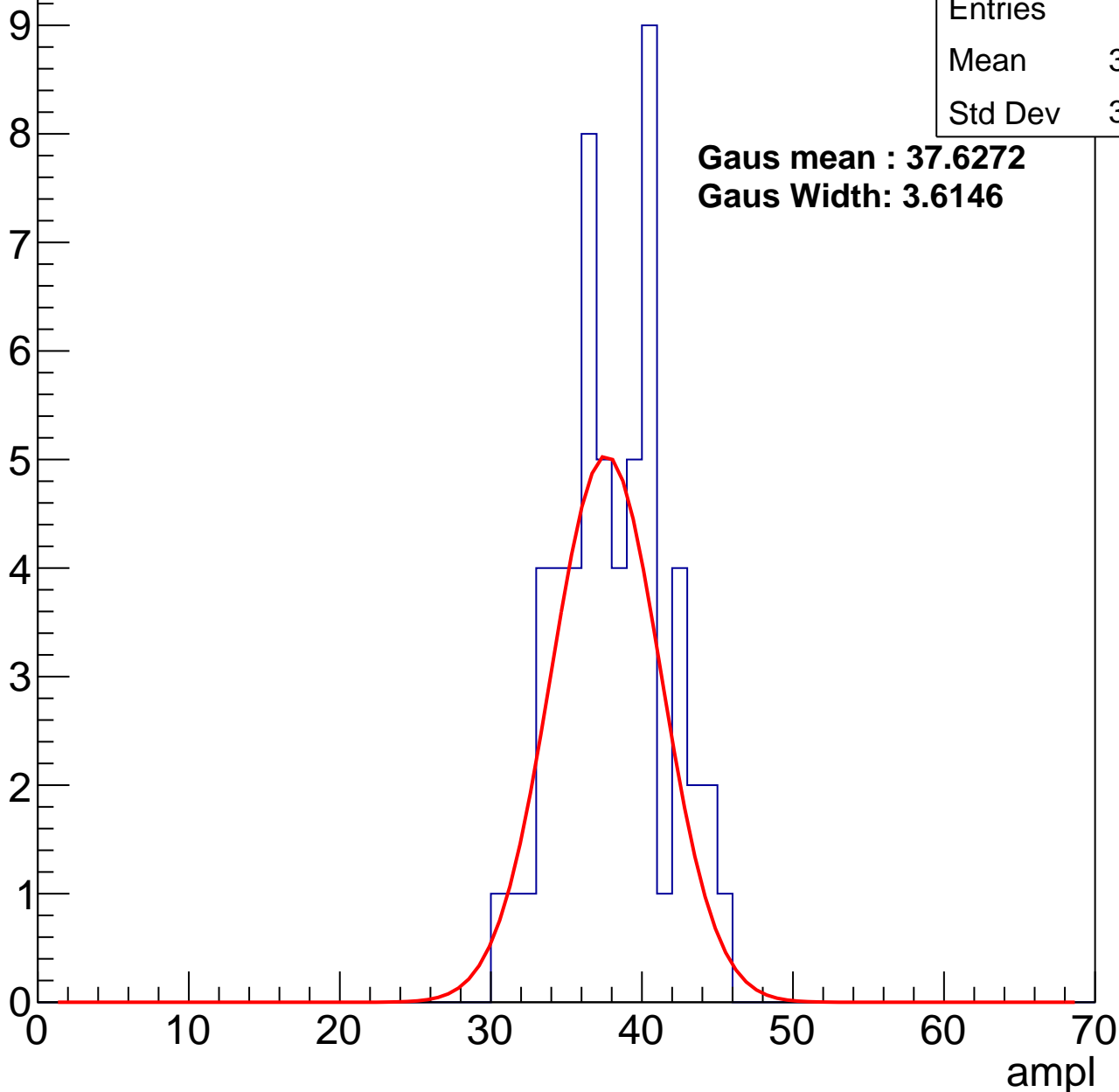
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	37.66
Std Dev	3.445

Gaus mean : 37.6272

Gaus Width: 3.6146



B0L001S, U18-ch104, adc2

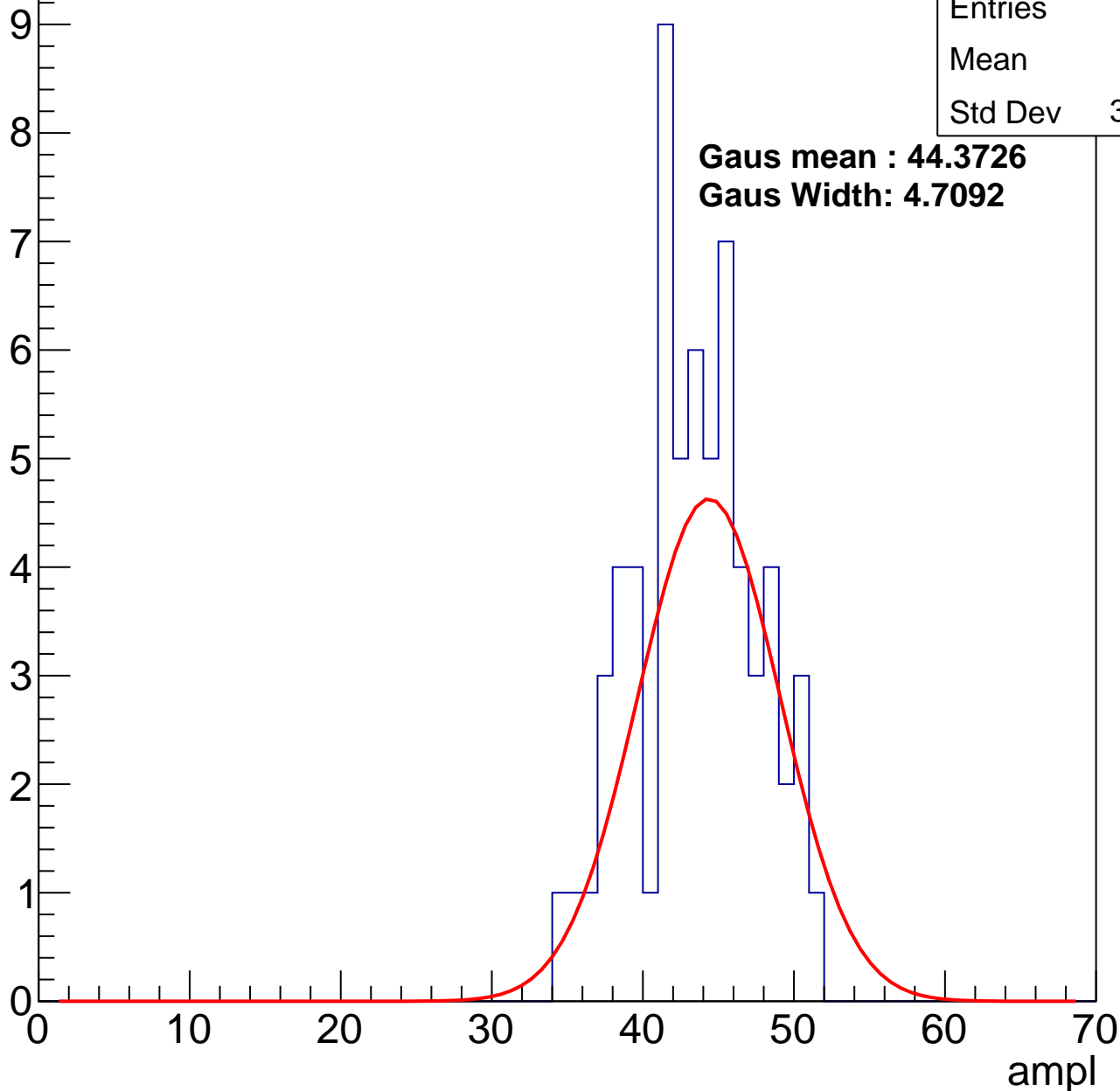
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43
Std Dev	3.984

Gaus mean : 44.3726

Gaus Width: 4.7092

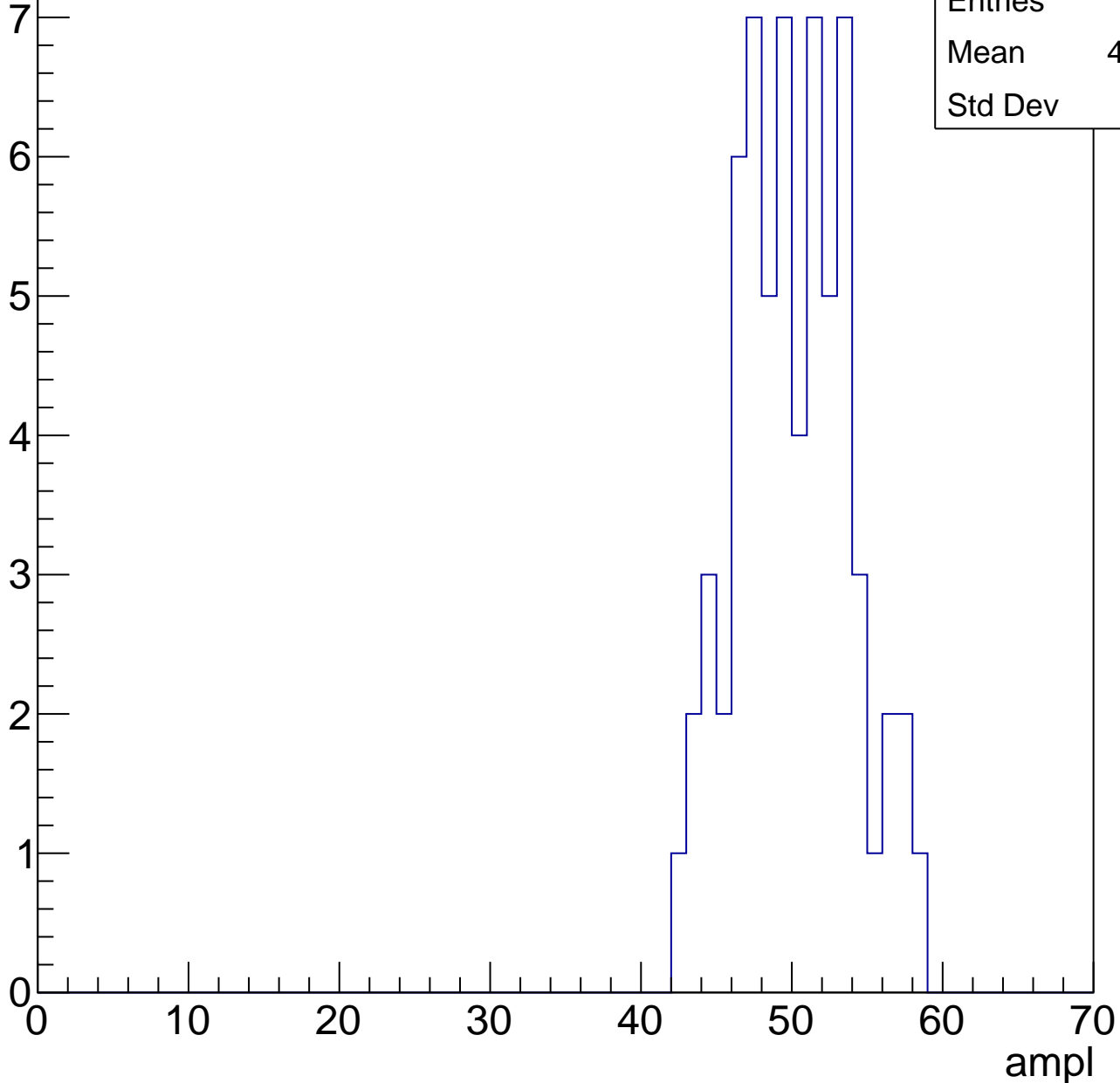


B0L001S, U18-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.65
Std Dev	3.71

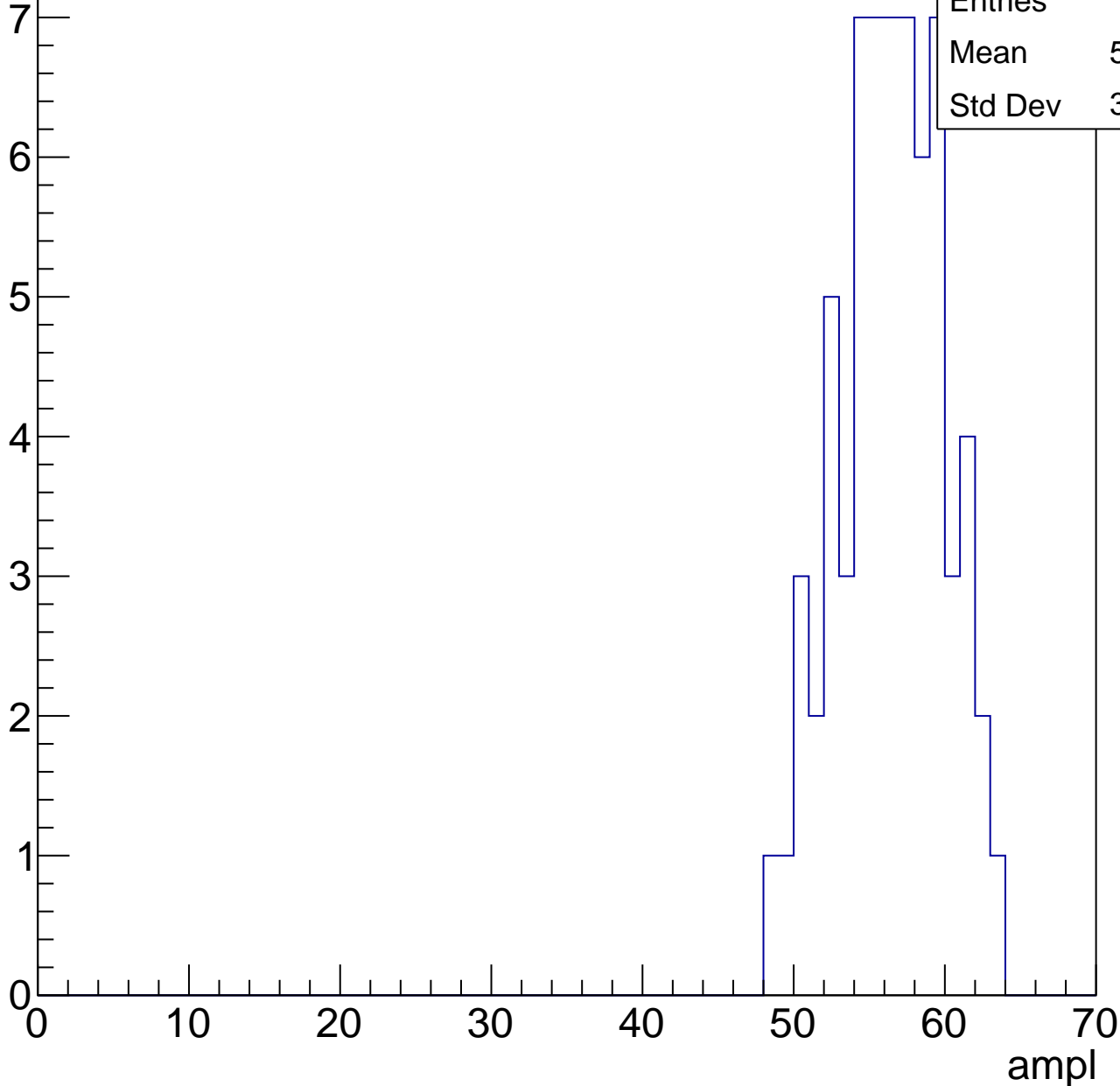


B0L001S, U18-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	55.97
Std Dev	3.429

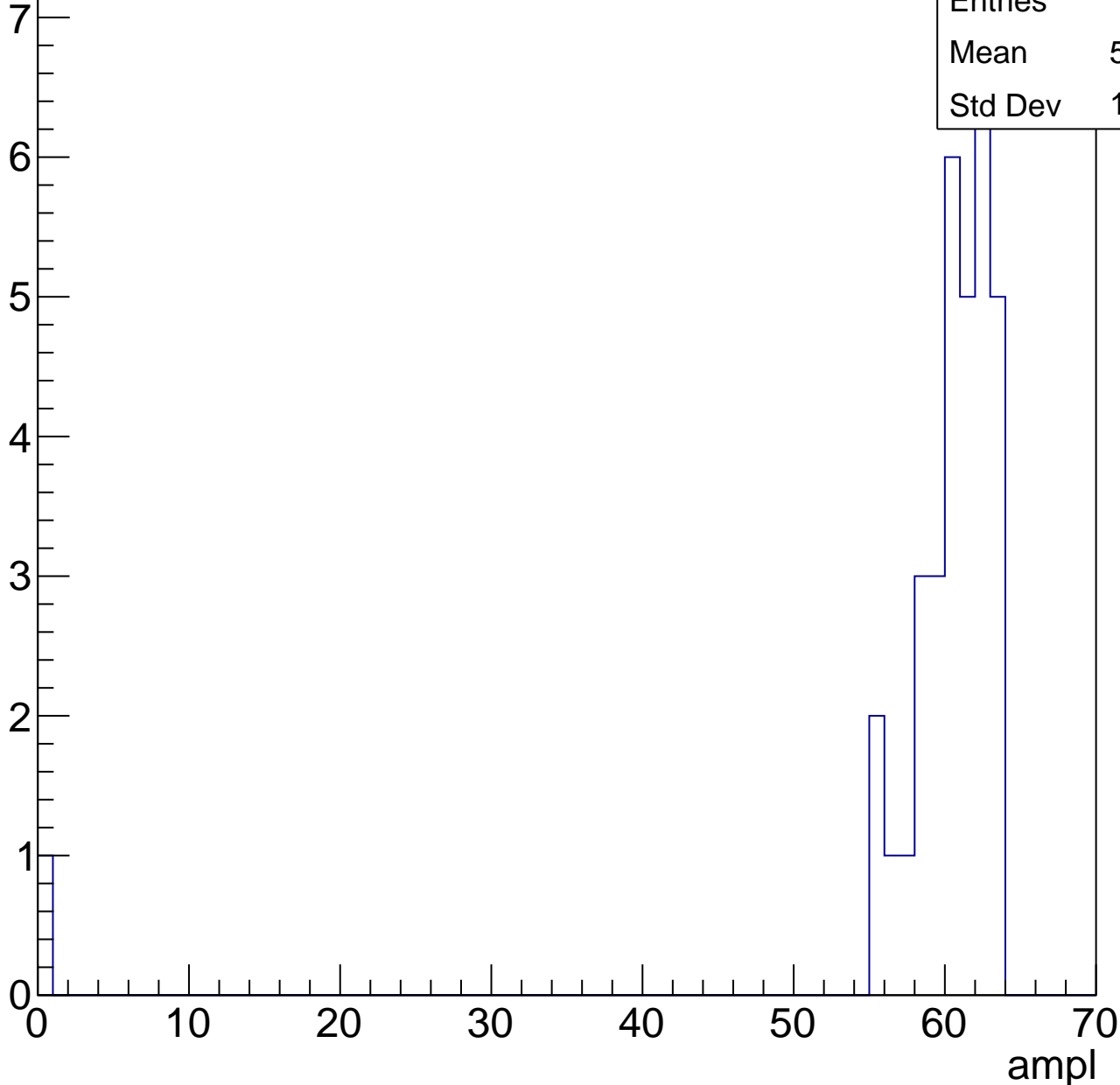


B0L001S, U18-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.47
Std Dev	10.42



B0L001S, U18-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch105, adc0

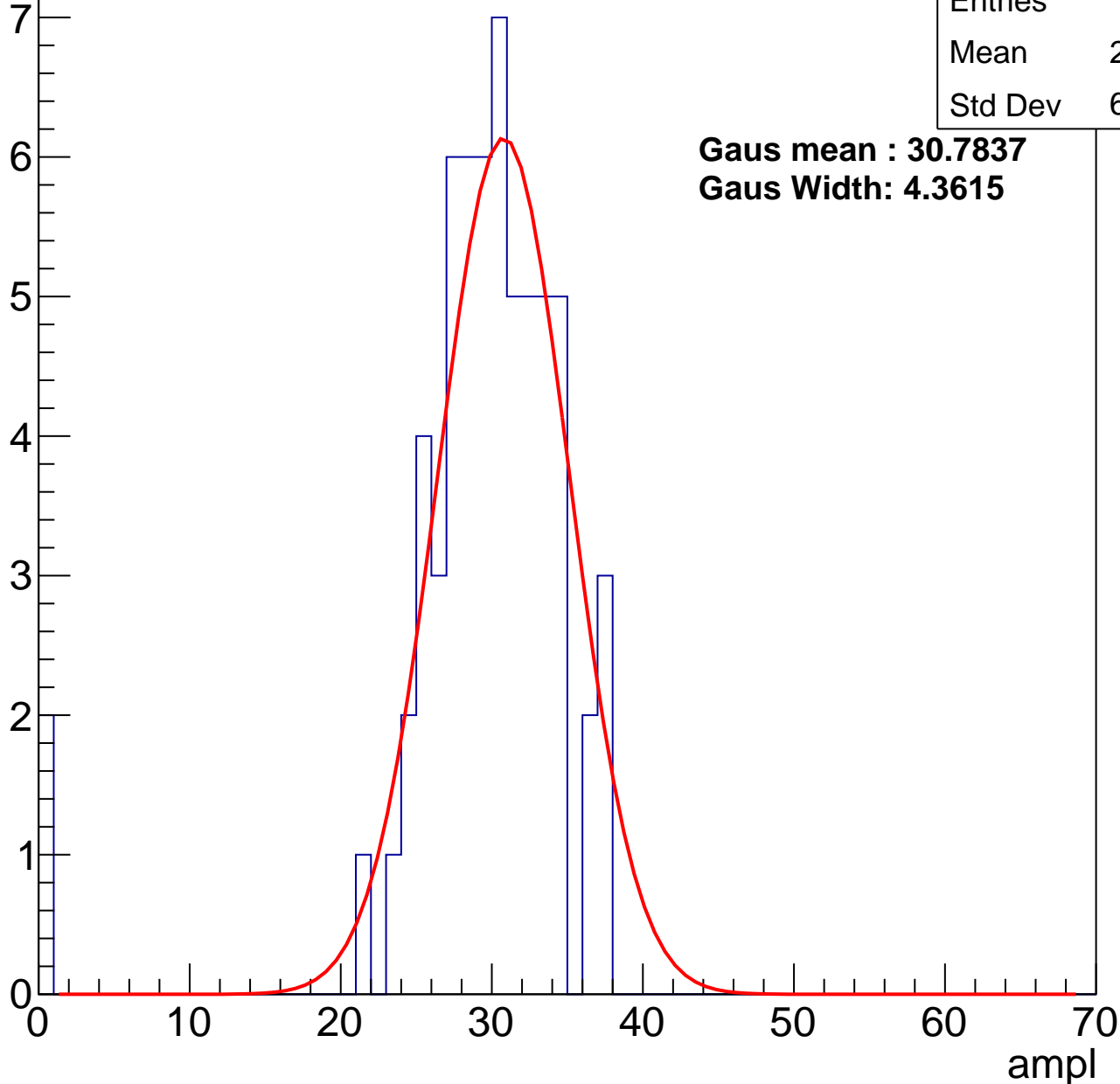
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	28.84
Std Dev	6.323

Gaus mean : 30.7837

Gaus Width: 4.3615



B0L001S, U18-ch105, adc1

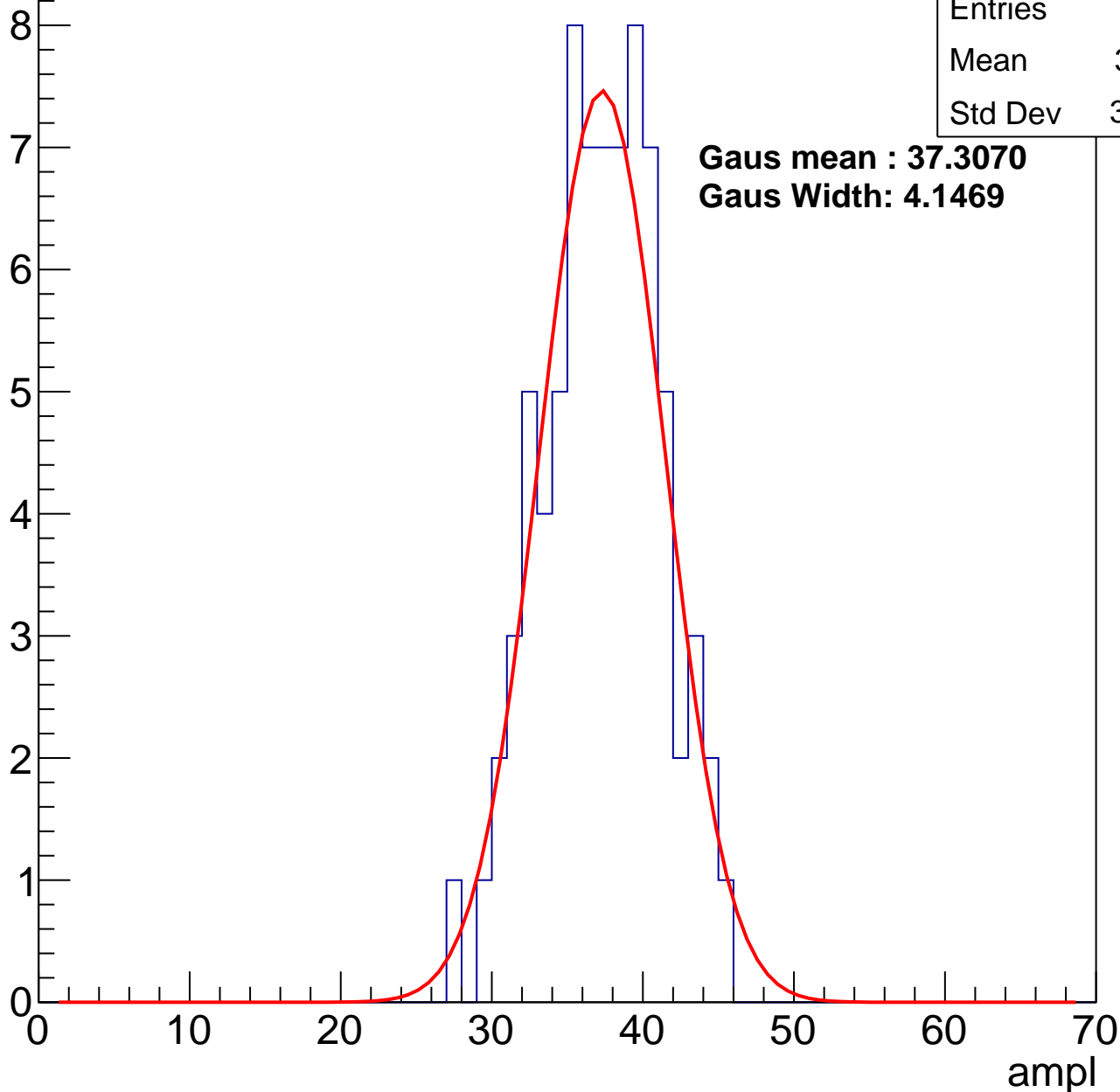
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.81
Std Dev	3.827

Gaus mean : 37.3070

Gaus Width: 4.1469



B0L001S, U18-ch105, adc2

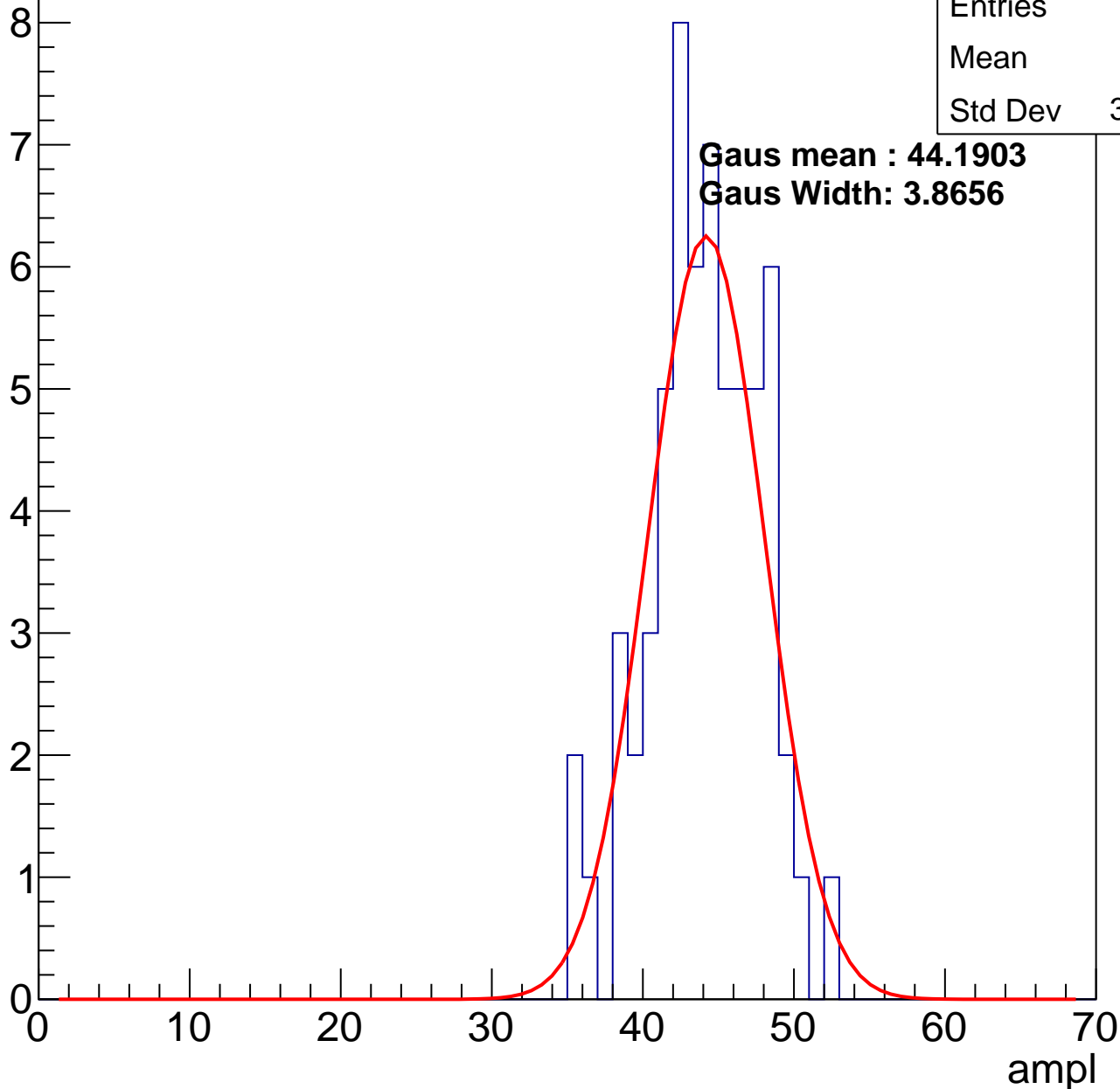
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.6
Std Dev	3.639

Gaus mean : 44.1903

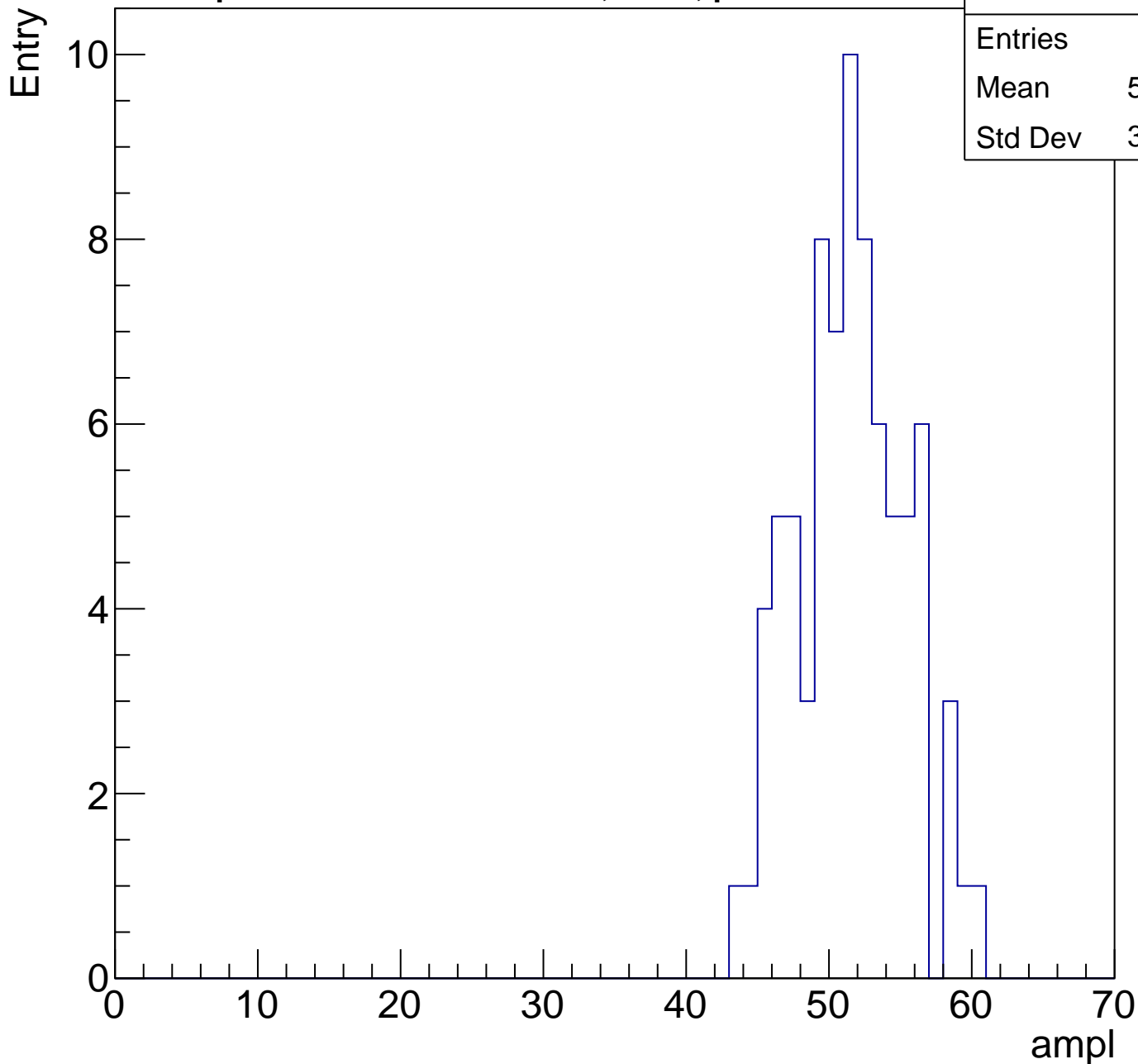
Gaus Width: 3.8656



B0L001S, U18-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	51.09
Std Dev	3.776

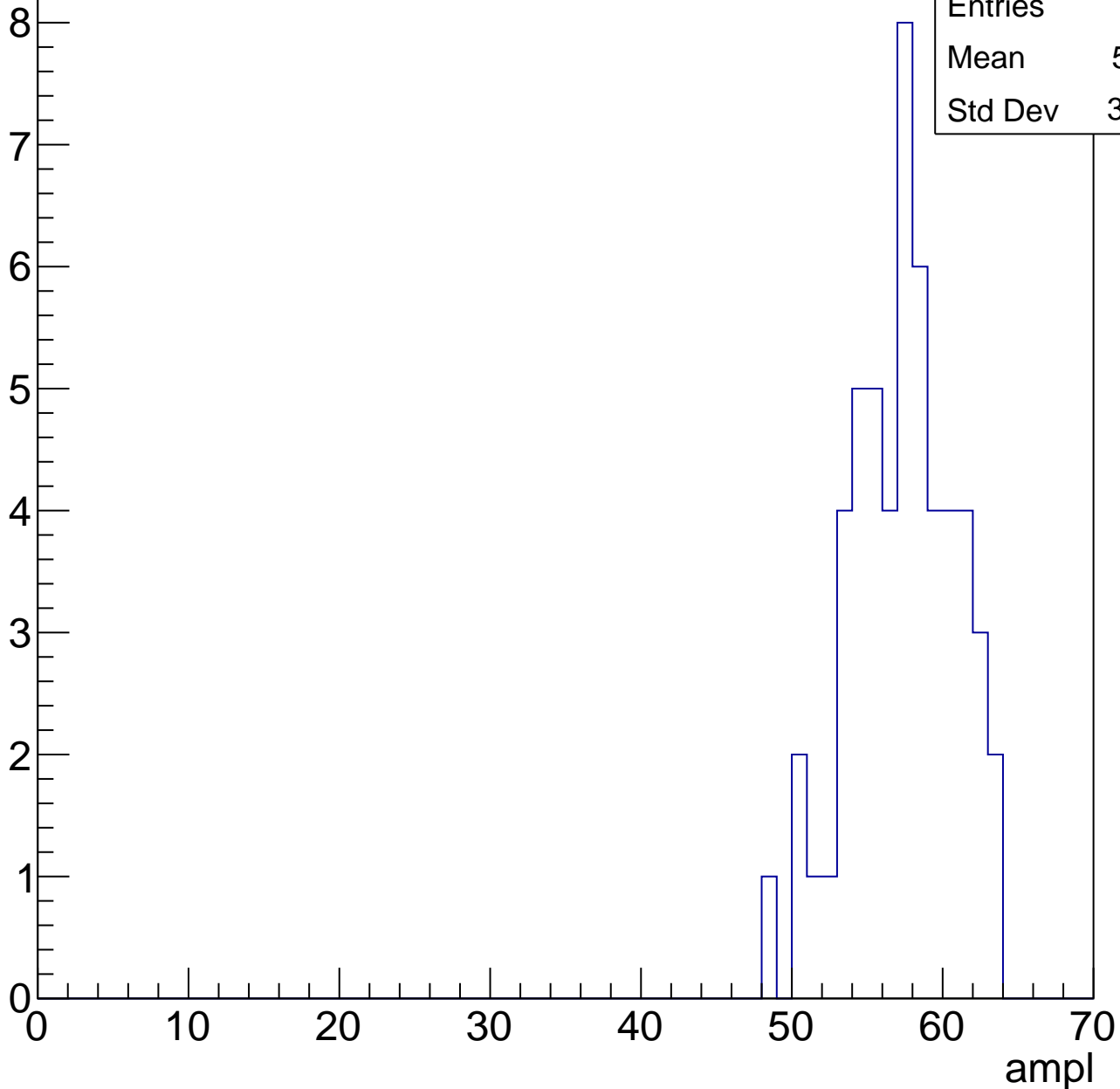


B0L001S, U18-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.81
Std Dev	3.443

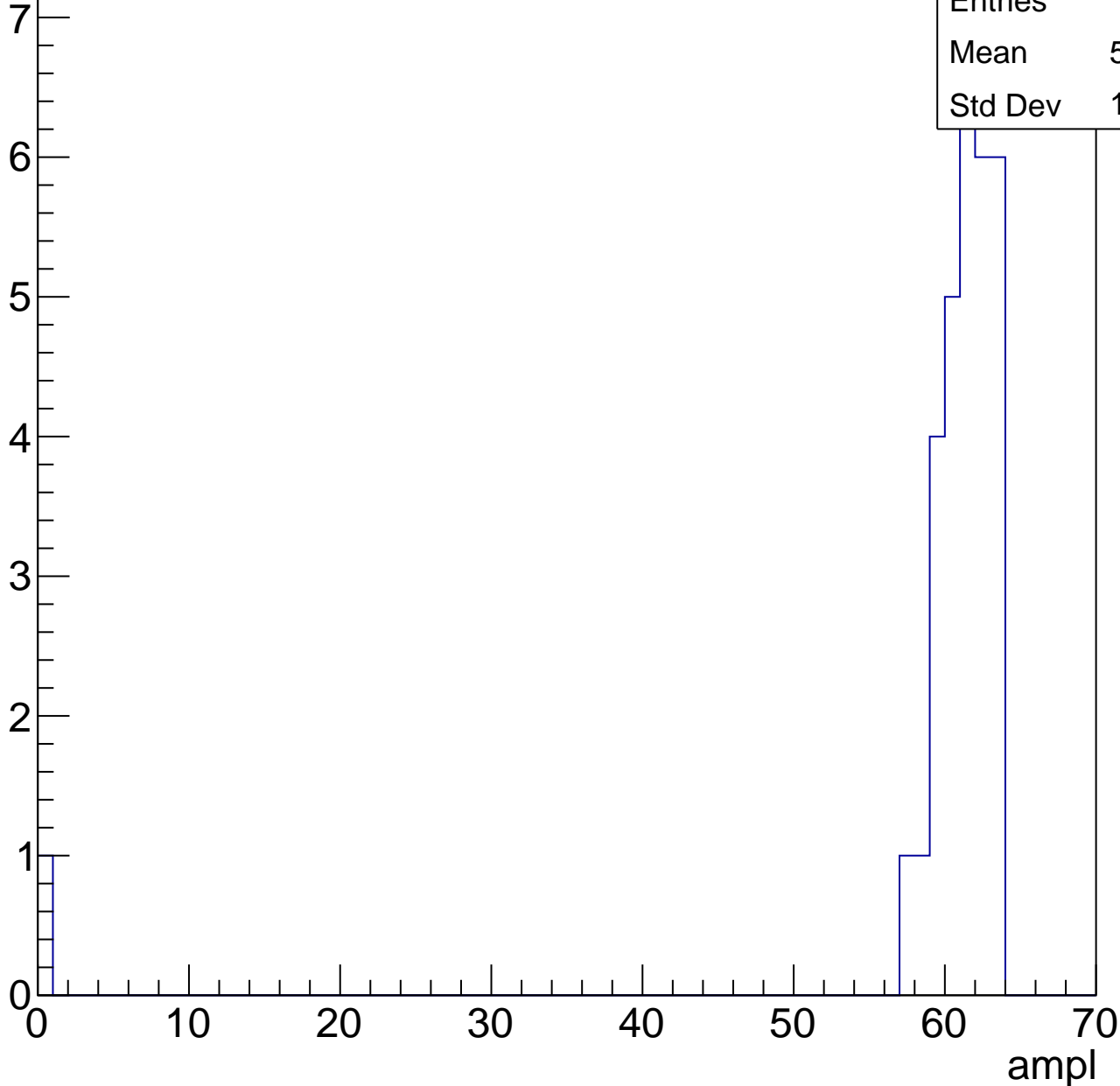


B0L001S, U18-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

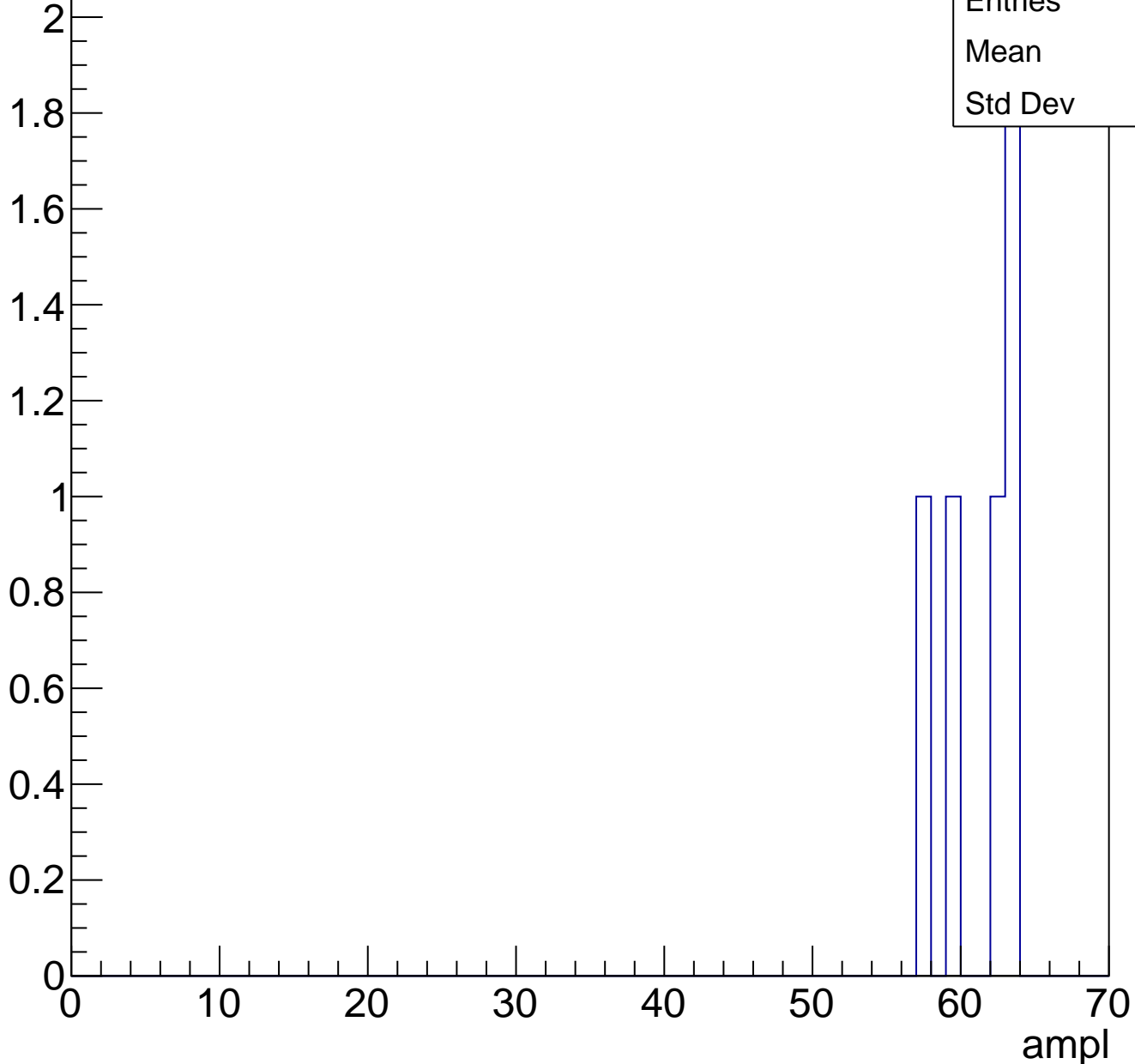
Entries	31
Mean	58.97
Std Dev	10.88



B0L001S, U18-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	60.8
Std Dev	2.4

B0L001S, U18-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch106, adc0

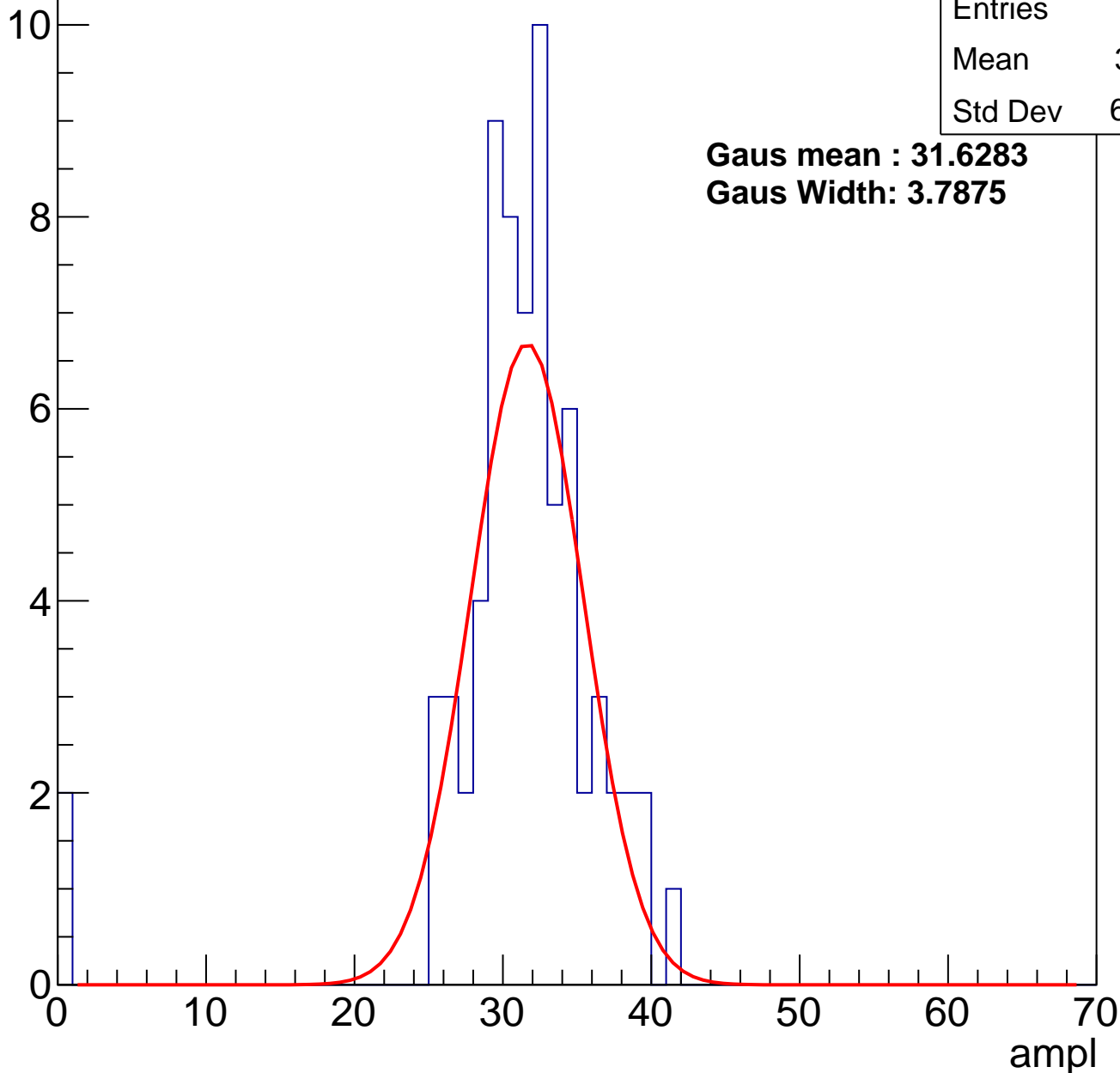
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	30.61
Std Dev	6.274

Gaus mean : 31.6283

Gaus Width: 3.7875

Entry



B0L001S, U18-ch106, adc1

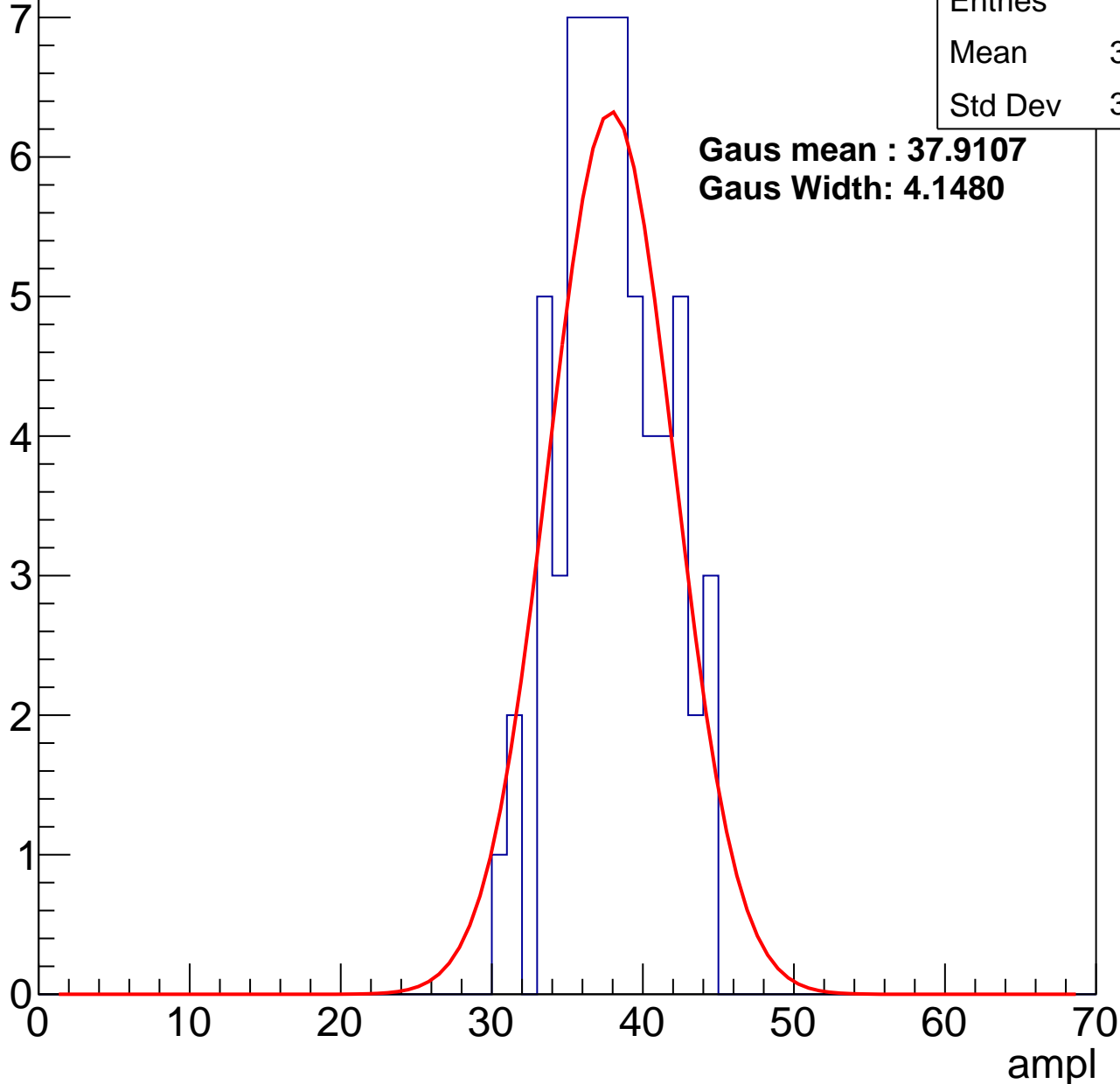
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.55
Std Dev	3.397

Gaus mean : 37.9107

Gaus Width: 4.1480



B0L001S, U18-ch106, adc2

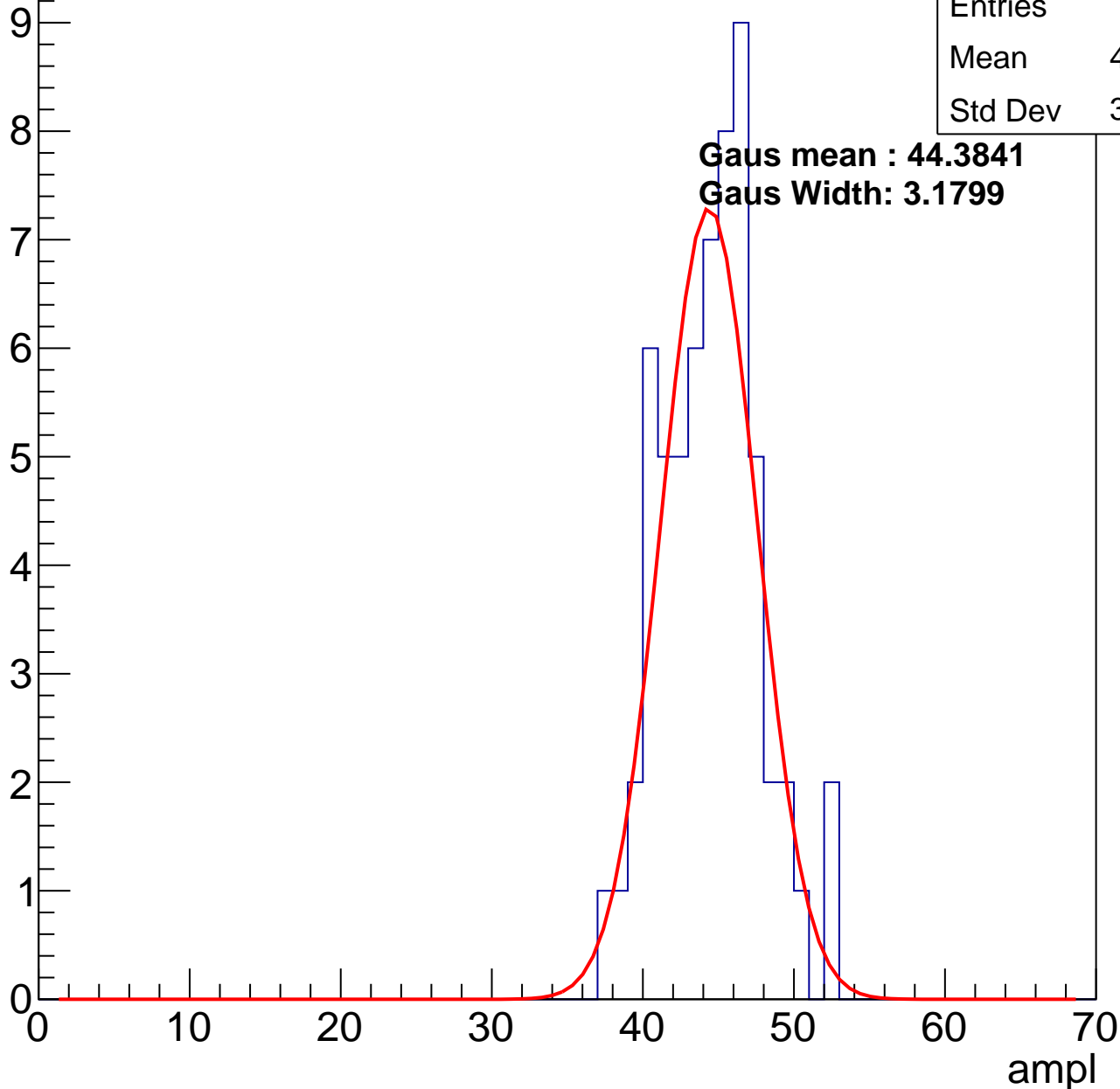
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.05
Std Dev	3.205

Gaus mean : 44.3841

Gaus Width: 3.1799

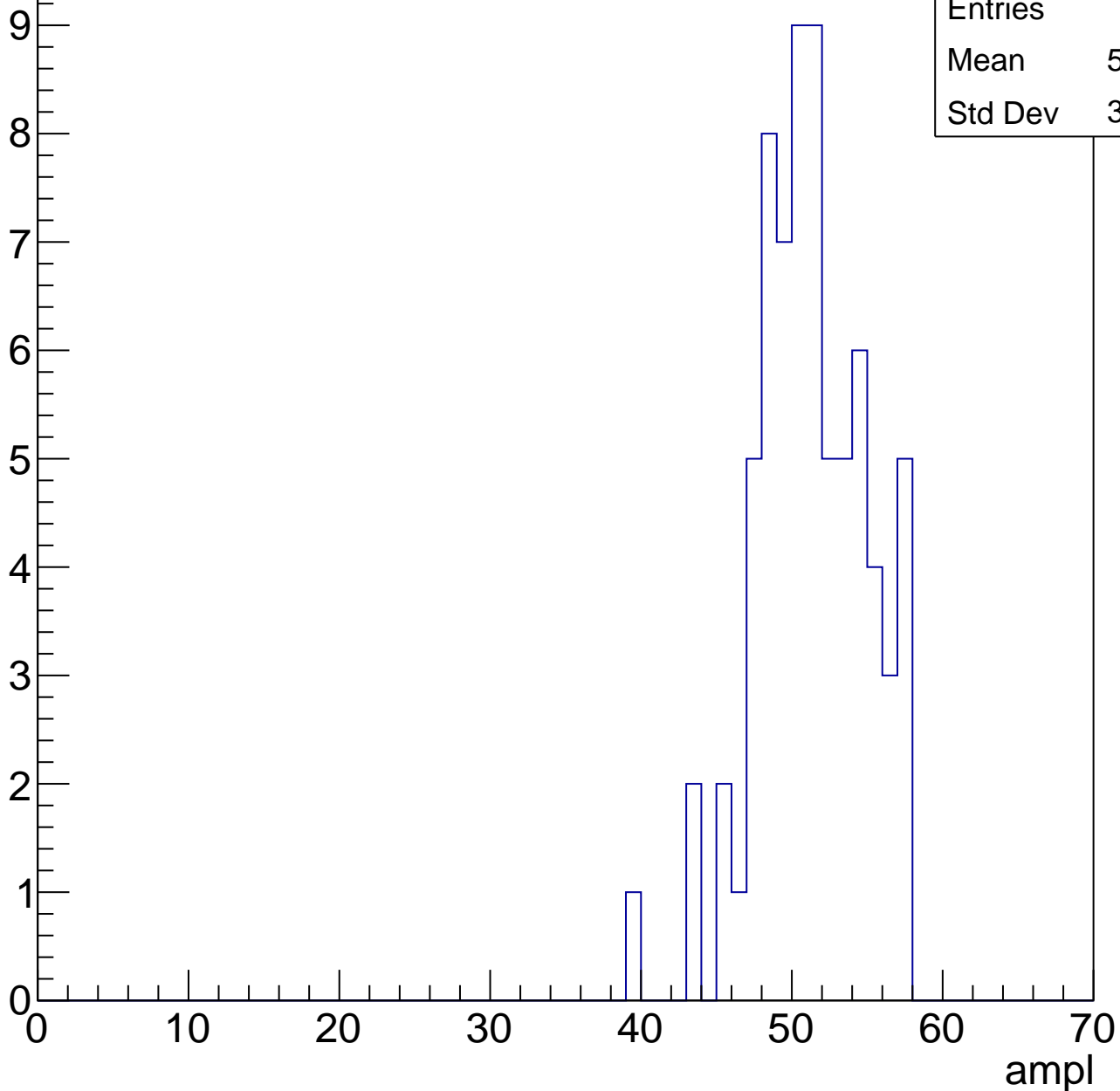


B0L001S, U18-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	50.75
Std Dev	3.635

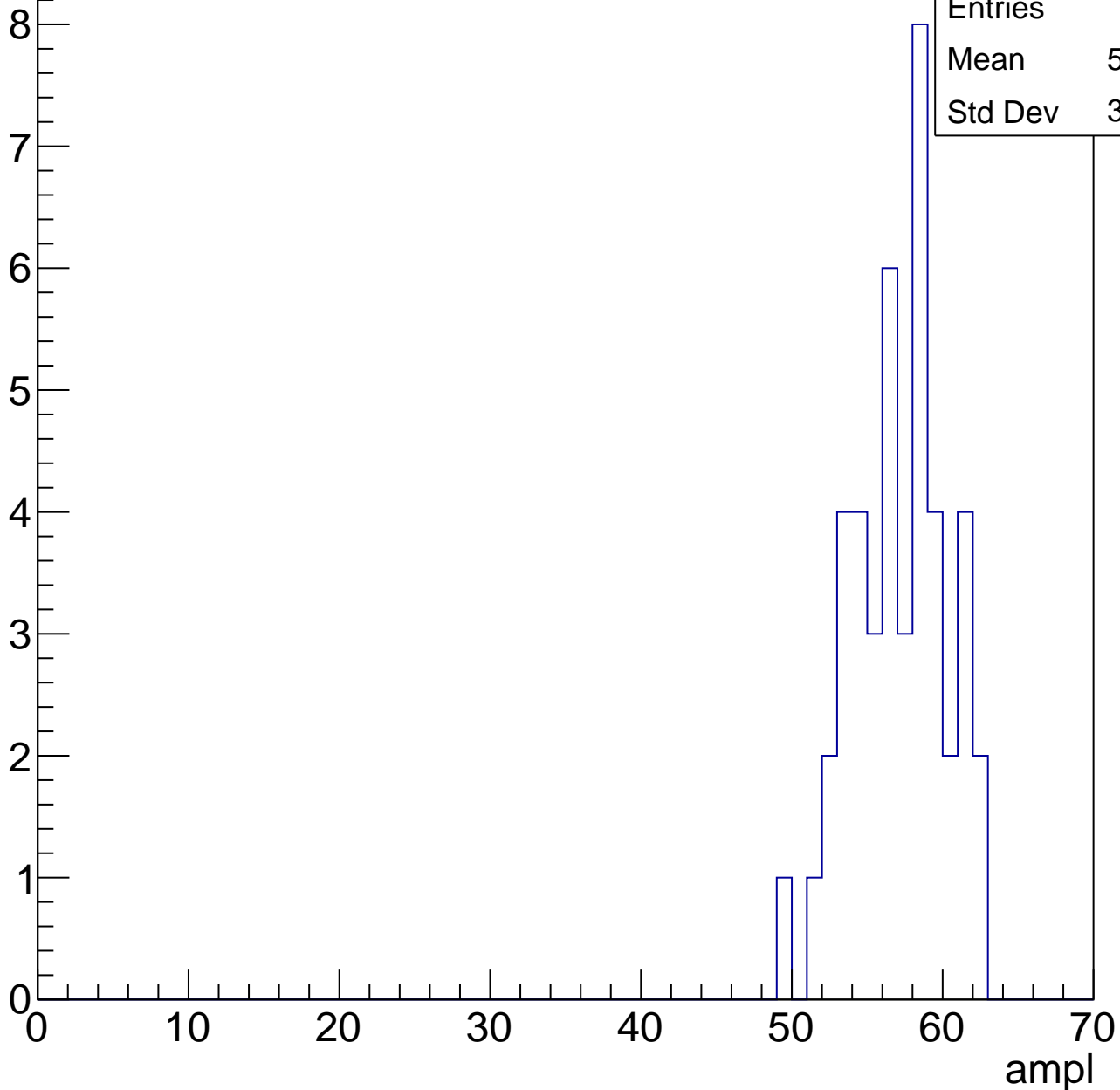


B0L001S, U18-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	56.64
Std Dev	3.083



B0L001S, U18-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	49
Mean	58.82
Std Dev	8.916

Entry

10

8

6

4

2

0

0

10

20

30

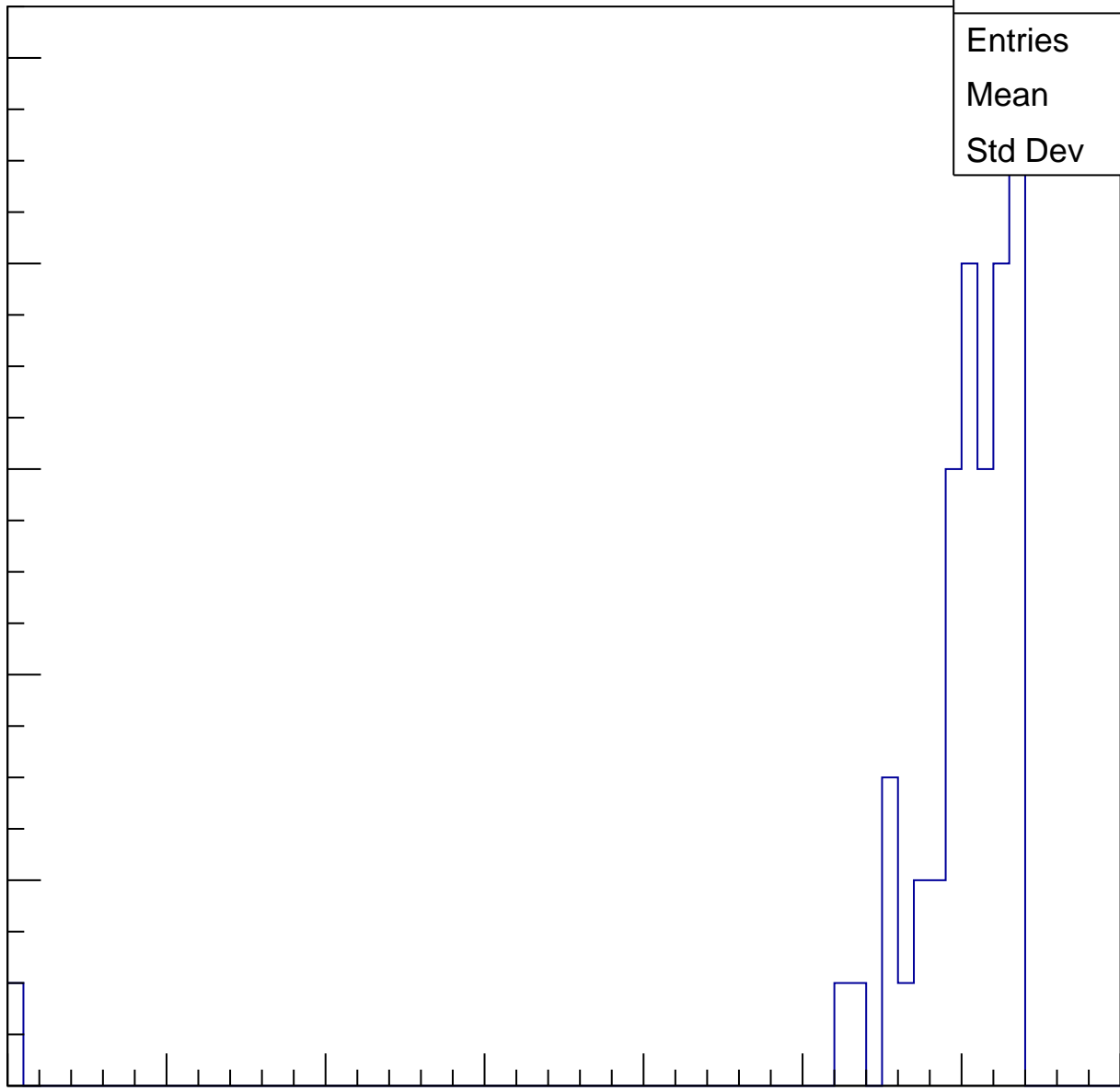
40

50

60

ampl

70



B0L001S, U18-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

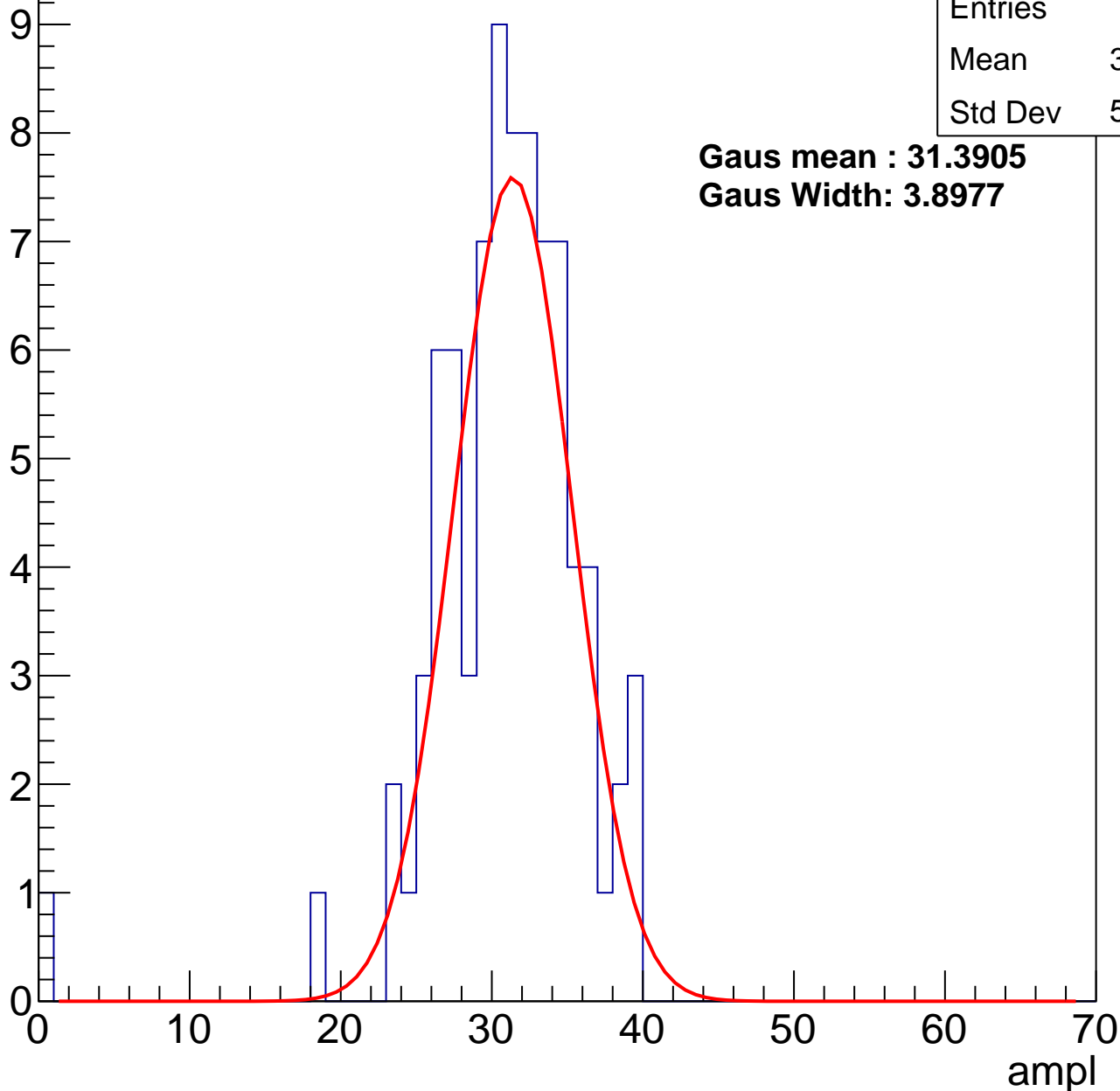
B0L001S, U18-ch107, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	30.42
Std Dev	5.248

Gaus mean : 31.3905
Gaus Width: 3.8977



B0L001S, U18-ch107, adc1

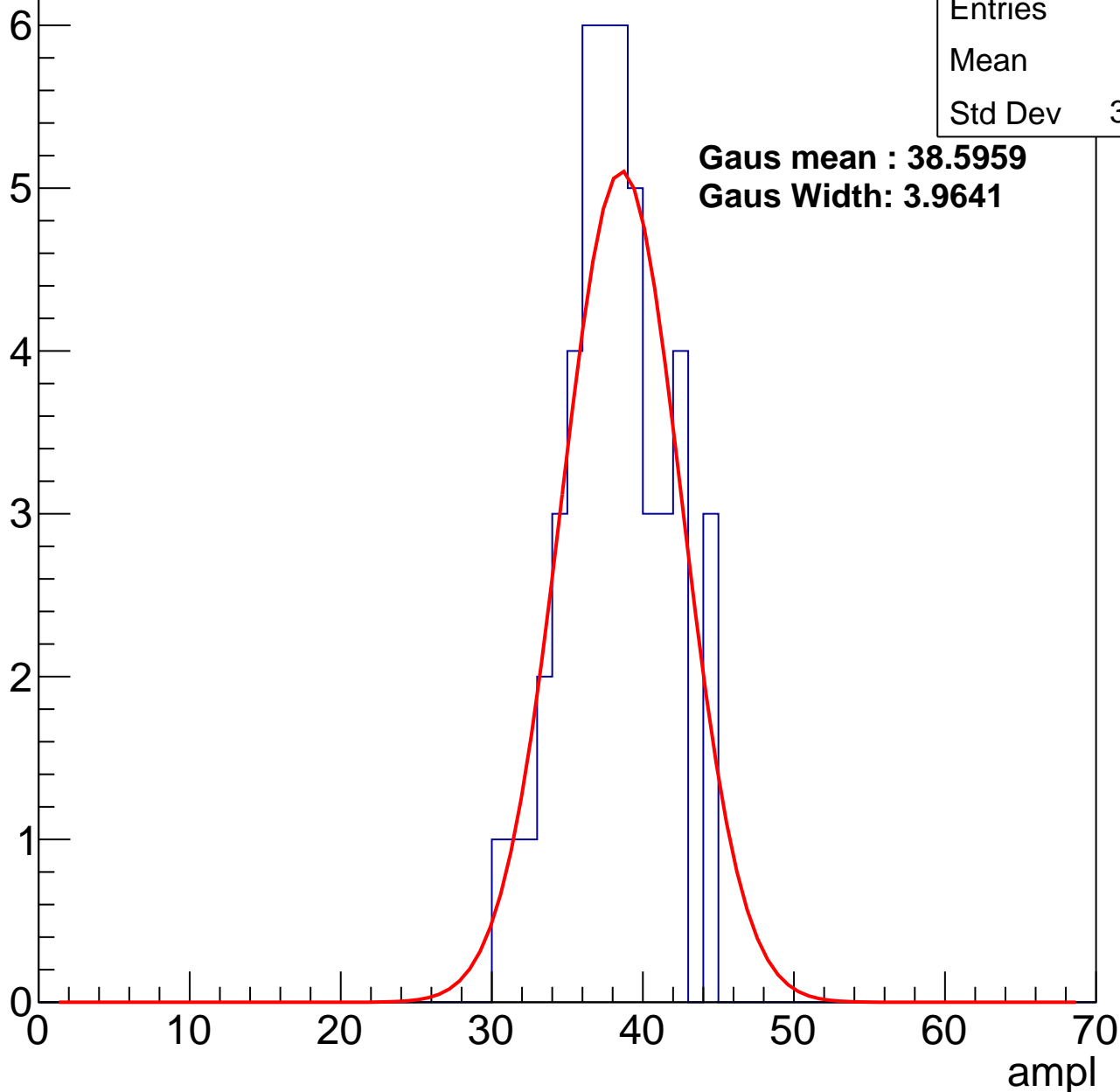
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	37.6
Std Dev	3.296

Gaus mean : 38.5959

Gaus Width: 3.9641



B0L001S, U18-ch107, adc2

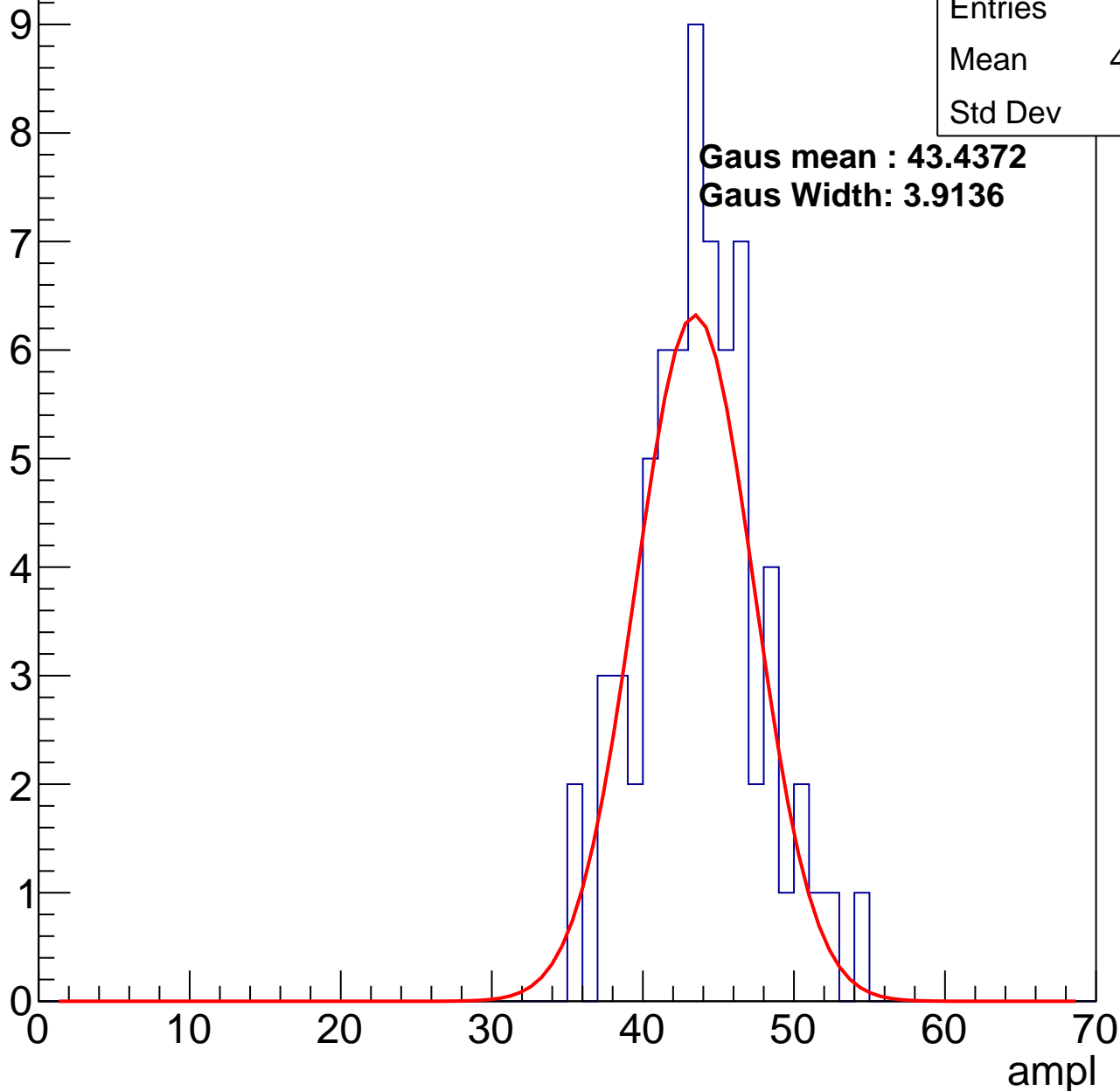
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.38
Std Dev	3.9

Gaus mean : 43.4372

Gaus Width: 3.9136

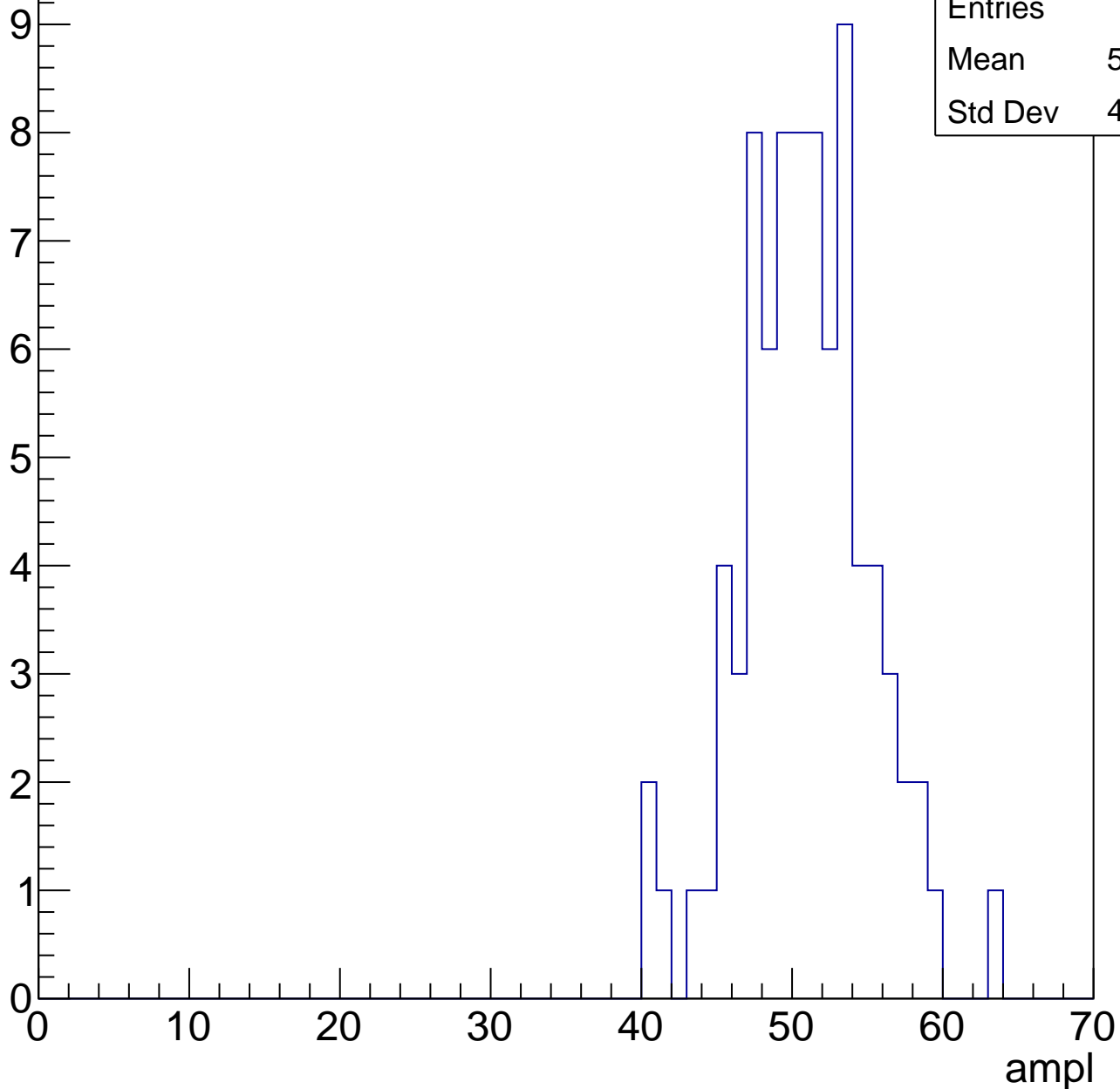


B0L001S, U18-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	50.43
Std Dev	4.225

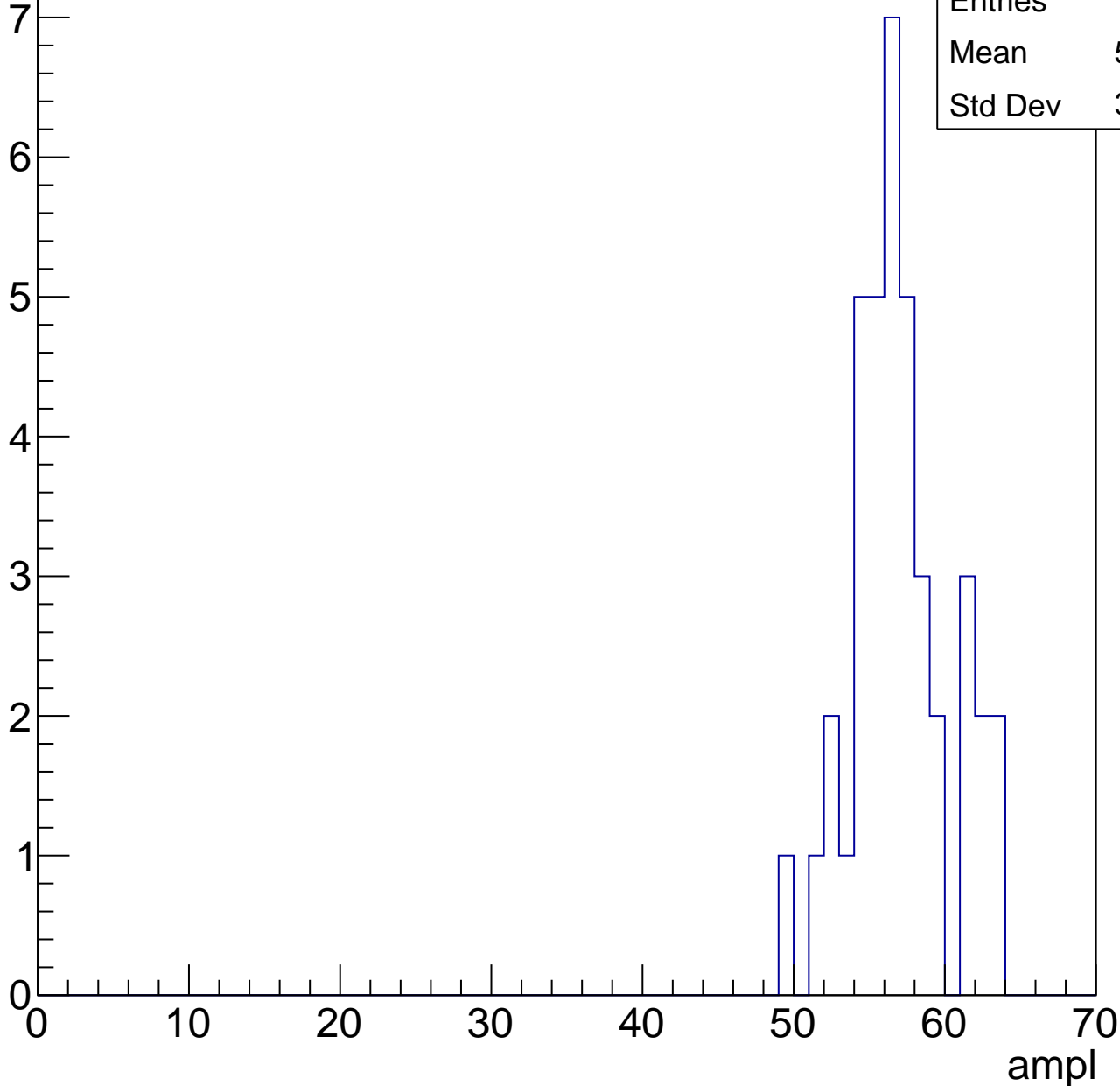


B0L001S, U18-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	56.51
Std Dev	3.241

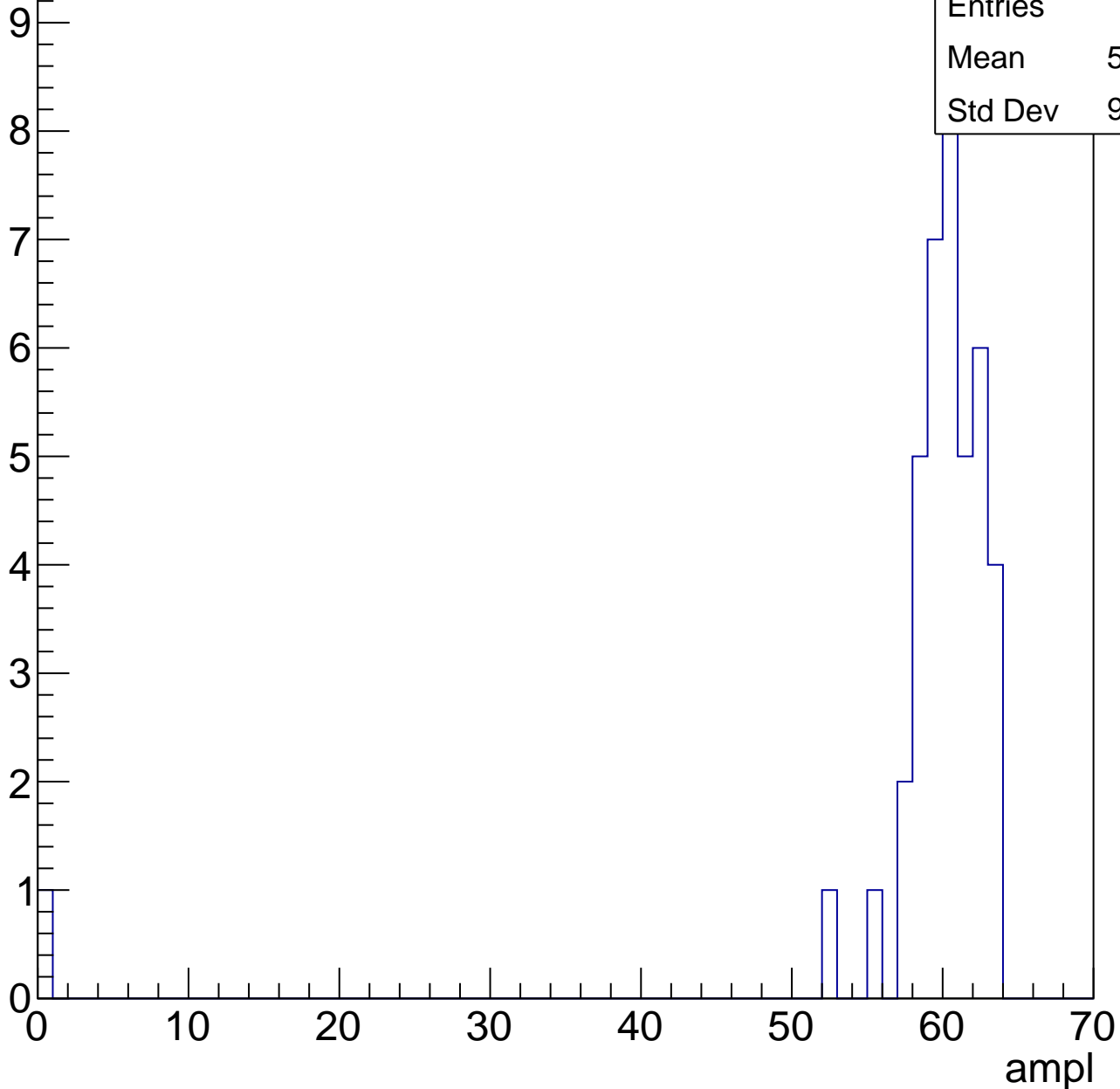


B0L001S, U18-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.37
Std Dev	9.486



B0L001S, U18-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.33
Std Dev	1.826

B0L001S, U18-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U18-ch108, adc0

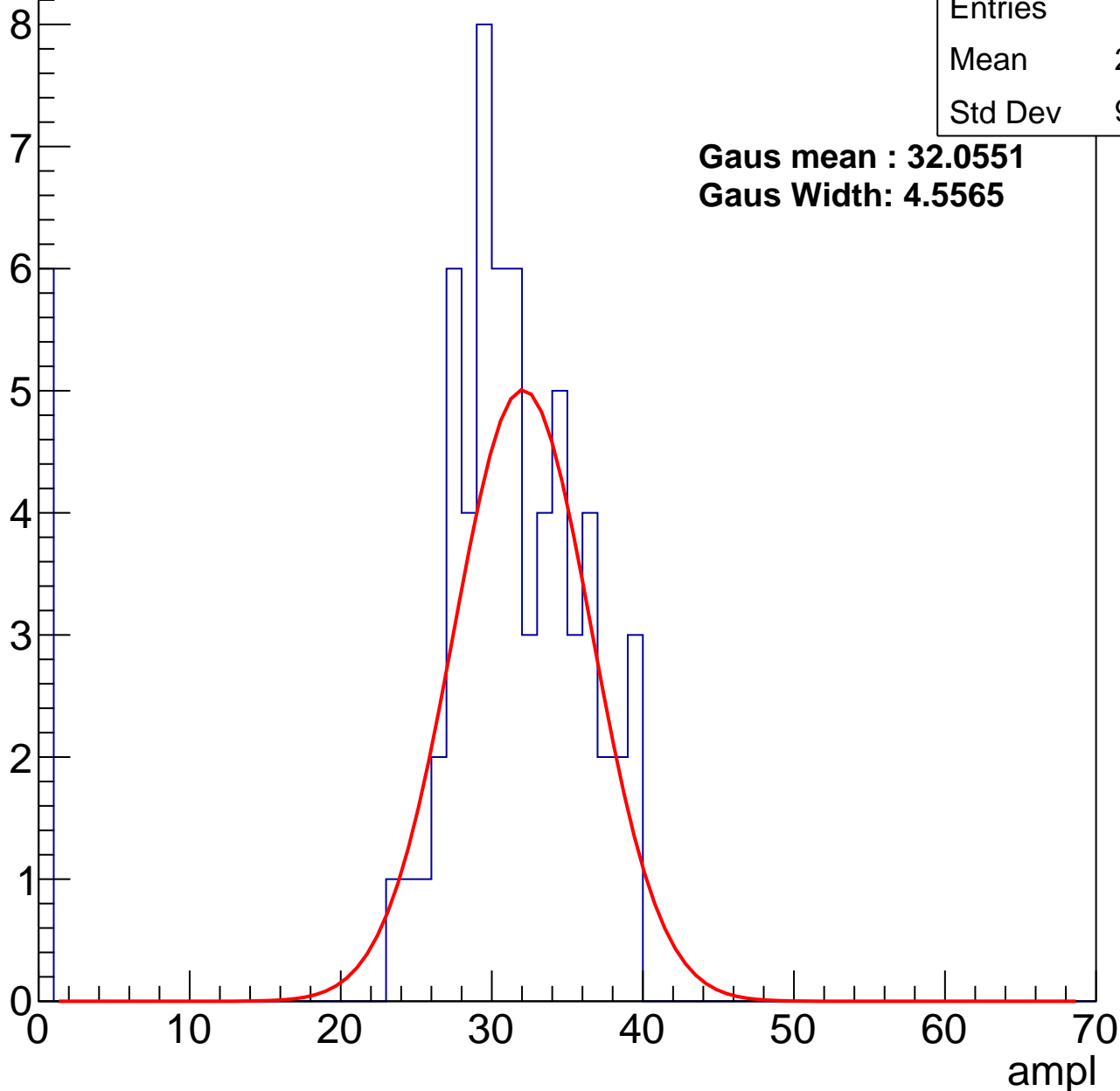
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	28.51
Std Dev	9.691

Gaus mean : 32.0551

Gaus Width: 4.5565



B0L001S, U18-ch108, adc1

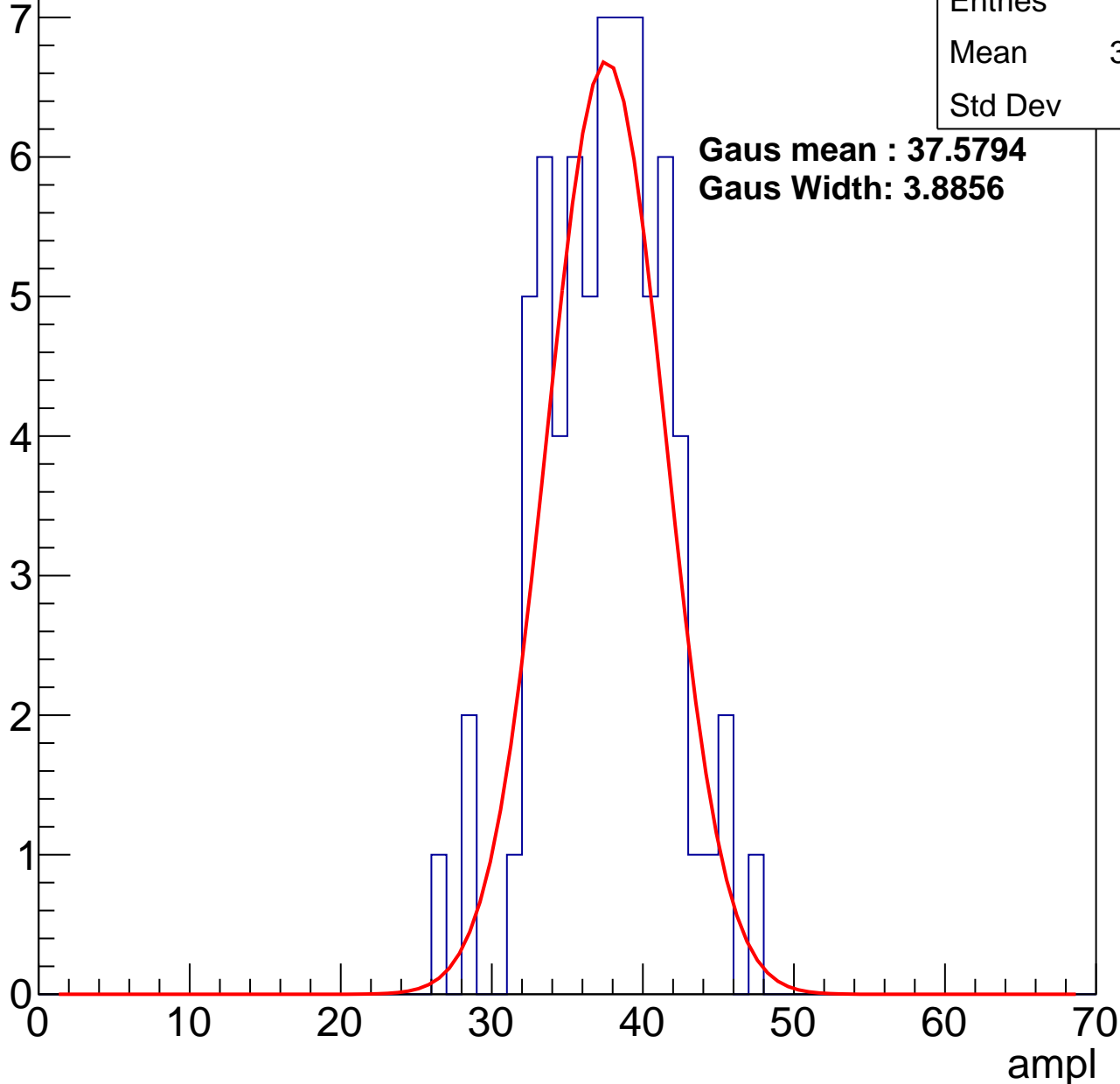
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.08
Std Dev	4.1

Gaus mean : 37.5794

Gaus Width: 3.8856



B0L001S, U18-ch108, adc2

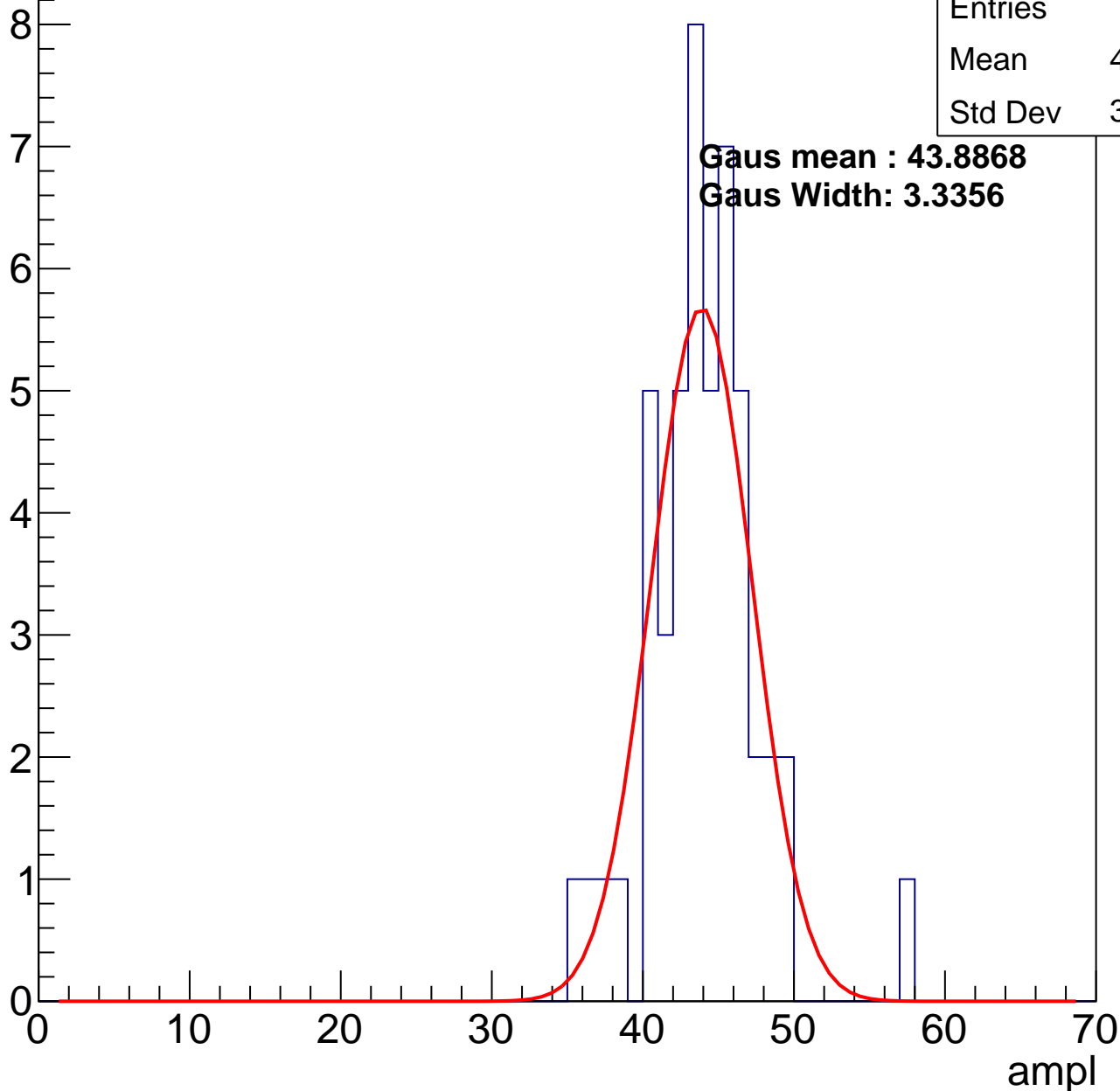
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	43.53
Std Dev	3.643

Gaus mean : 43.8868

Gaus Width: 3.3356

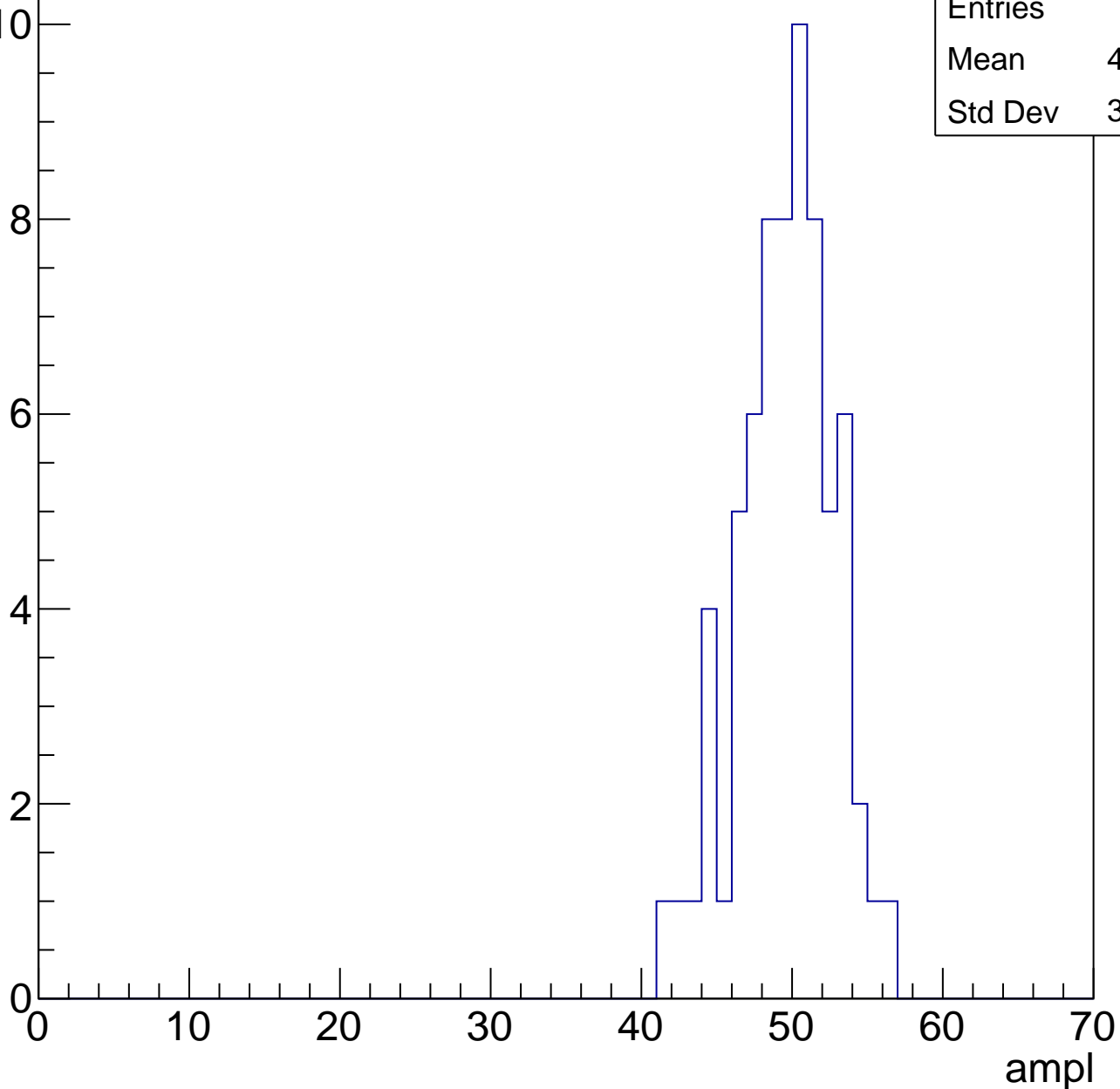


B0L001S, U18-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	49.12
Std Dev	3.113

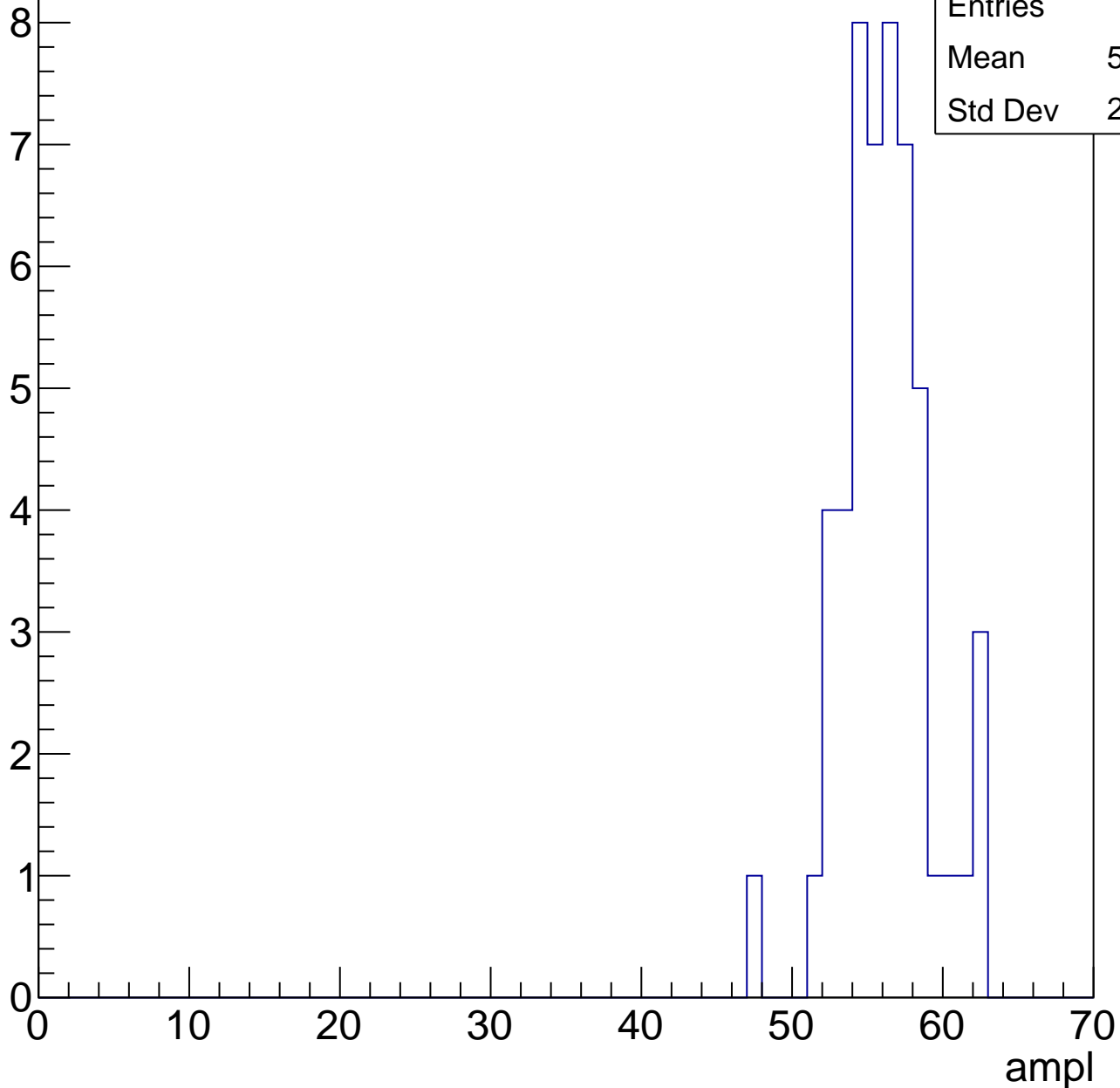


B0L001S, U18-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	55.65
Std Dev	2.882

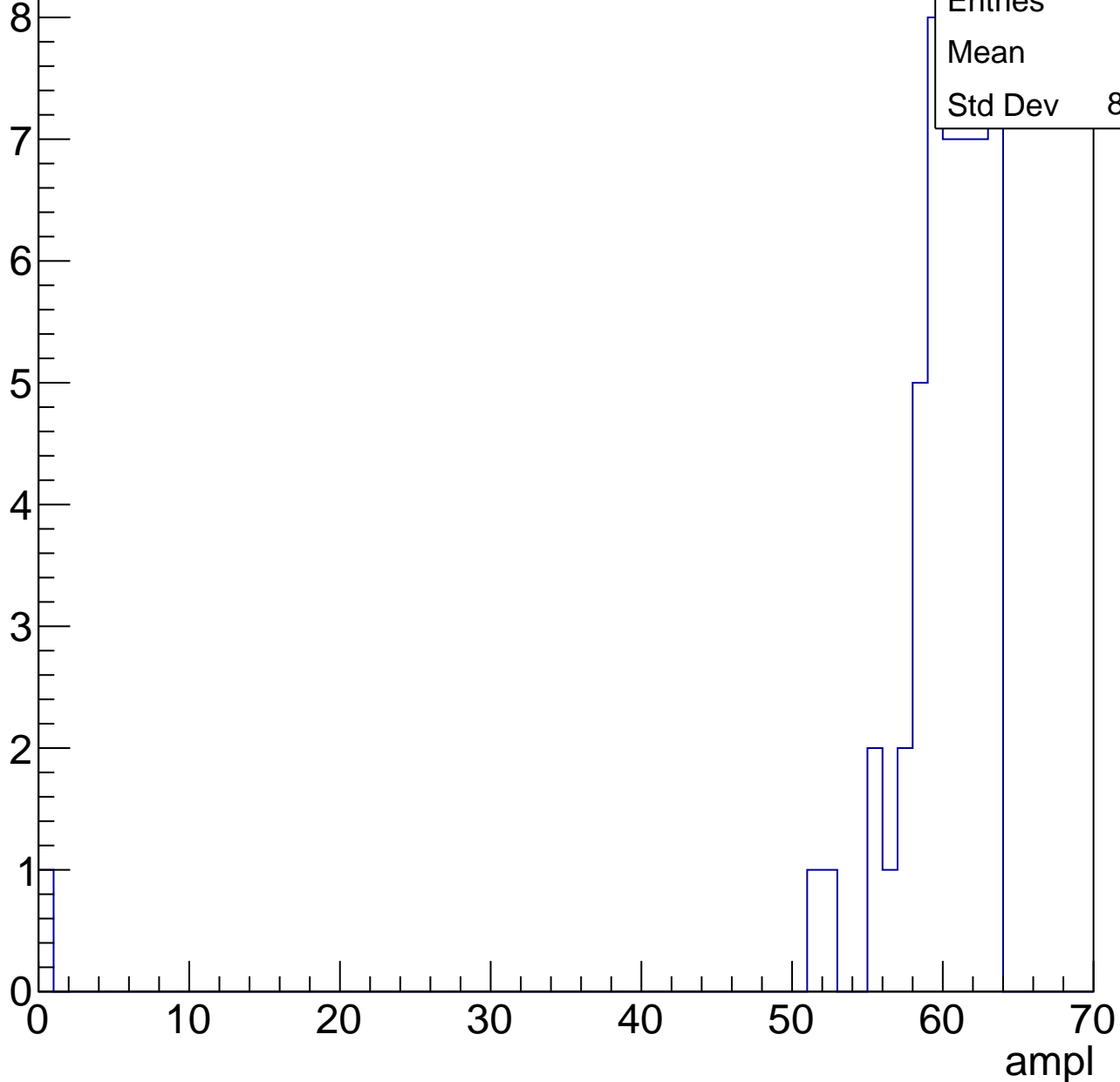


B0L001S, U18-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	58.6
Std Dev	8.793



B0L001S, U18-ch108, adc6

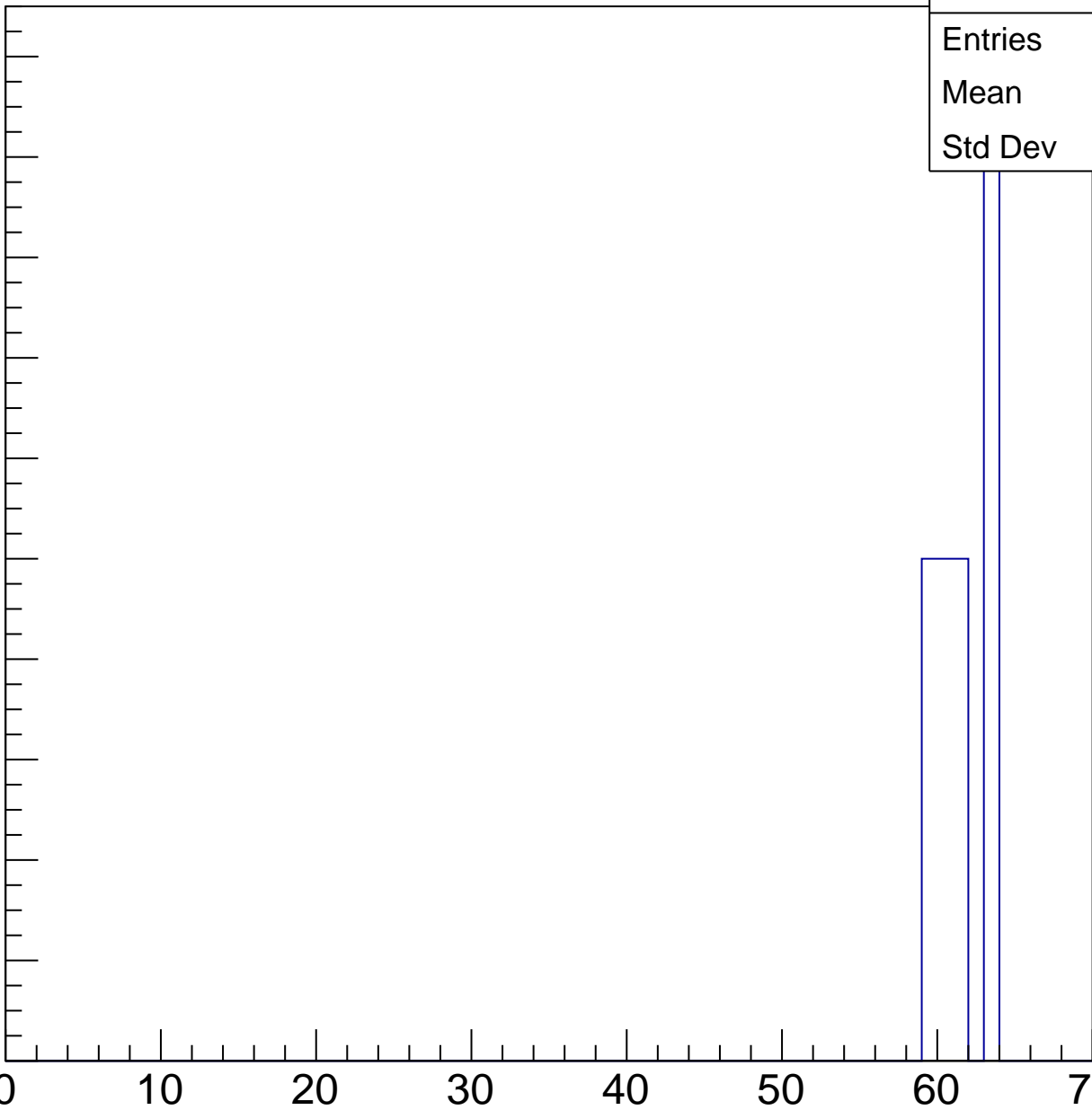
calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.6

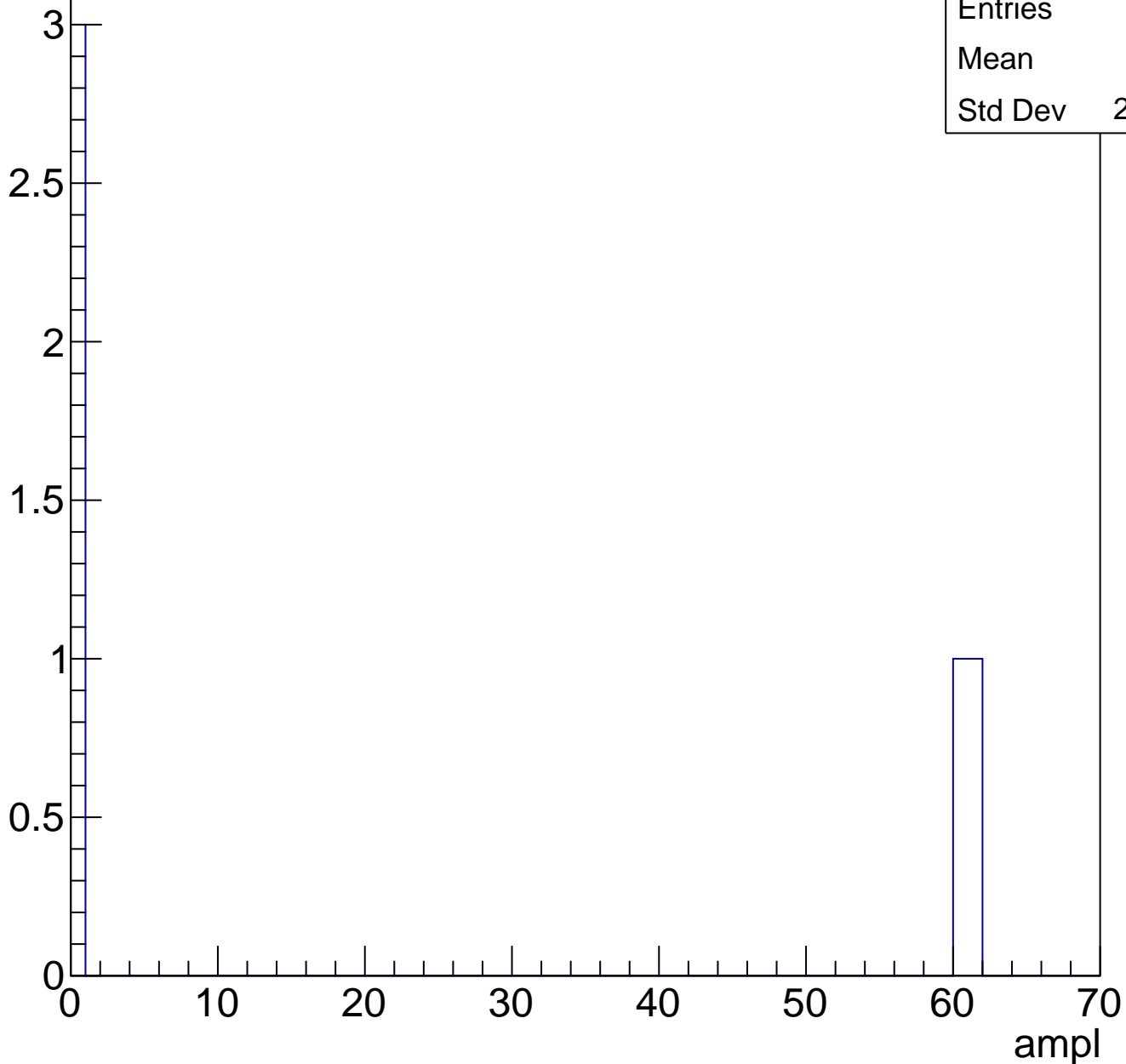
ampl



B0L001S, U18-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch109, adc0

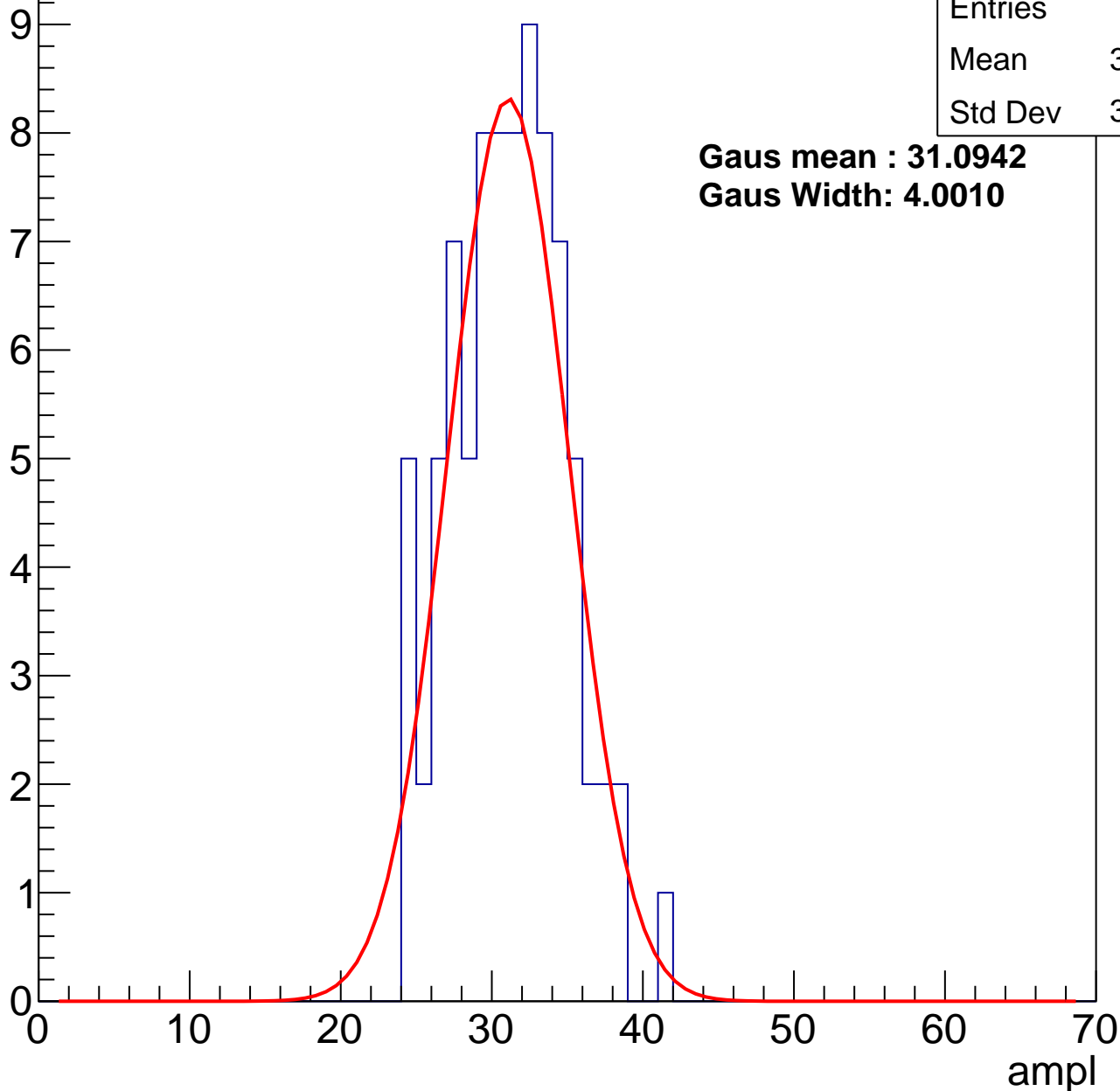
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	30.68
Std Dev	3.675

Gaus mean : 31.0942

Gaus Width: 4.0010



B0L001S, U18-ch109, adc1

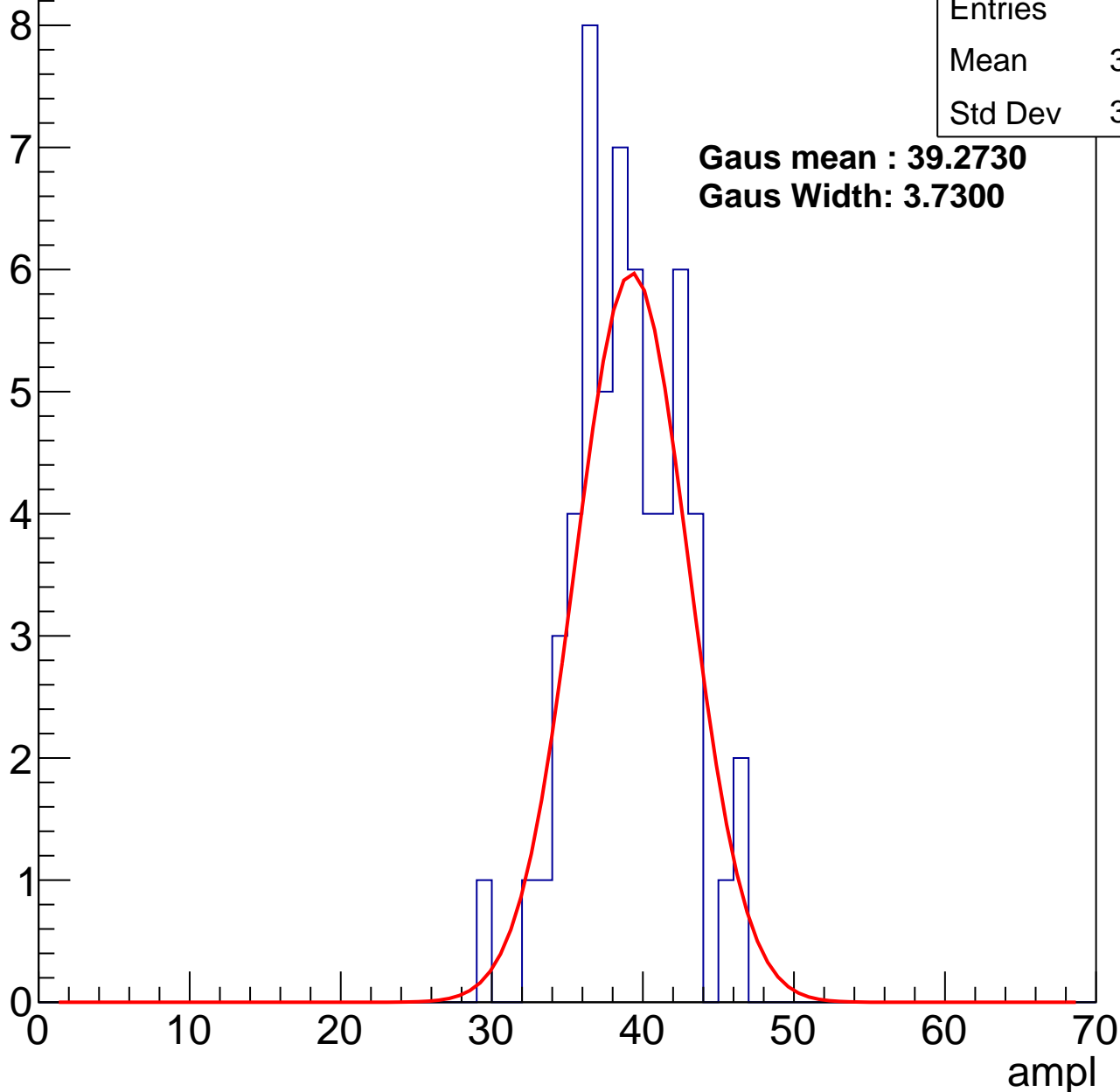
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	38.49
Std Dev	3.455

Gaus mean : 39.2730

Gaus Width: 3.7300



B0L001S, U18-ch109, adc2

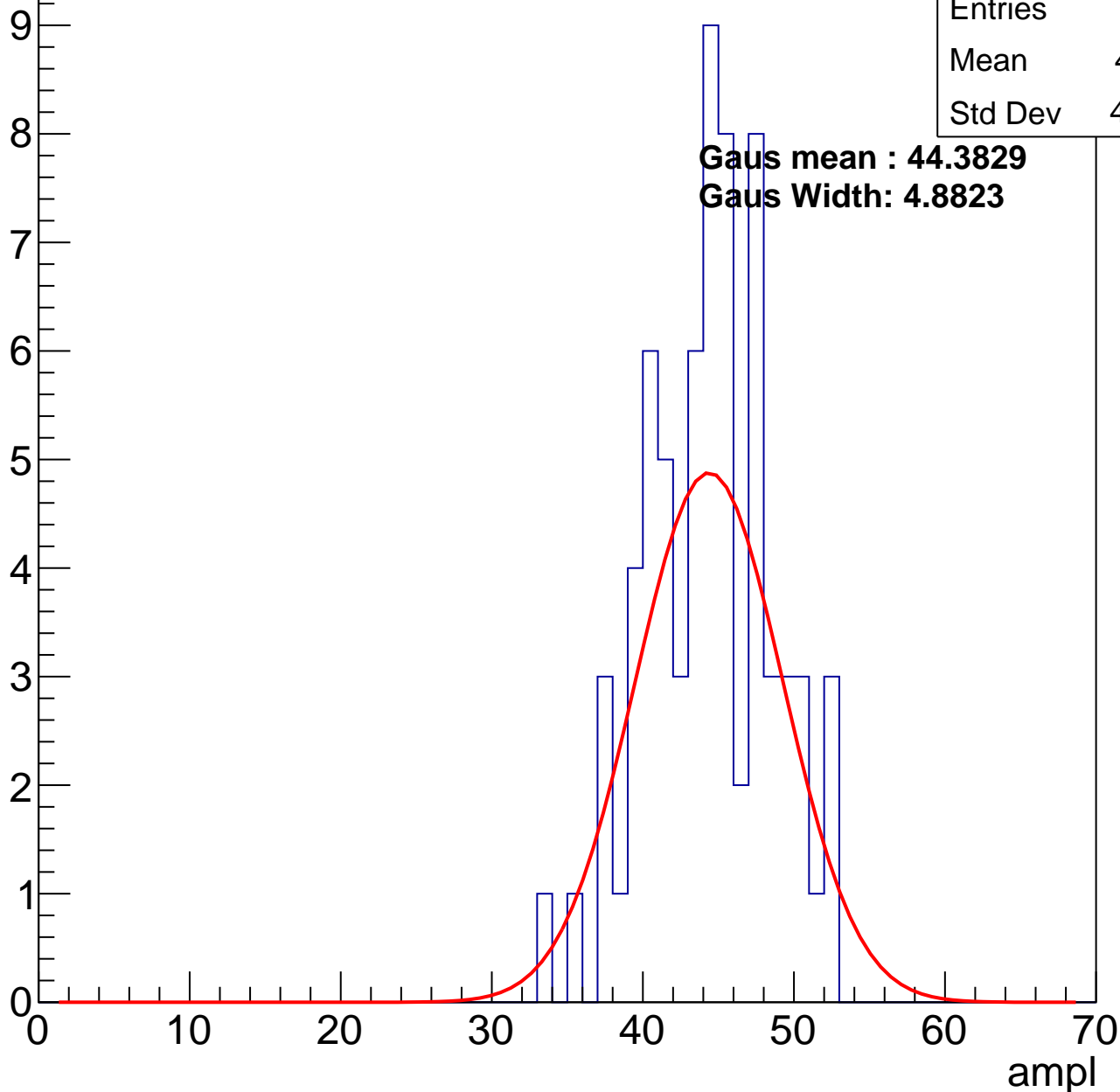
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.91
Std Dev	4.146

Gaus mean : 44.3829

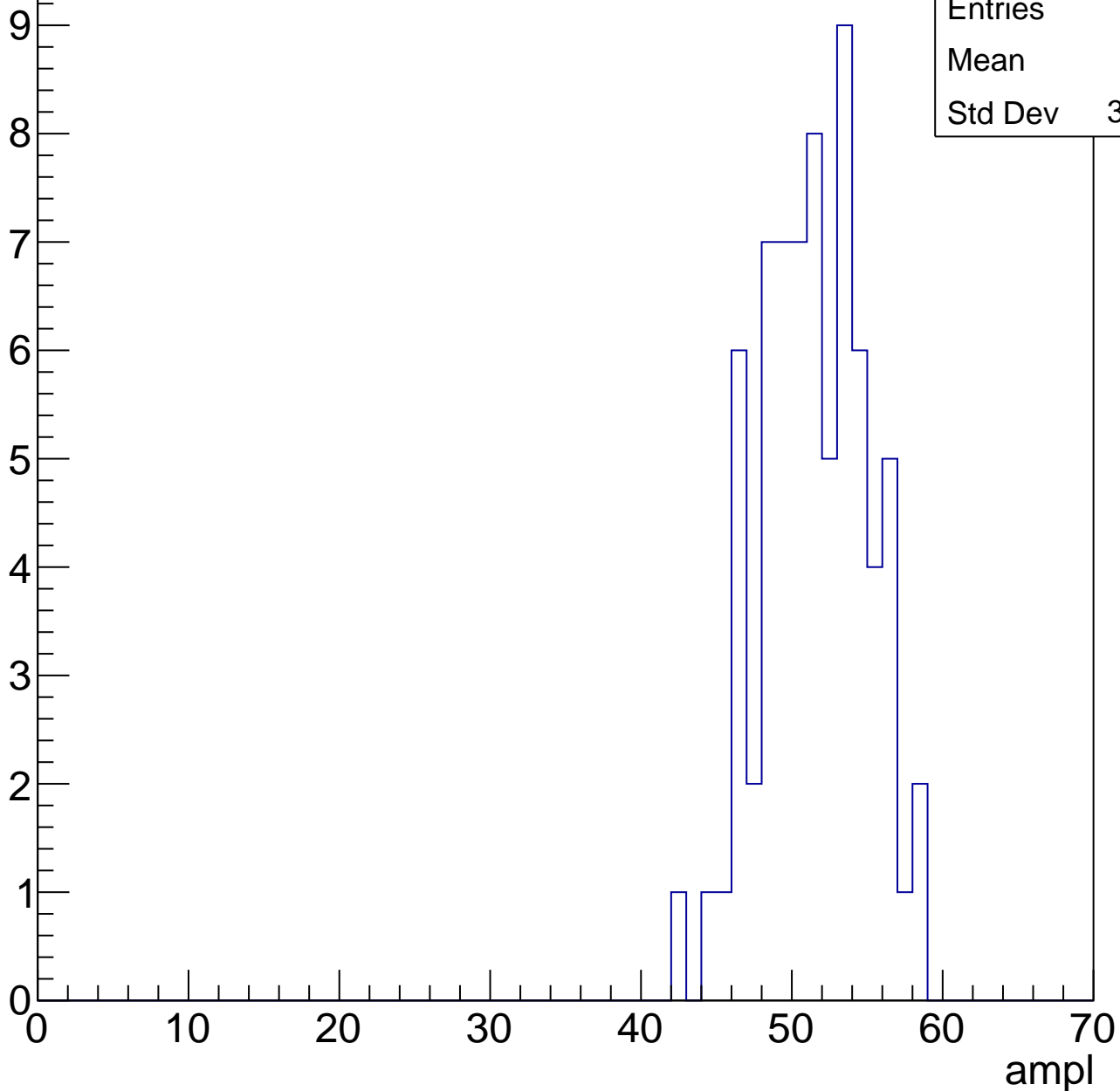
Gaus Width: 4.8823



B0L001S, U18-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

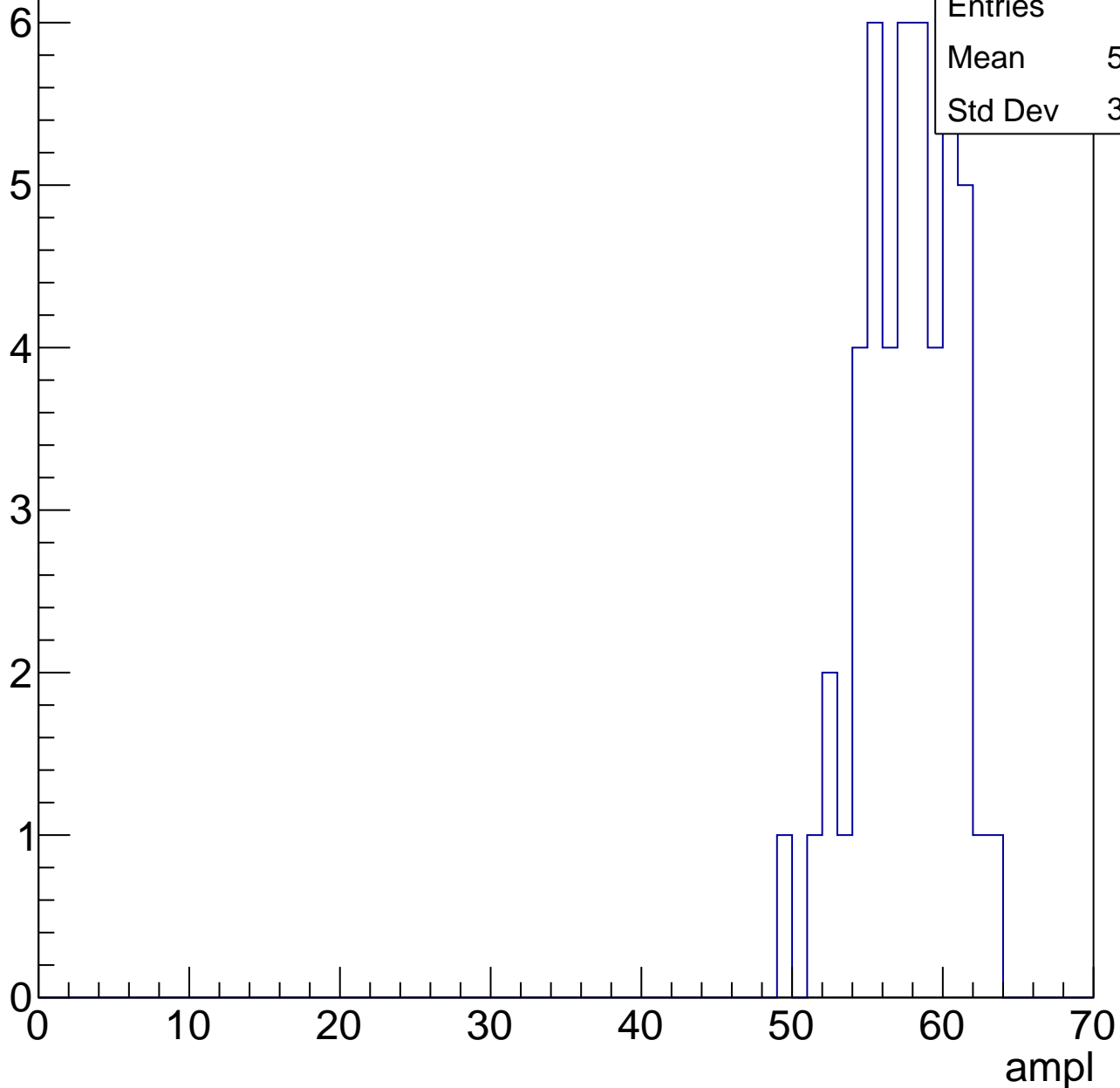


B0L001S, U18-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

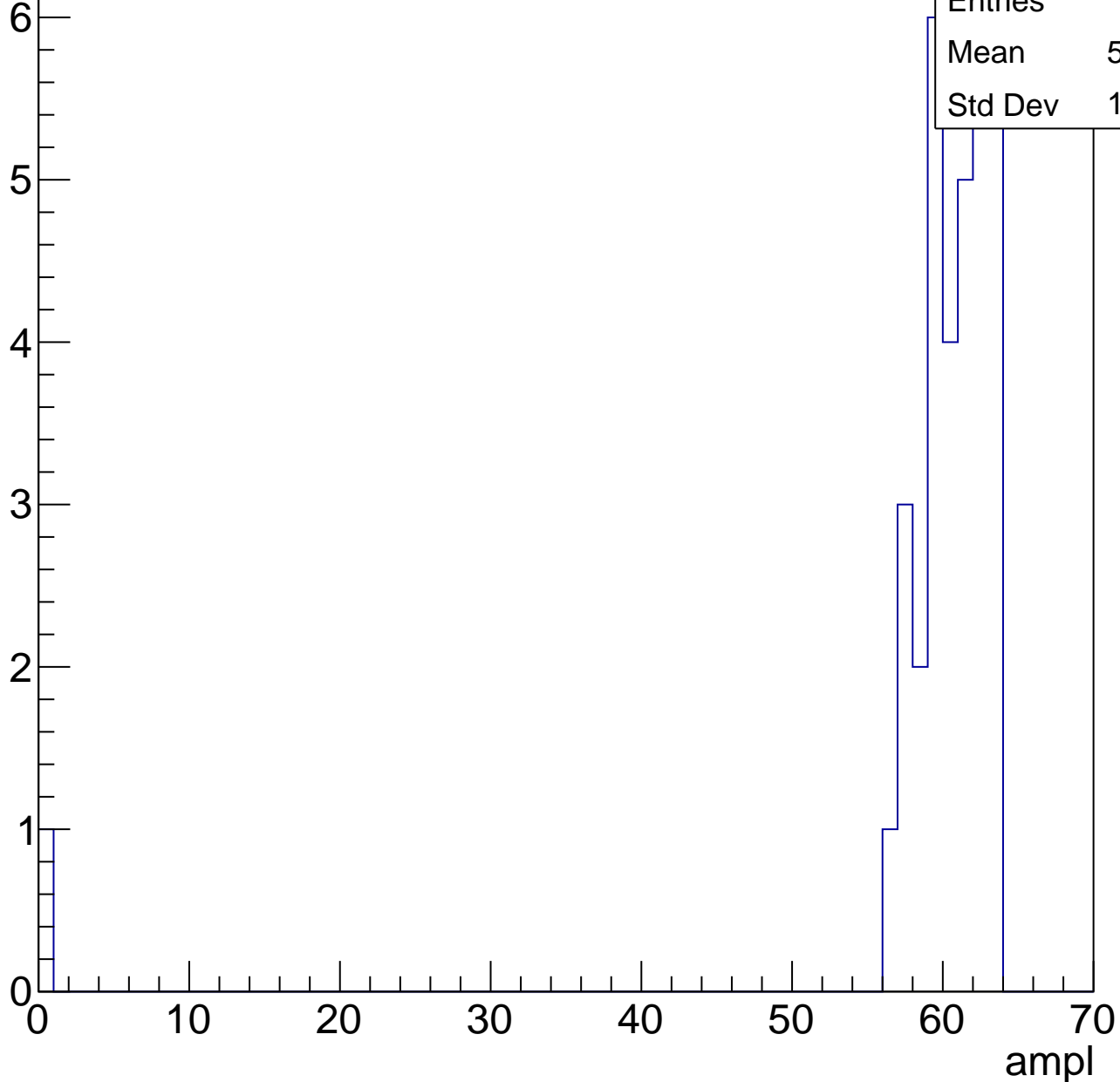
Entries	48
Mean	57.15
Std Dev	3.048



B0L001S, U18-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch110, adc0

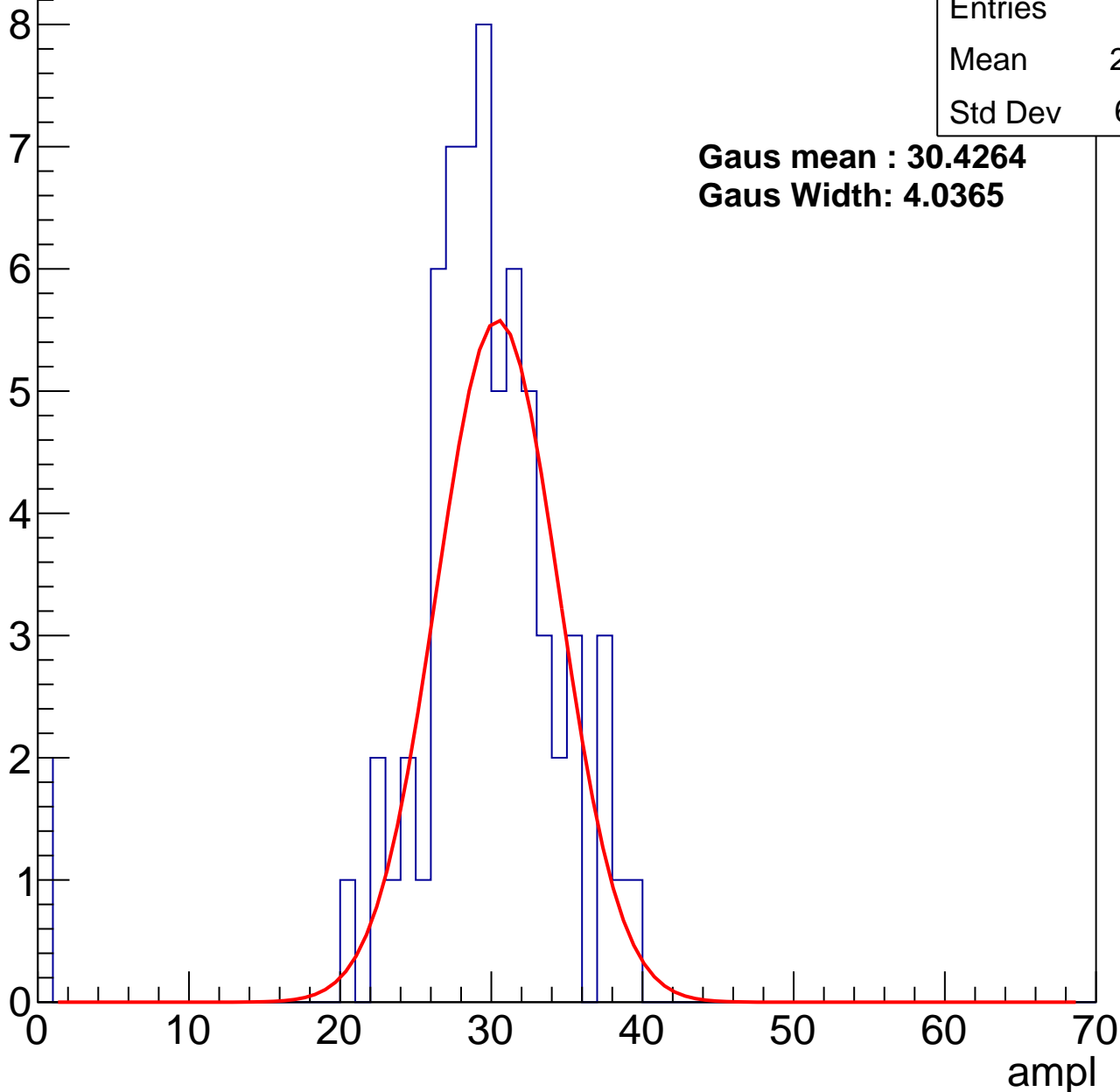
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	28.62
Std Dev	6.391

Gaus mean : 30.4264

Gaus Width: 4.0365



B0L001S, U18-ch110, adc1

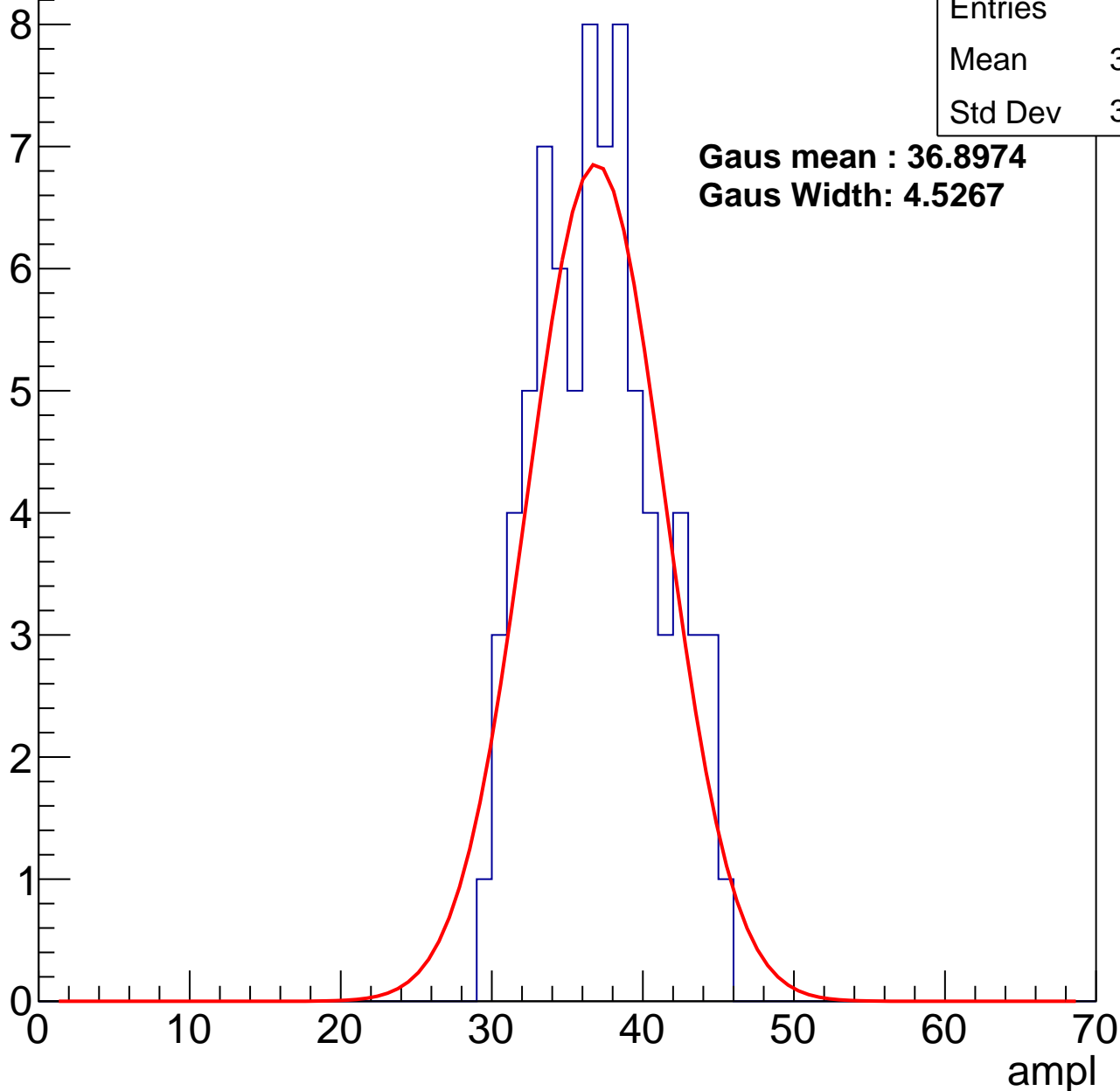
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.57
Std Dev	3.933

Gaus mean : 36.8974

Gaus Width: 4.5267



B0L001S, U18-ch110, adc2

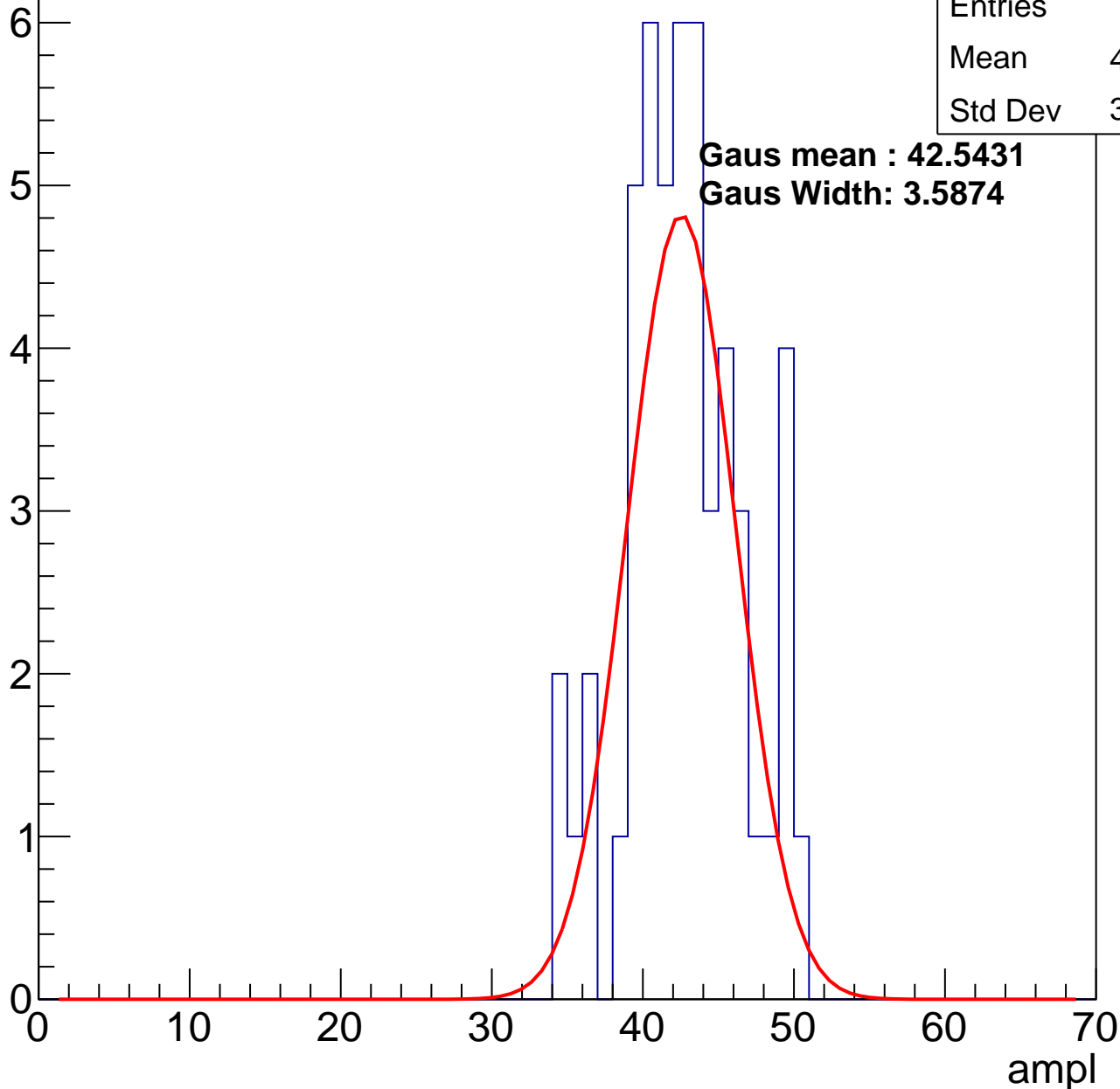
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	42.24
Std Dev	3.858

Gaus mean : 42.5431

Gaus Width: 3.5874

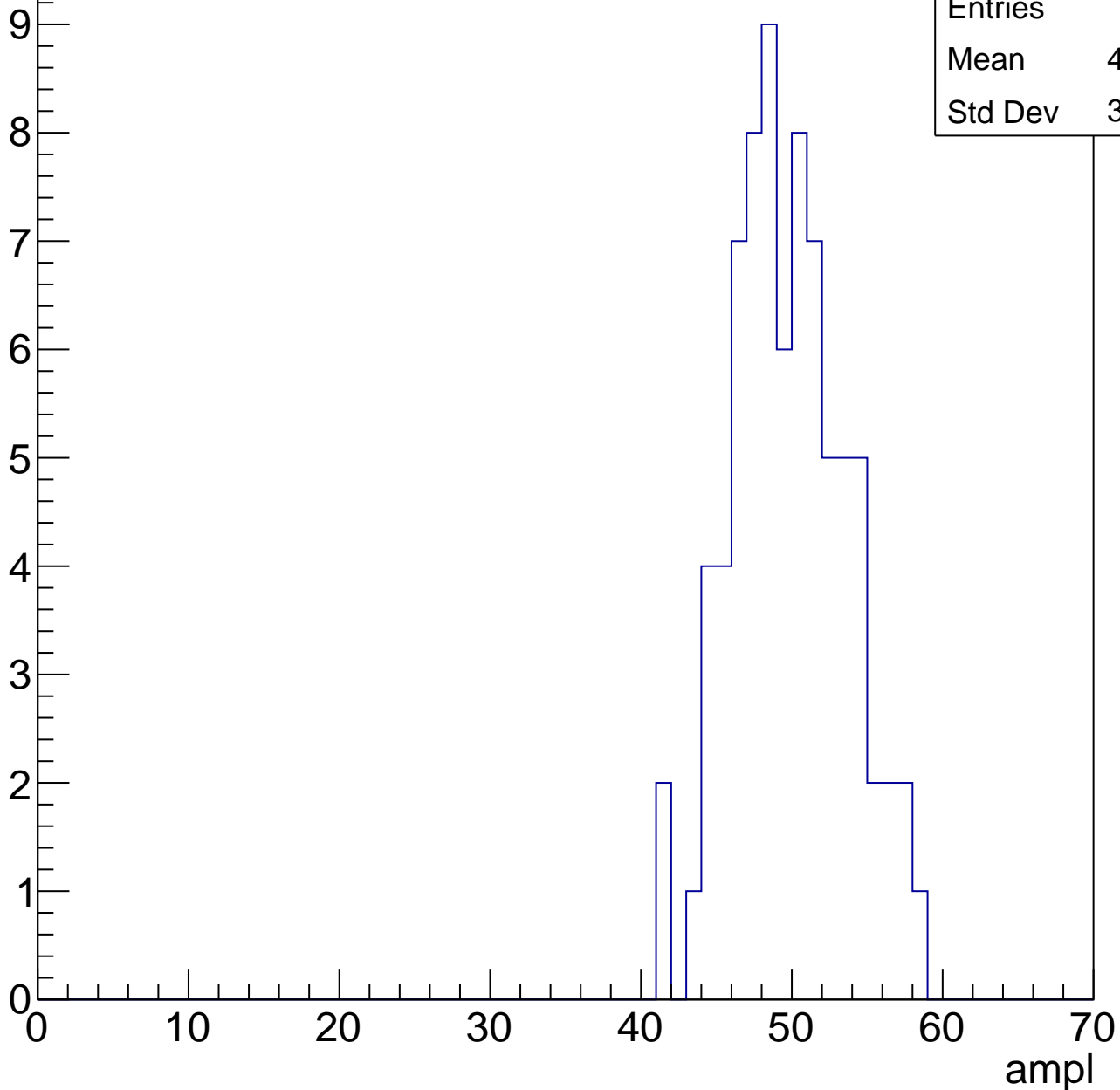


B0L001S, U18-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	49.37
Std Dev	3.732

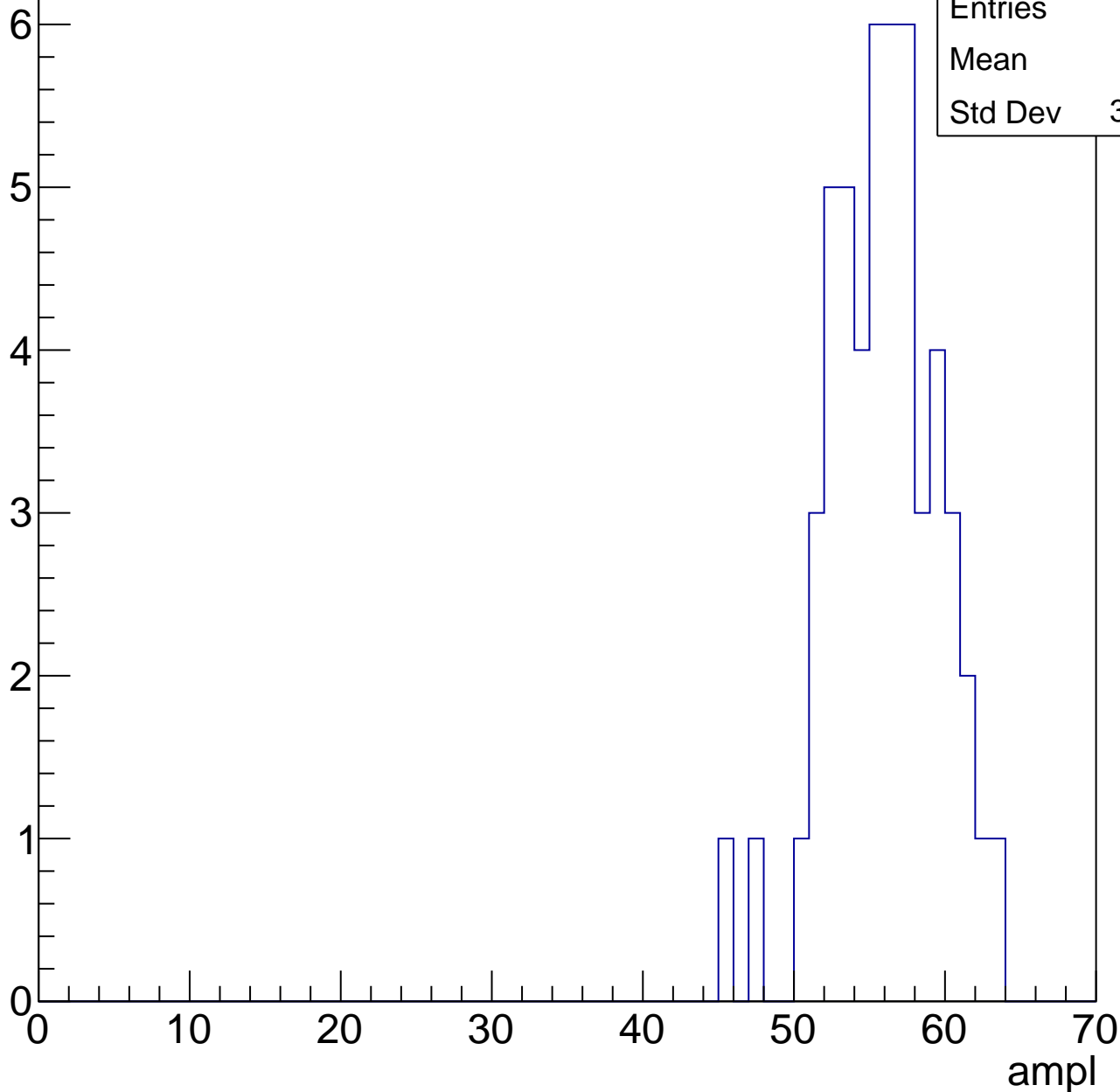


B0L001S, U18-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	55.4
Std Dev	3.612

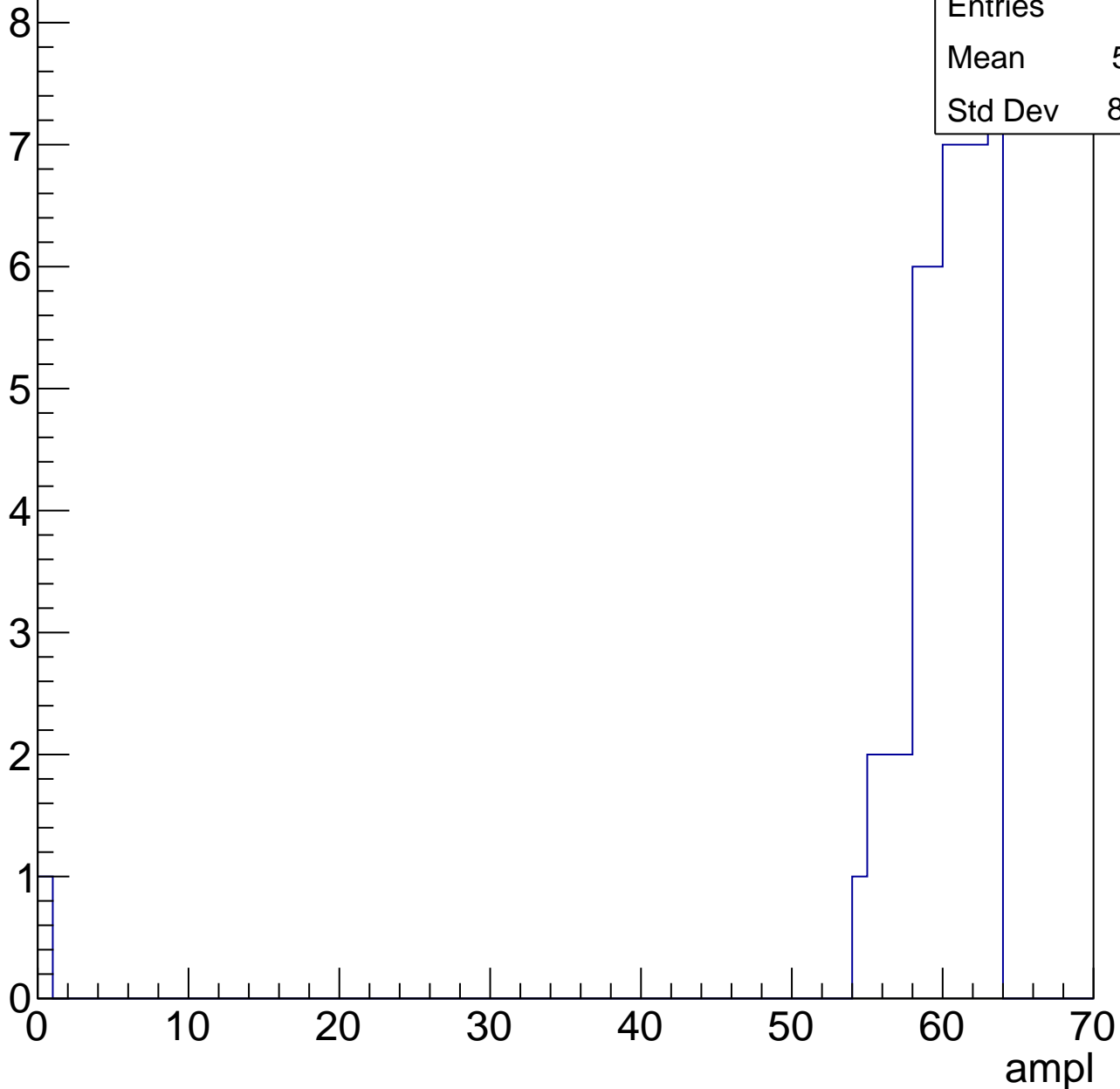


B0L001S, U18-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.71
Std Dev	8.797



B0L001S, U18-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch111, adc0

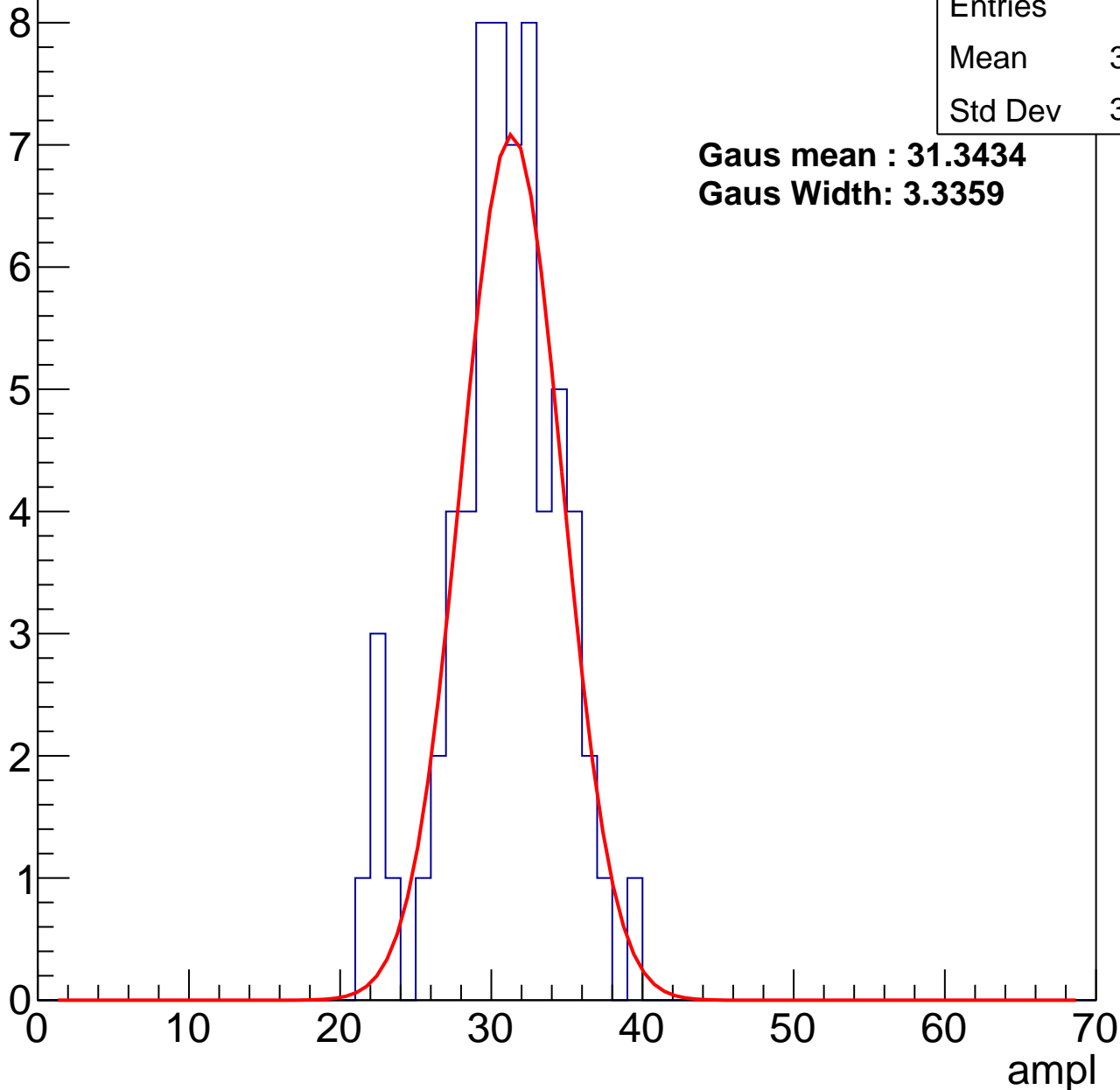
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.34
Std Dev	3.726

Gaus mean : 31.3434

Gaus Width: 3.3359



B0L001S, U18-ch111, adc1

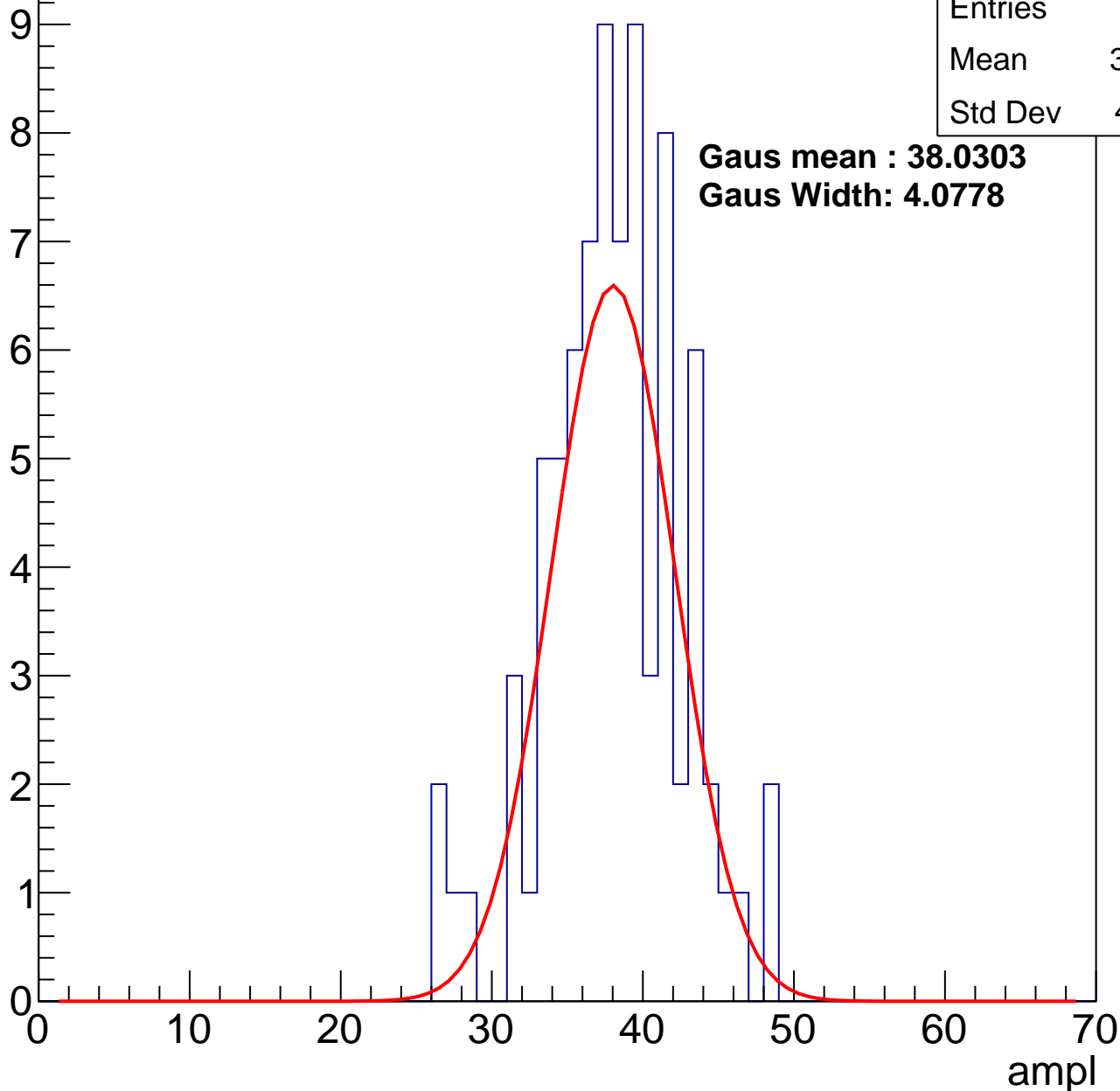
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.58
Std Dev	4.491

Gaus mean : 38.0303

Gaus Width: 4.0778



B0L001S, U18-ch111, adc2

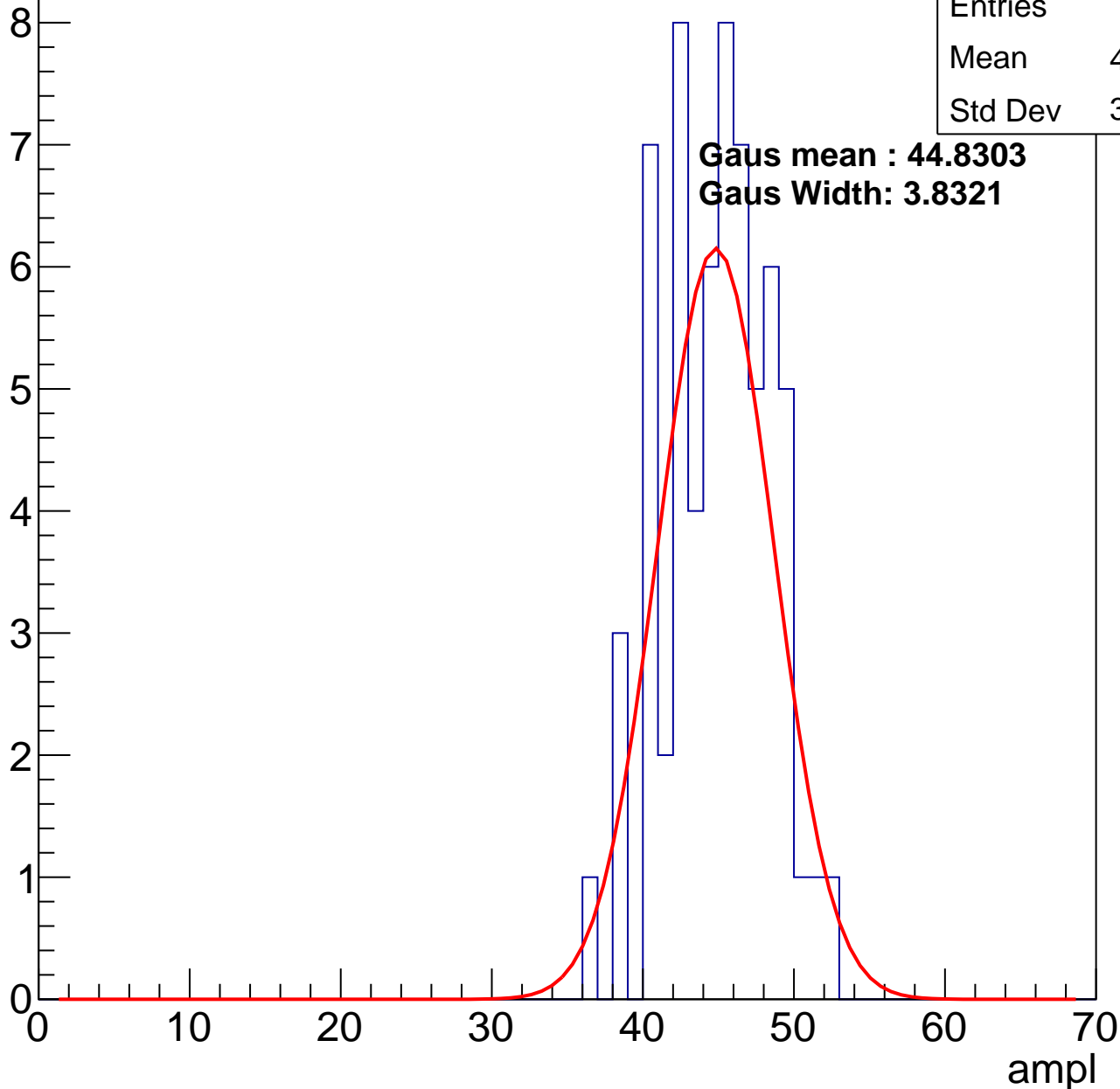
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.42
Std Dev	3.459

Gaus mean : 44.8303

Gaus Width: 3.8321

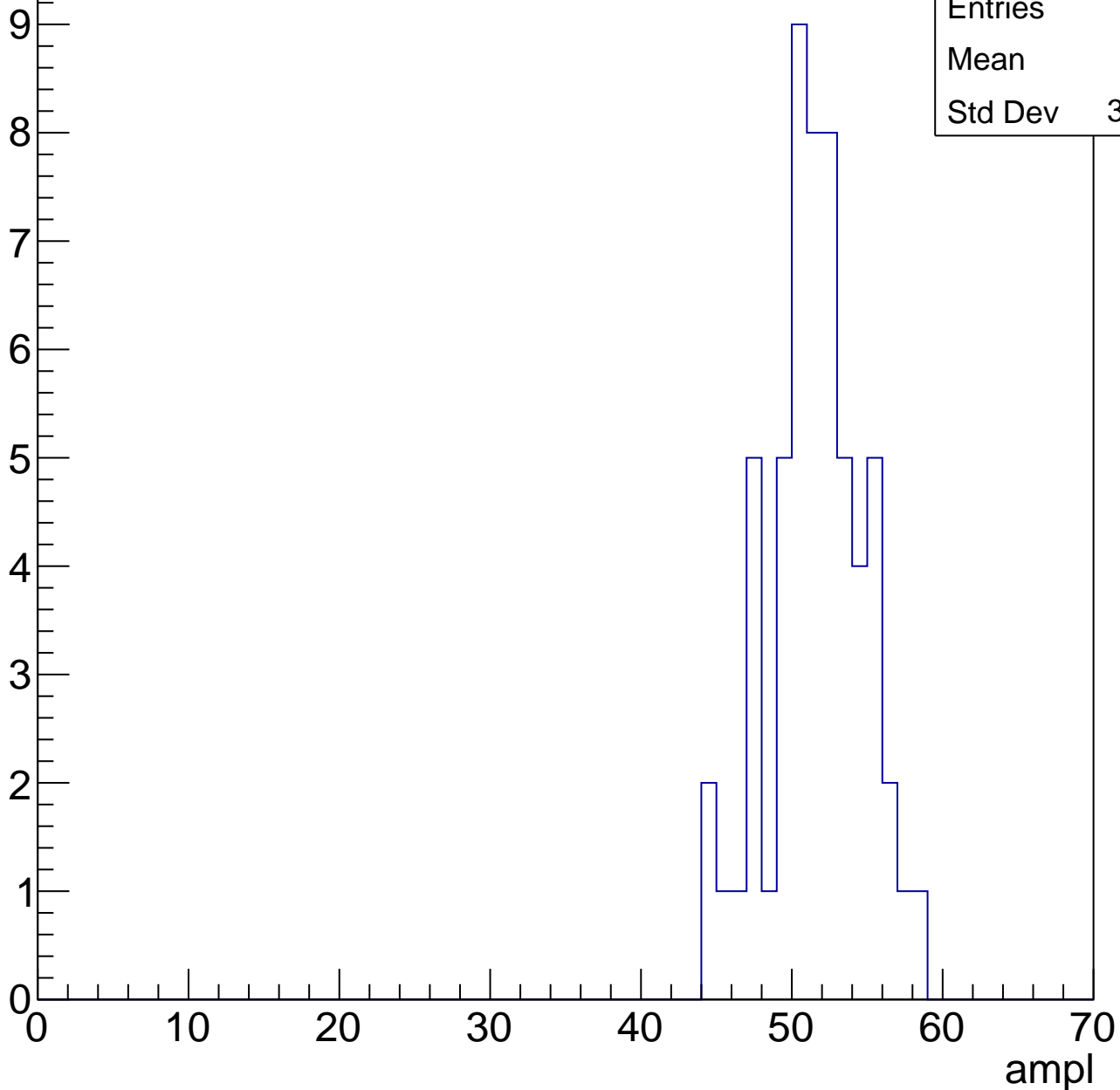


B0L001S, U18-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	51.1
Std Dev	3.094

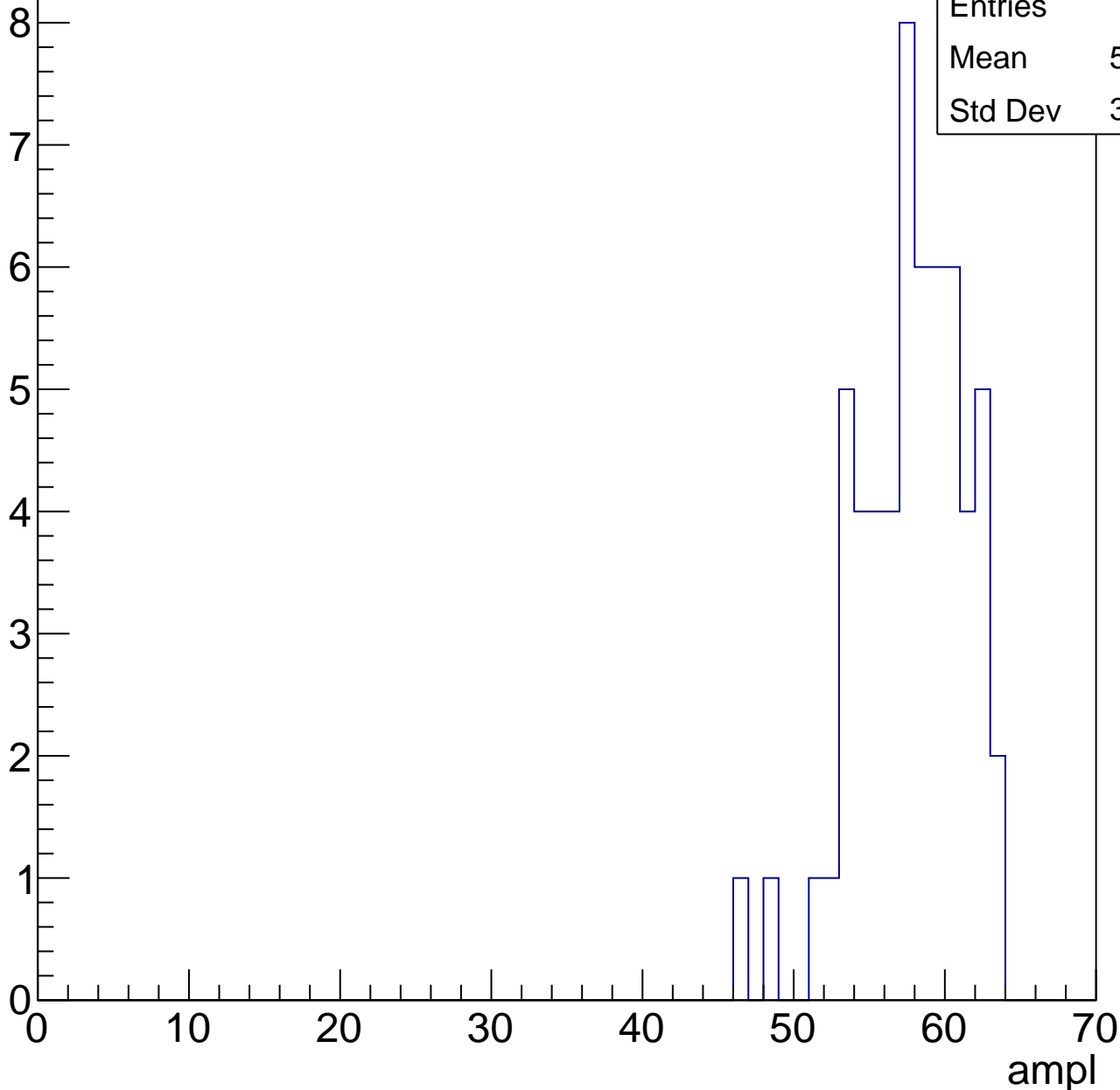


B0L001S, U18-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.24
Std Dev	3.573

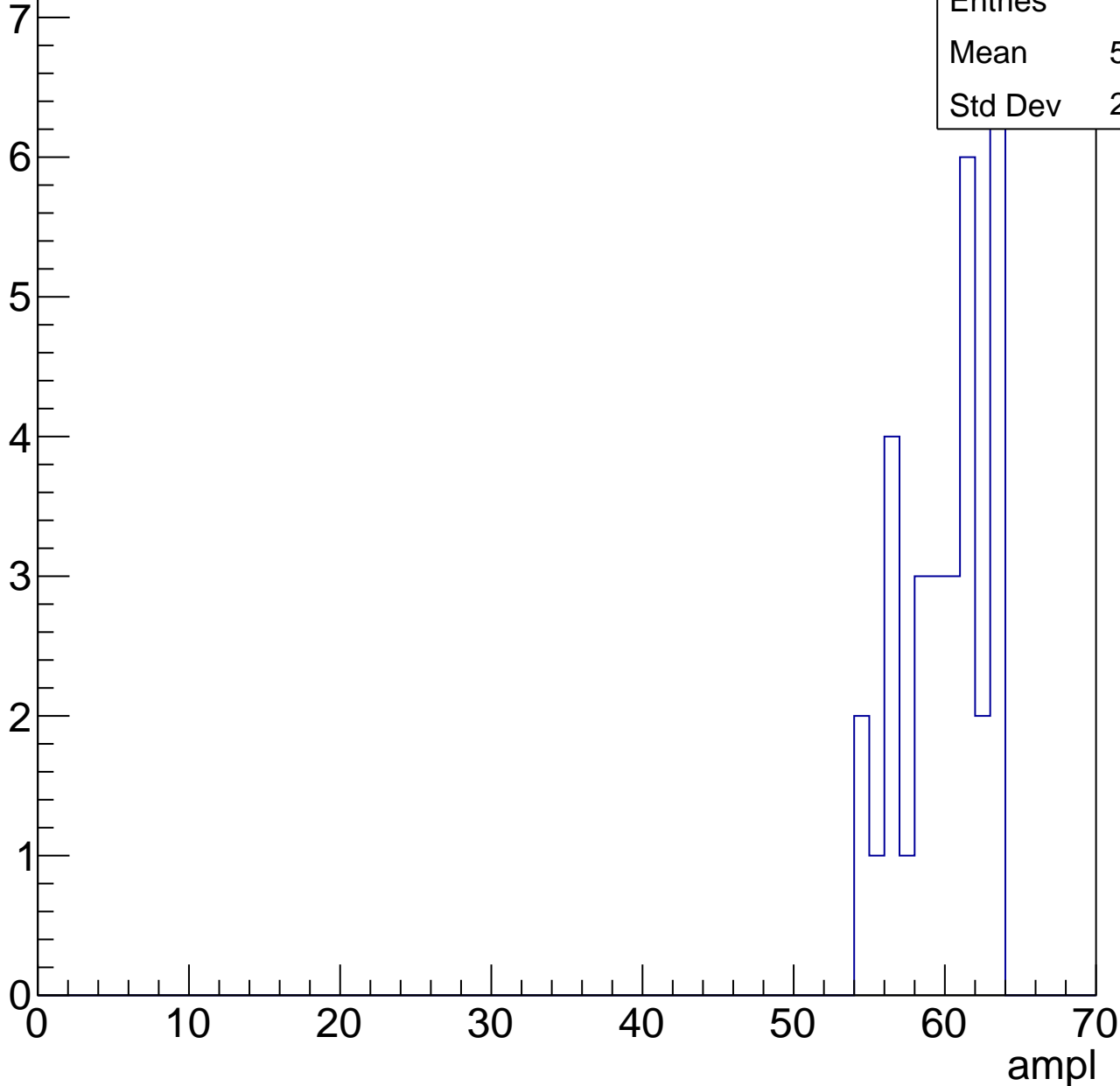


B0L001S, U18-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

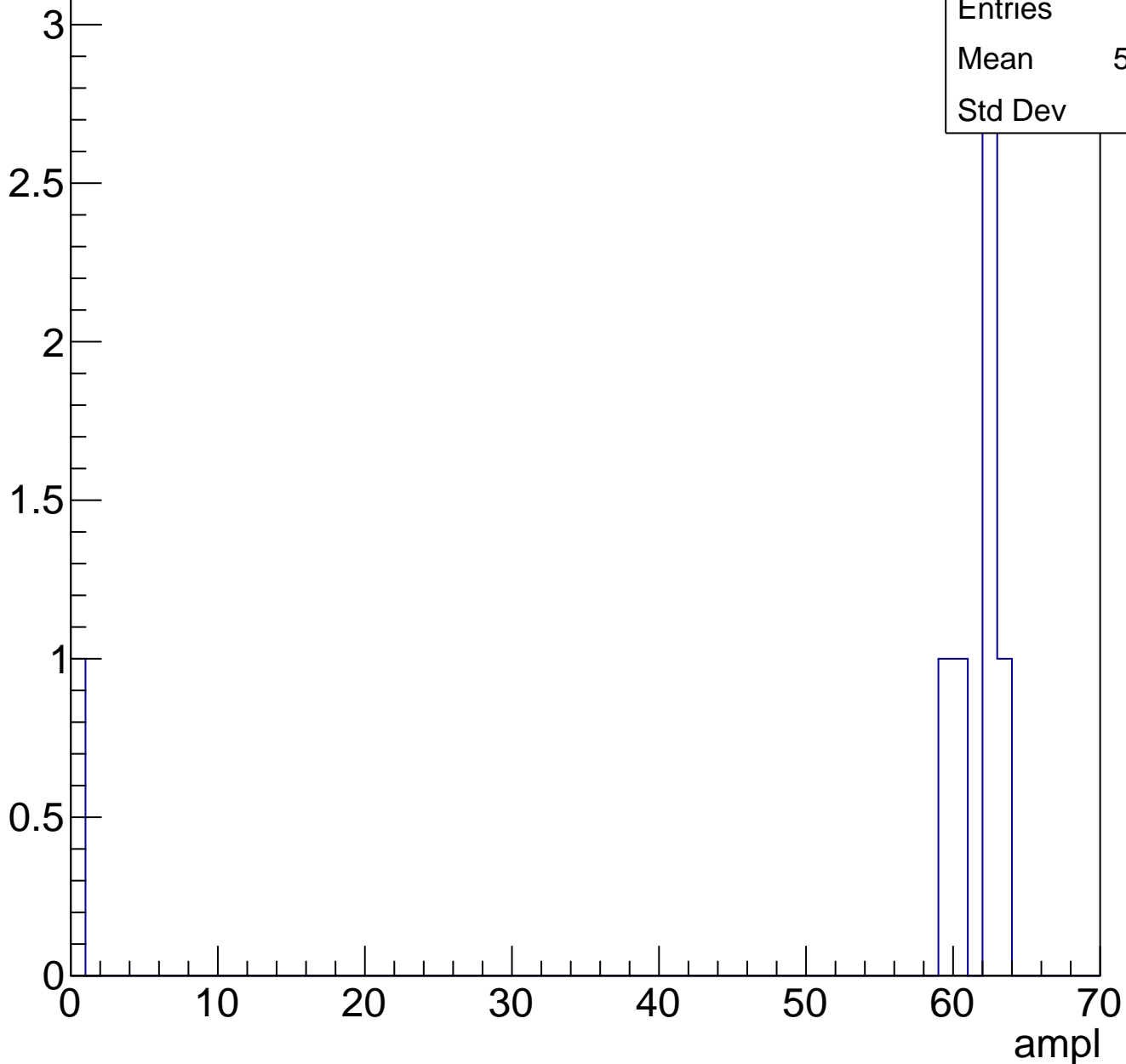
Entries	32
Mean	59.56
Std Dev	2.828



B0L001S, U18-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch112, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	29.81
Std Dev	5.483

Gaus mean : 30.8280

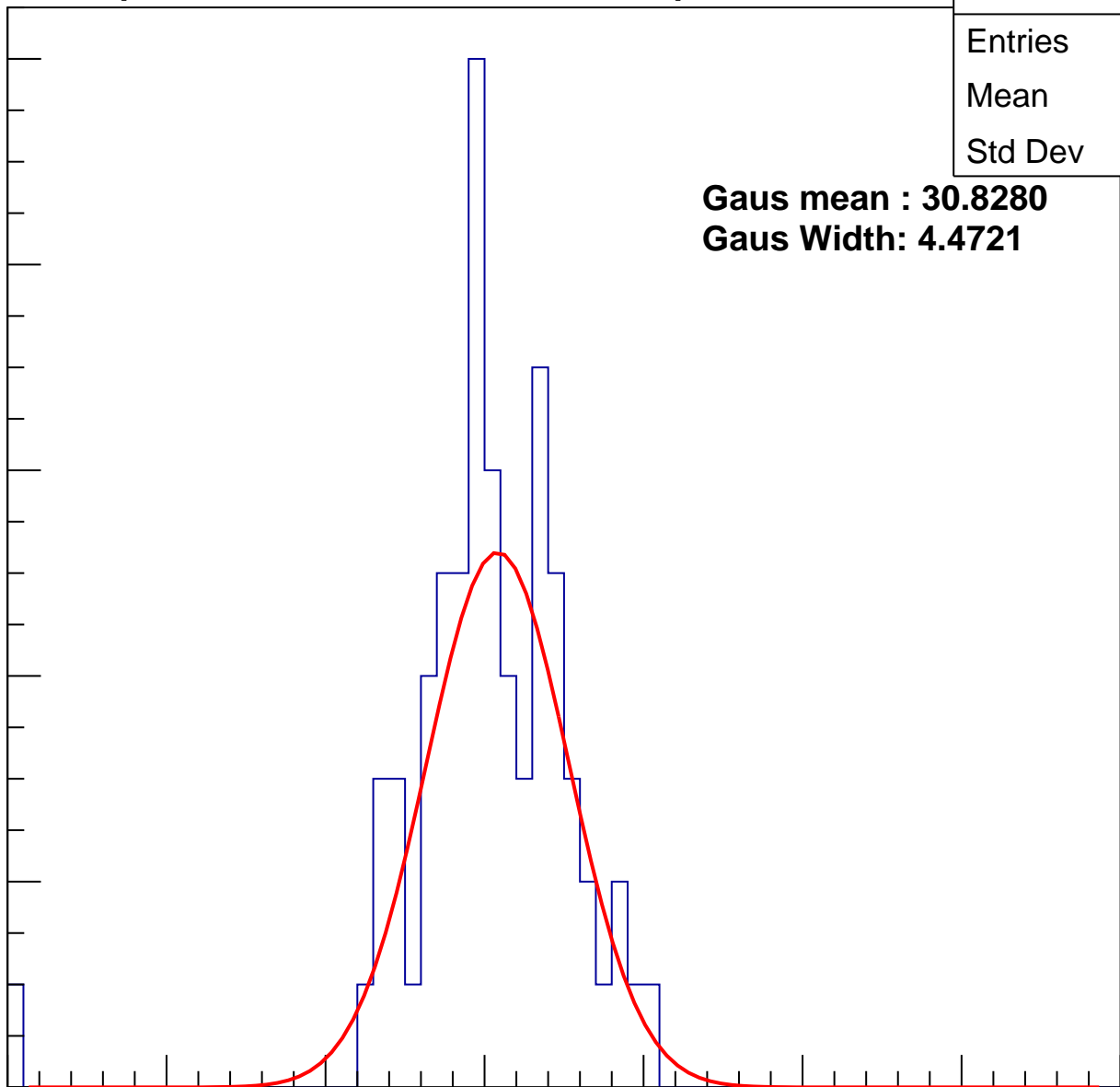
Gaus Width: 4.4721

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch112, adc1

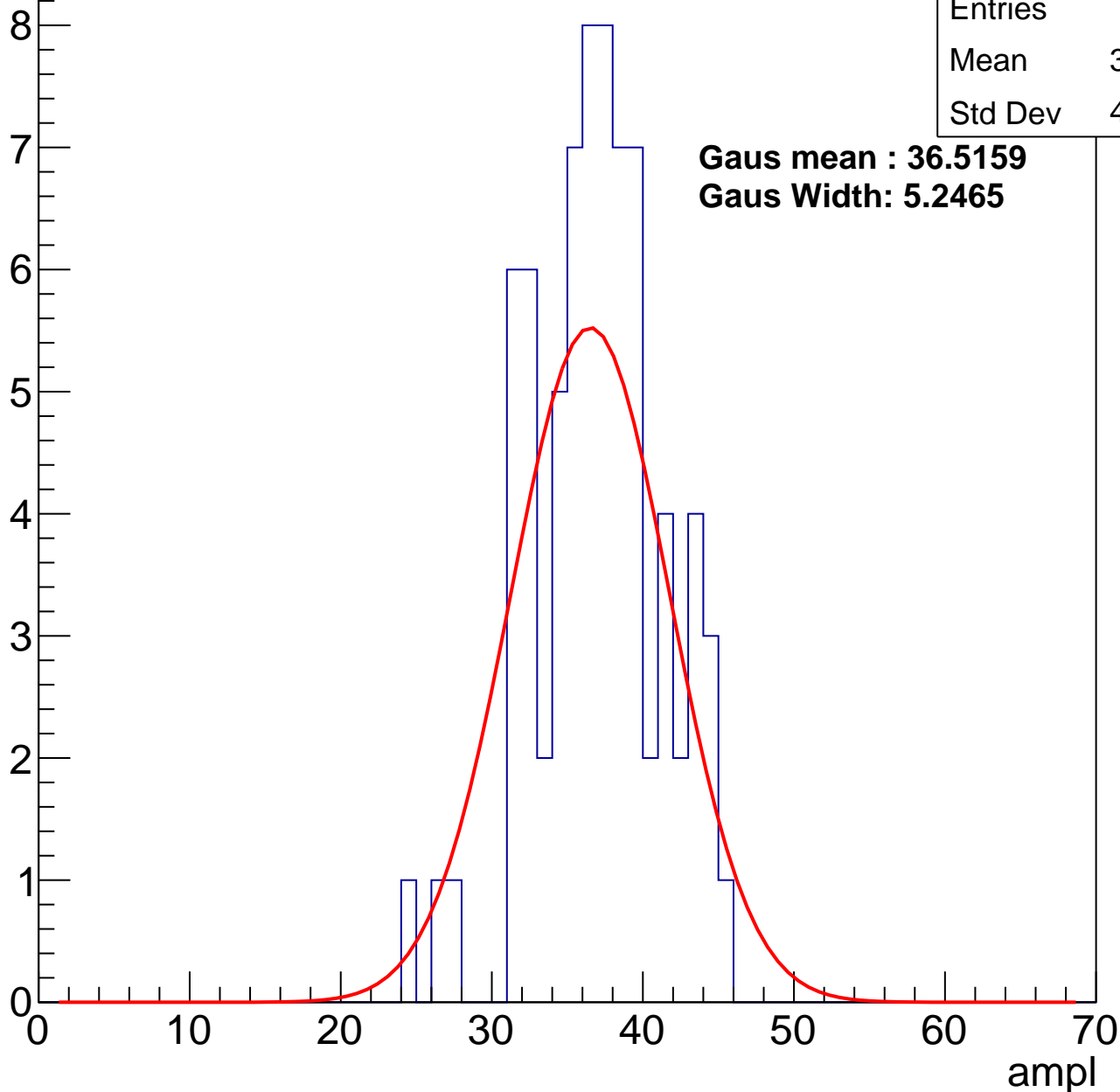
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.48
Std Dev	4.275

Gaus mean : 36.5159

Gaus Width: 5.2465



B0L001S, U18-ch112, adc2

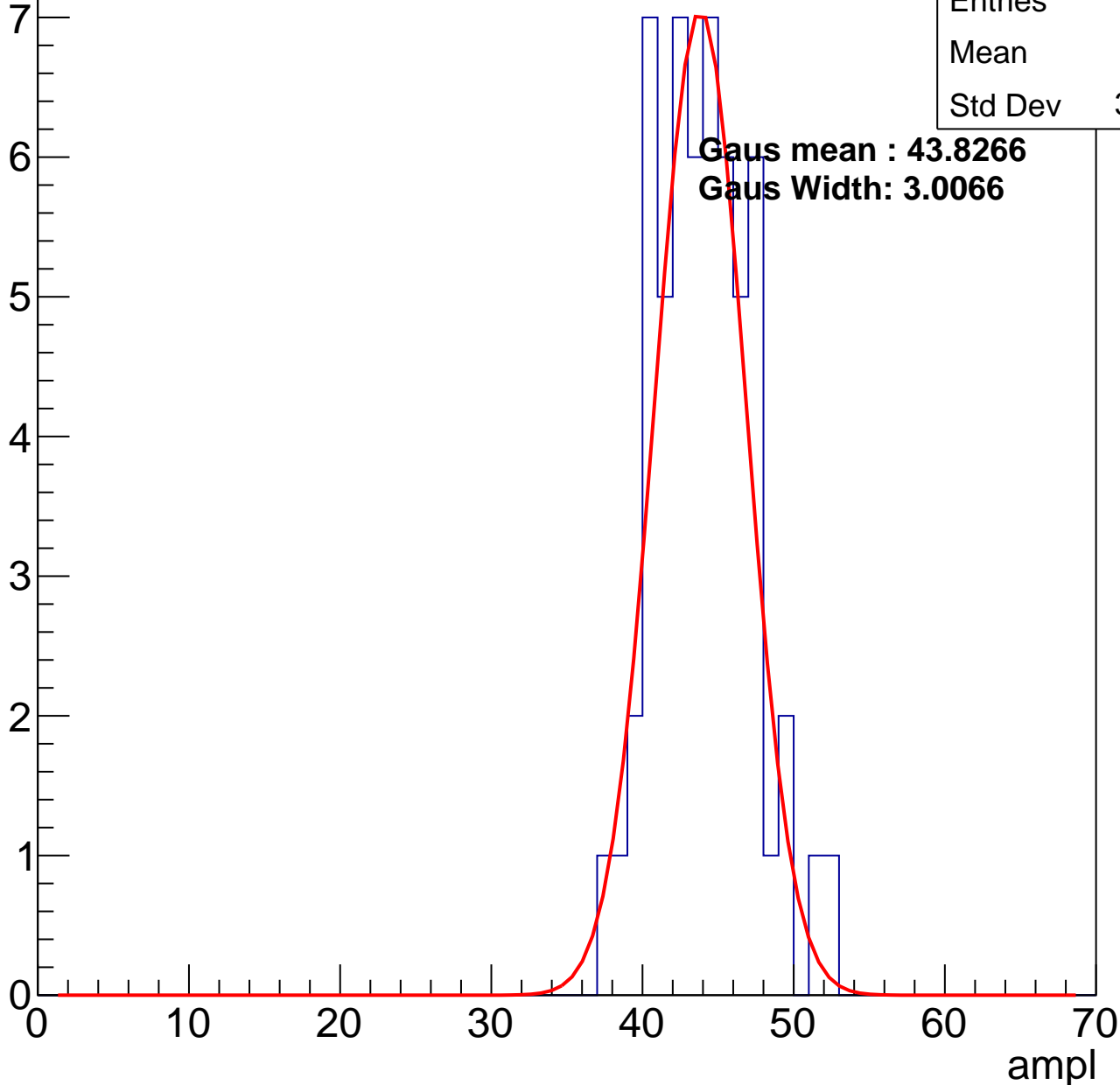
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.6
Std Dev	3.151

Gaus mean : 43.8266

Gaus Width: 3.0066

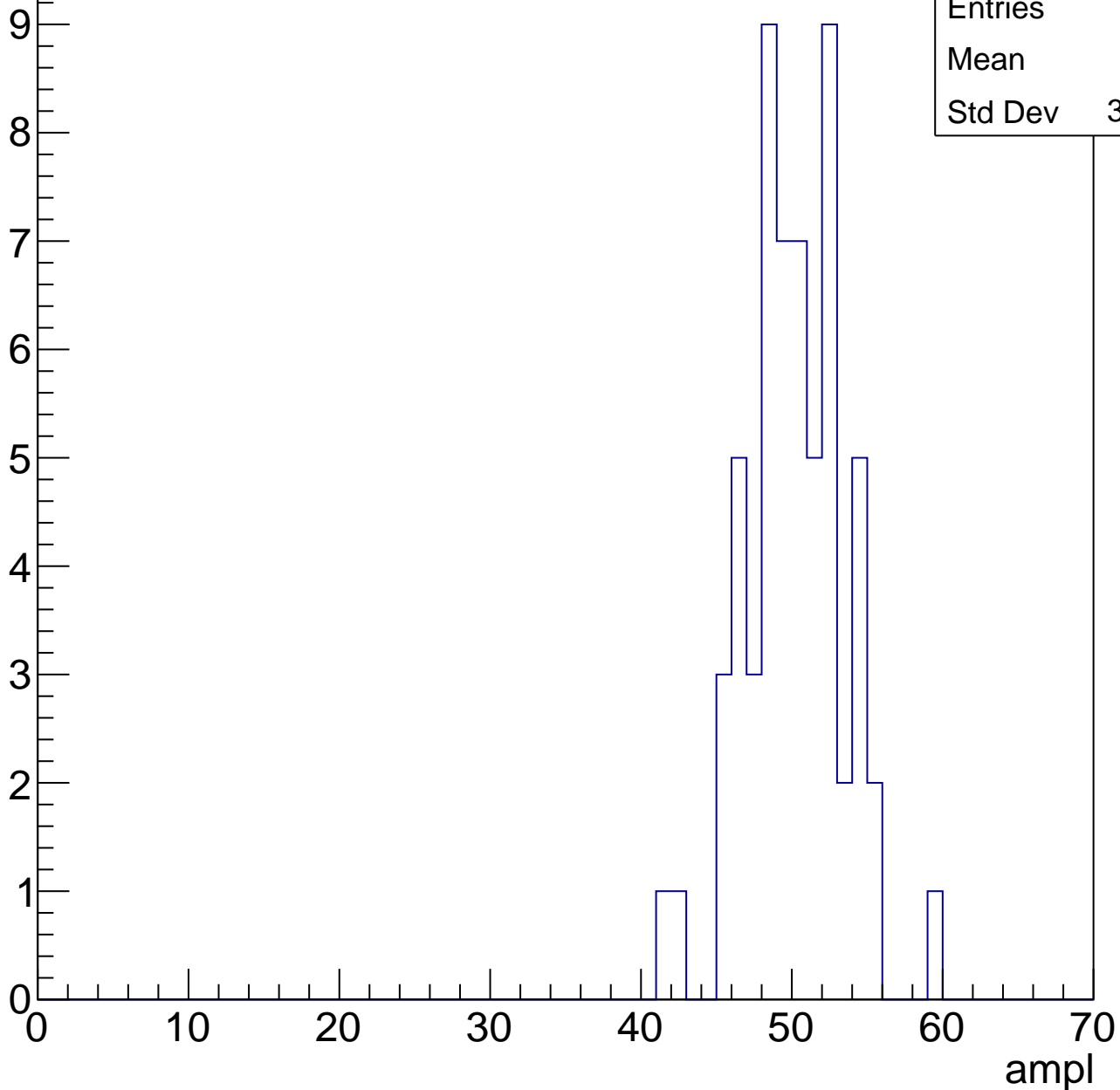


B0L001S, U18-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	49.7
Std Dev	3.252

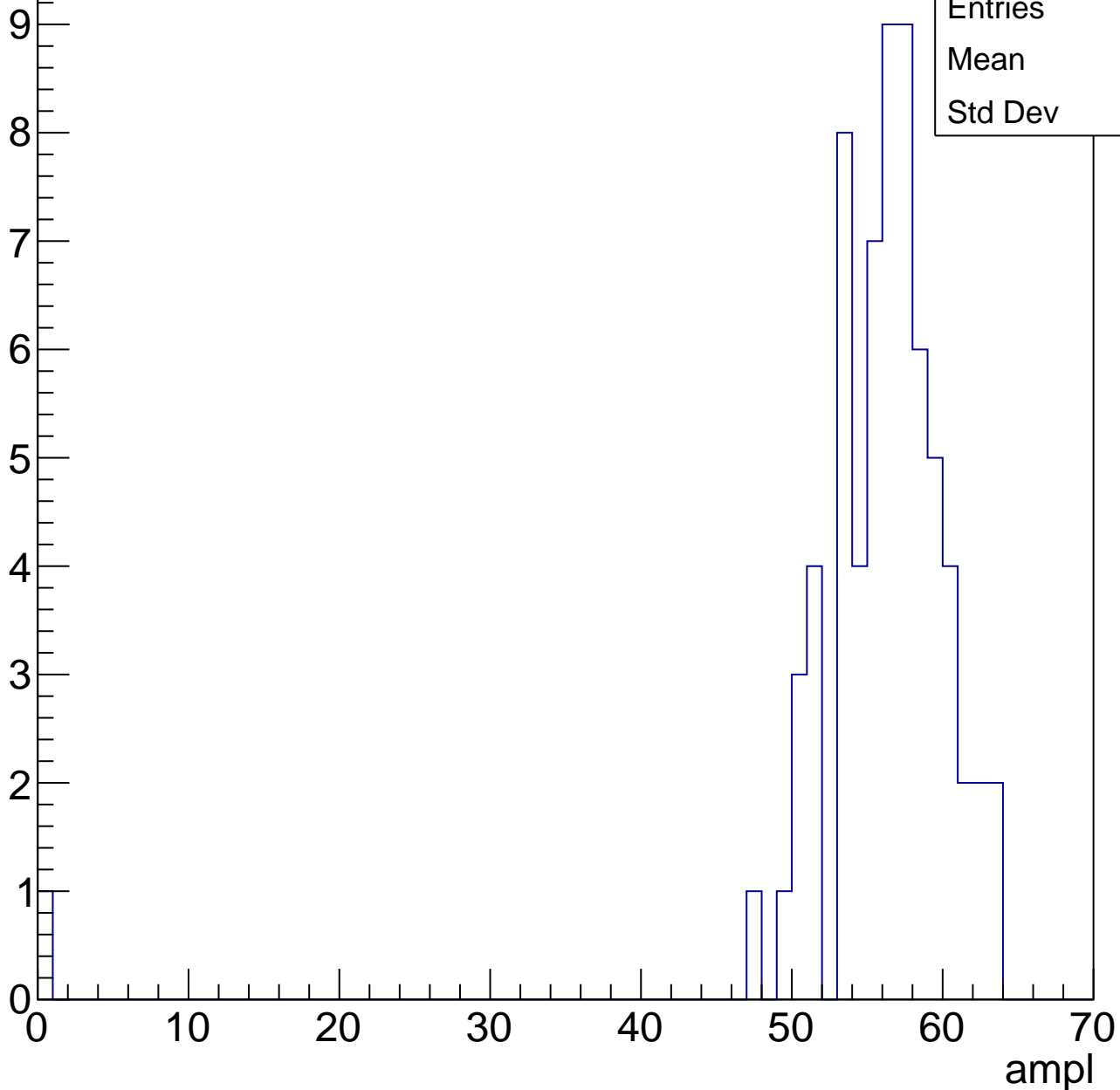


B0L001S, U18-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	55.1
Std Dev	7.55

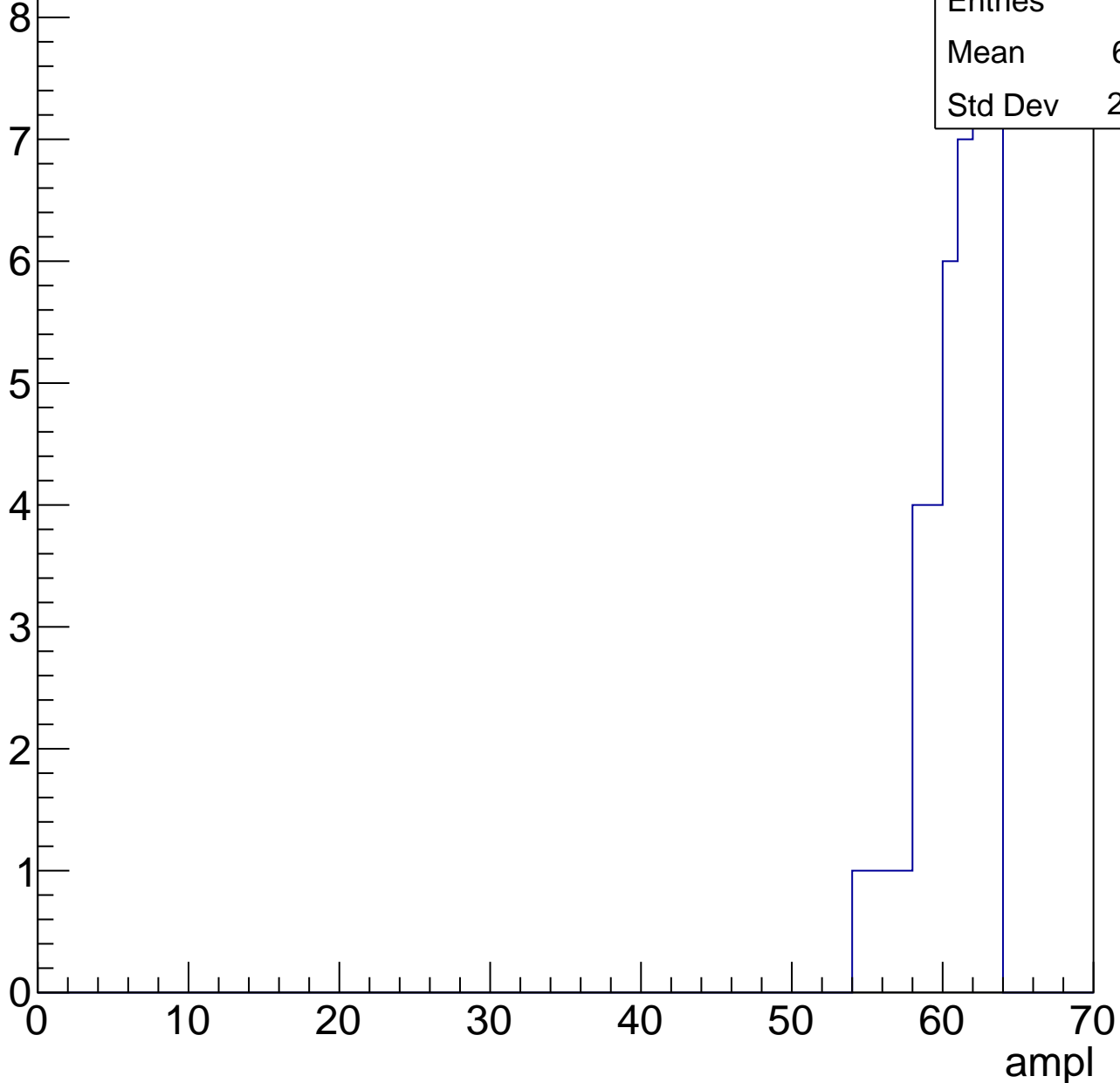


B0L001S, U18-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	60.41
Std Dev	2.263



B0L001S, U18-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch113, adc0

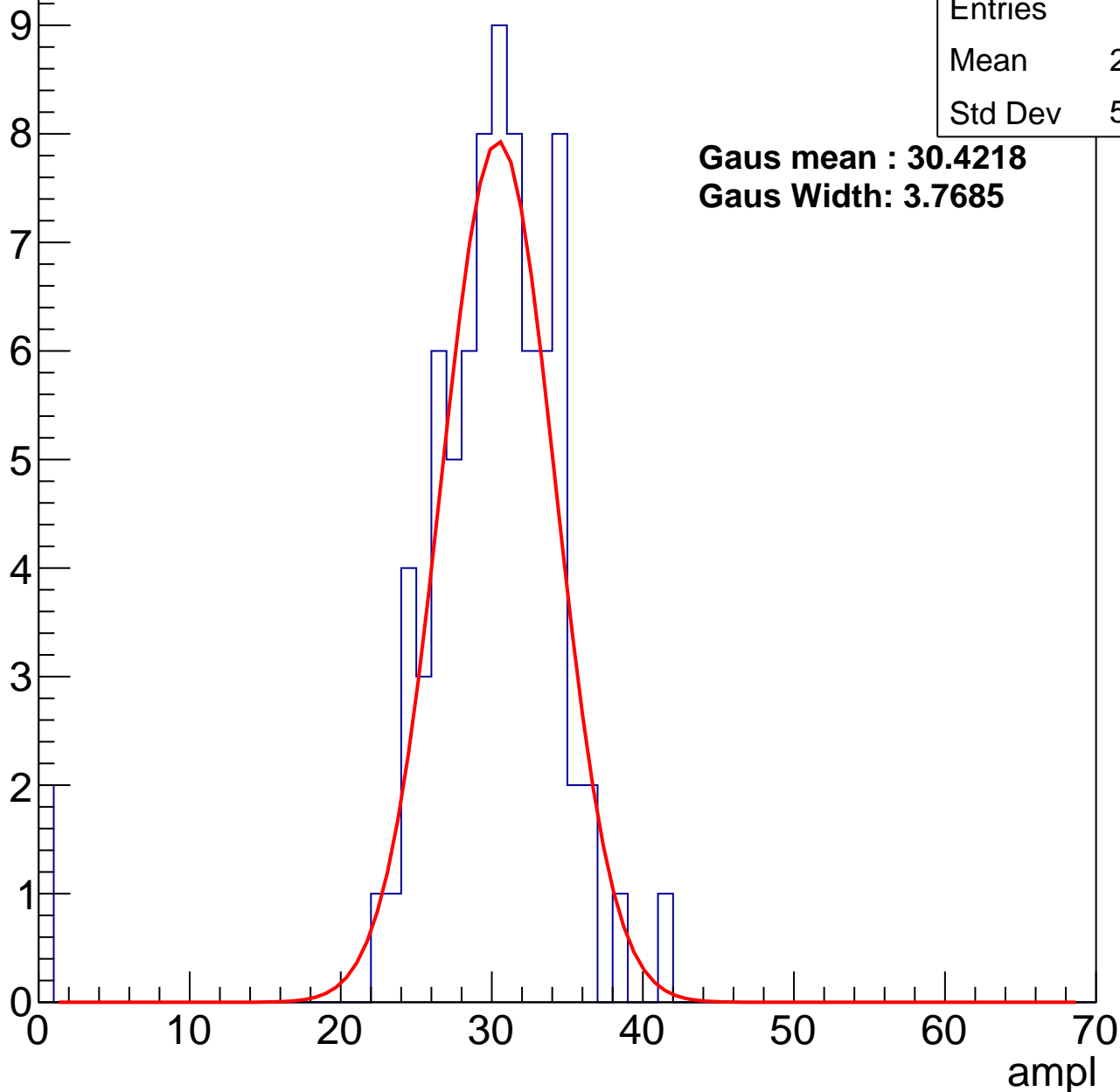
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	29.22
Std Dev	5.929

Gaus mean : 30.4218

Gaus Width: 3.7685



B0L001S, U18-ch113, adc1

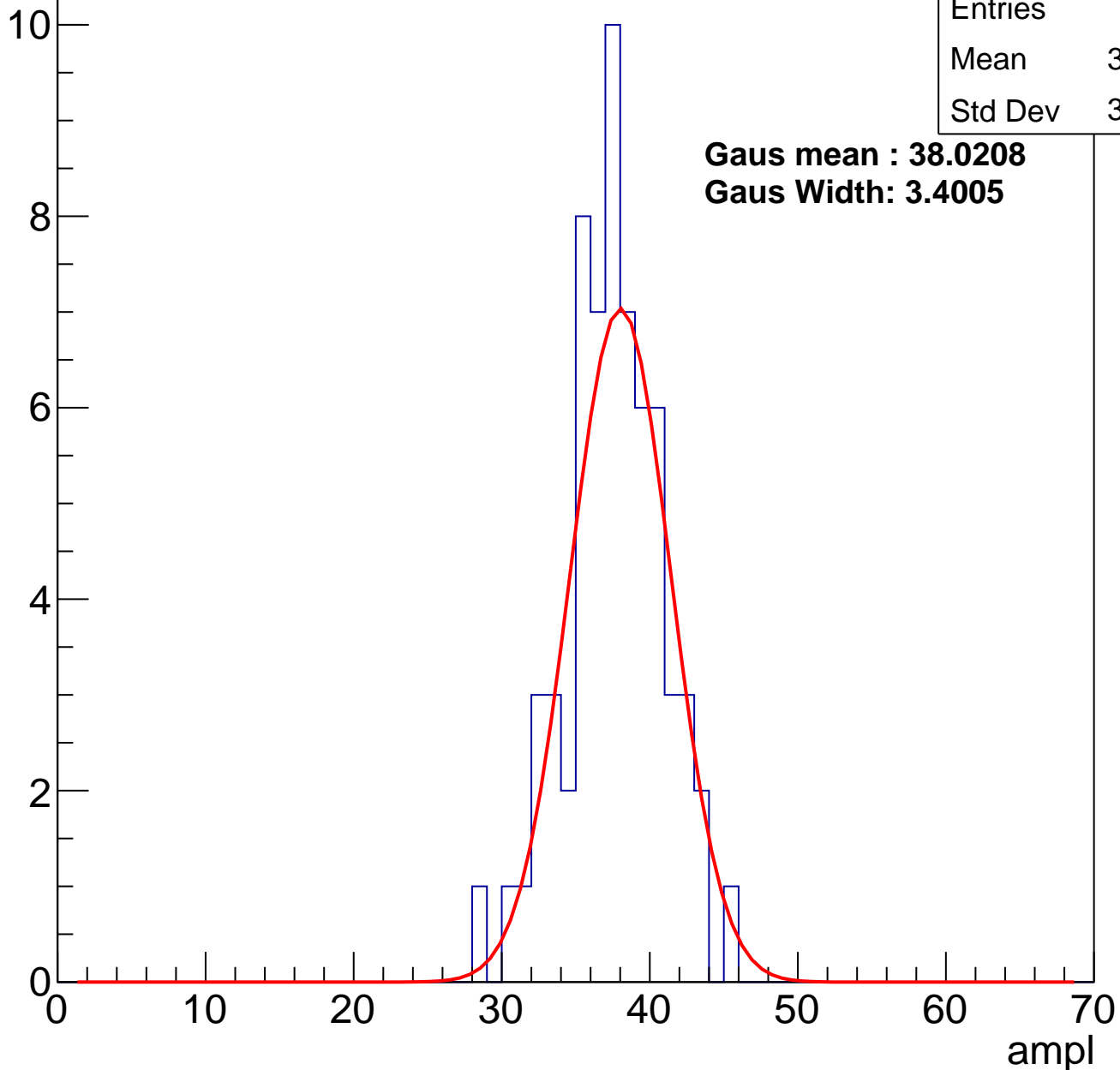
calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	37.09
Std Dev	3.282

Gaus mean : 38.0208

Gaus Width: 3.4005

Entry



B0L001S, U18-ch113, adc2

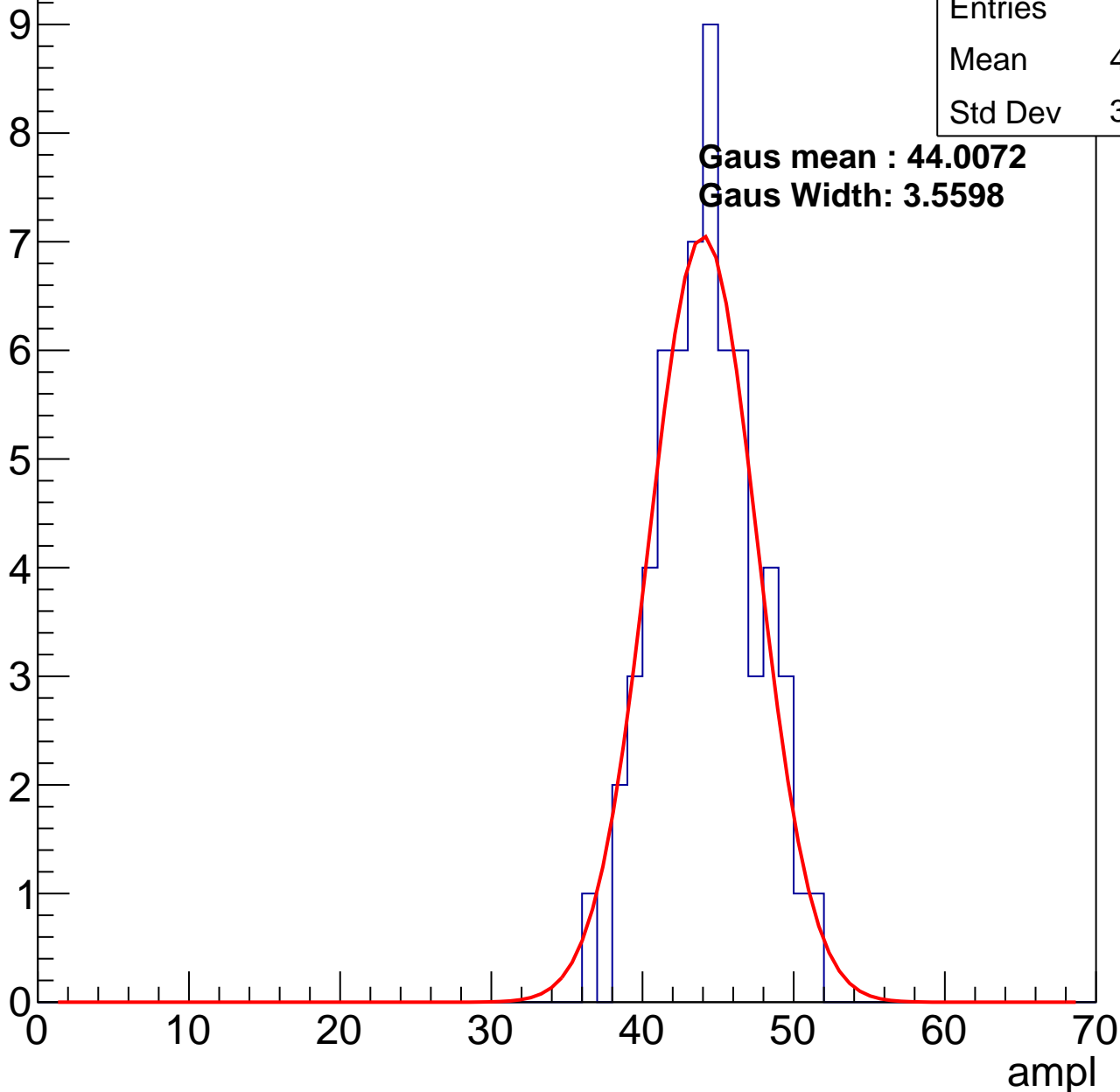
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.73
Std Dev	3.204

Gaus mean : 44.0072

Gaus Width: 3.5598

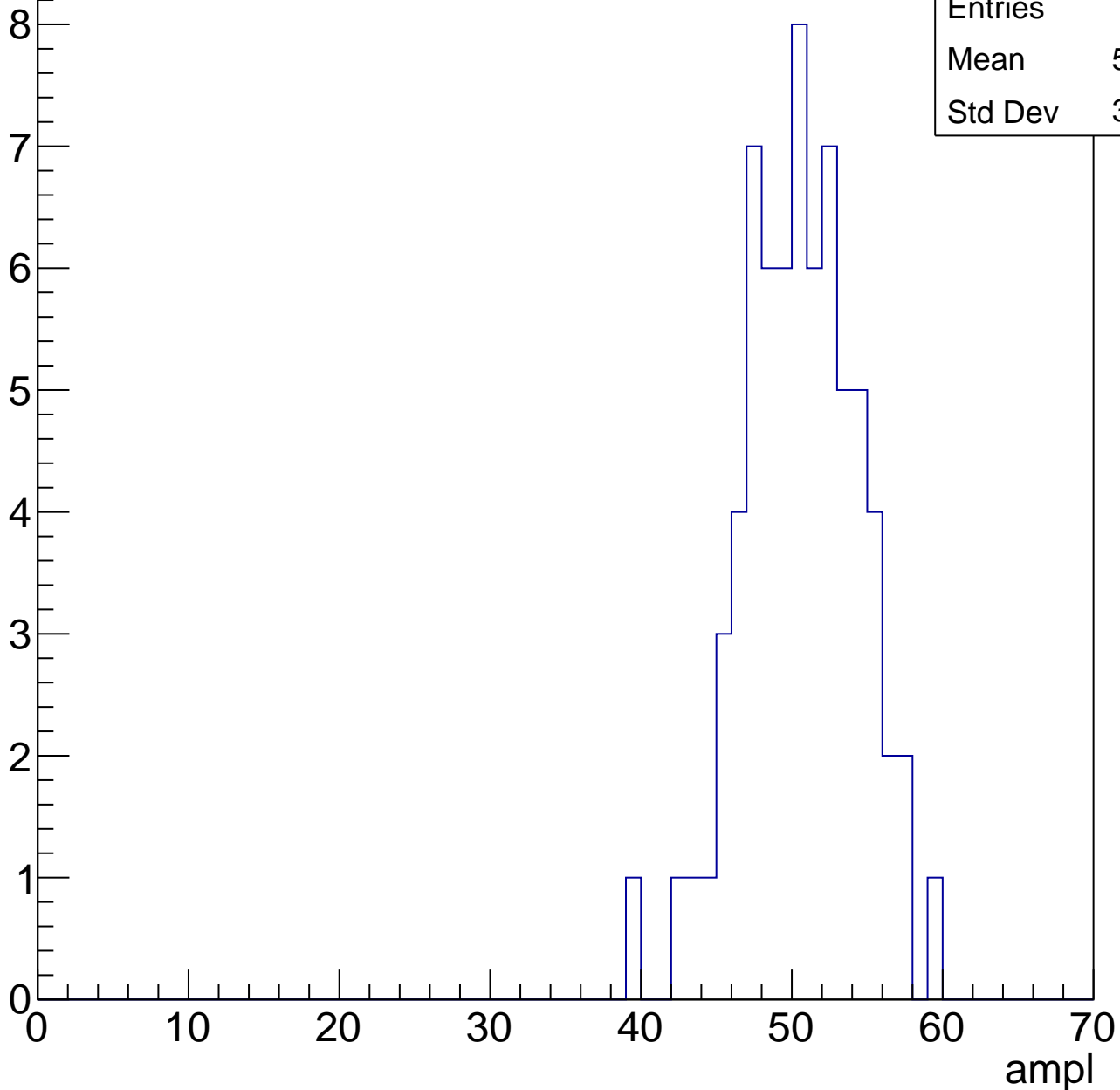


B0L001S, U18-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.11
Std Dev	3.801

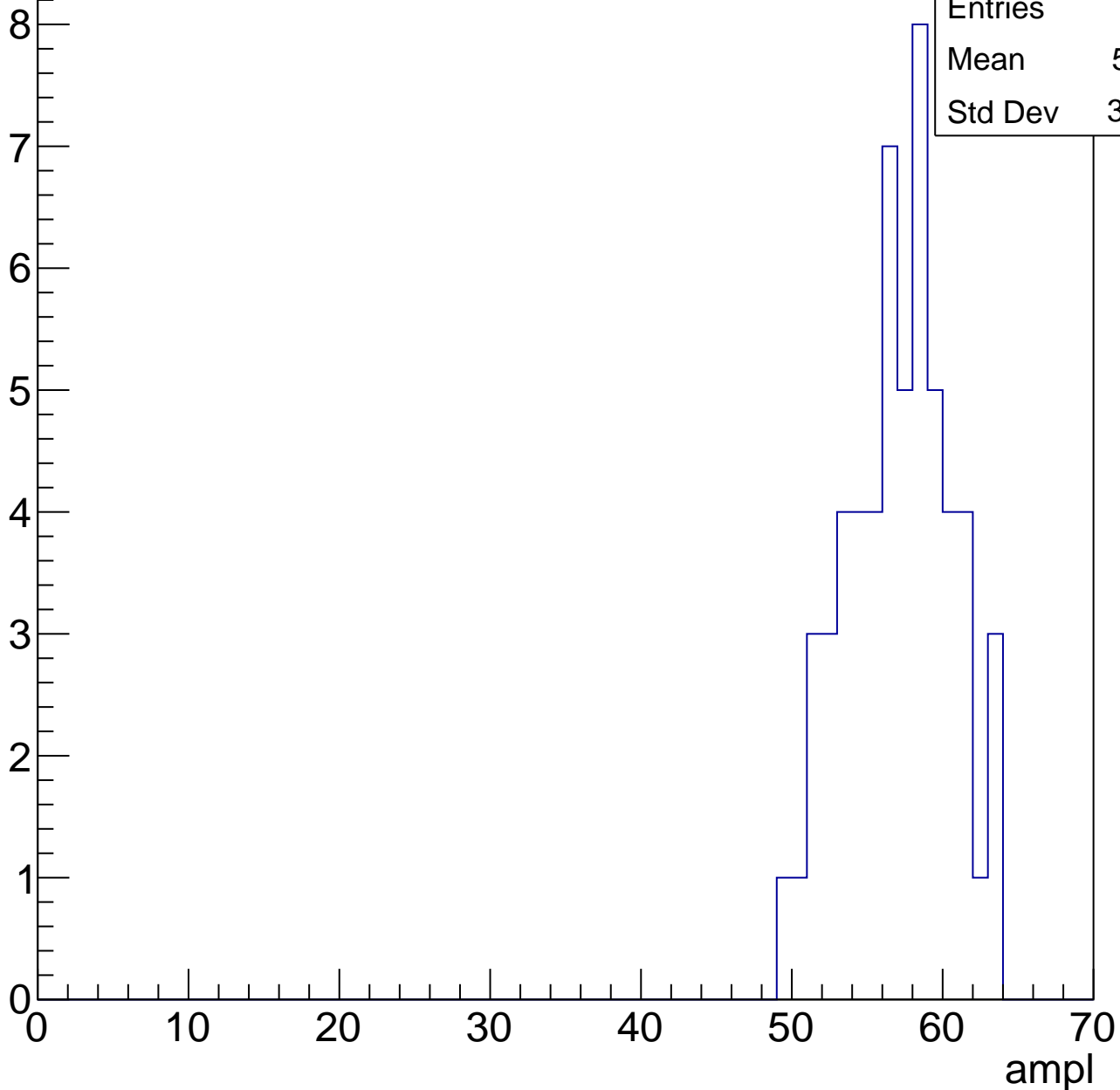


B0L001S, U18-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.61
Std Dev	3.432

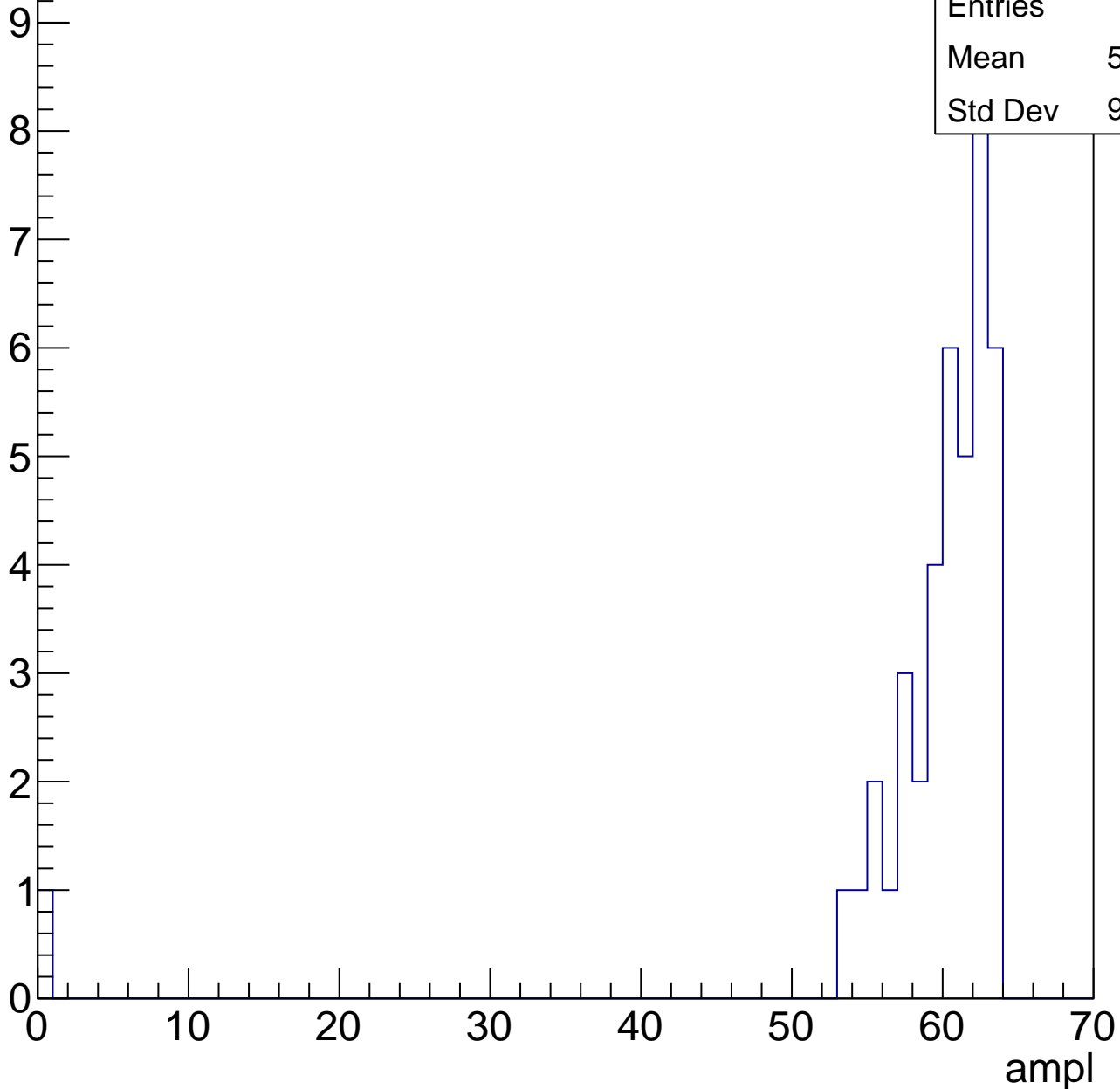


B0L001S, U18-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.46
Std Dev	9.612



B0L001S, U18-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch114, adc0

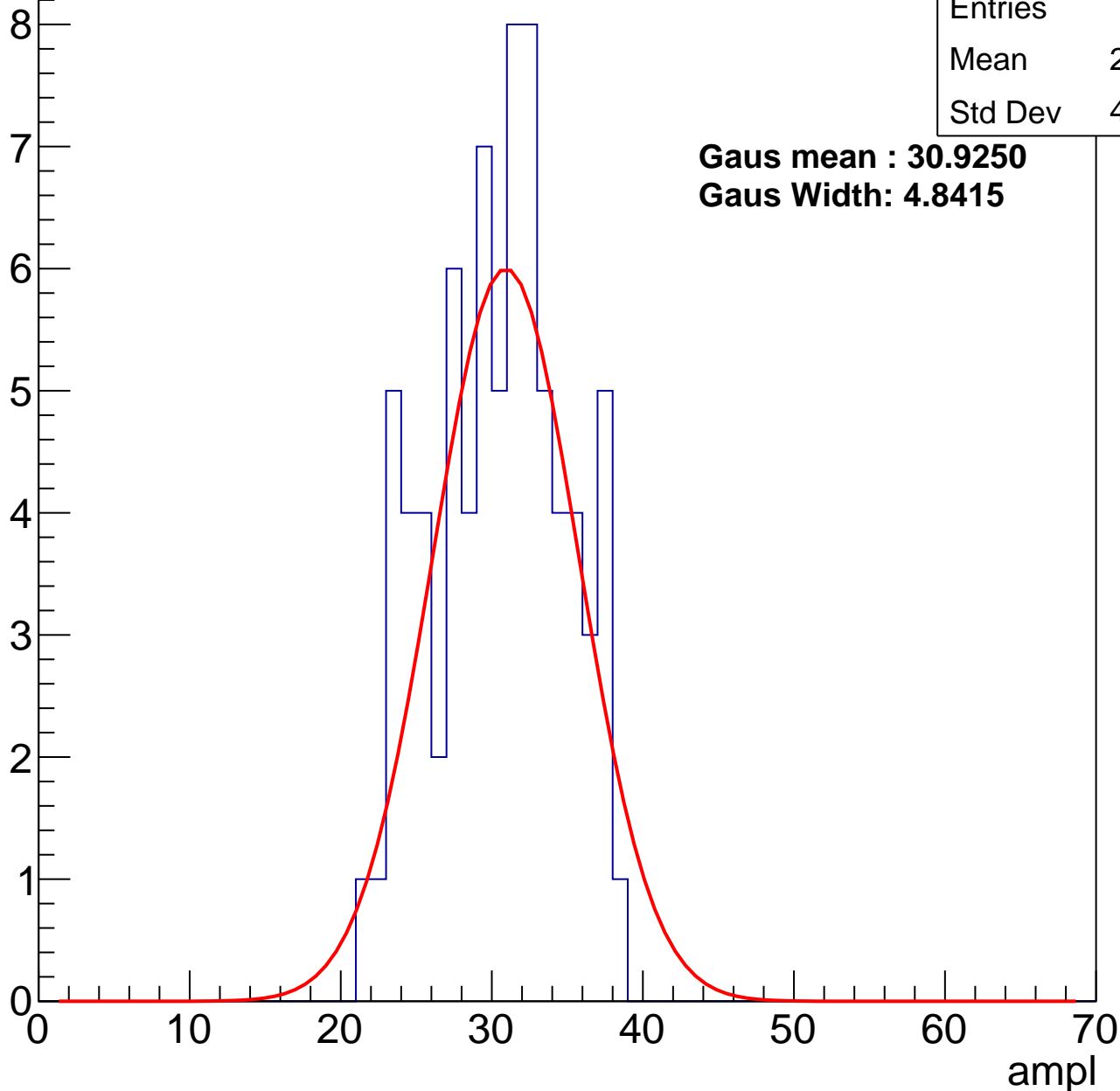
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	29.99
Std Dev	4.278

Gaus mean : 30.9250

Gaus Width: 4.8415



B0L001S, U18-ch114, adc1

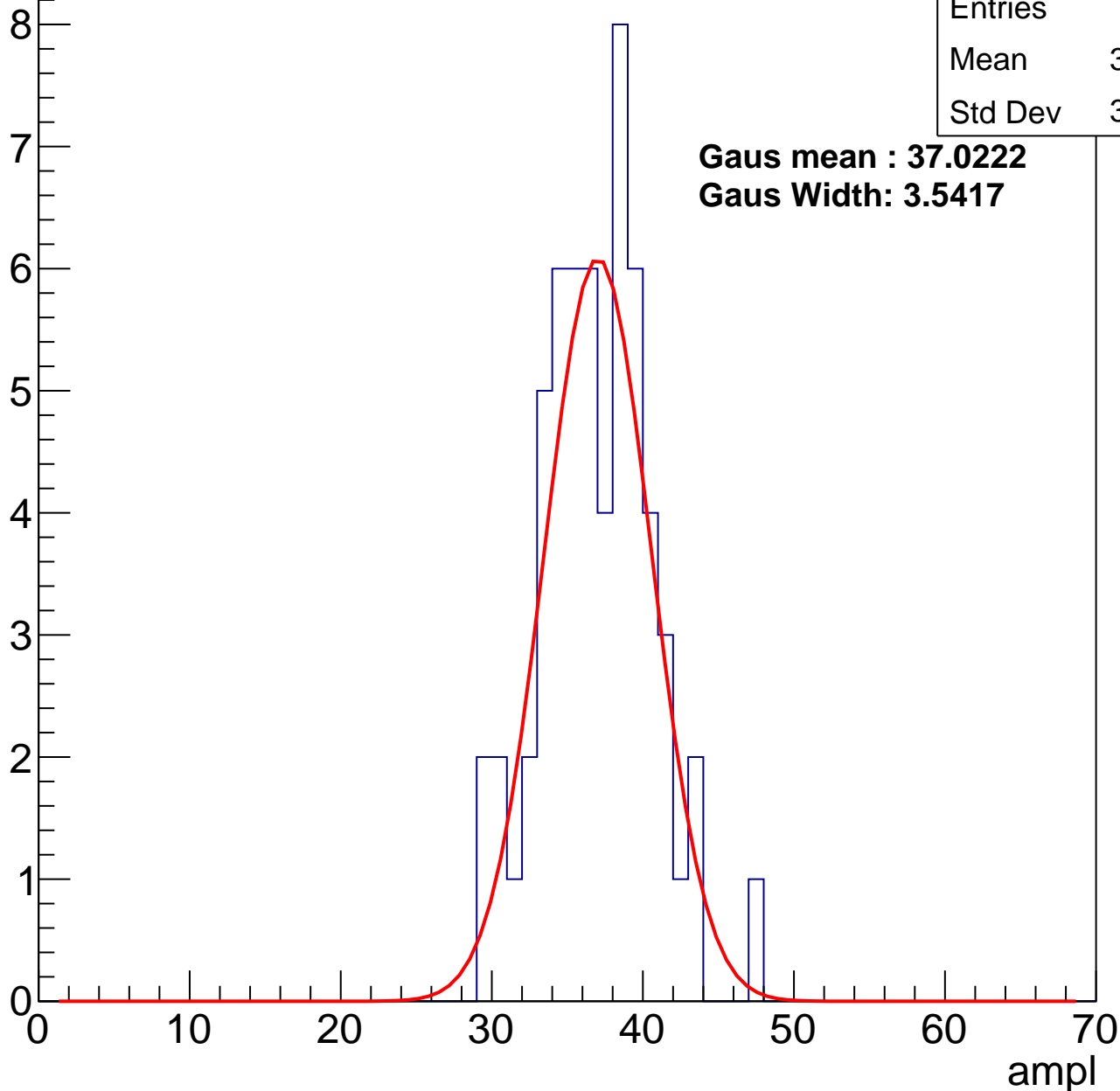
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	36.47
Std Dev	3.619

Gaus mean : 37.0222

Gaus Width: 3.5417



B0L001S, U18-ch114, adc2

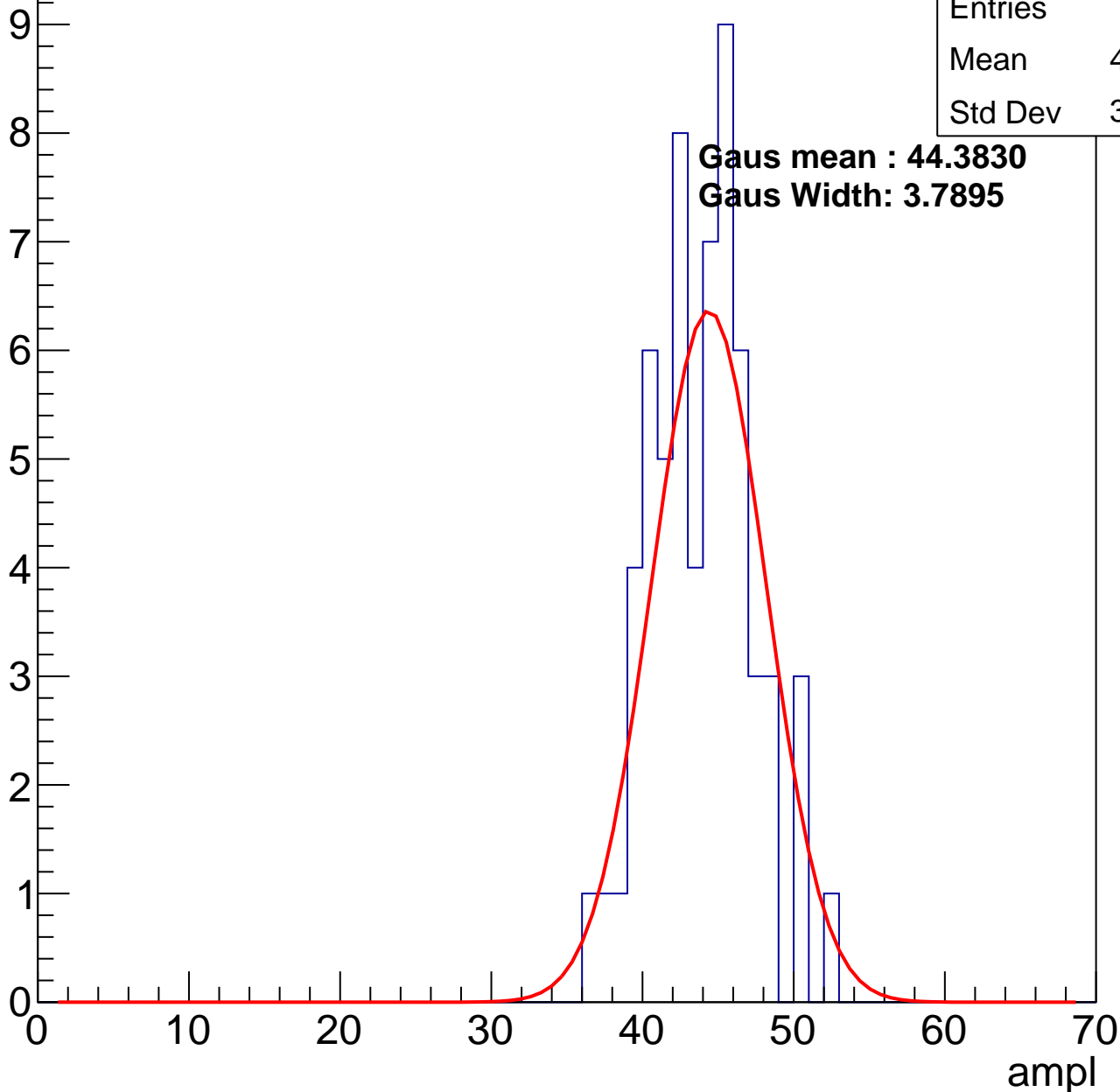
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.48
Std Dev	3.335

Gaus mean : 44.3830

Gaus Width: 3.7895

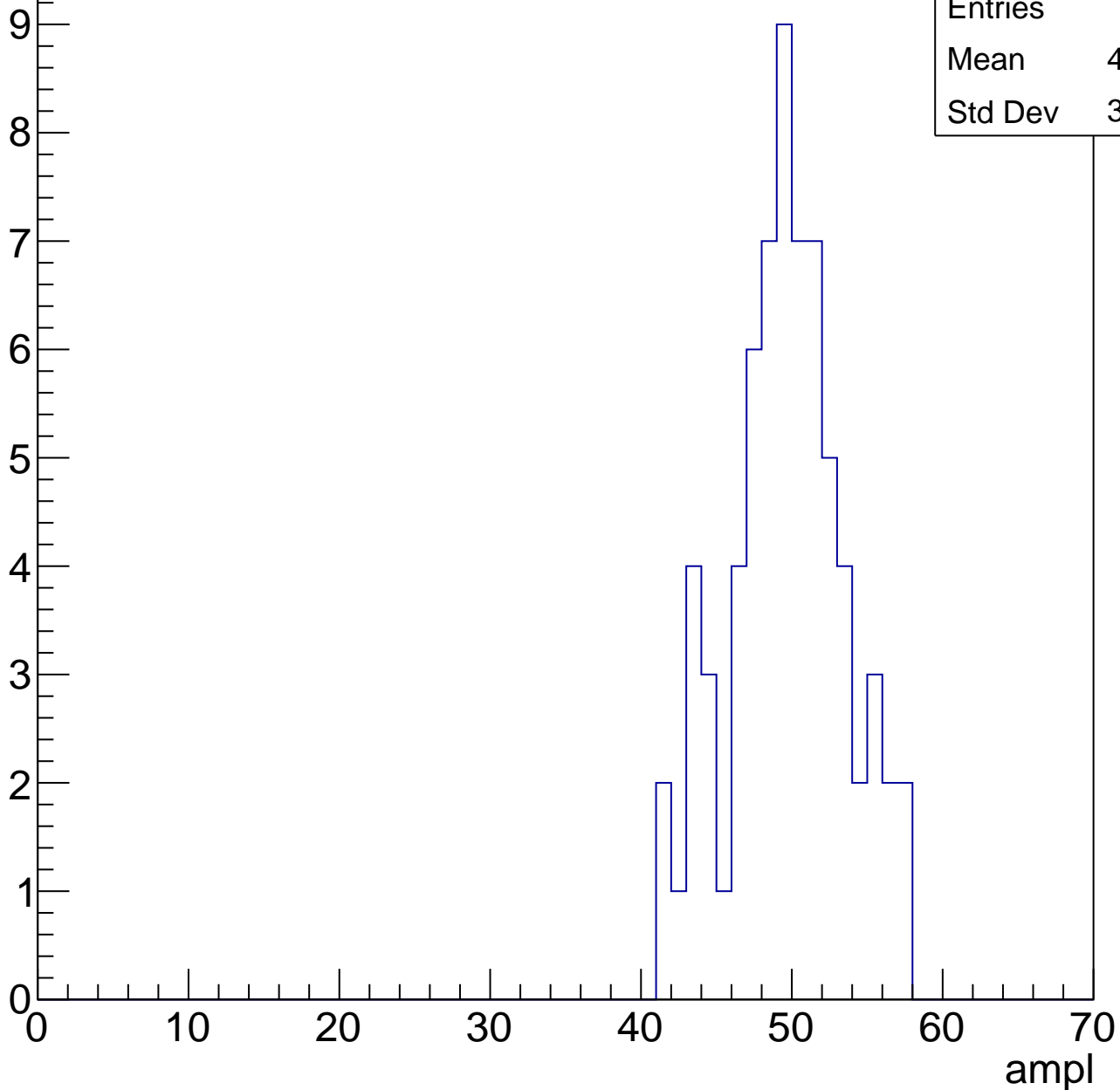


B0L001S, U18-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	49.19
Std Dev	3.816

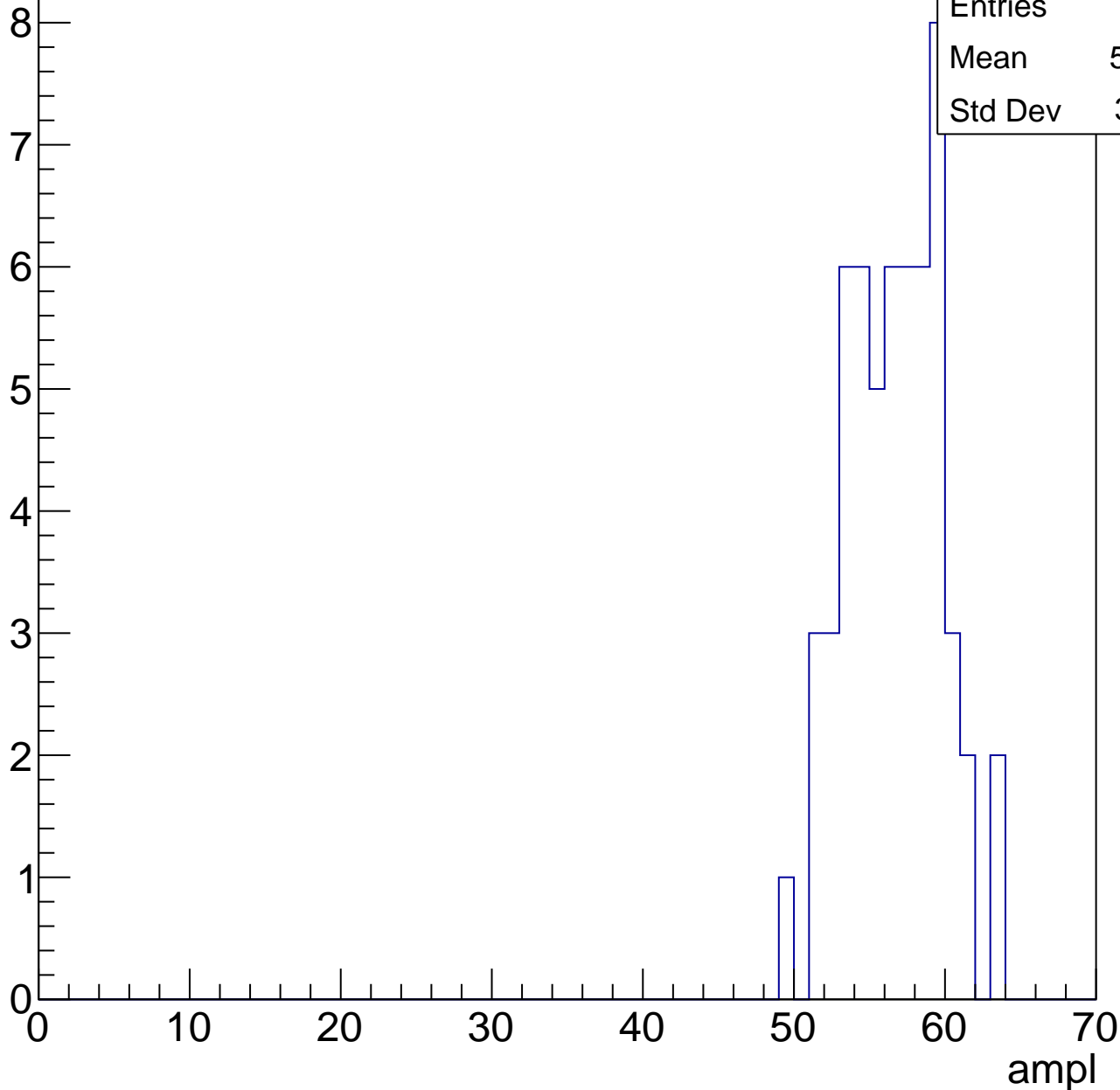


B0L001S, U18-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.16
Std Dev	3.111



B0L001S, U18-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	39
Mean	59.67
Std Dev	2.535

Entry

10

8

6

4

2

0

0

10

20

30

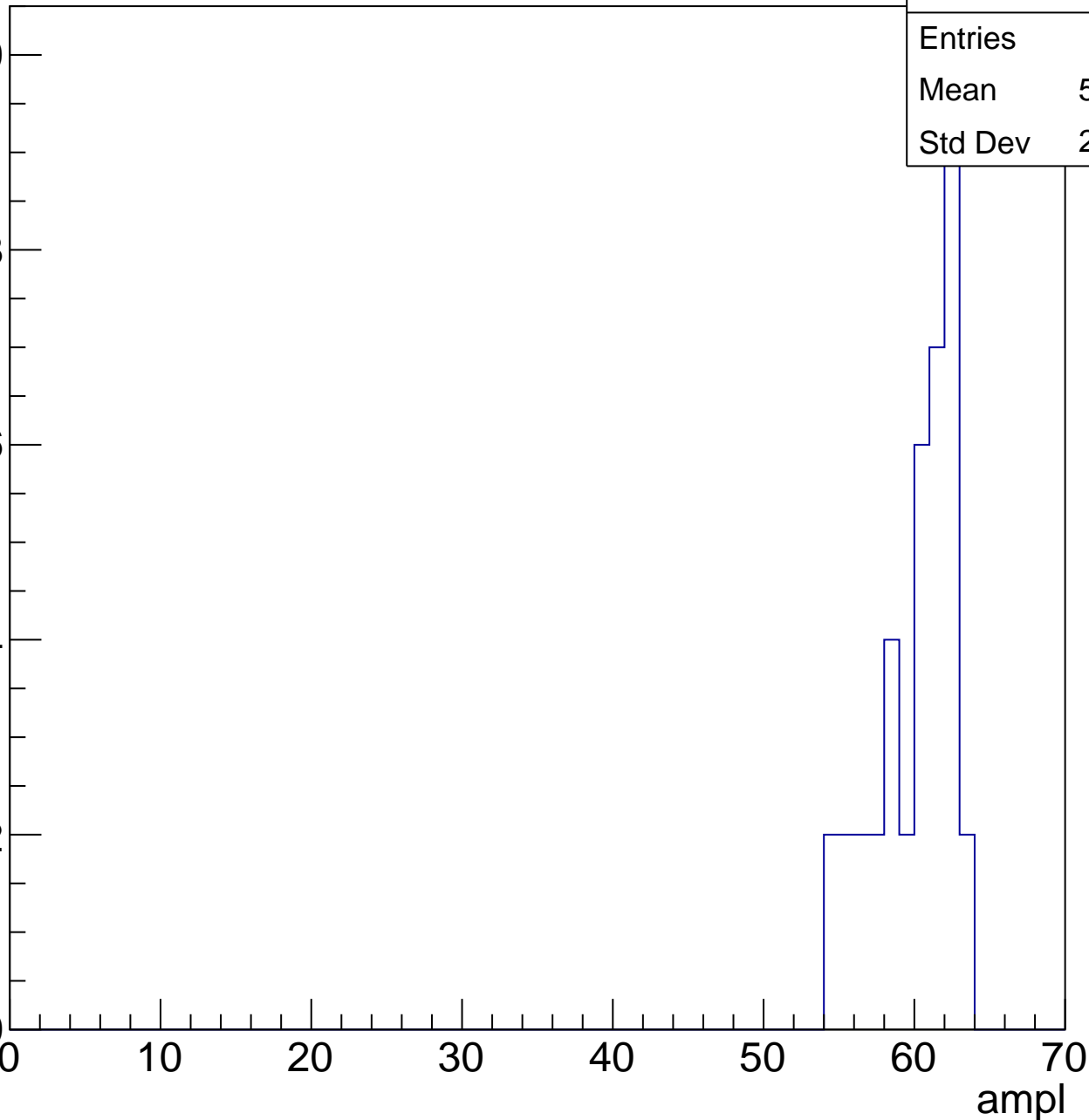
40

50

60

ampl

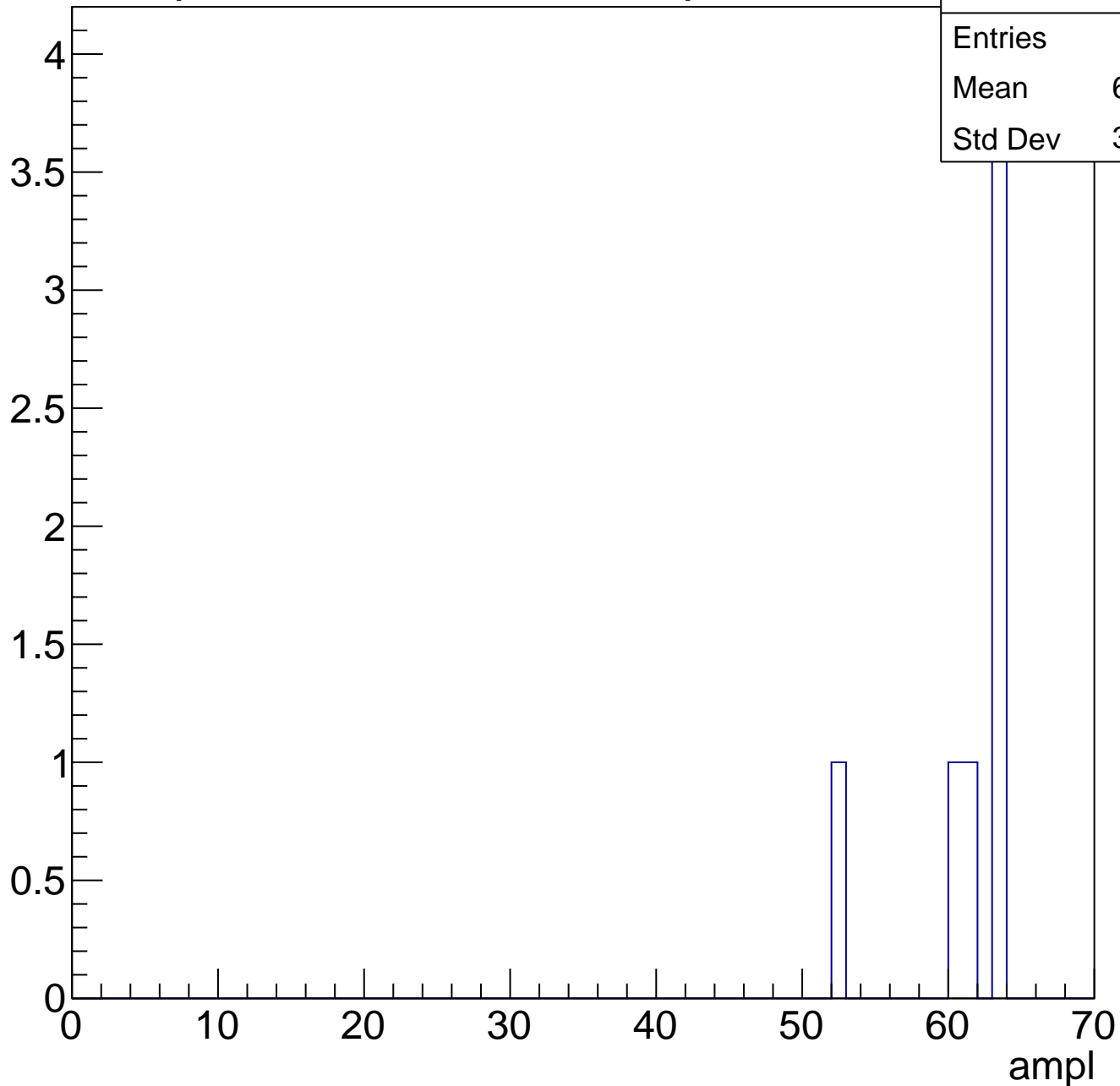
70



B0L001S, U18-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch115, adc0

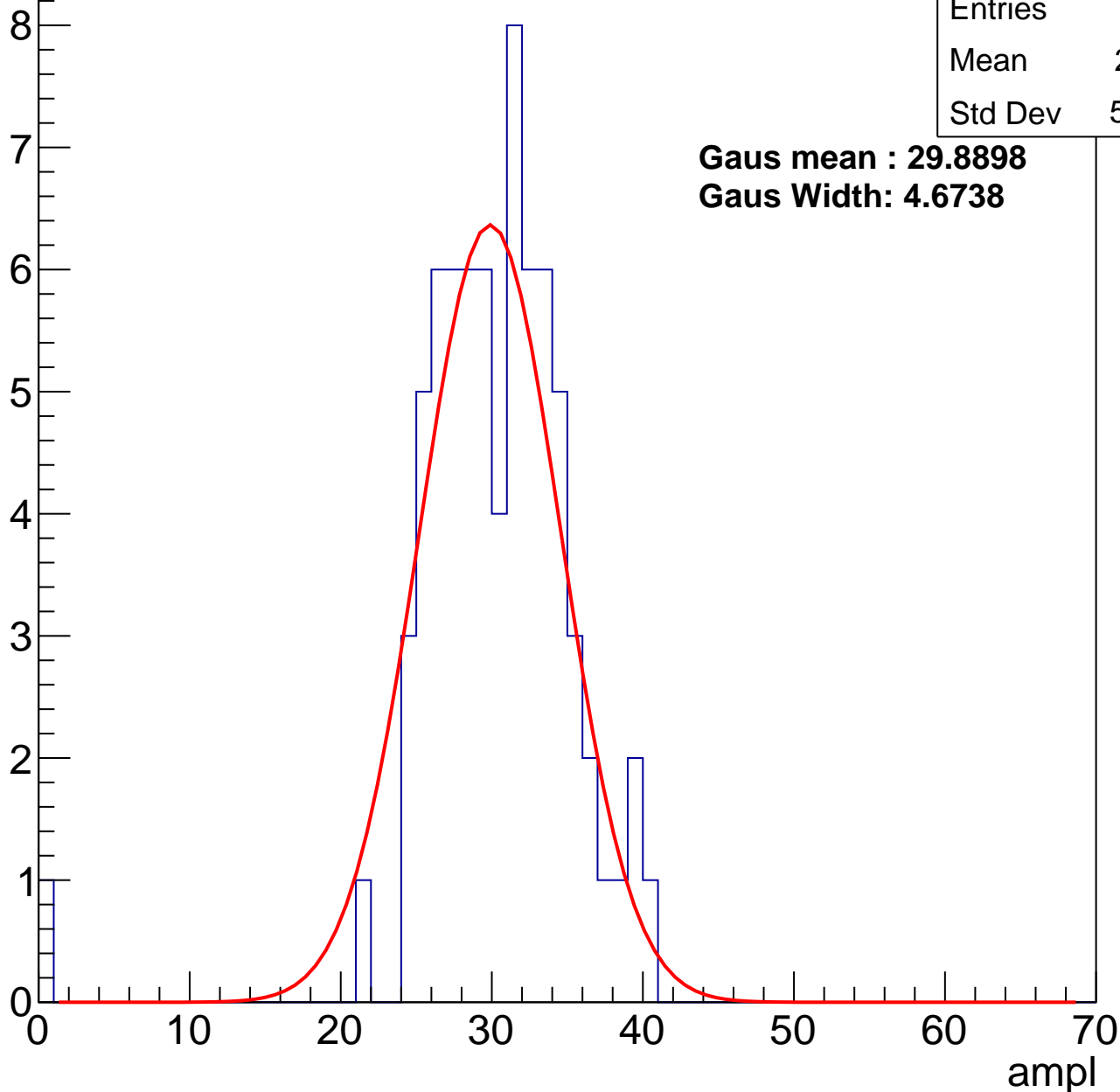
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.81
Std Dev	5.337

Gaus mean : 29.8898

Gaus Width: 4.6738



B0L001S, U18-ch115, adc1

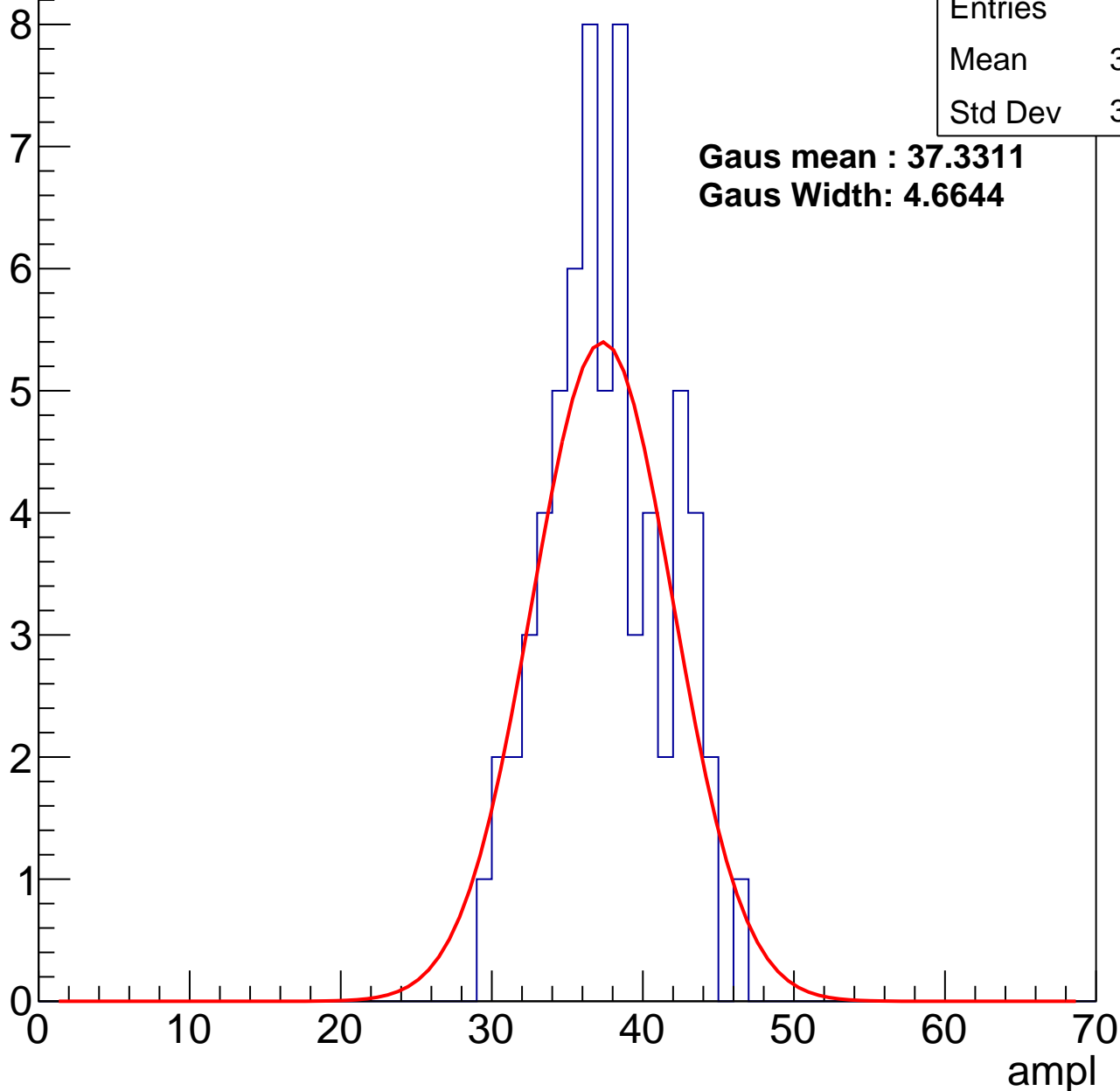
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.09
Std Dev	3.894

Gaus mean : 37.3311

Gaus Width: 4.6644



B0L001S, U18-ch115, adc2

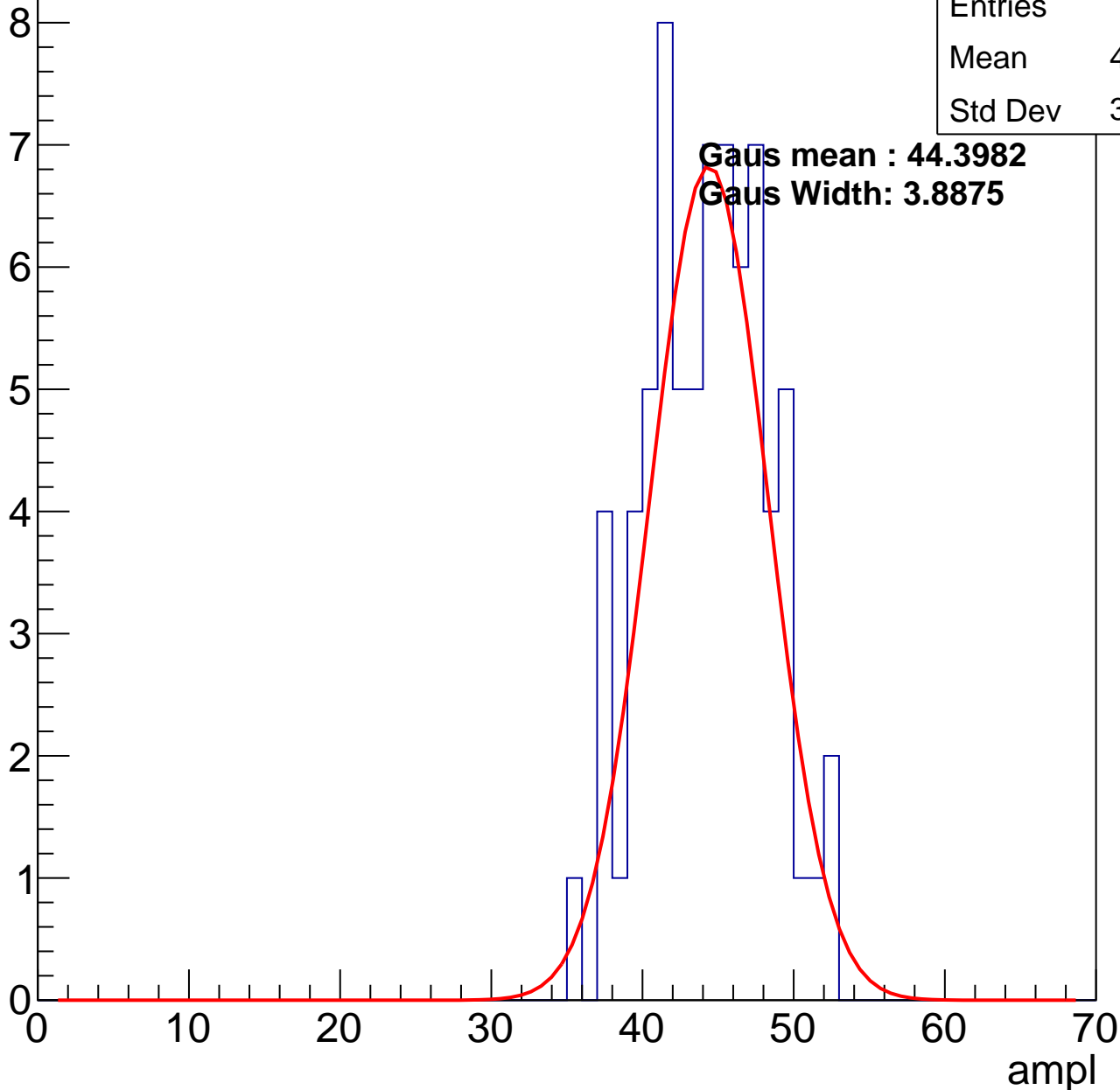
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.84
Std Dev	3.854

Gaus mean : 44.3982

Gaus Width: 3.8875

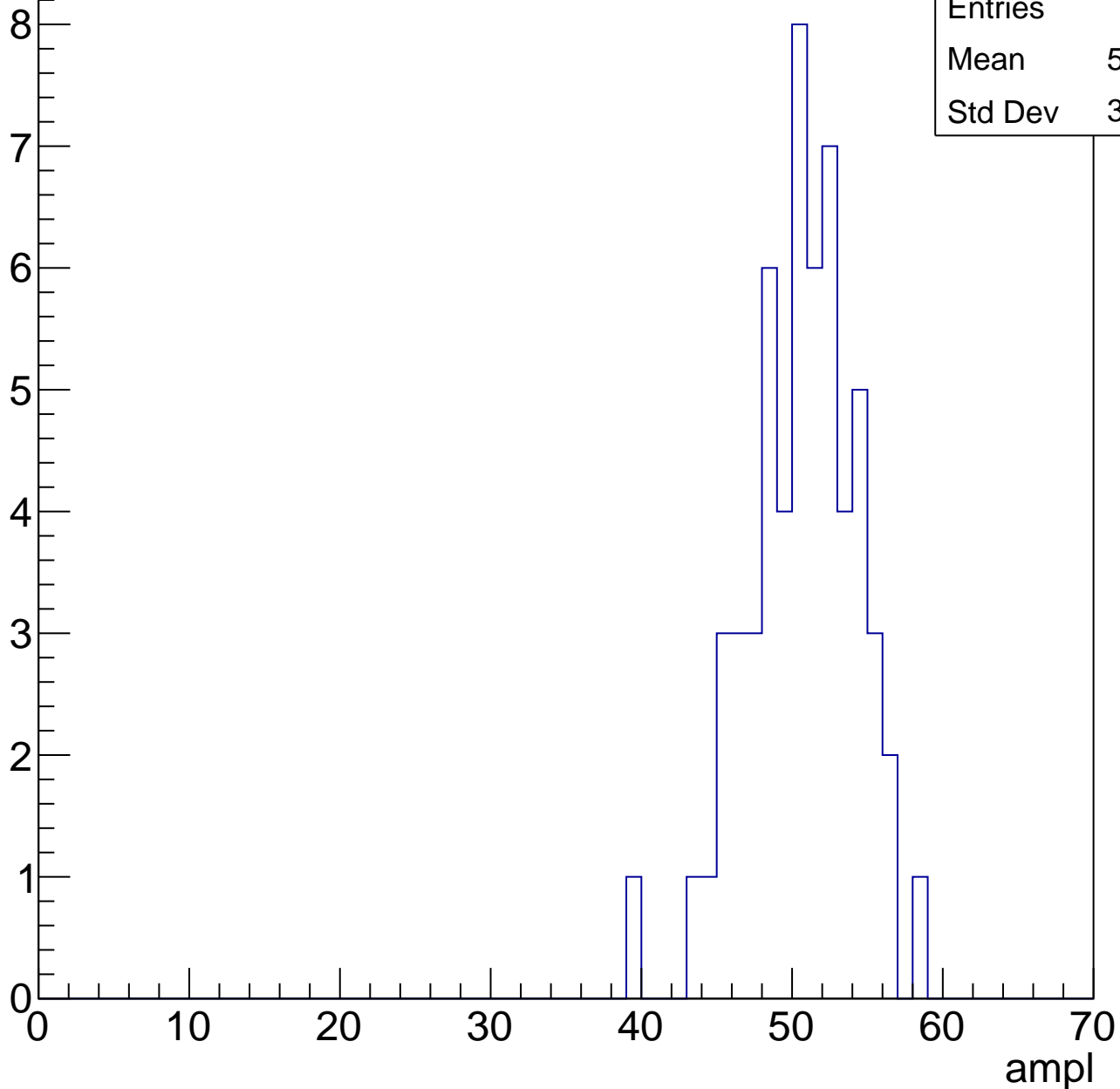


B0L001S, U18-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.19
Std Dev	3.579

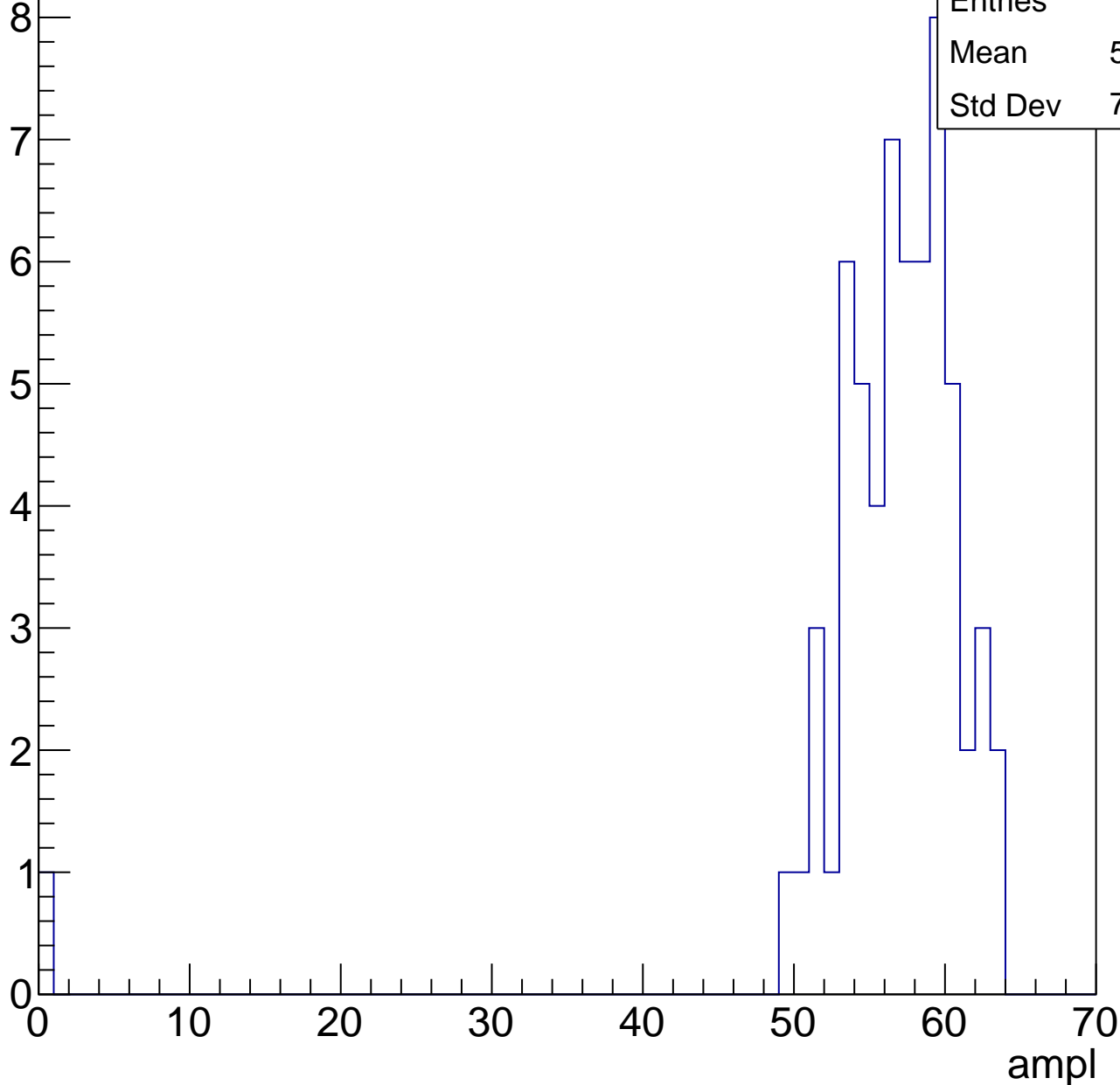


B0L001S, U18-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	55.74
Std Dev	7.917

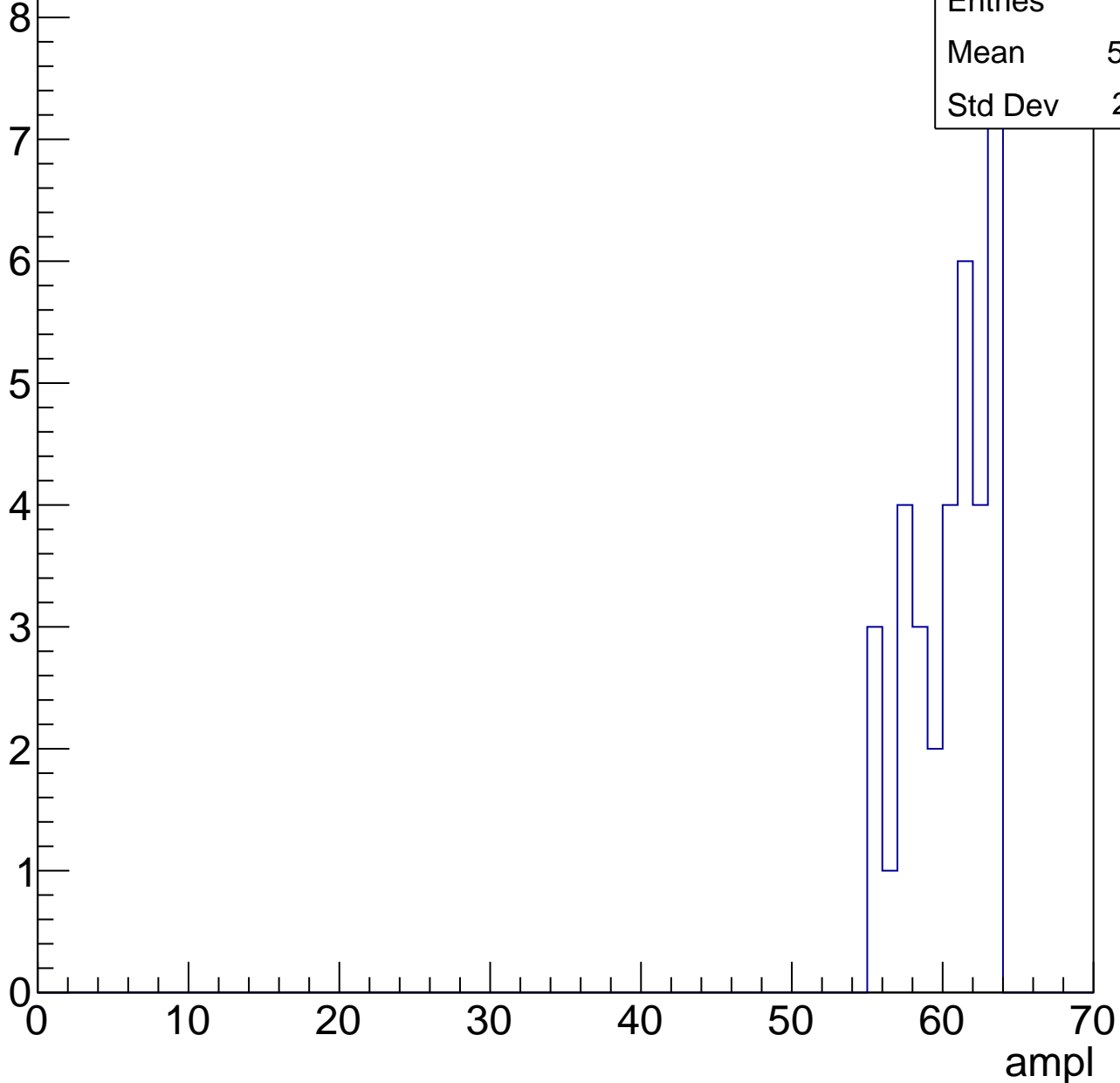


B0L001S, U18-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	59.97
Std Dev	2.591



B0L001S, U18-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

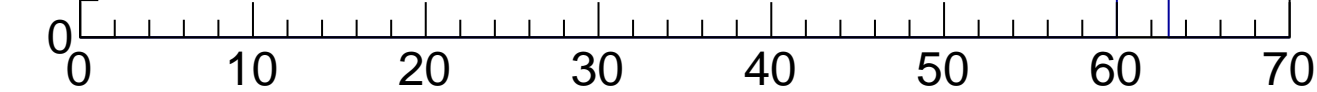
1

0.5

0

ampl

Entries	6
Mean	61.33
Std Dev	0.7454



B0L001S, U18-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch116, adc0

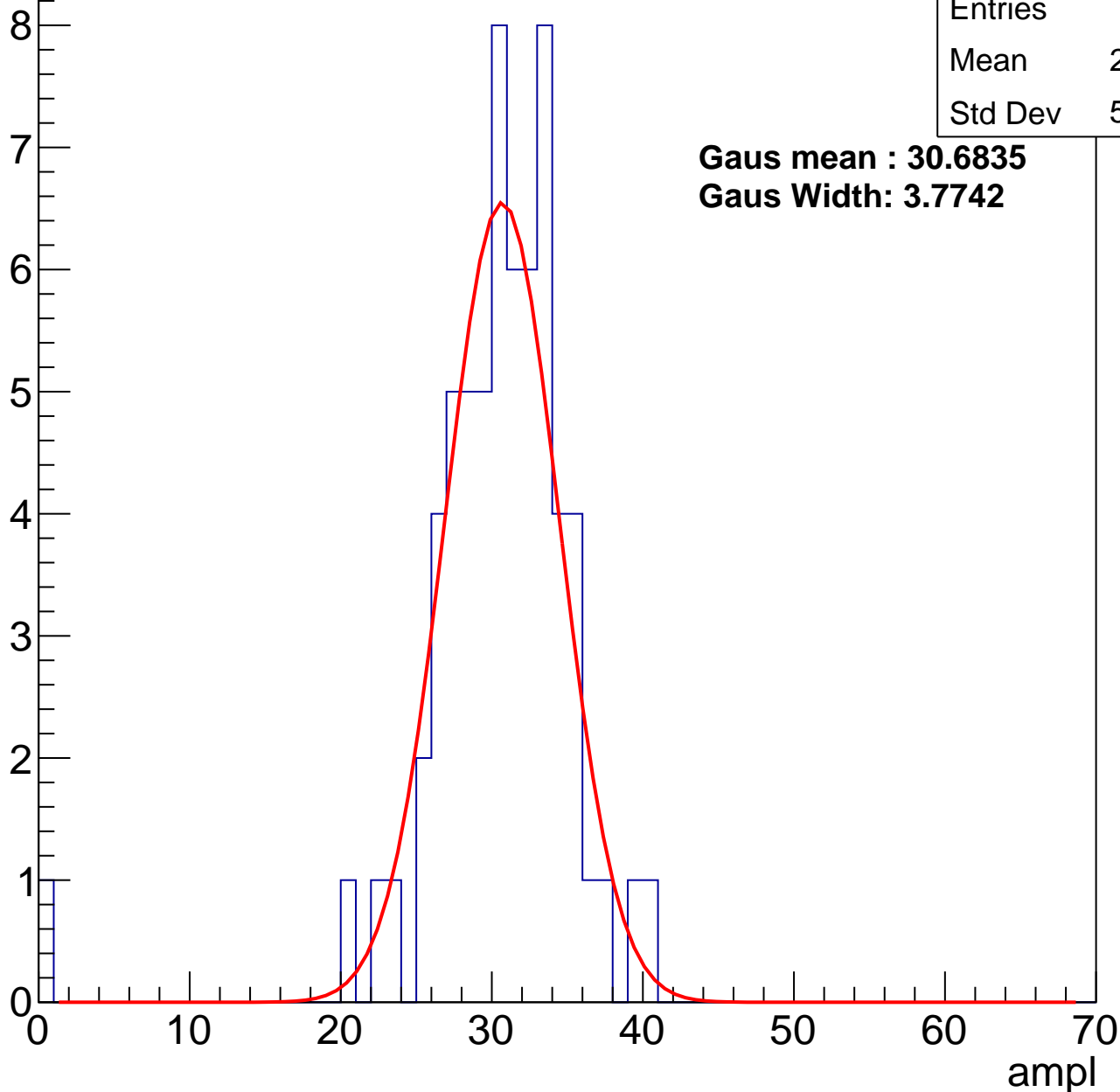
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.98
Std Dev	5.305

Gaus mean : 30.6835

Gaus Width: 3.7742



B0L001S, U18-ch116, adc1

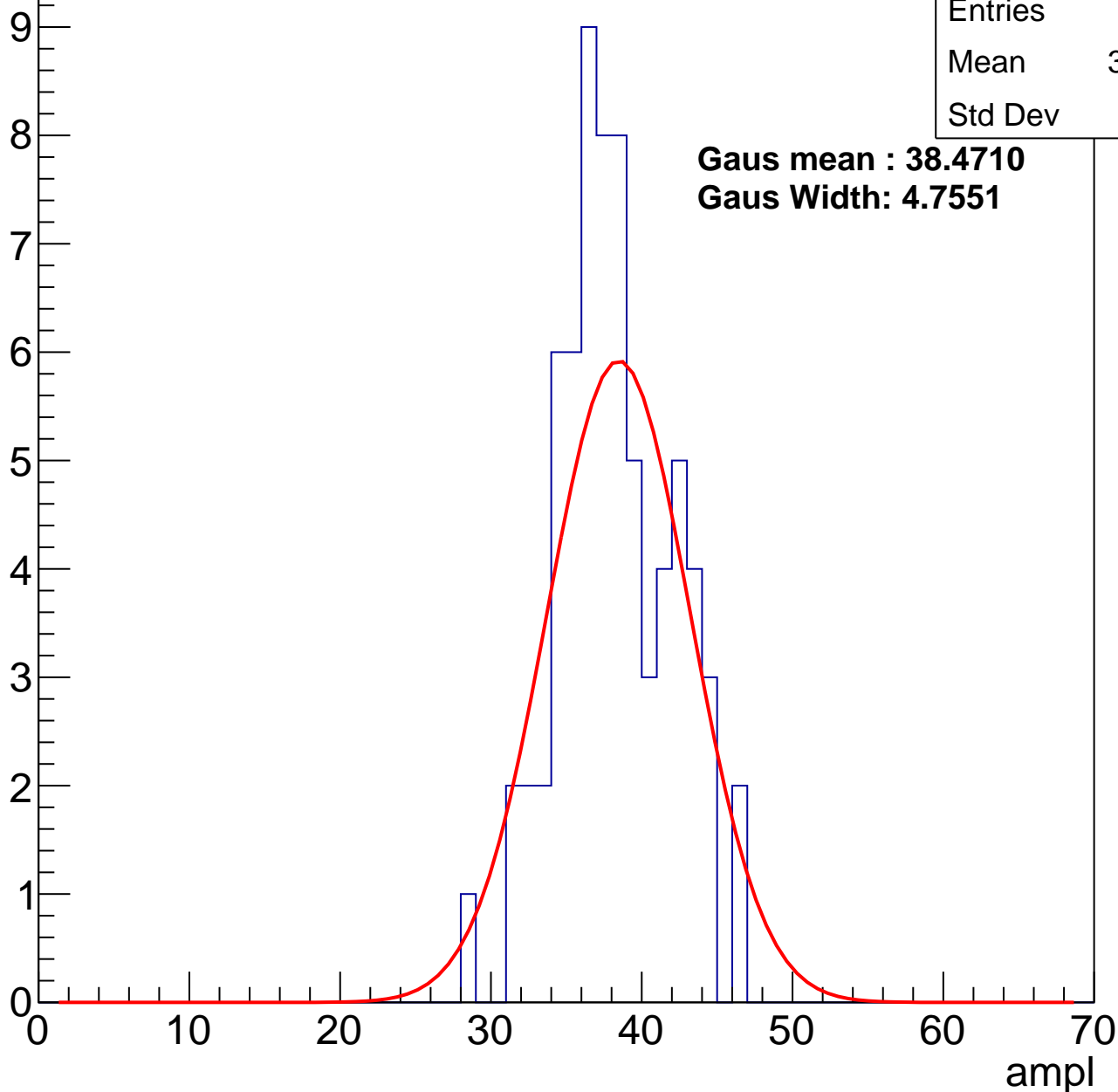
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.76
Std Dev	3.74

Gaus mean : 38.4710

Gaus Width: 4.7551

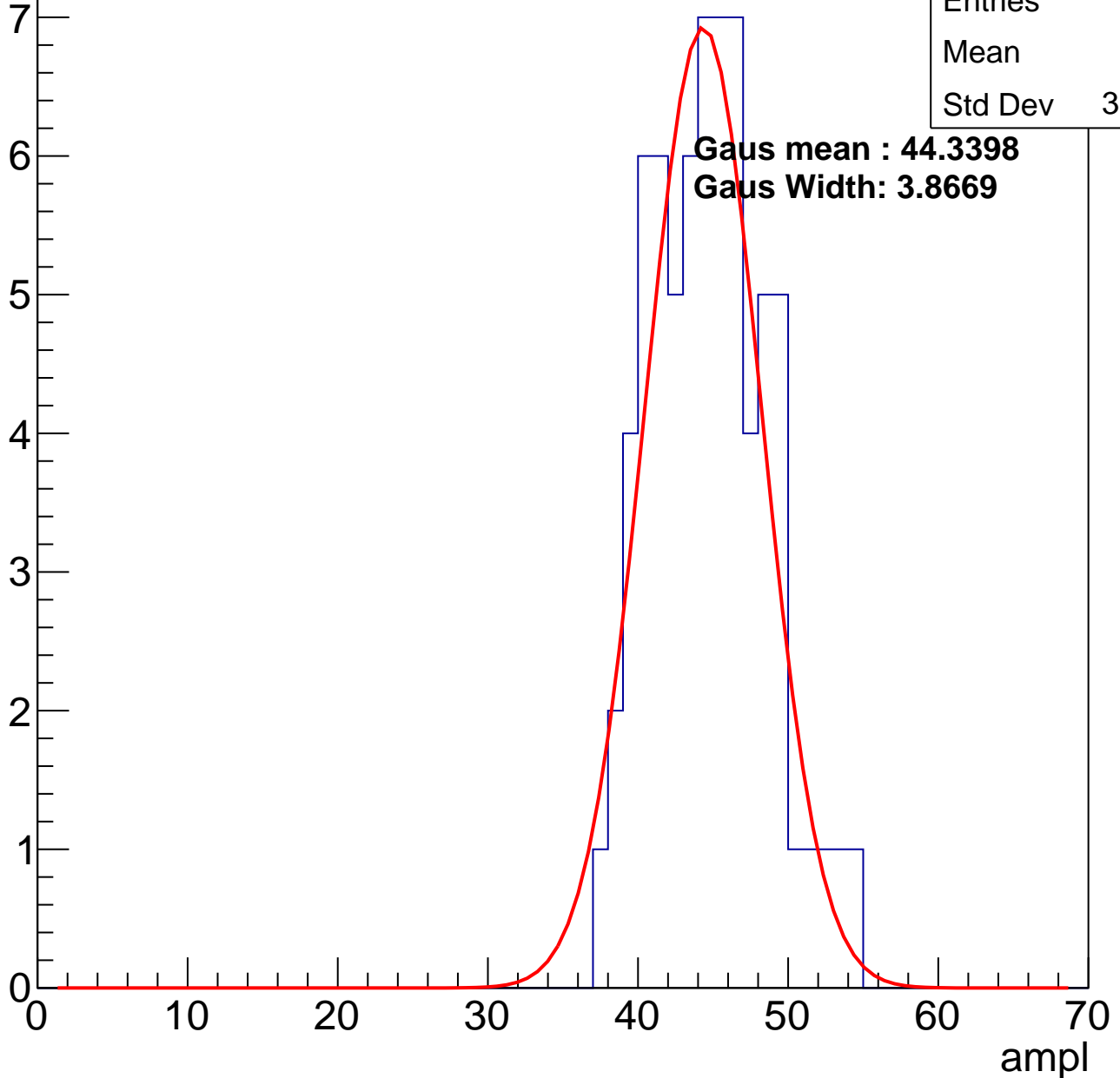


B0L001S, U18-ch116, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.3
Std Dev	3.773

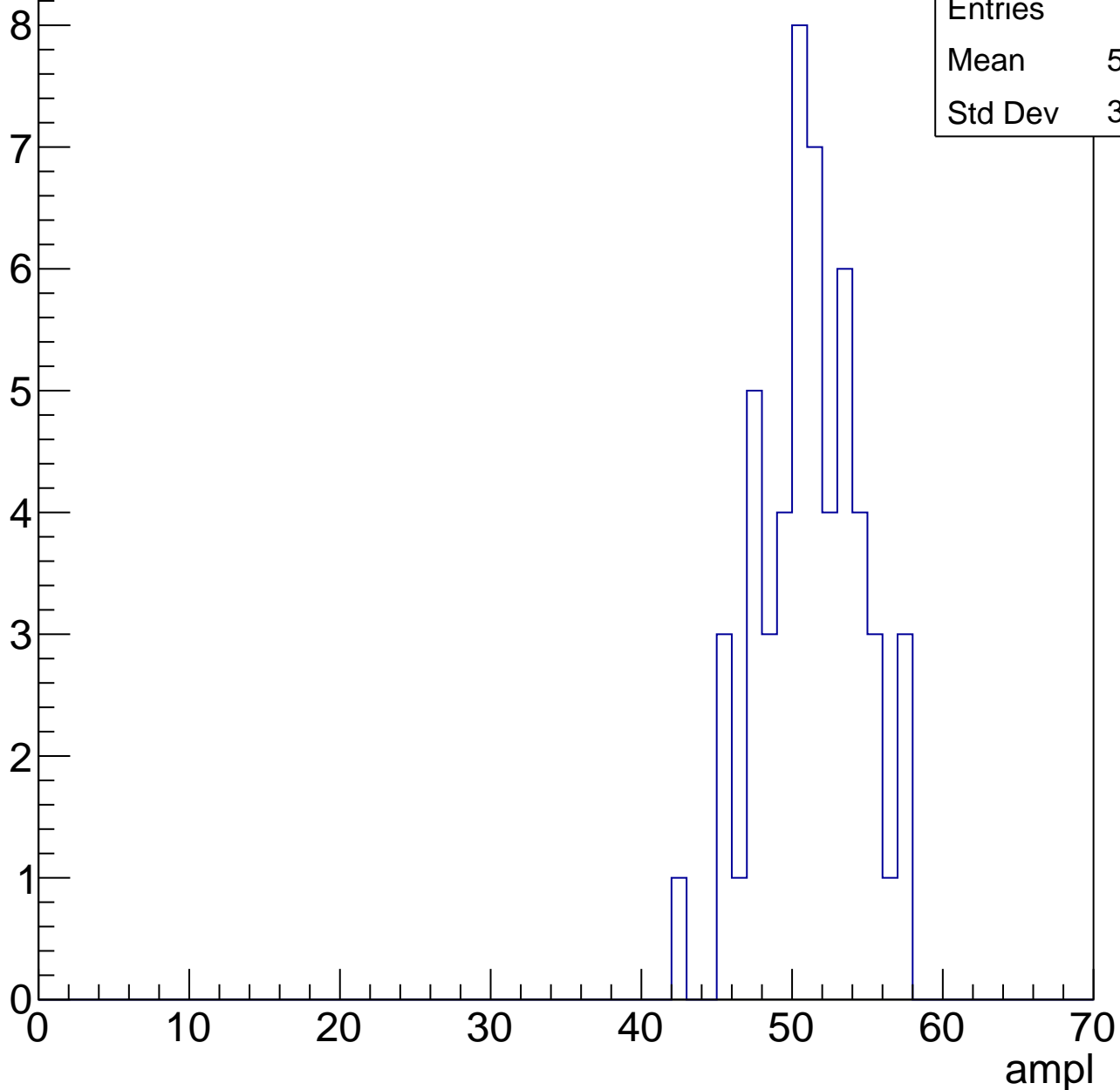


B0L001S, U18-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.74
Std Dev	3.326

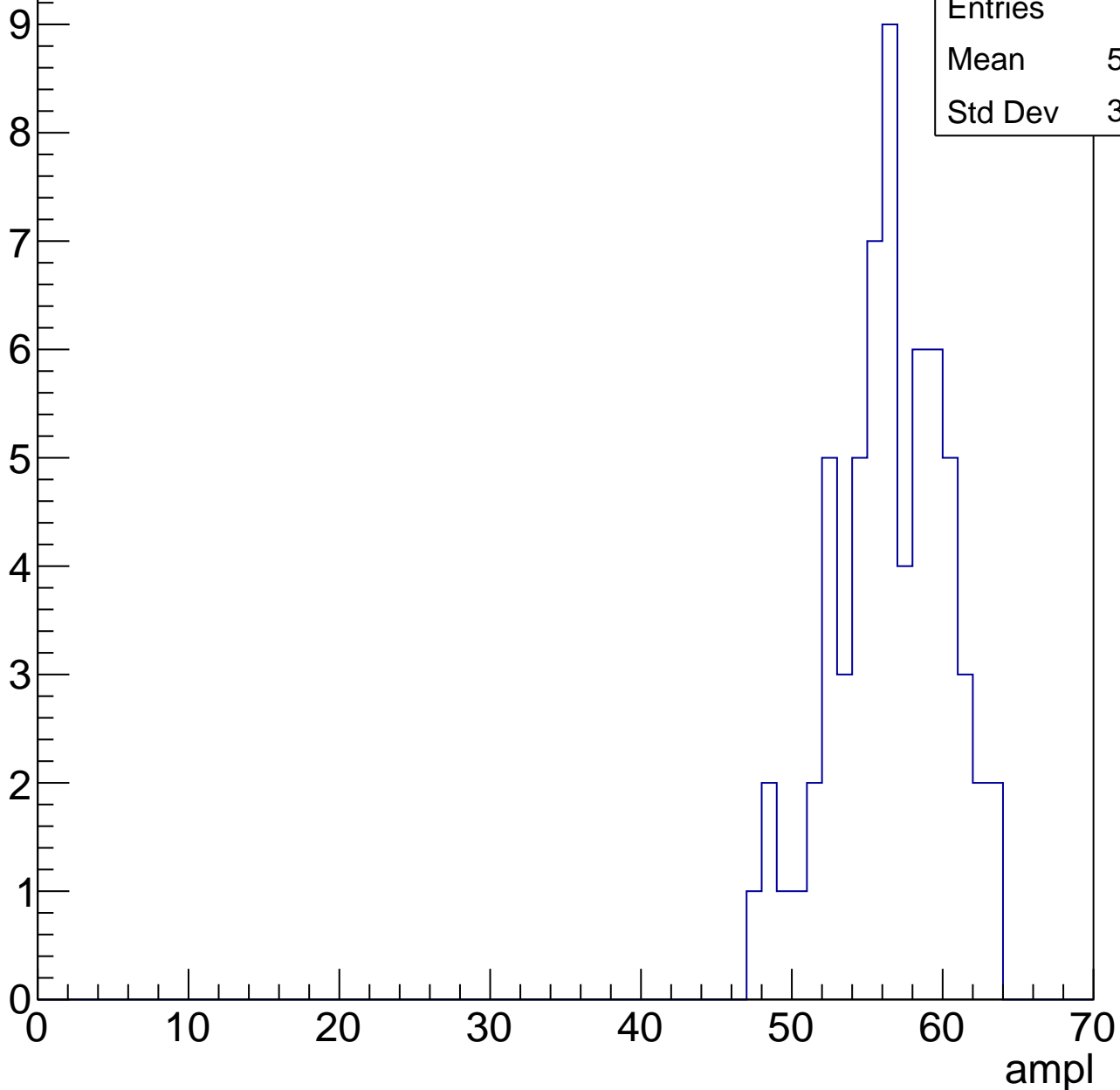


B0L001S, U18-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.02
Std Dev	3.719

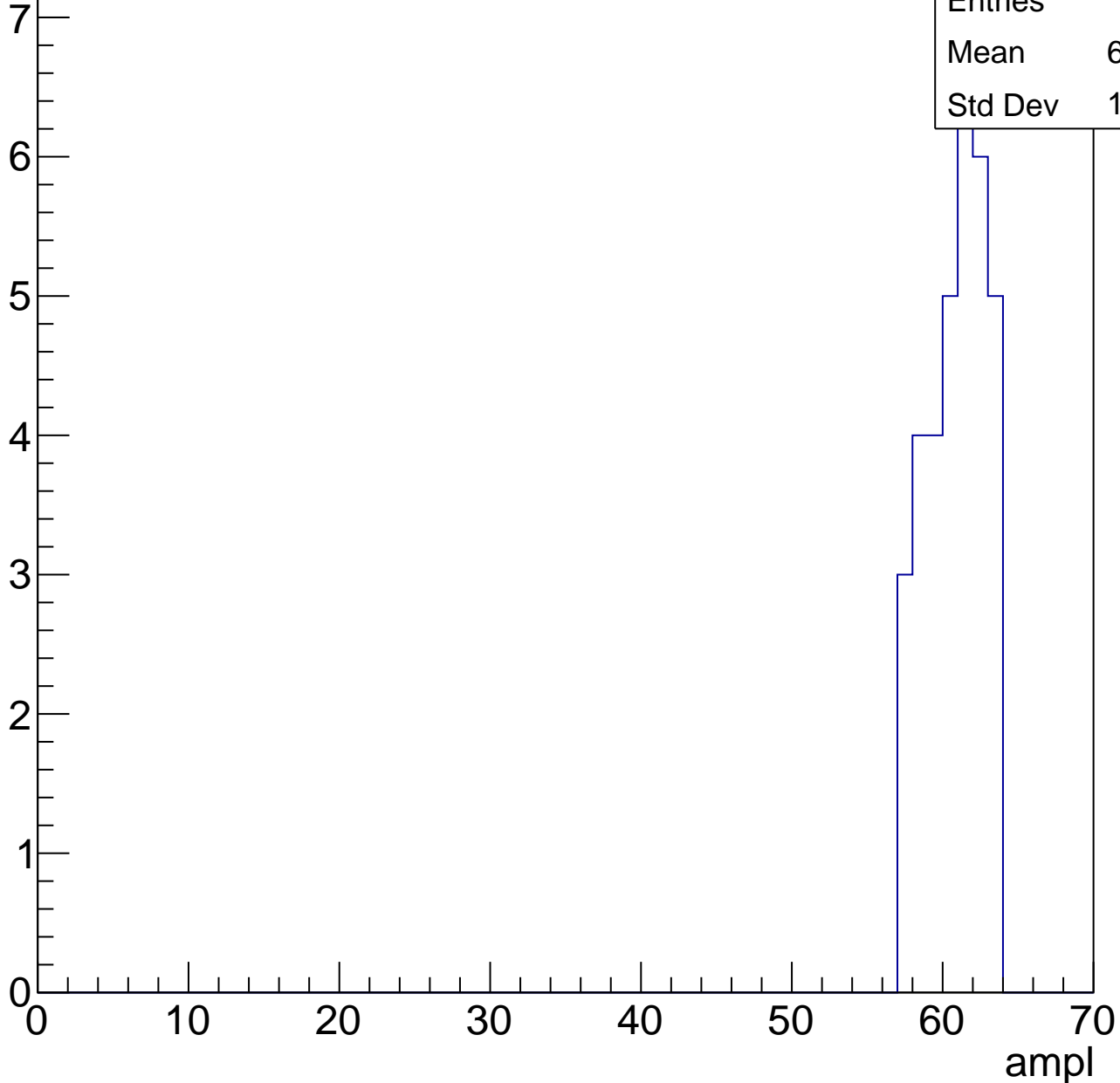


B0L001S, U18-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

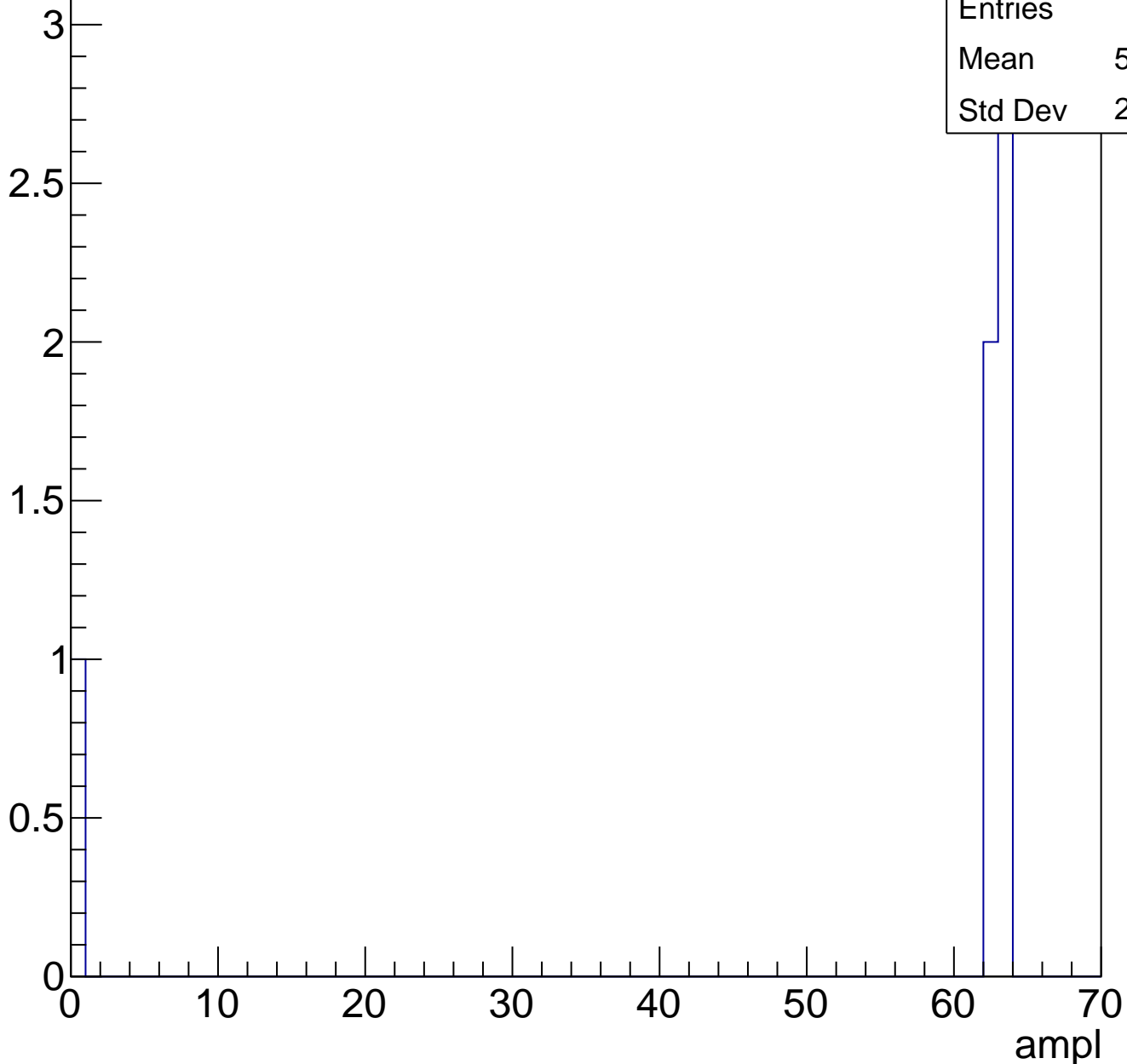
Entries	34
Mean	60.38
Std Dev	1.863



B0L001S, U18-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch117, adc0

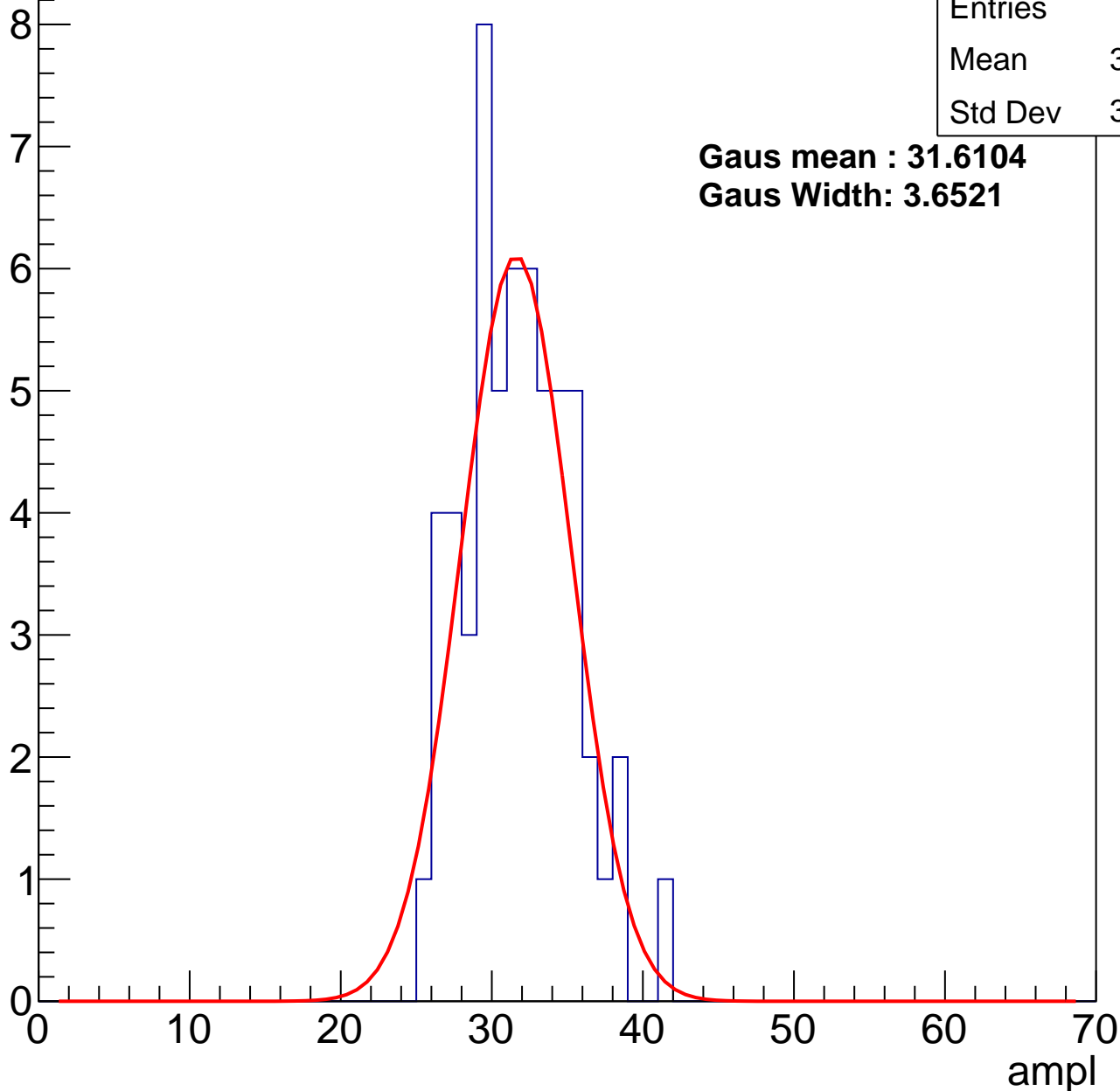
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.33
Std Dev	3.456

Gaus mean : 31.6104

Gaus Width: 3.6521



B0L001S, U18-ch117, adc1

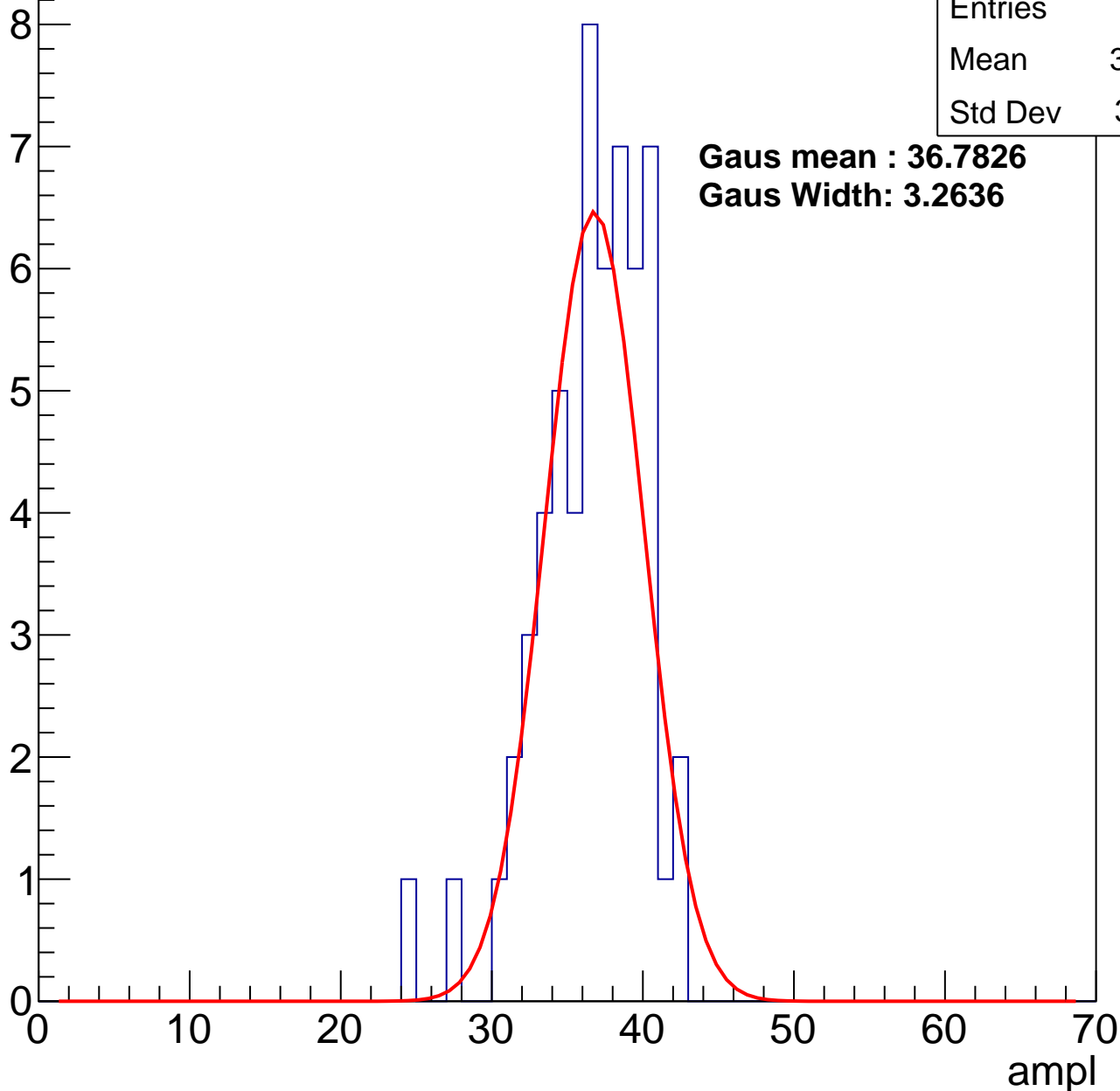
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	36.14
Std Dev	3.511

Gaus mean : 36.7826

Gaus Width: 3.2636



B0L001S, U18-ch117, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	44.05
Std Dev	3.687

Gaus mean : 44.3557

Gaus Width: 3.4144

10

8

6

4

2

0

0

10

20

30

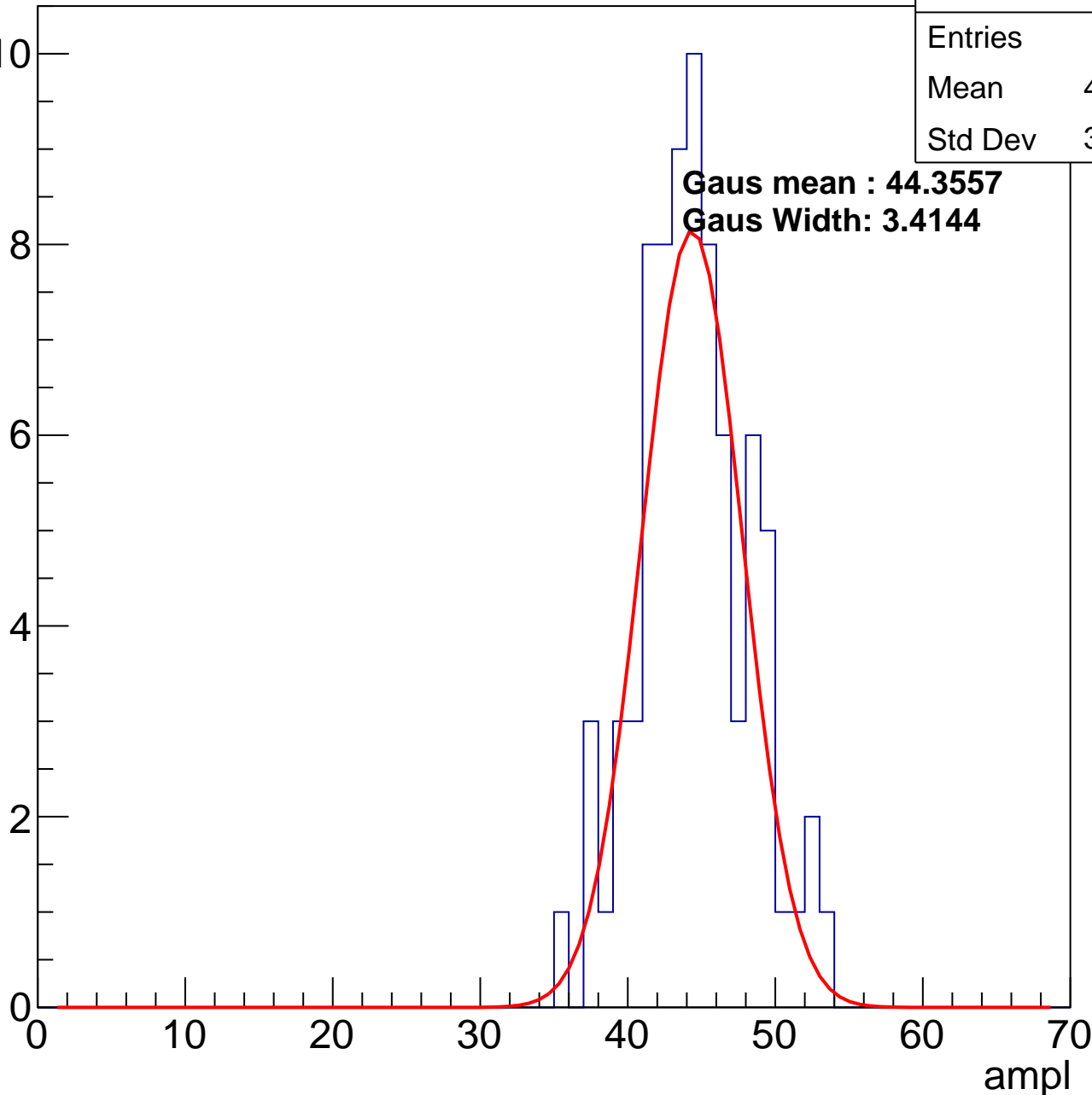
40

50

60

70

ampl

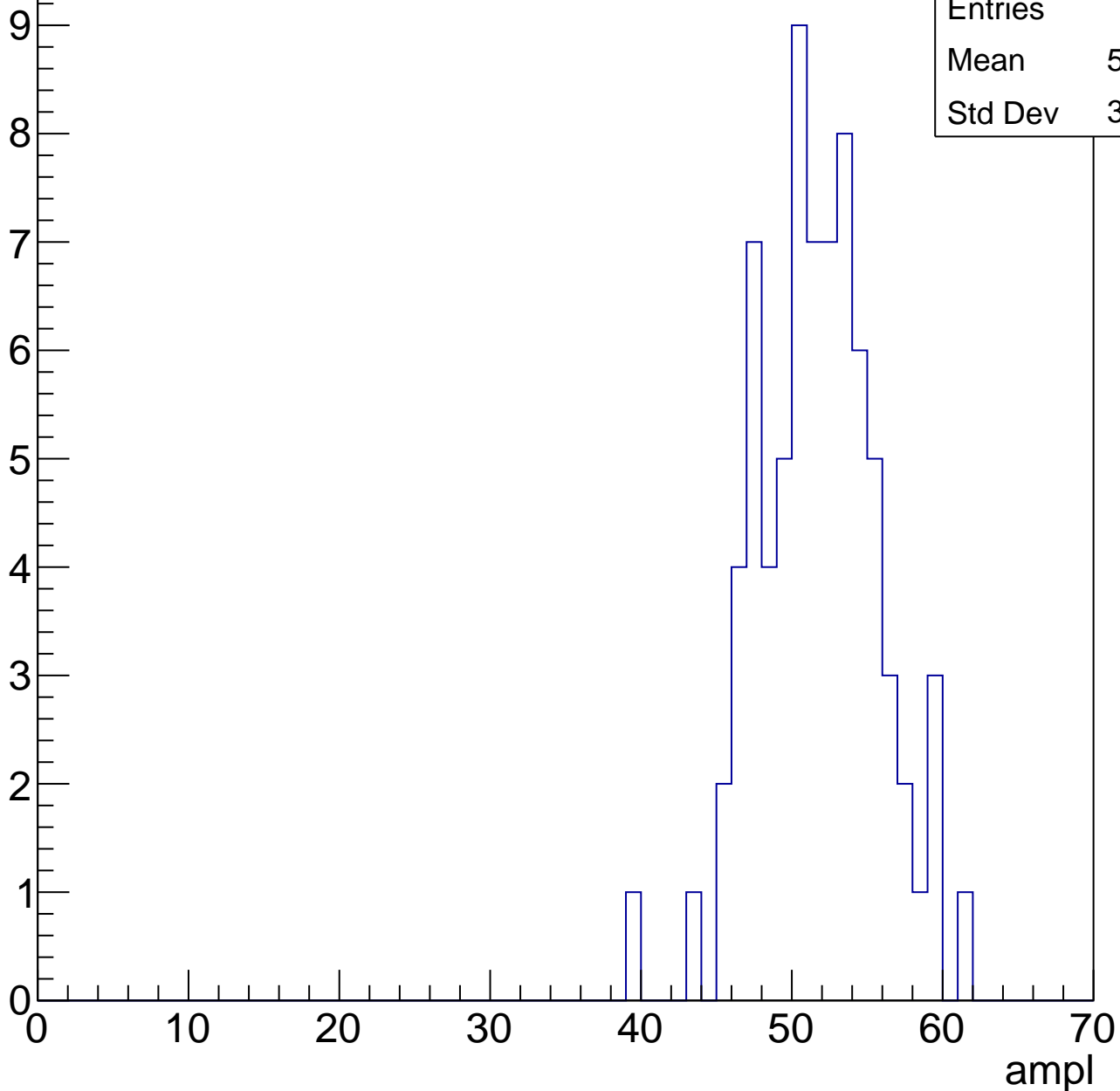


B0L001S, U18-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

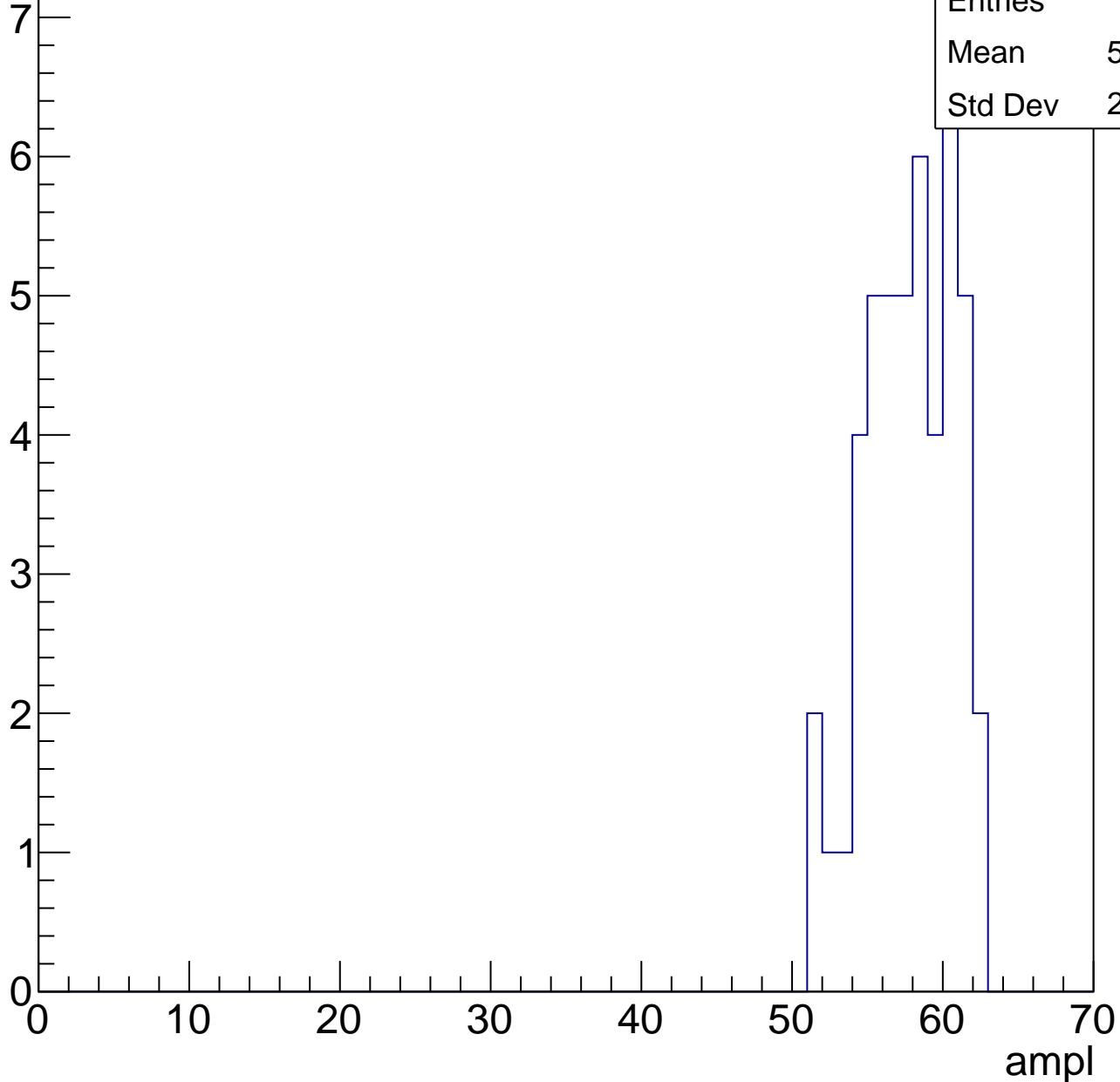
Entries	76
Mean	51.24
Std Dev	3.996



B0L001S, U18-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	24
Mean	59.88
Std Dev	2.315

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

60

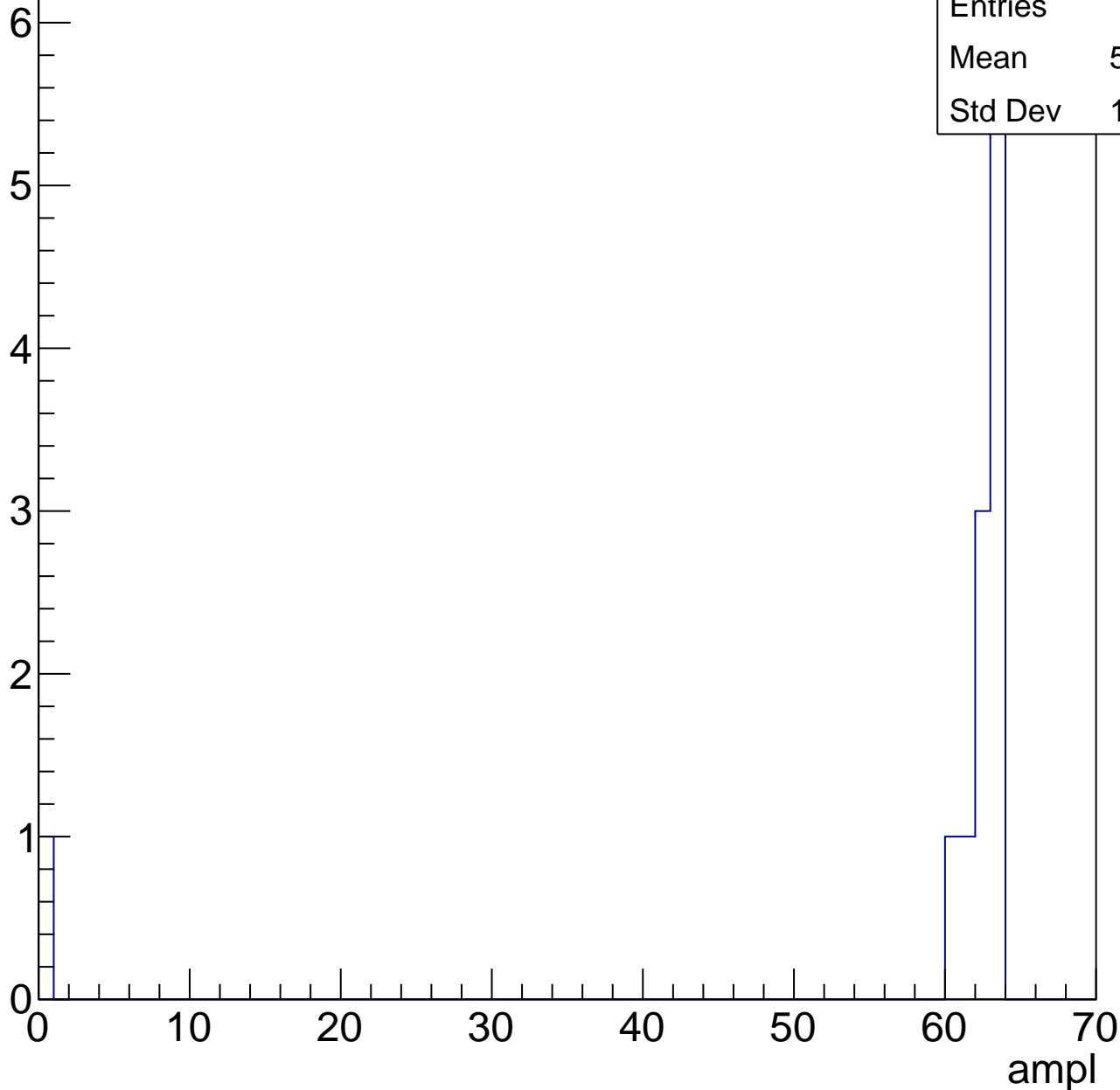
70

B0L001S, U18-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	12
Mean	57.08
Std Dev	17.24



B0L001S, U18-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L001S, U18-ch118, adc0

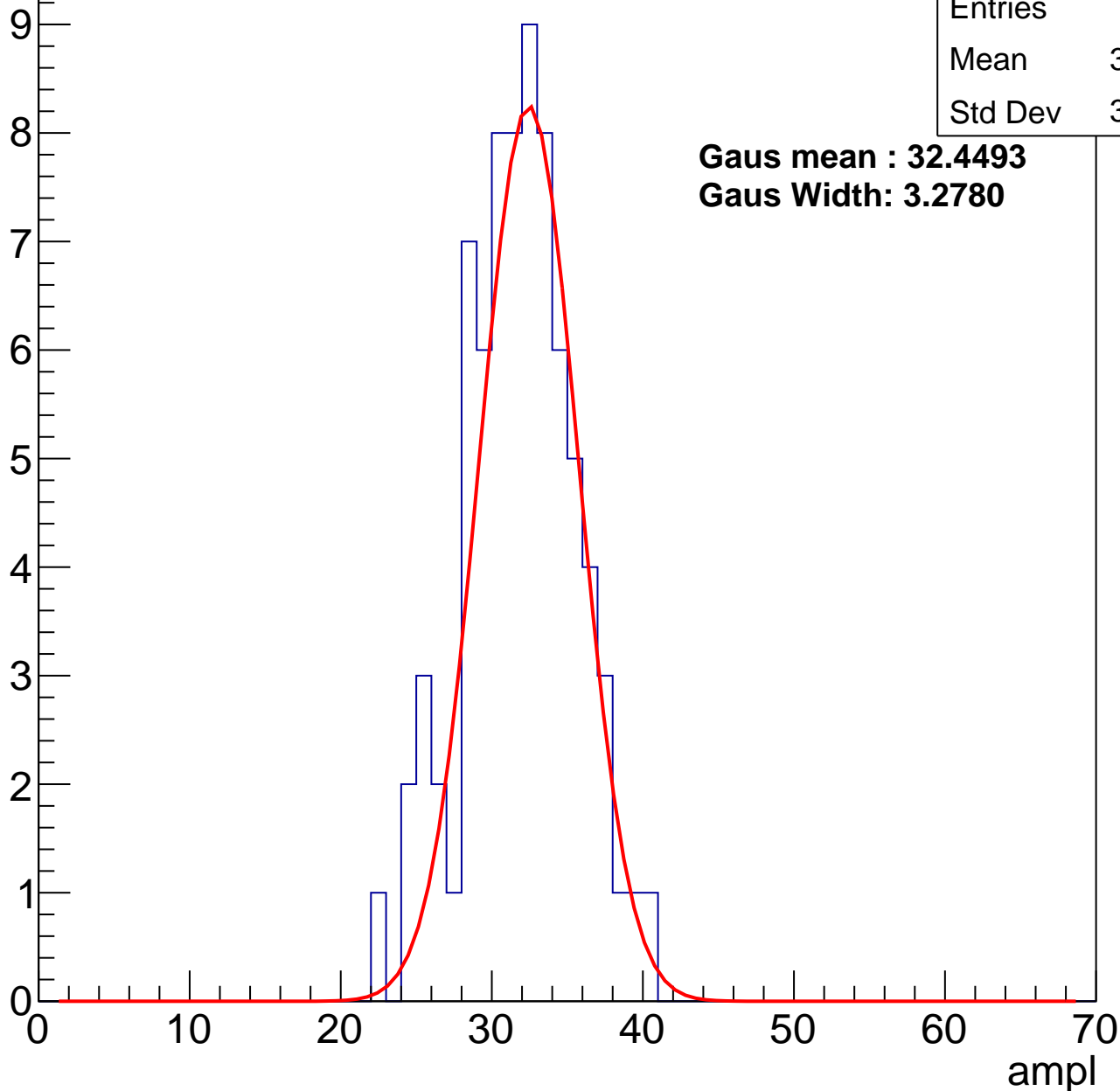
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	31.38
Std Dev	3.653

Gaus mean : 32.4493

Gaus Width: 3.2780



B0L001S, U18-ch118, adc1

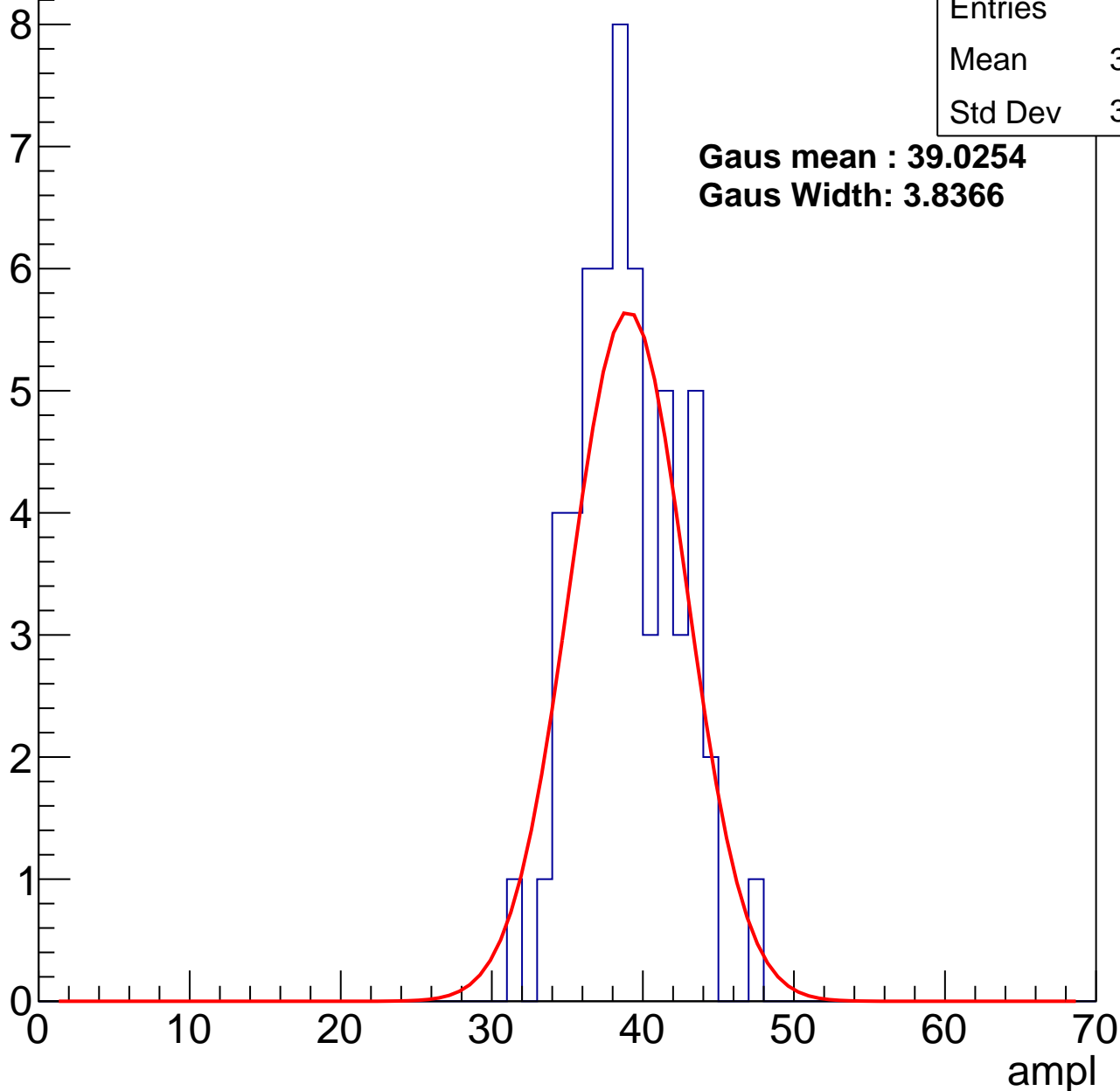
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	38.49
Std Dev	3.247

Gaus mean : 39.0254

Gaus Width: 3.8366



B0L001S, U18-ch118, adc2

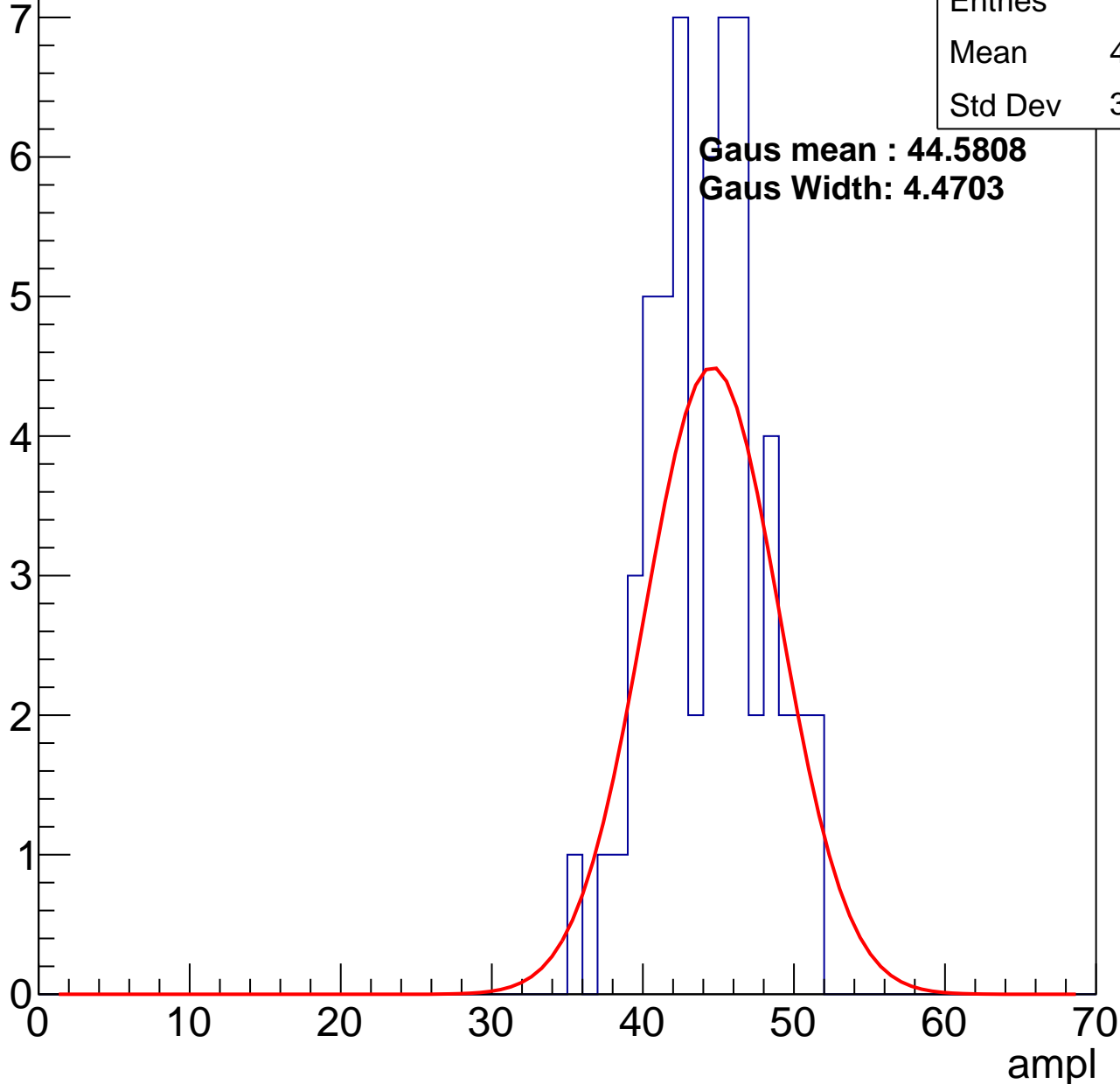
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.84
Std Dev	3.583

Gaus mean : 44.5808

Gaus Width: 4.4703

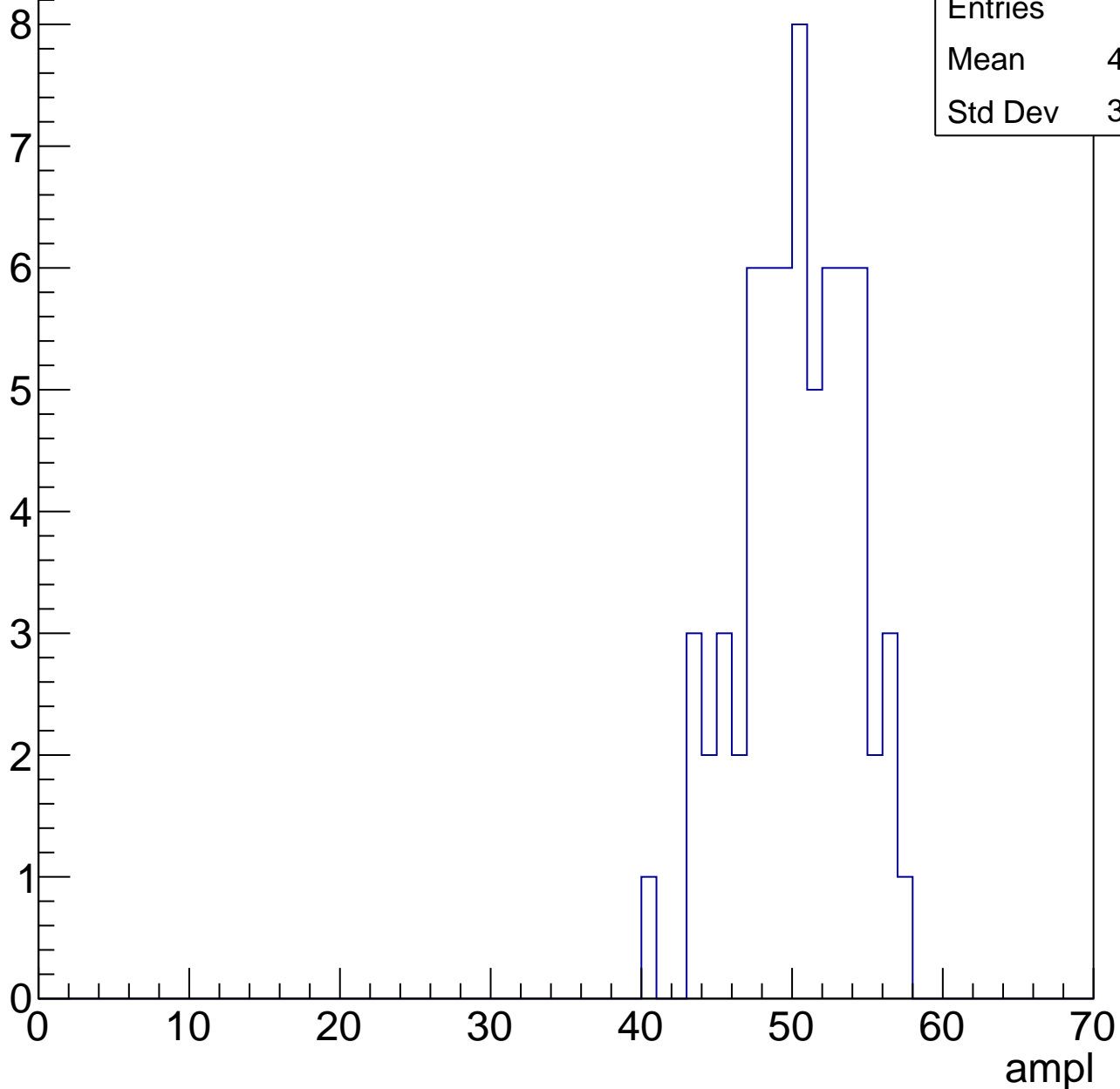


B0L001S, U18-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.88
Std Dev	3.683

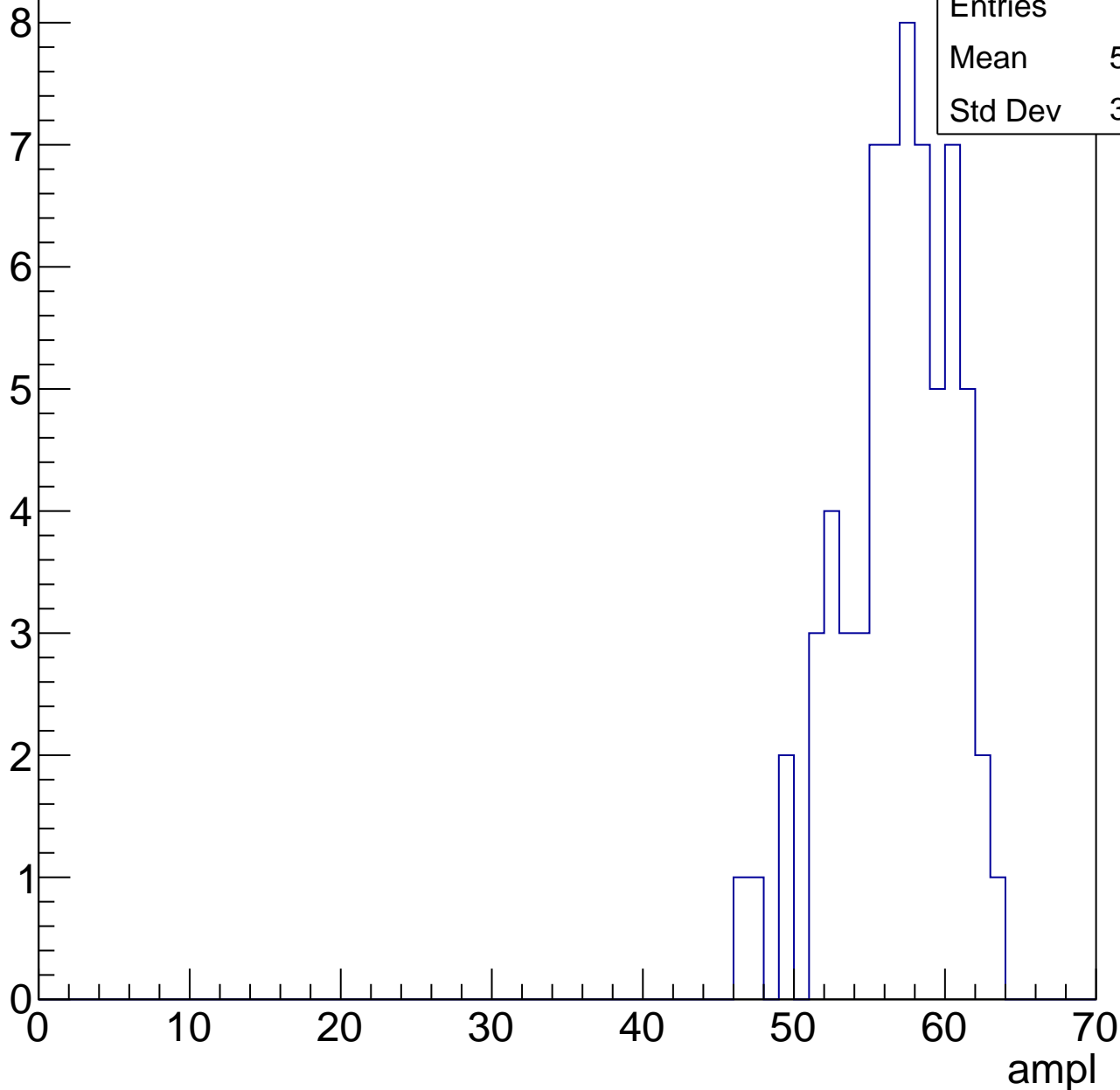


B0L001S, U18-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.35
Std Dev	3.682

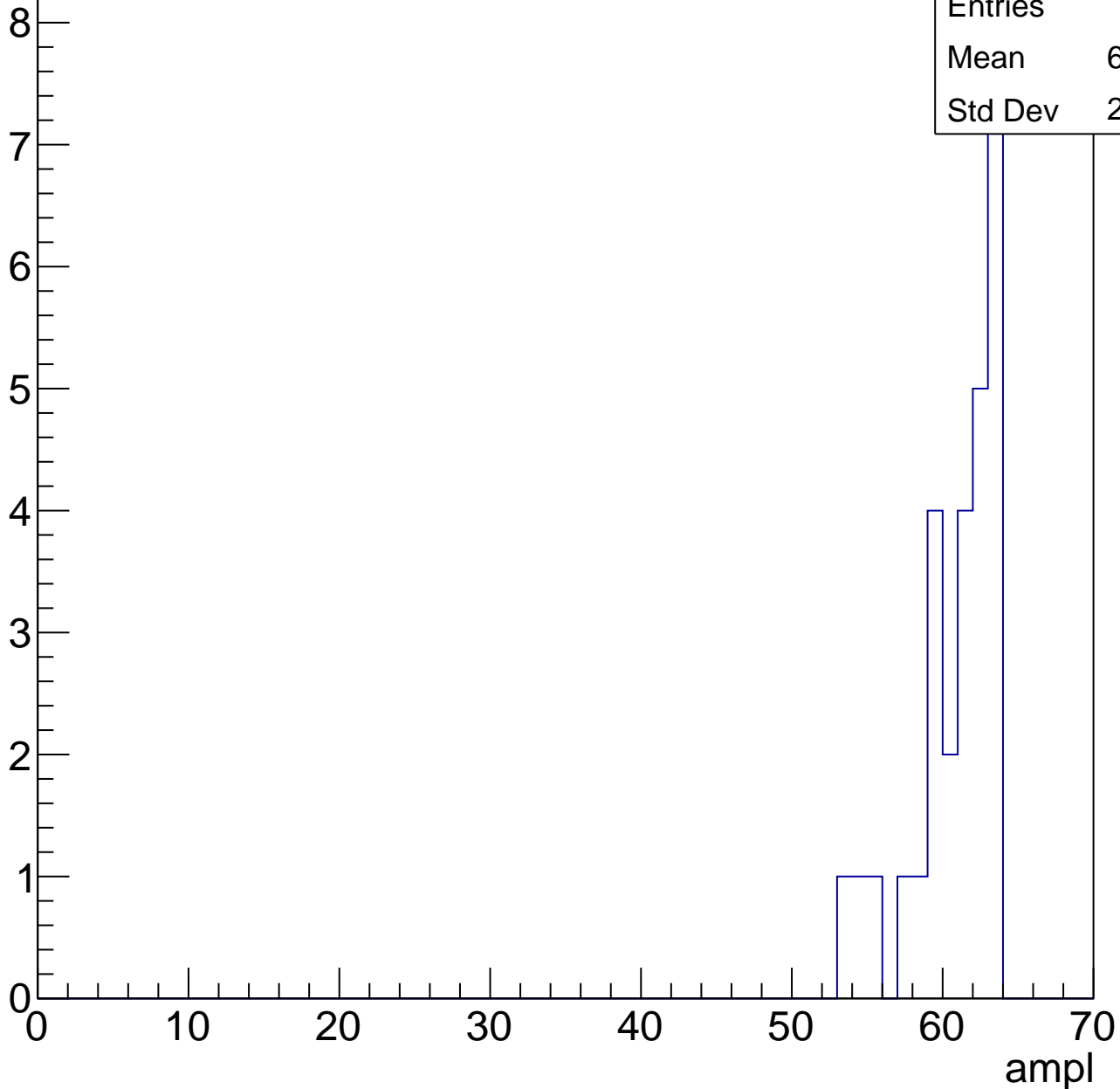


B0L001S, U18-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

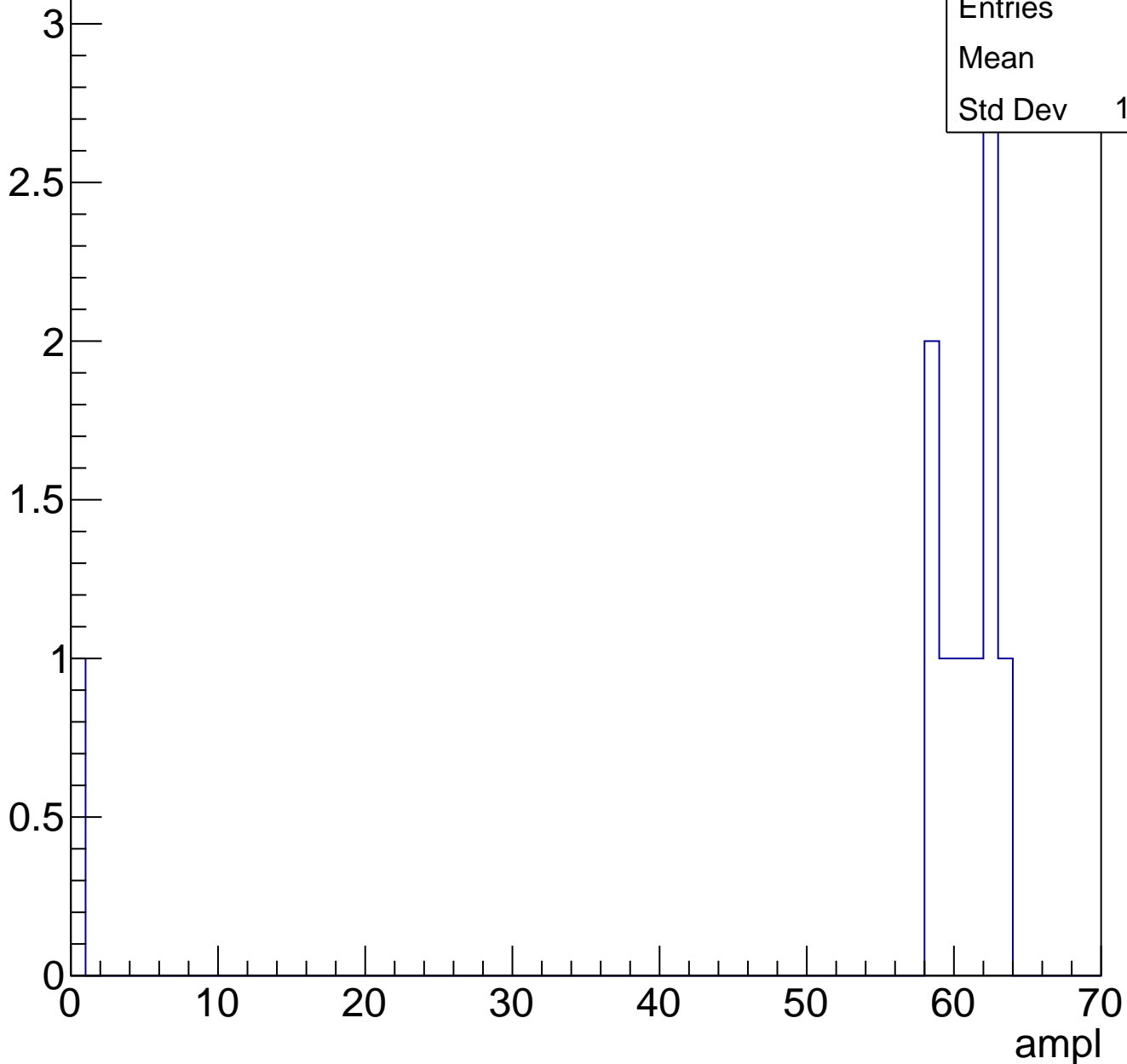
Entries	28
Mean	60.39
Std Dev	2.795



B0L001S, U18-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch119, adc0

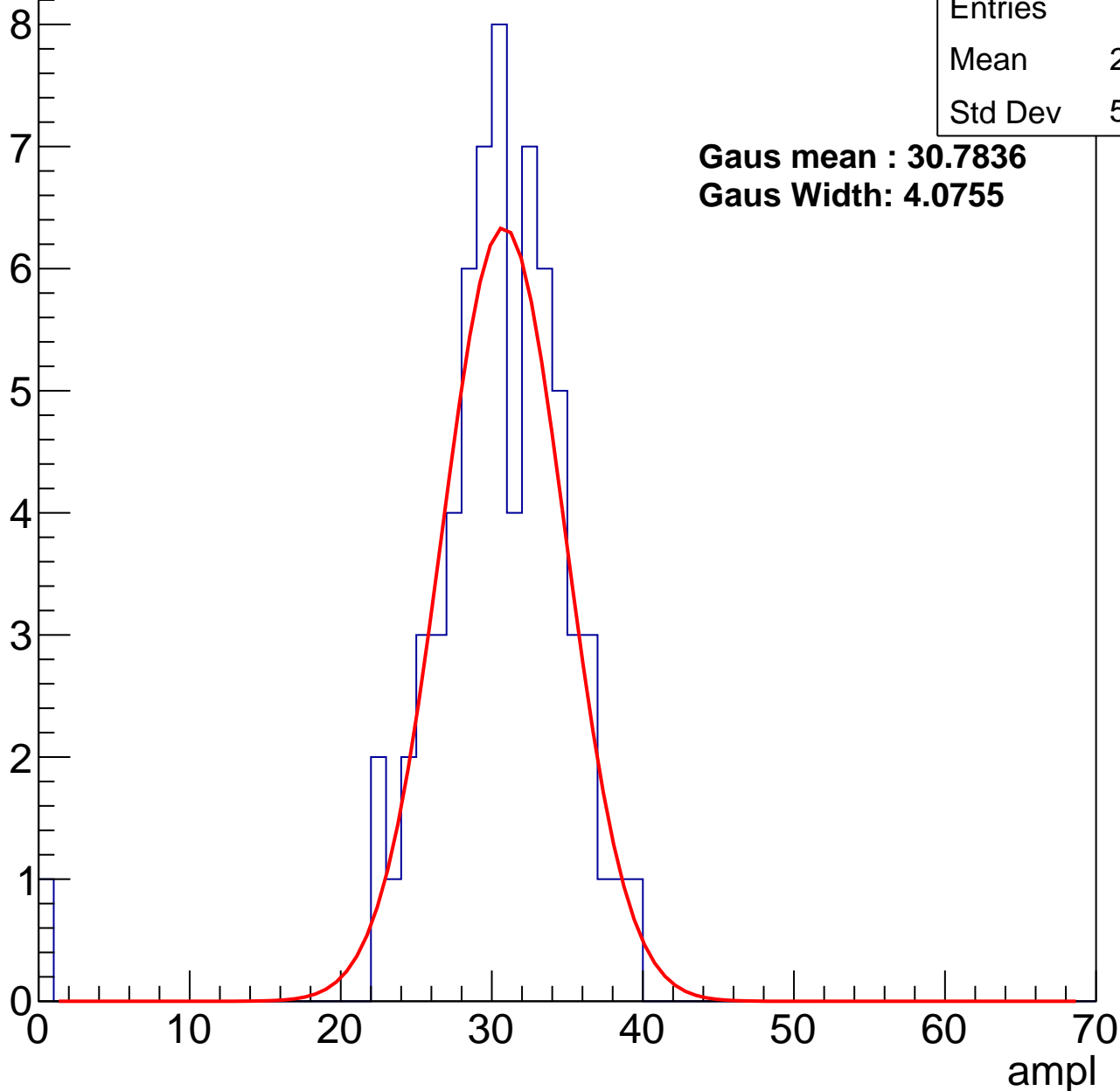
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.85
Std Dev	5.259

Gaus mean : 30.7836

Gaus Width: 4.0755



B0L001S, U18-ch119, adc1

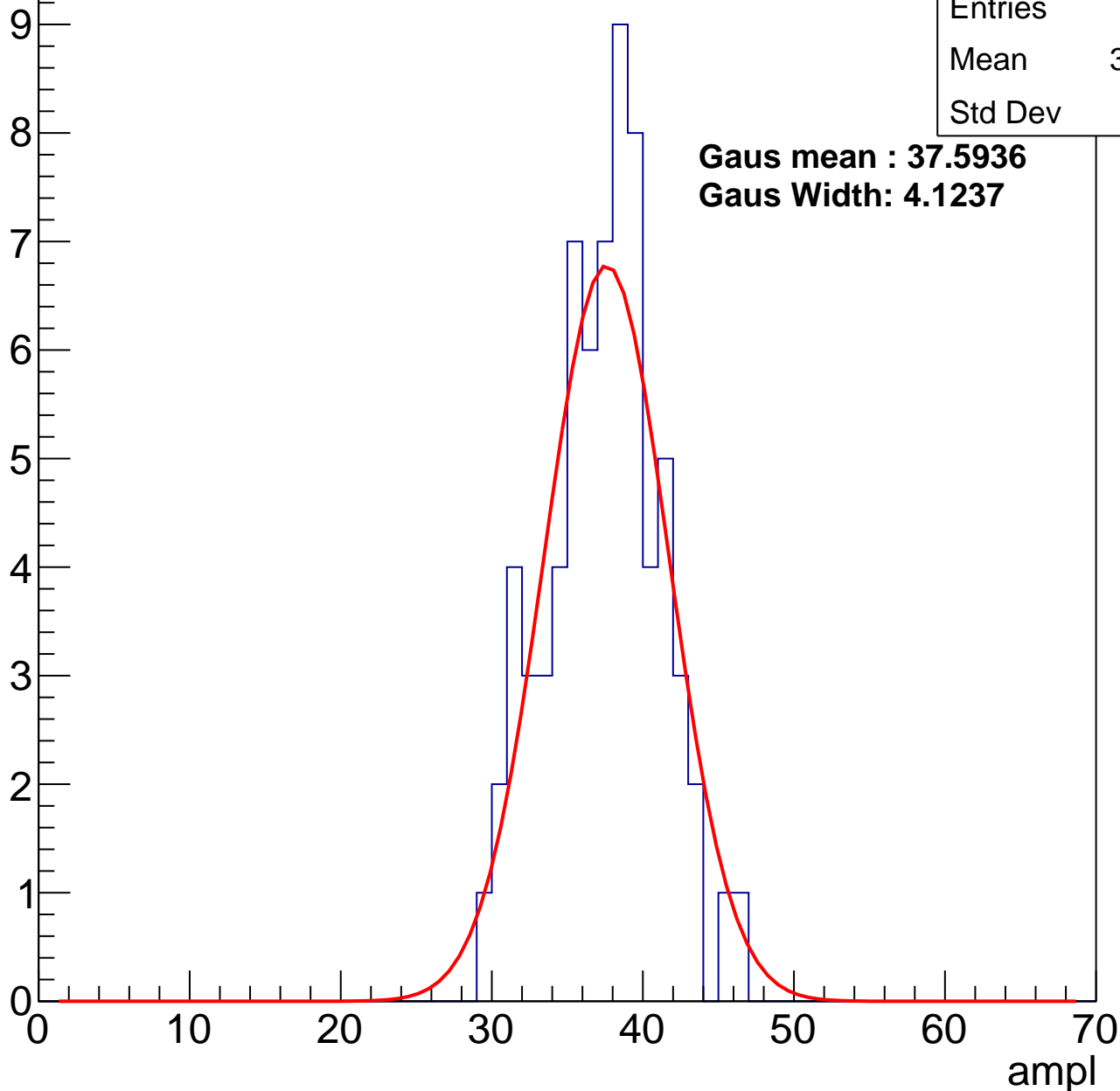
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.94
Std Dev	3.68

Gaus mean : 37.5936

Gaus Width: 4.1237



B0L001S, U18-ch119, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

78

Mean

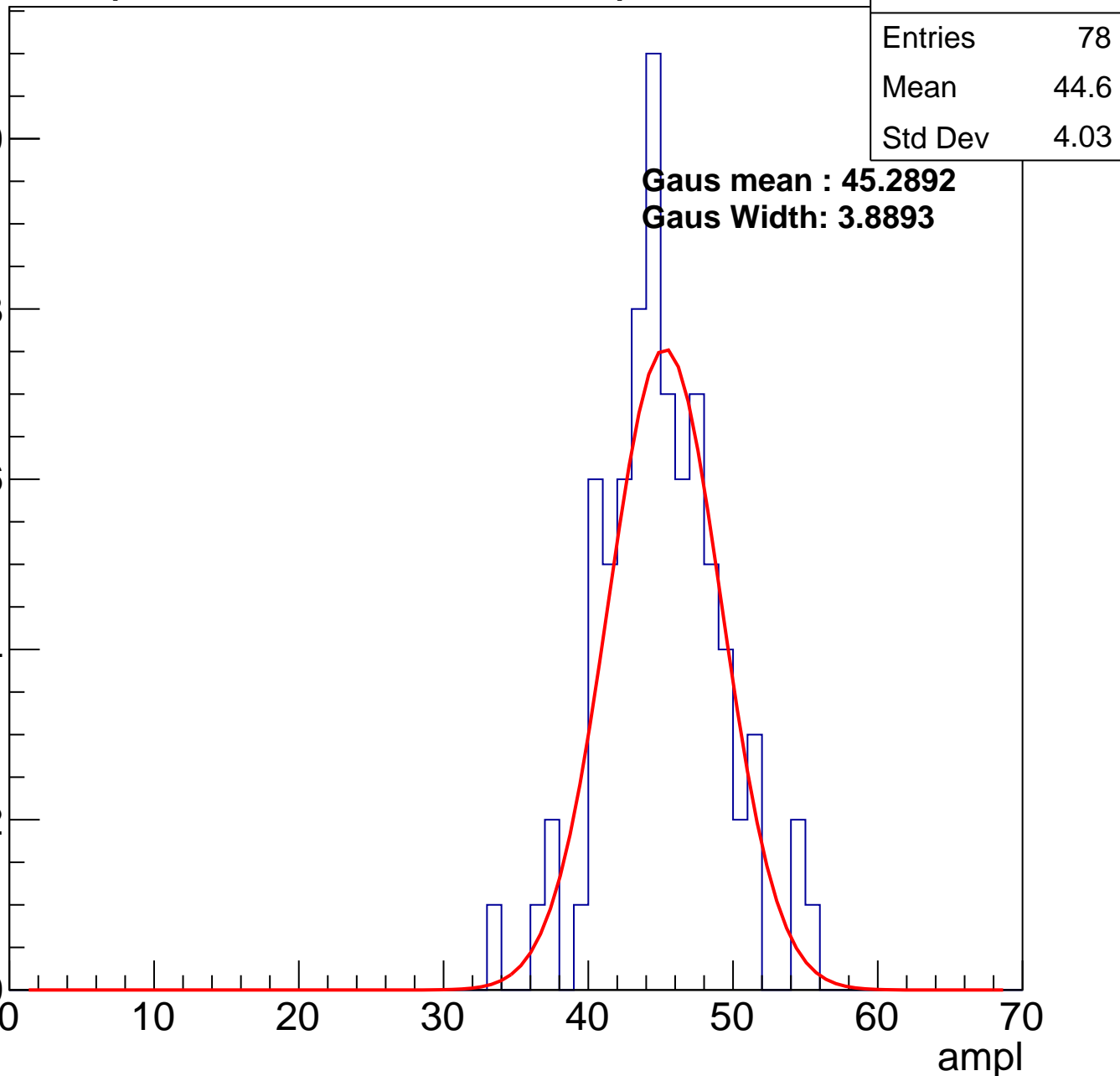
44.6

Std Dev

4.03

Gaus mean : 45.2892

Gaus Width: 3.8893

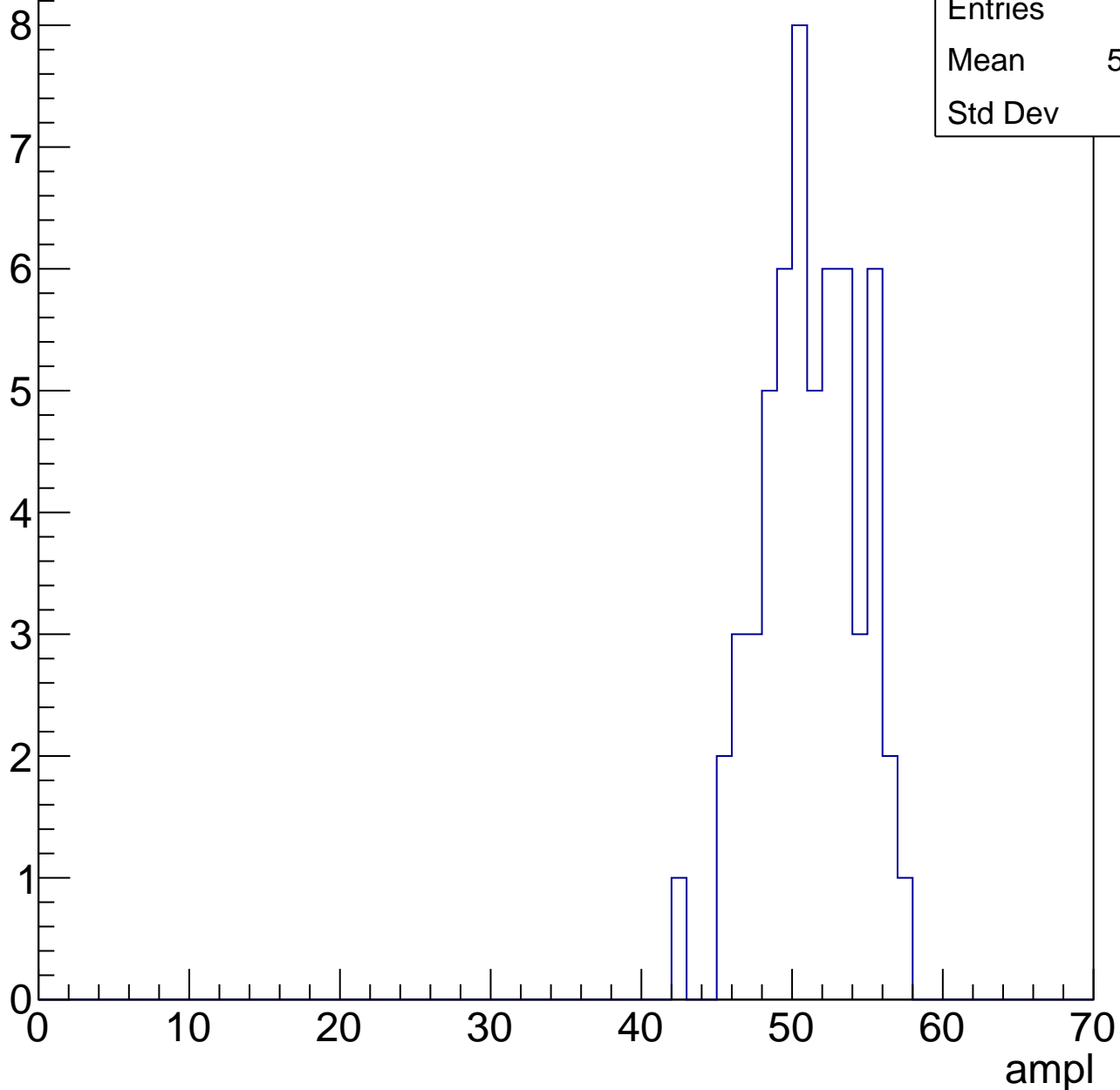


B0L001S, U18-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

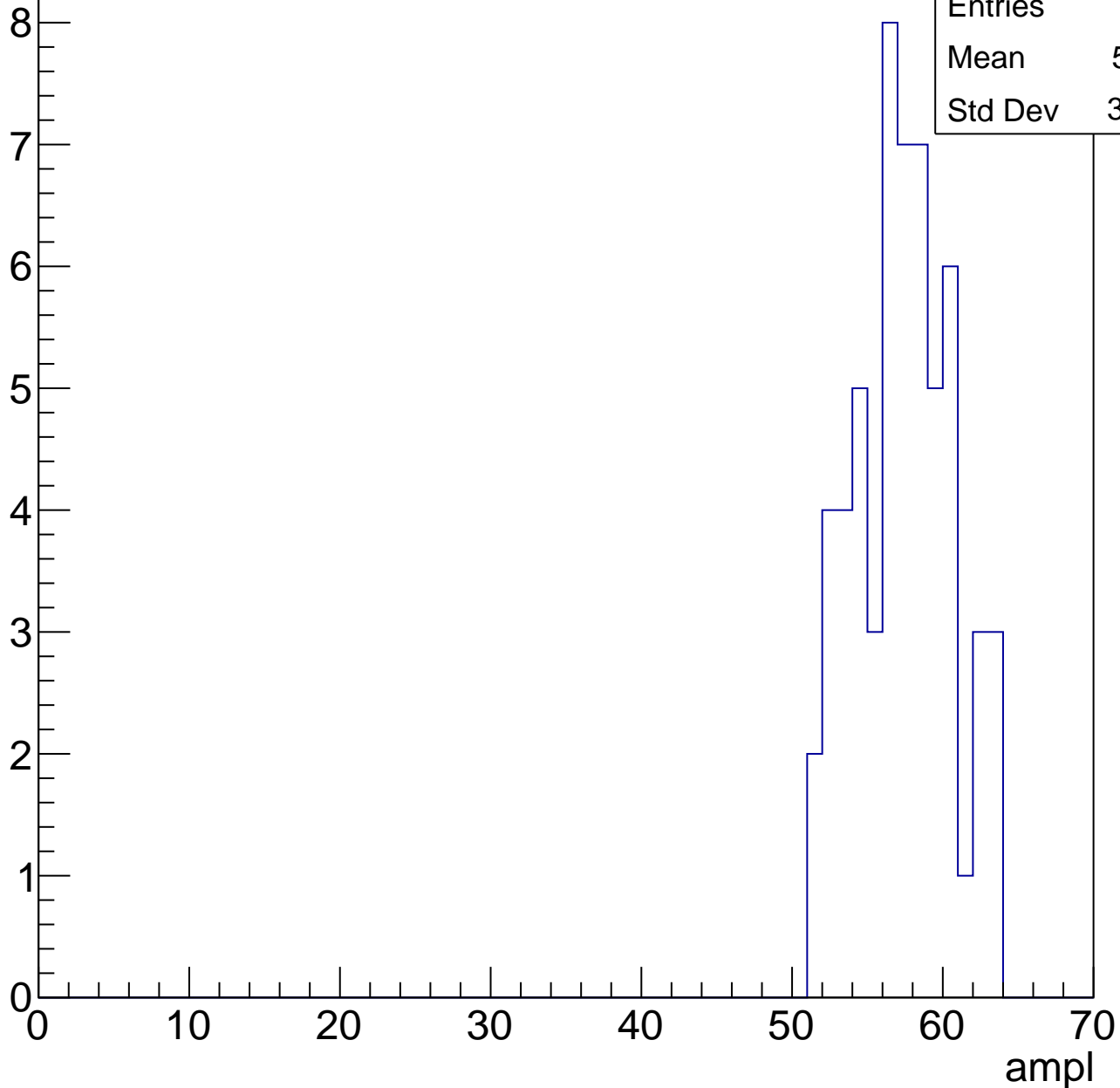
Entries	57
Mean	50.72
Std Dev	3.21



B0L001S, U18-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



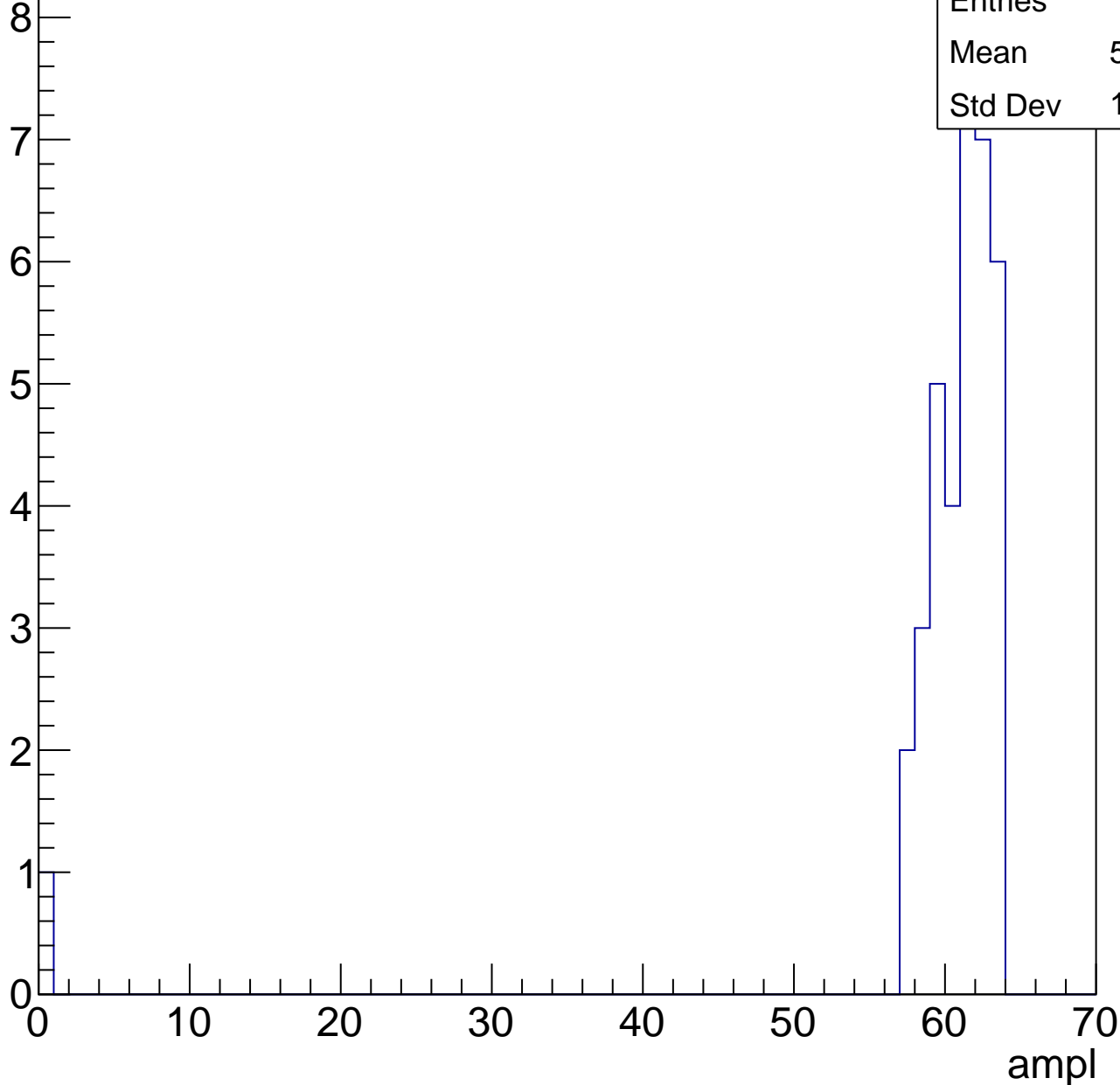
Entries	58
Mean	56.91
Std Dev	3.164

B0L001S, U18-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.97
Std Dev	10.12



B0L001S, U18-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch120, adc0

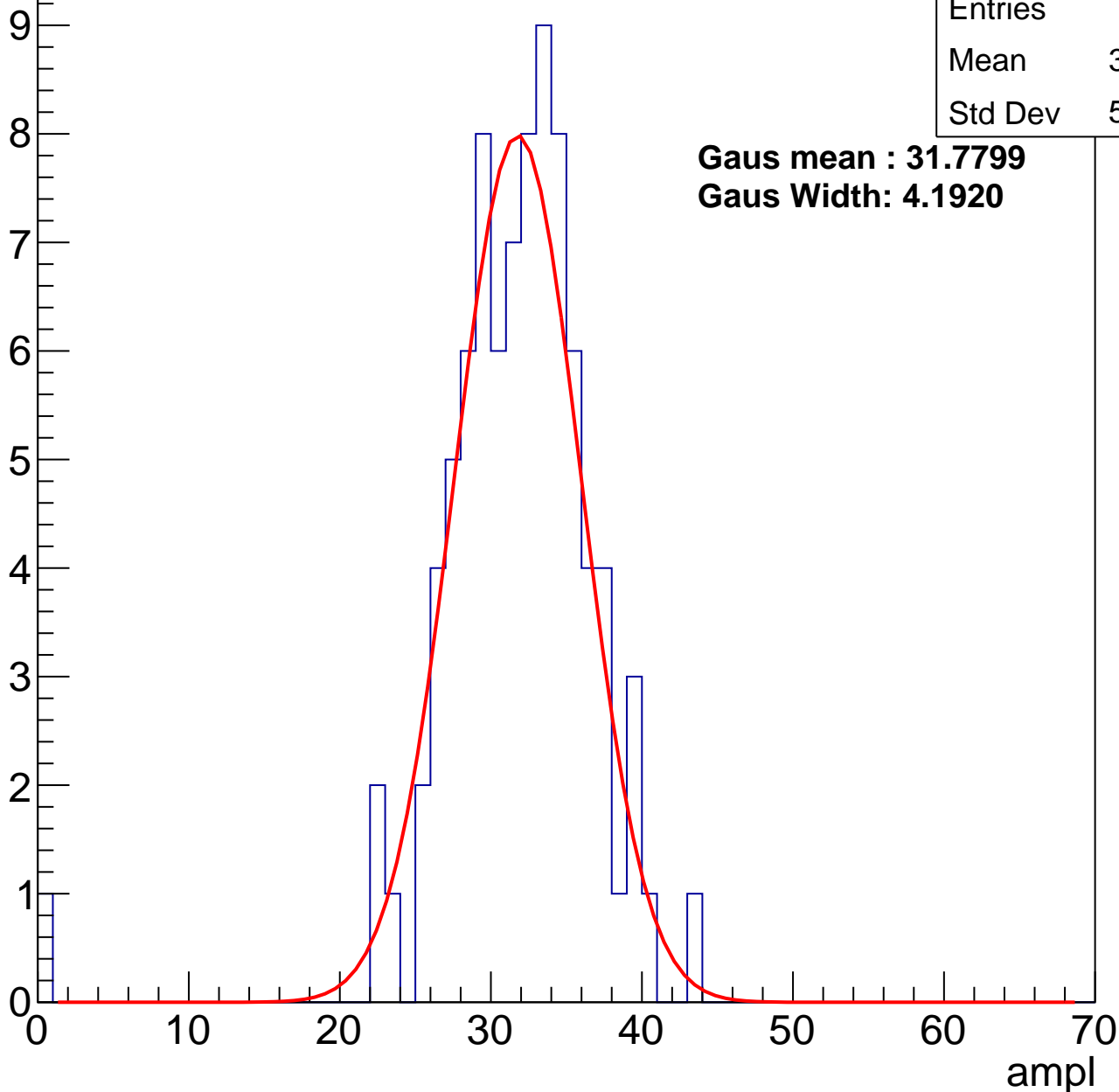
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	31.24
Std Dev	5.304

Gaus mean : 31.7799

Gaus Width: 4.1920



B0L001S, U18-ch120, adc1

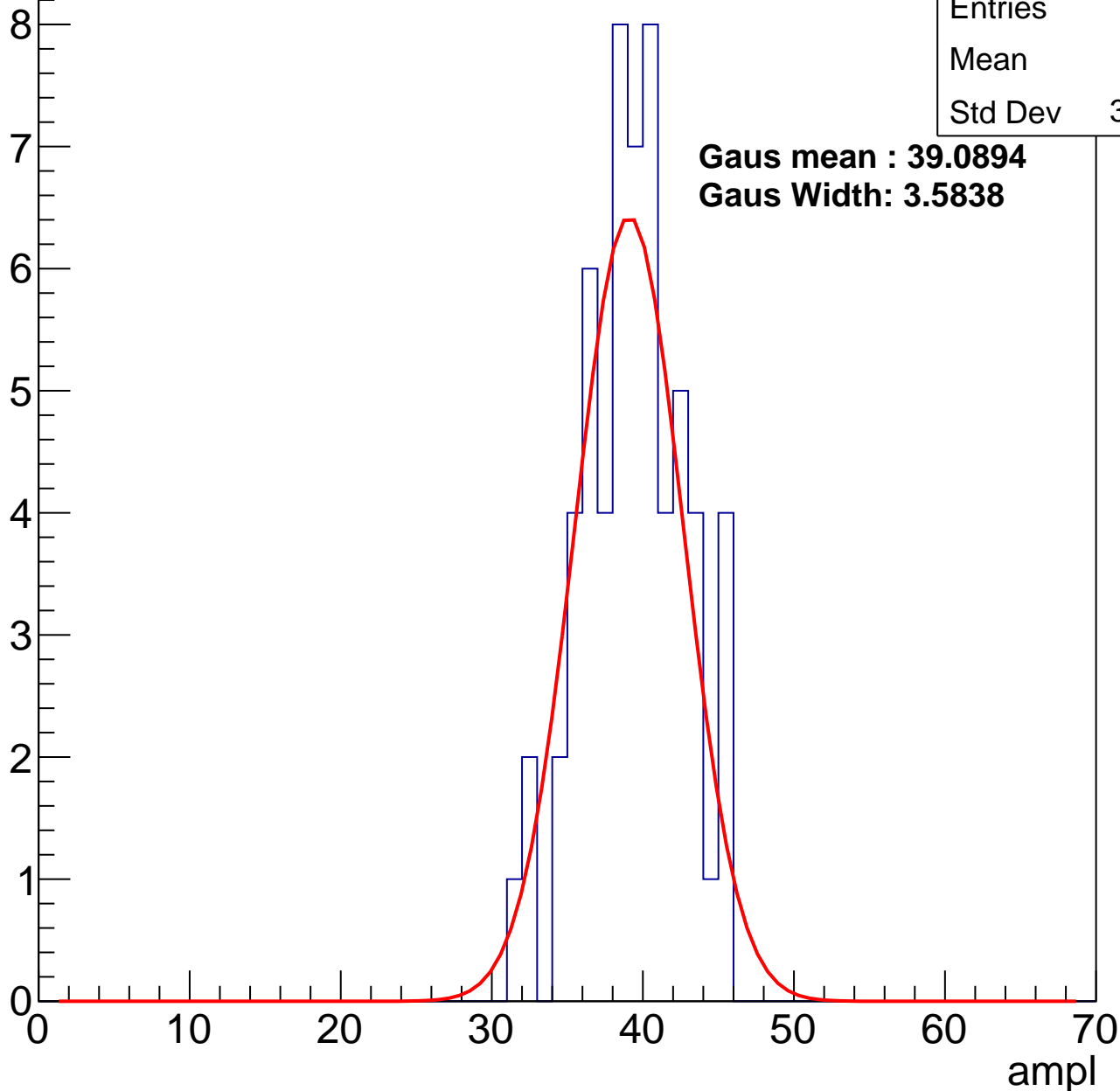
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.9
Std Dev	3.305

Gaus mean : 39.0894

Gaus Width: 3.5838



B0L001S, U18-ch120, adc2

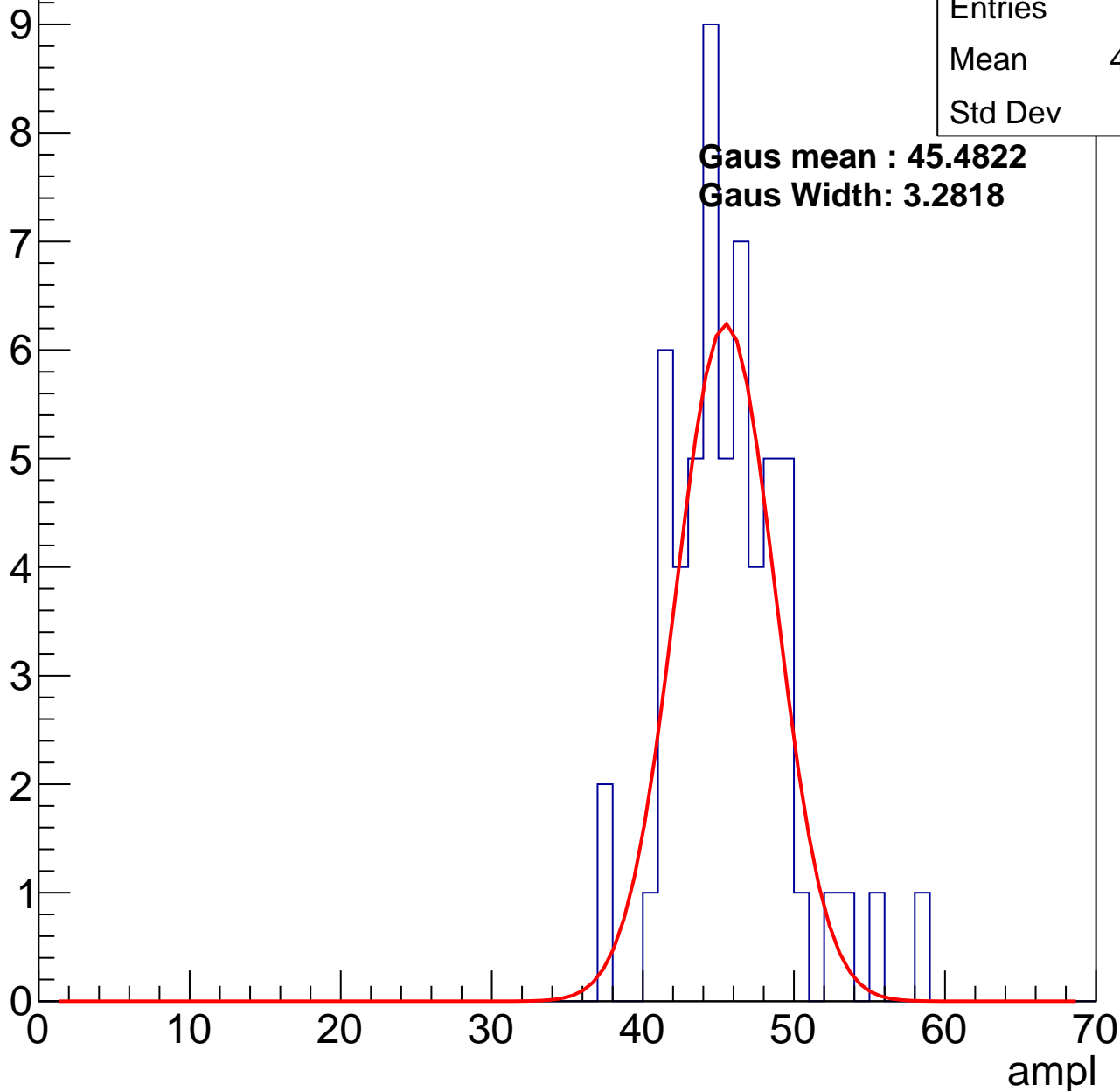
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.29
Std Dev	3.86

Gaus mean : 45.4822

Gaus Width: 3.2818

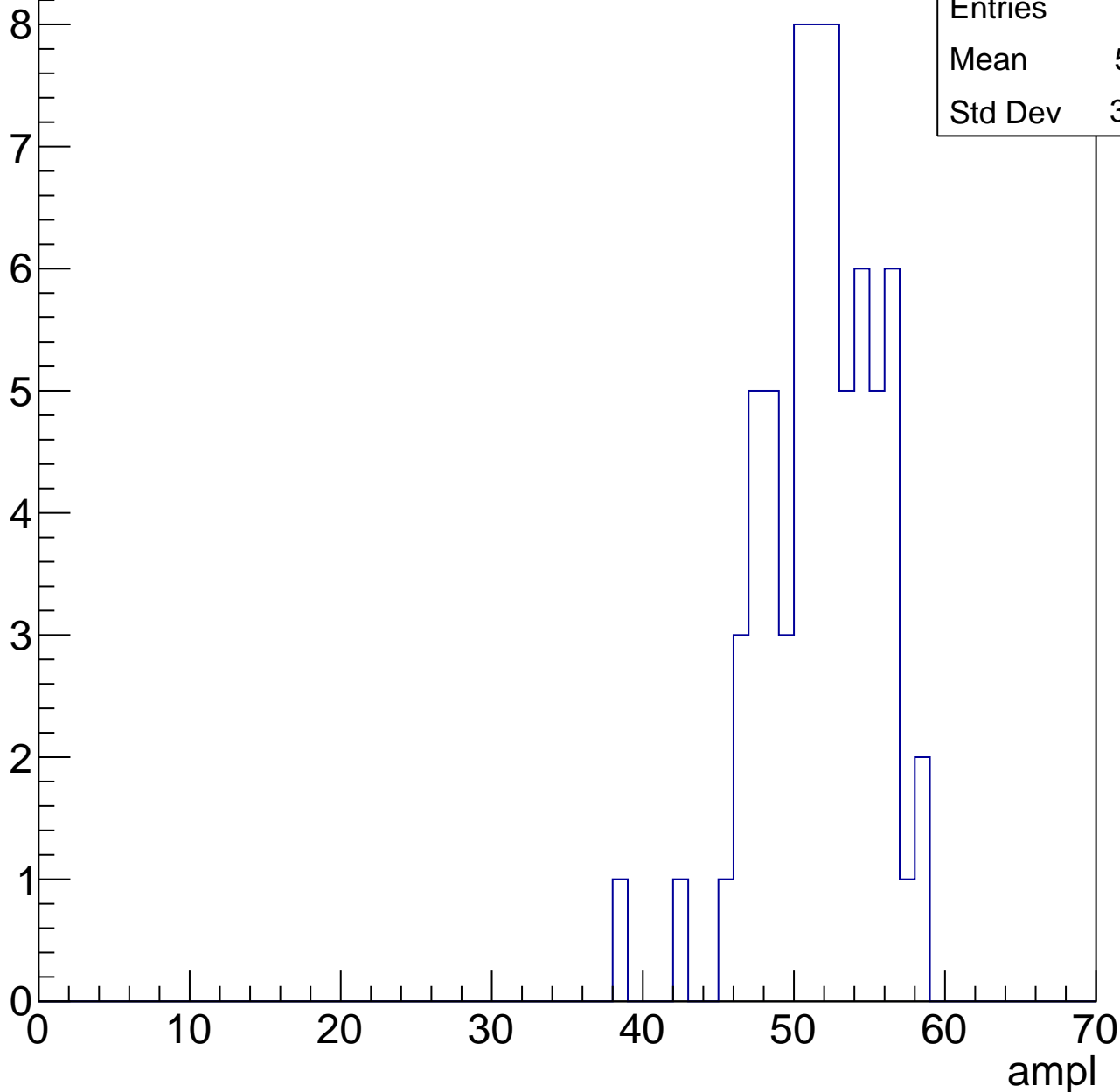


B0L001S, U18-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.21
Std Dev	3.736

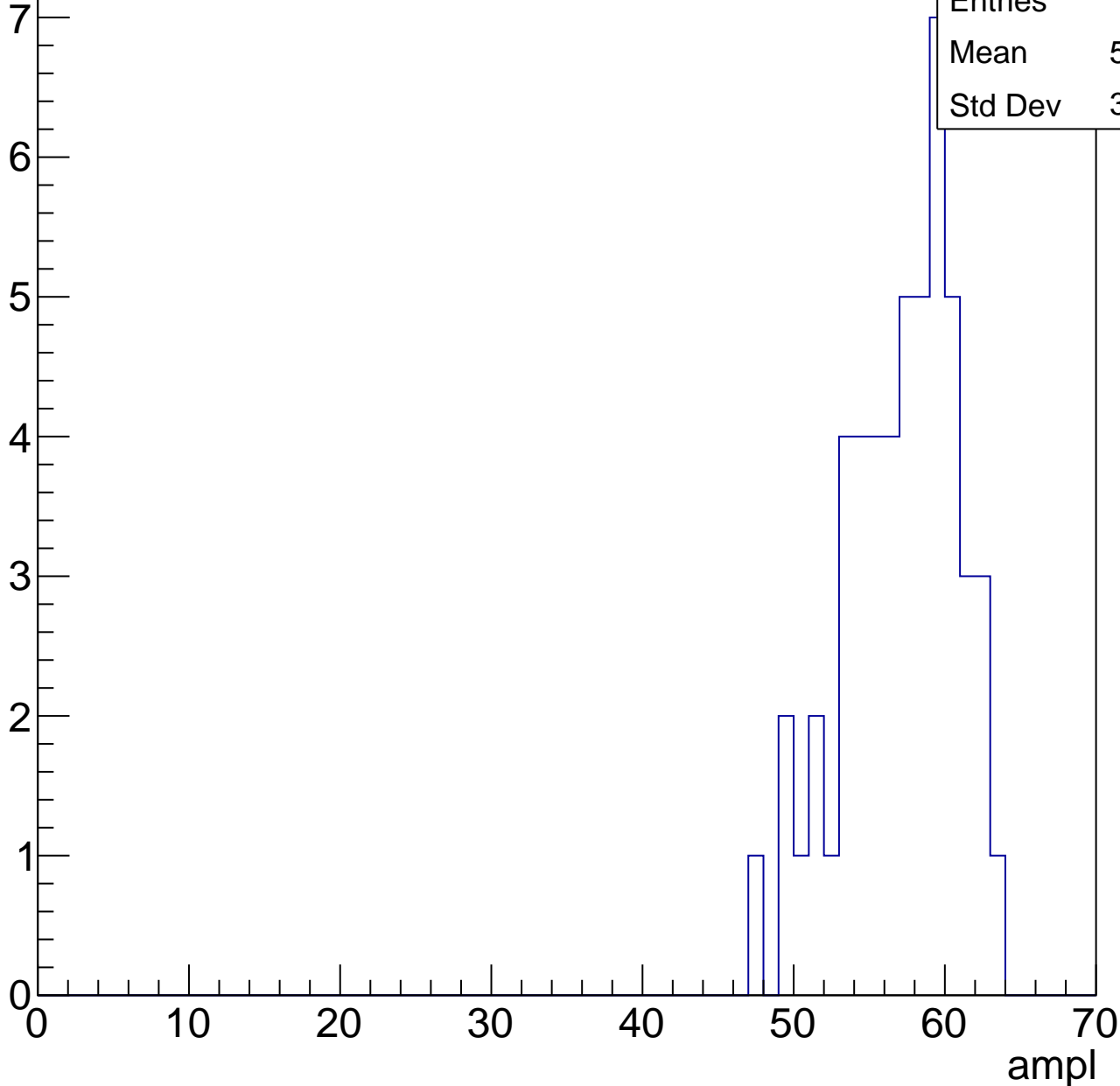


B0L001S, U18-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.56
Std Dev	3.723

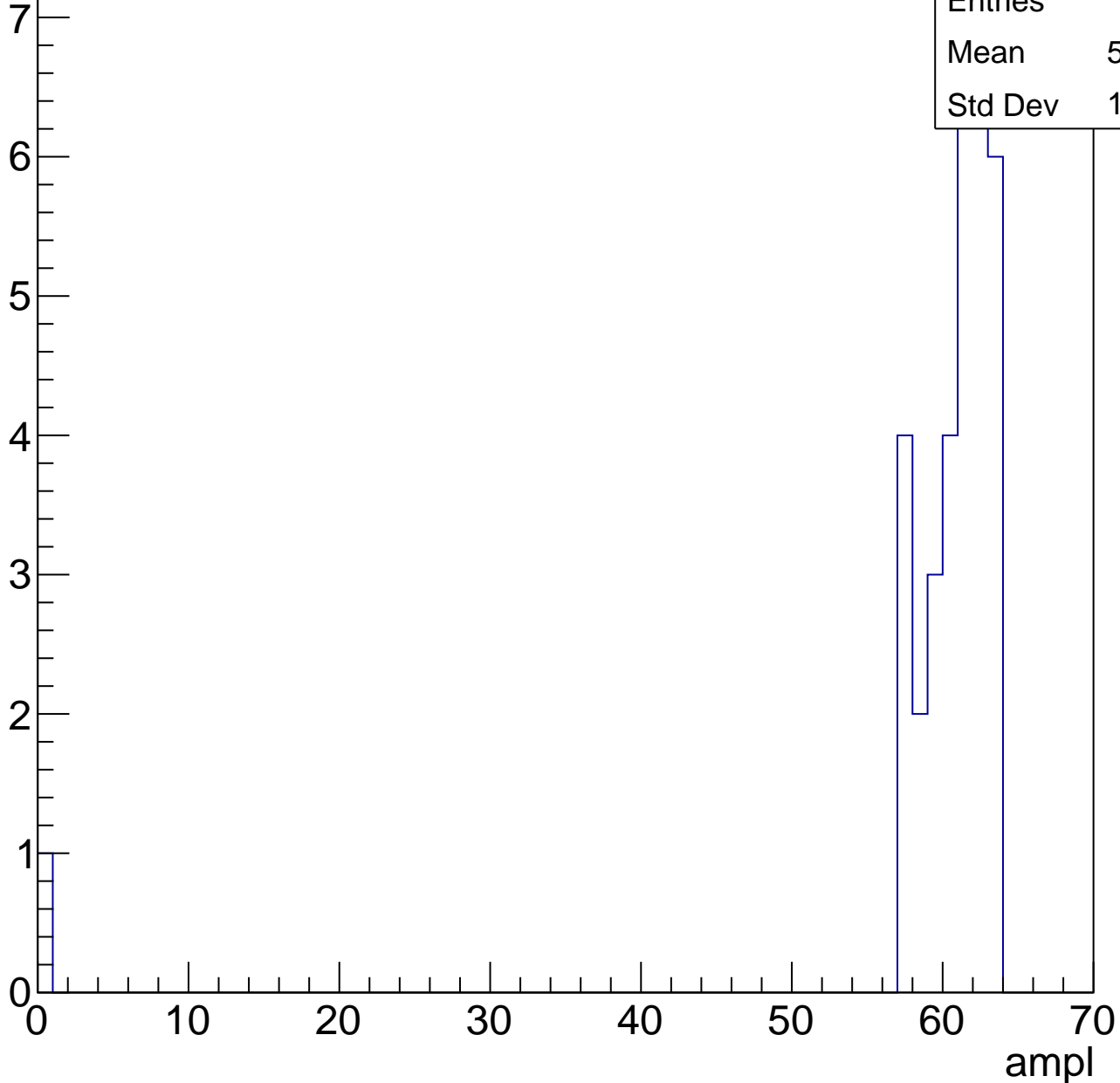


B0L001S, U18-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.82
Std Dev	10.42



B0L001S, U18-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U18-ch121, adc0

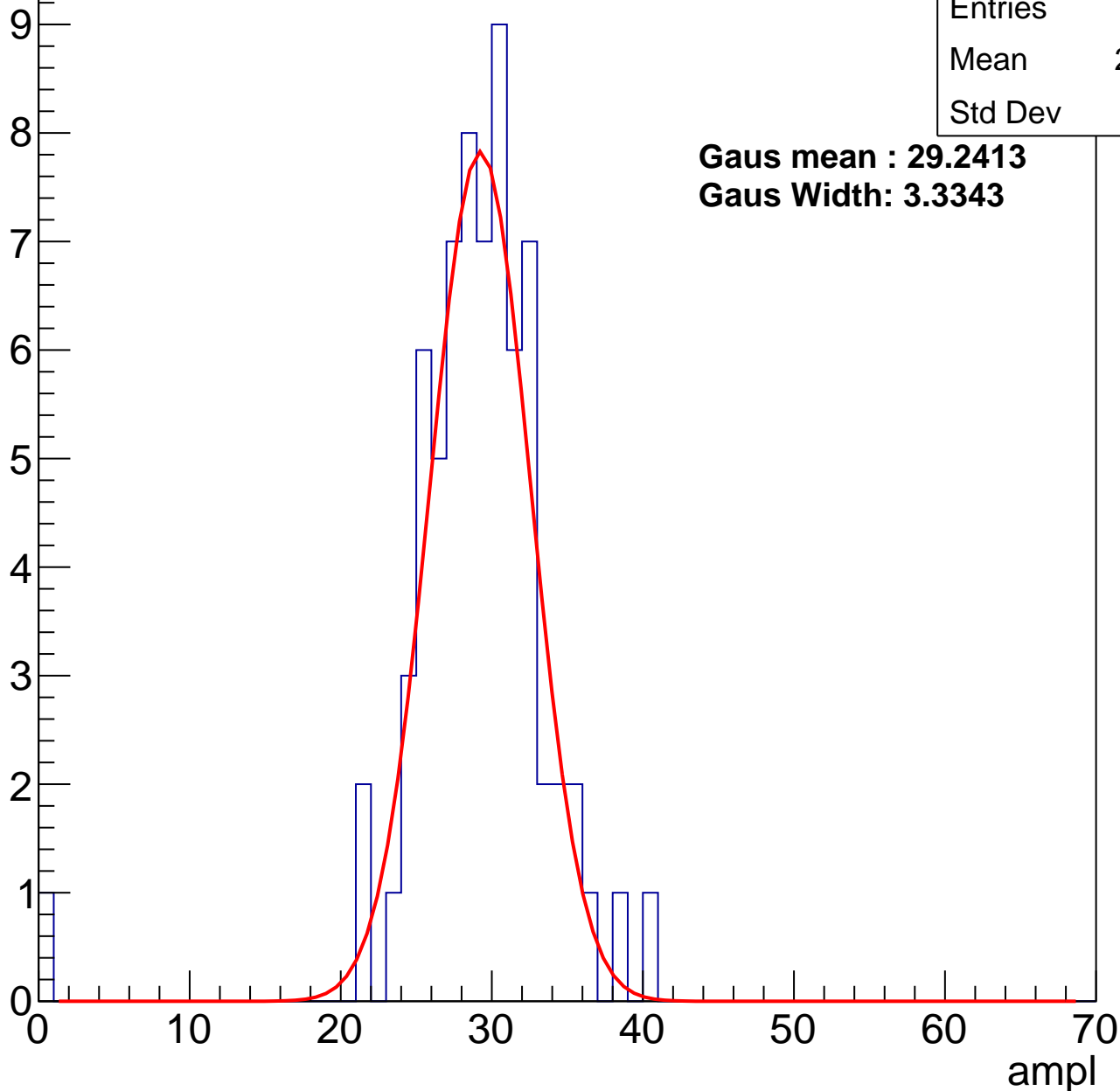
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	28.61
Std Dev	4.96

Gaus mean : 29.2413

Gaus Width: 3.3343



B0L001S, U18-ch121, adc1

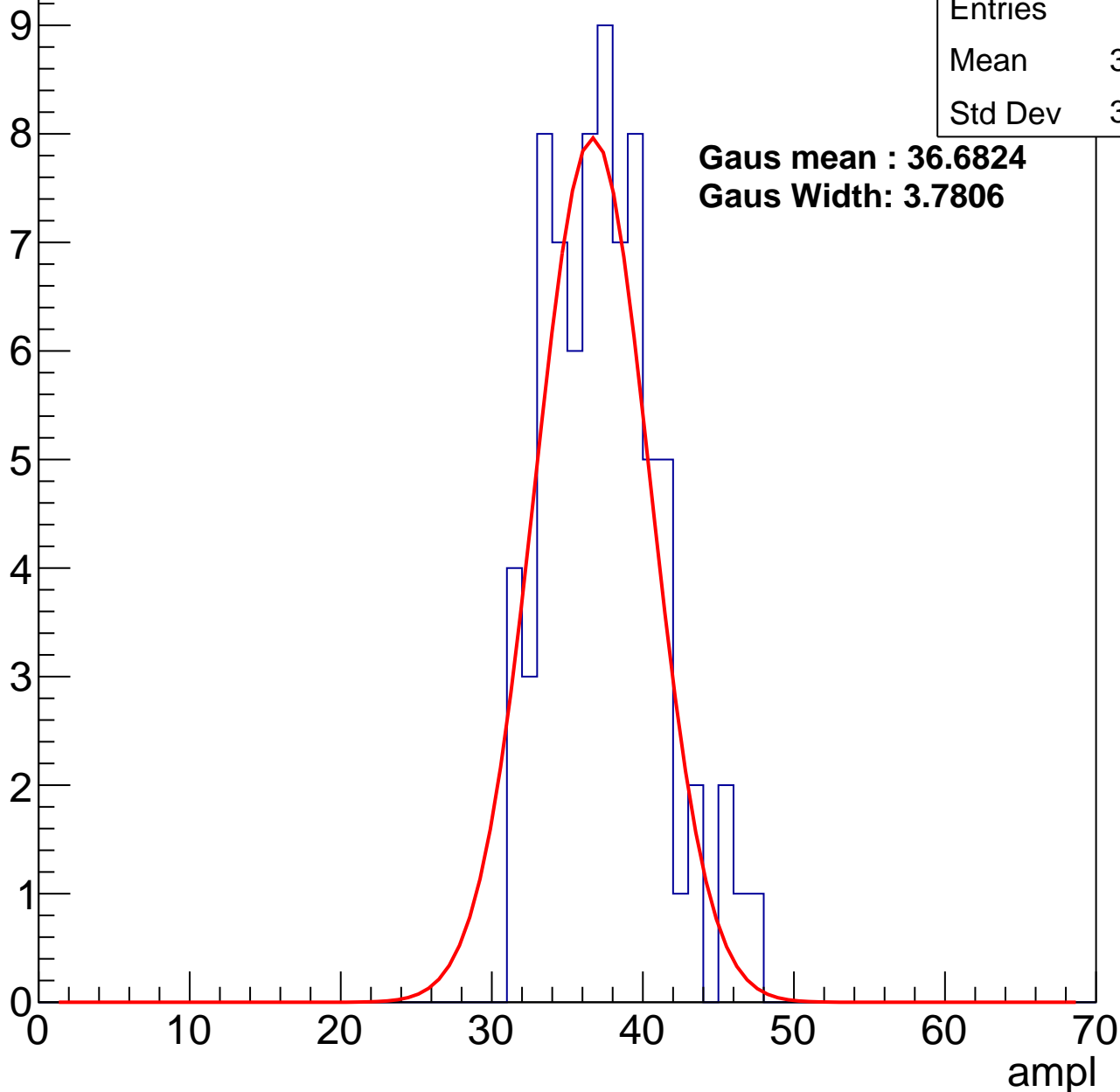
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.97
Std Dev	3.625

Gaus mean : 36.6824

Gaus Width: 3.7806



B0L001S, U18-ch121, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.47
Std Dev	3.682

Gaus mean : 44.2166

Gaus Width: 3.5457

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

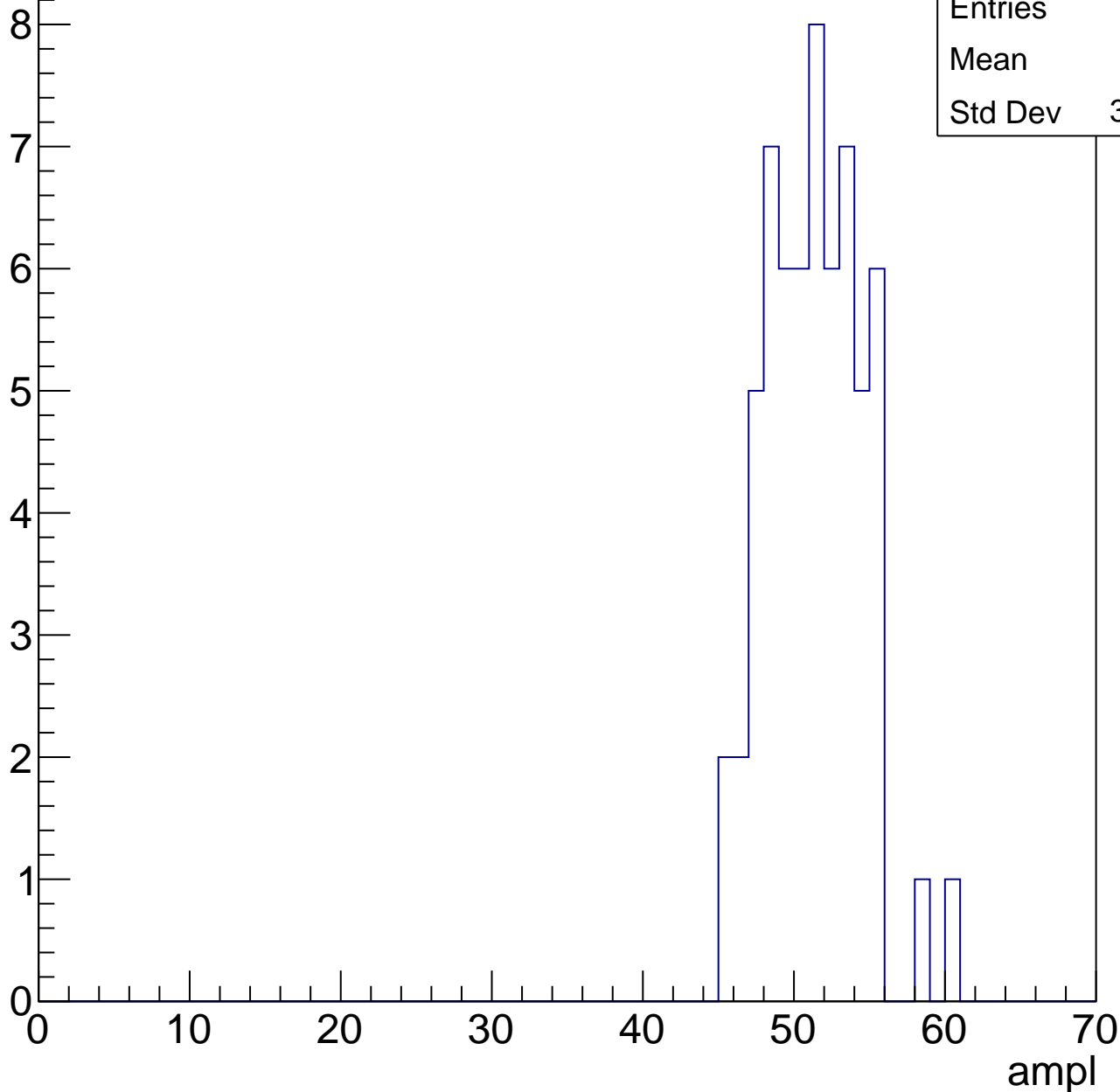
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B0L001S, U18-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.9
Std Dev	3.109

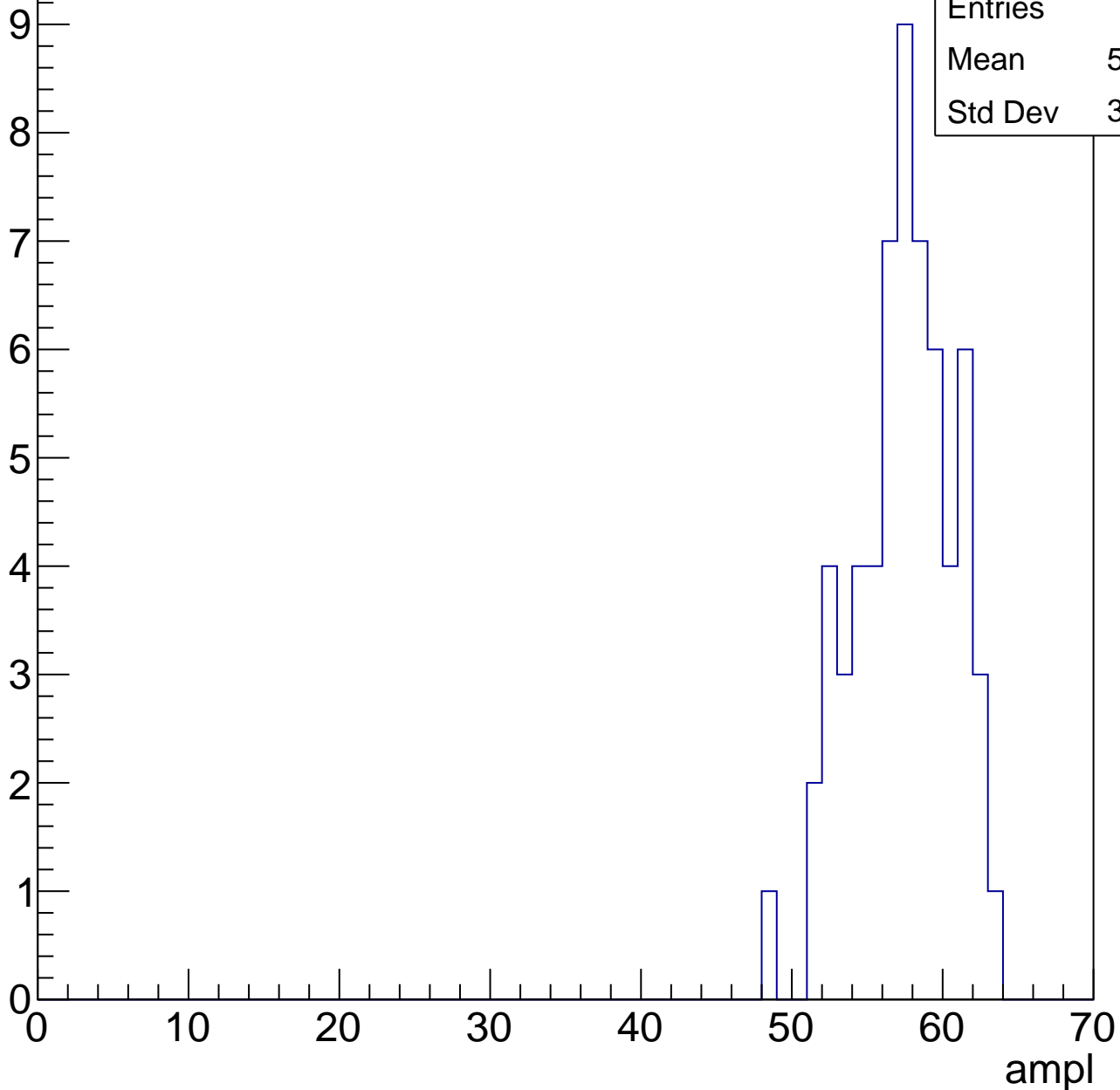


B0L001S, U18-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.93
Std Dev	3.223

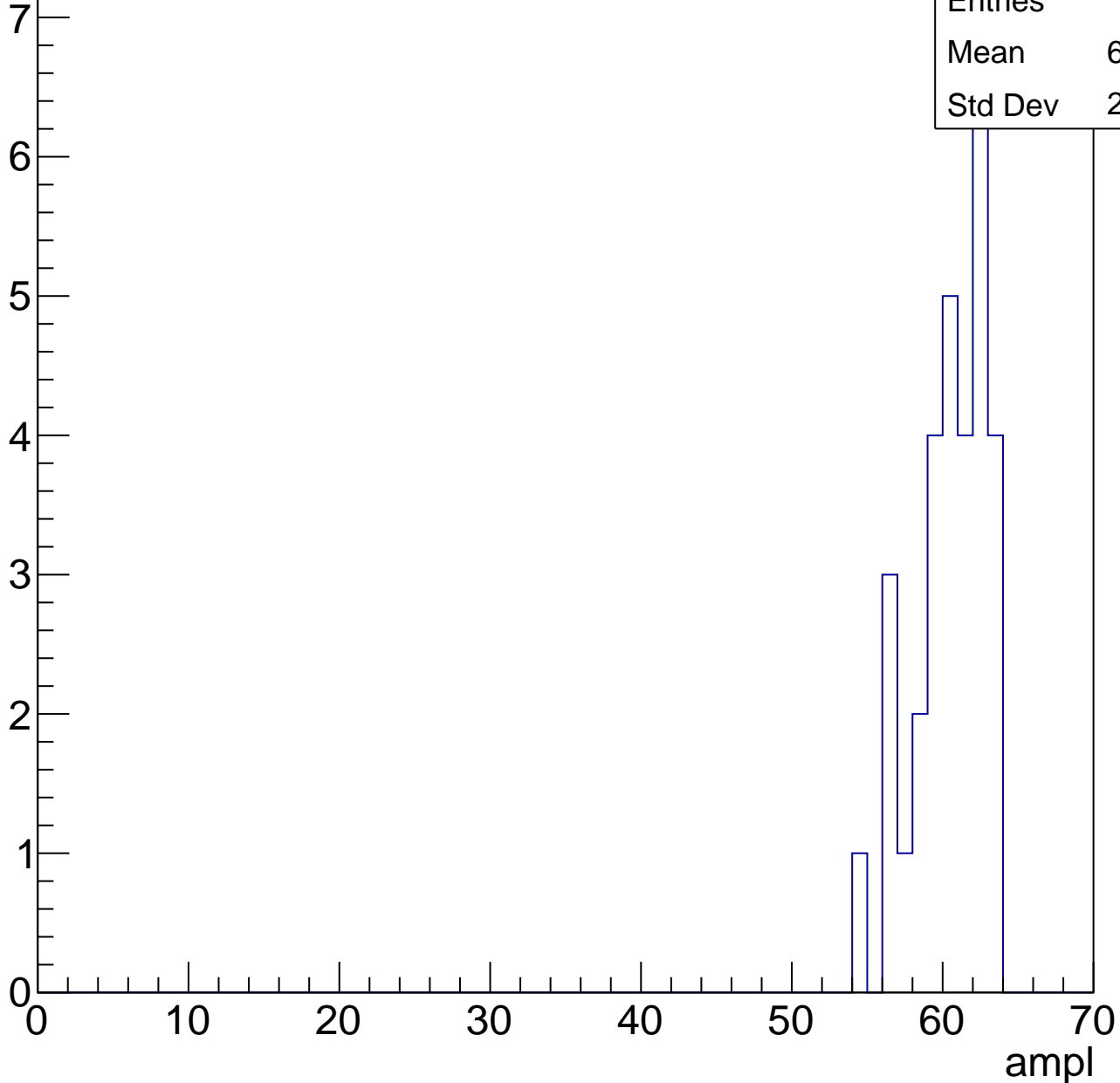


B0L001S, U18-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.03
Std Dev	2.362

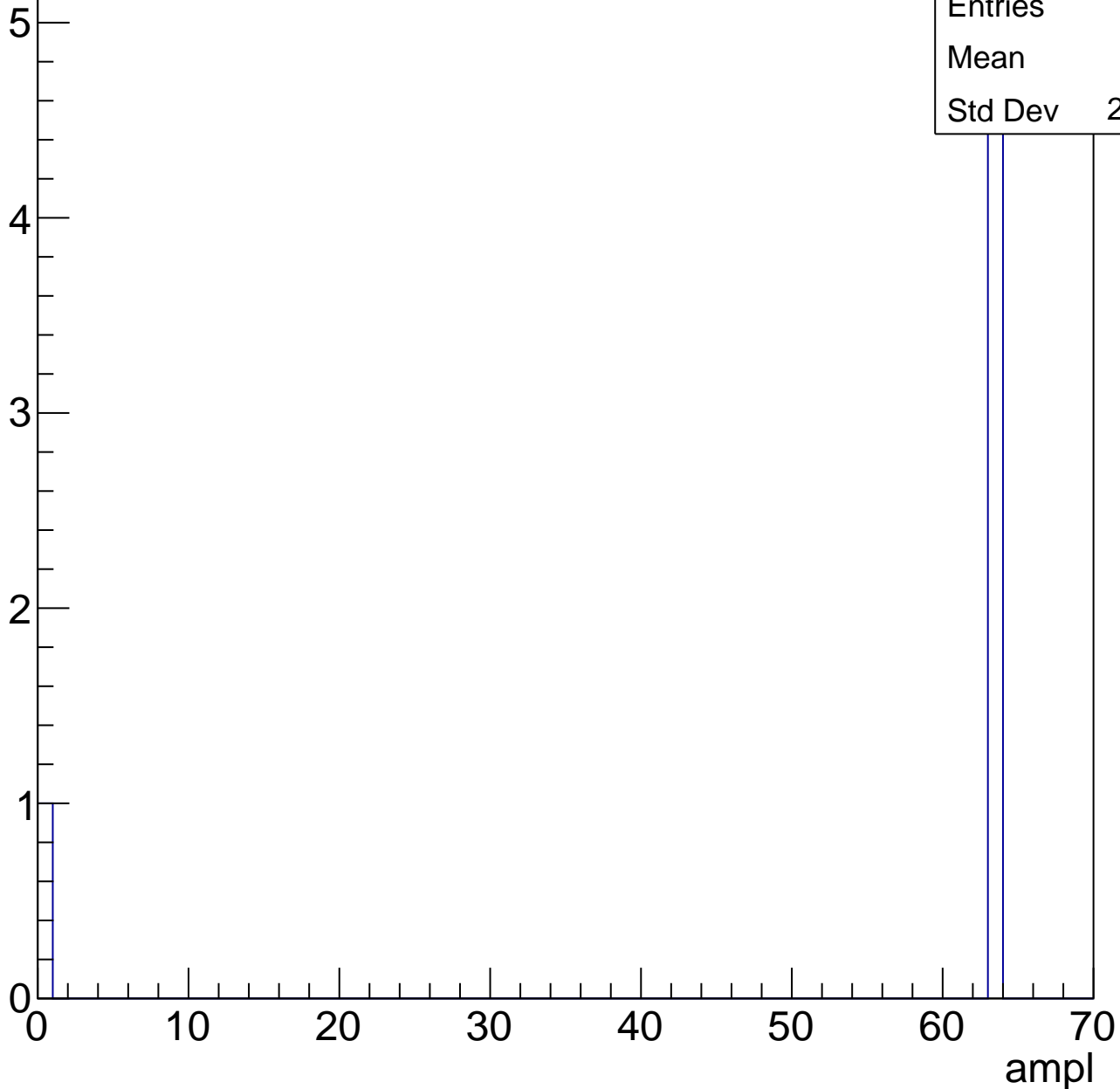


B0L001S, U18-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	6
Mean	52.5
Std Dev	23.48



B0L001S, U18-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U18-ch122, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	28.67
Std Dev	4.278

Gaus mean : 29.8082

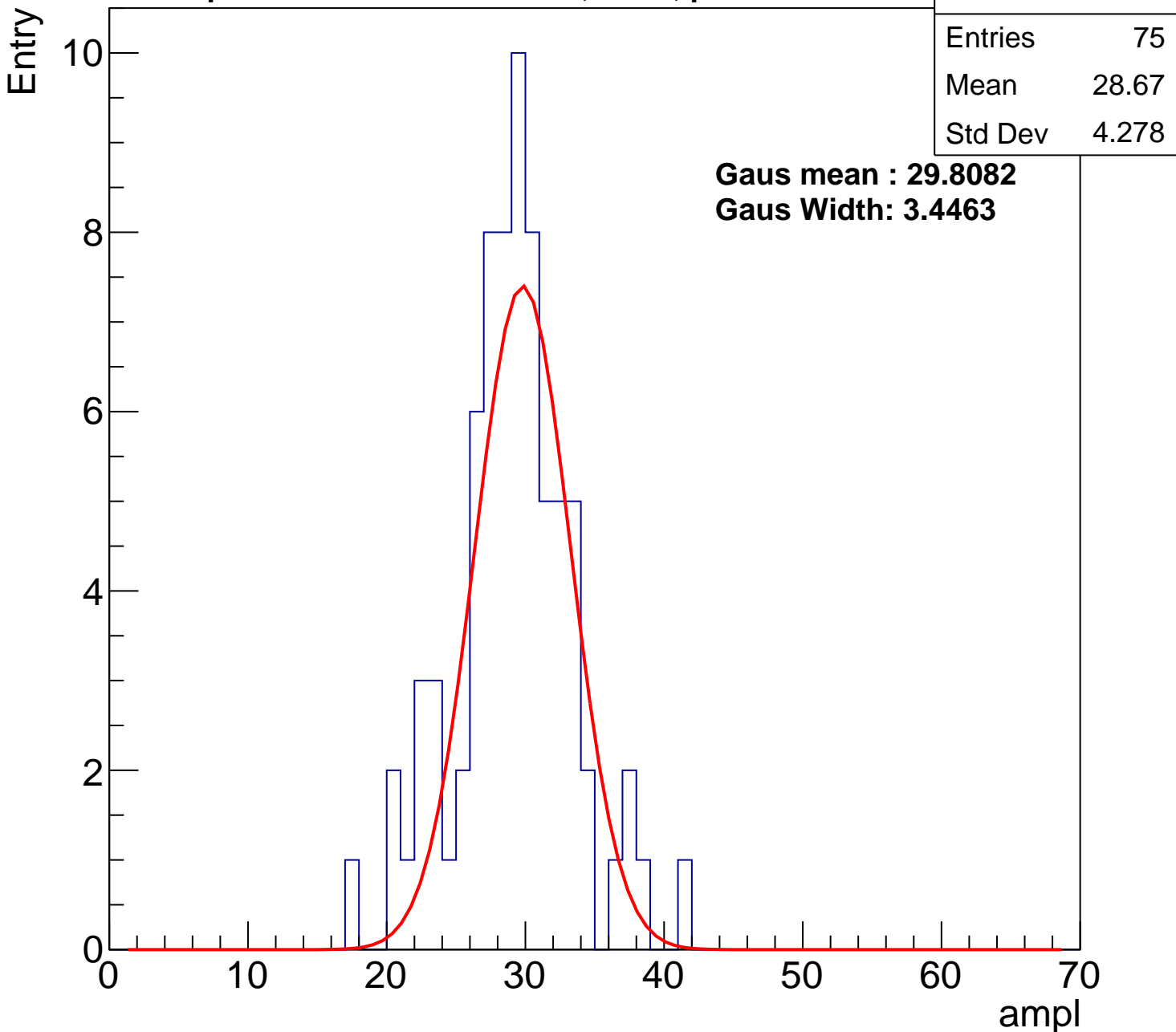
Gaus Width: 3.4463

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch122, adc1

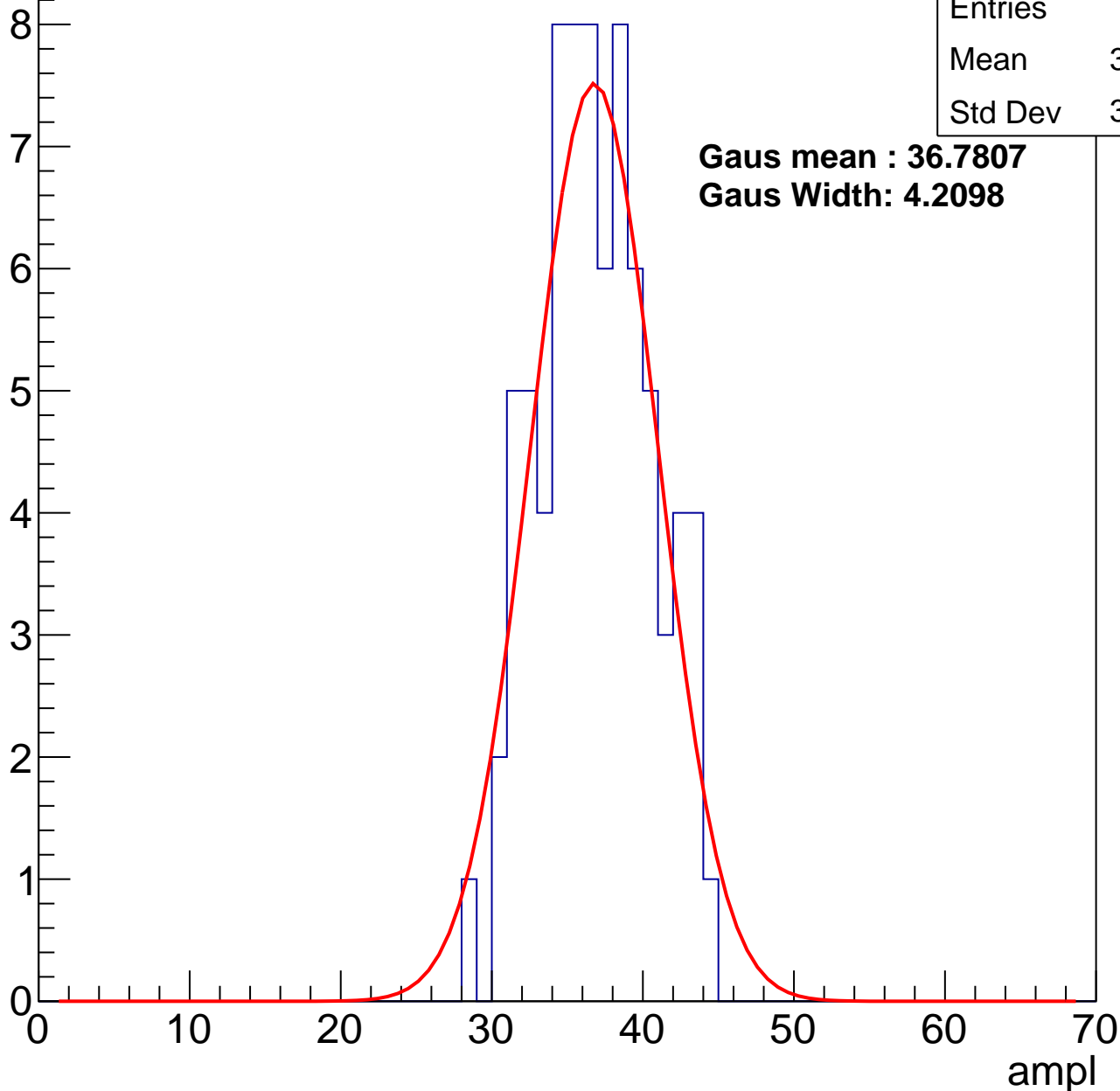
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.44
Std Dev	3.675

Gaus mean : 36.7807

Gaus Width: 4.2098



B0L001S, U18-ch122, adc2

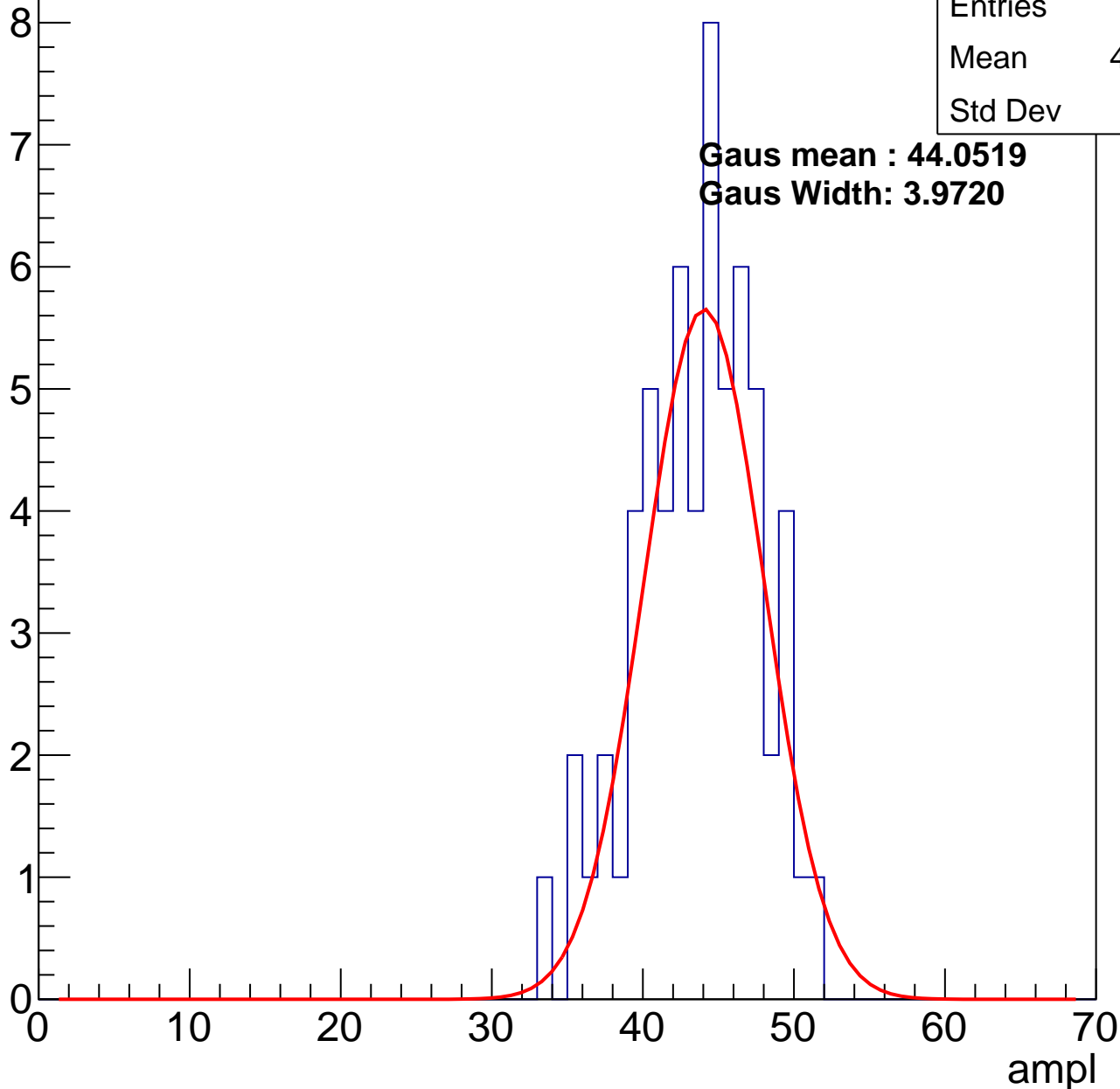
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.16
Std Dev	3.96

Gaus mean : 44.0519

Gaus Width: 3.9720

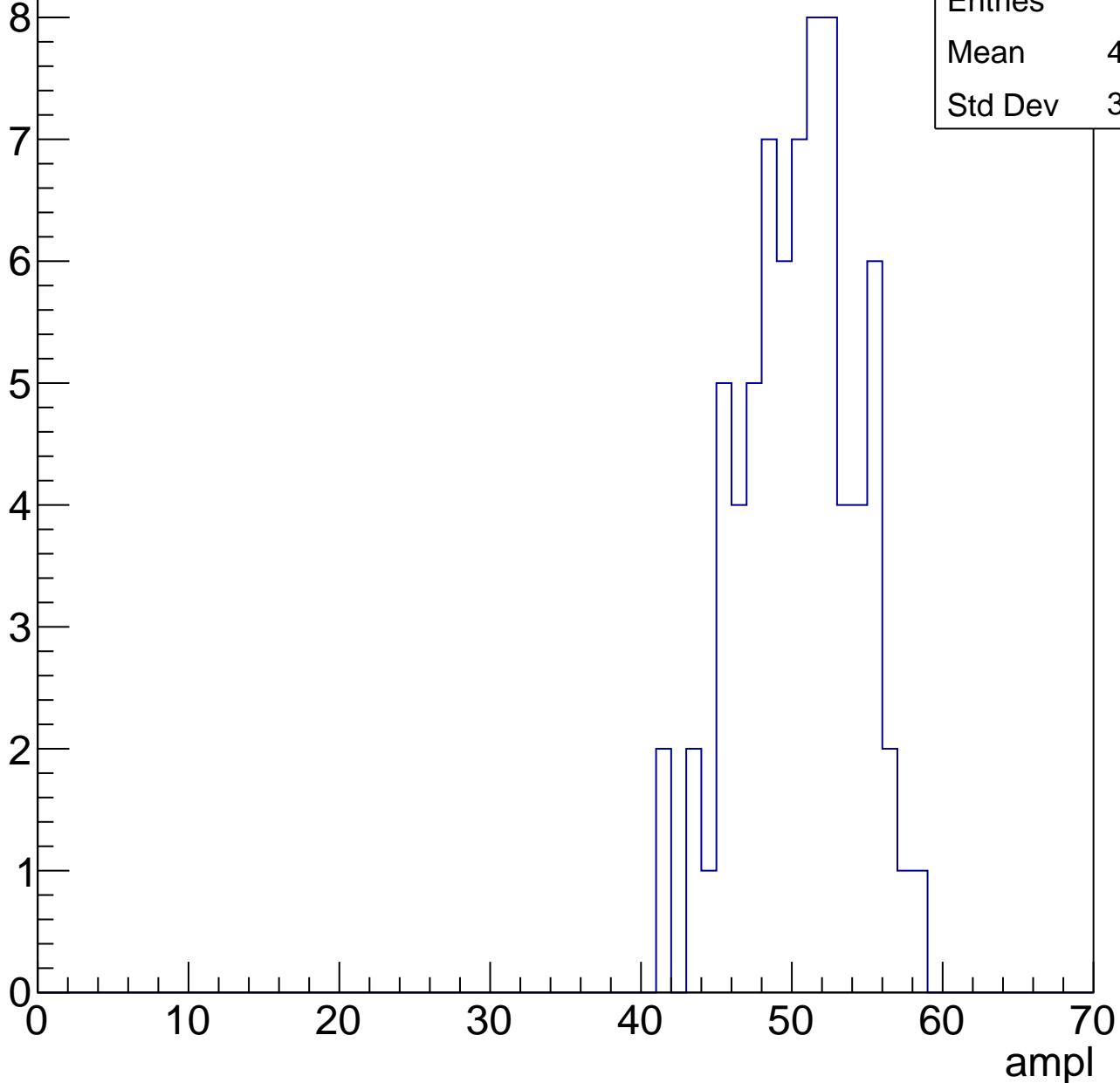


B0L001S, U18-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	49.93
Std Dev	3.772

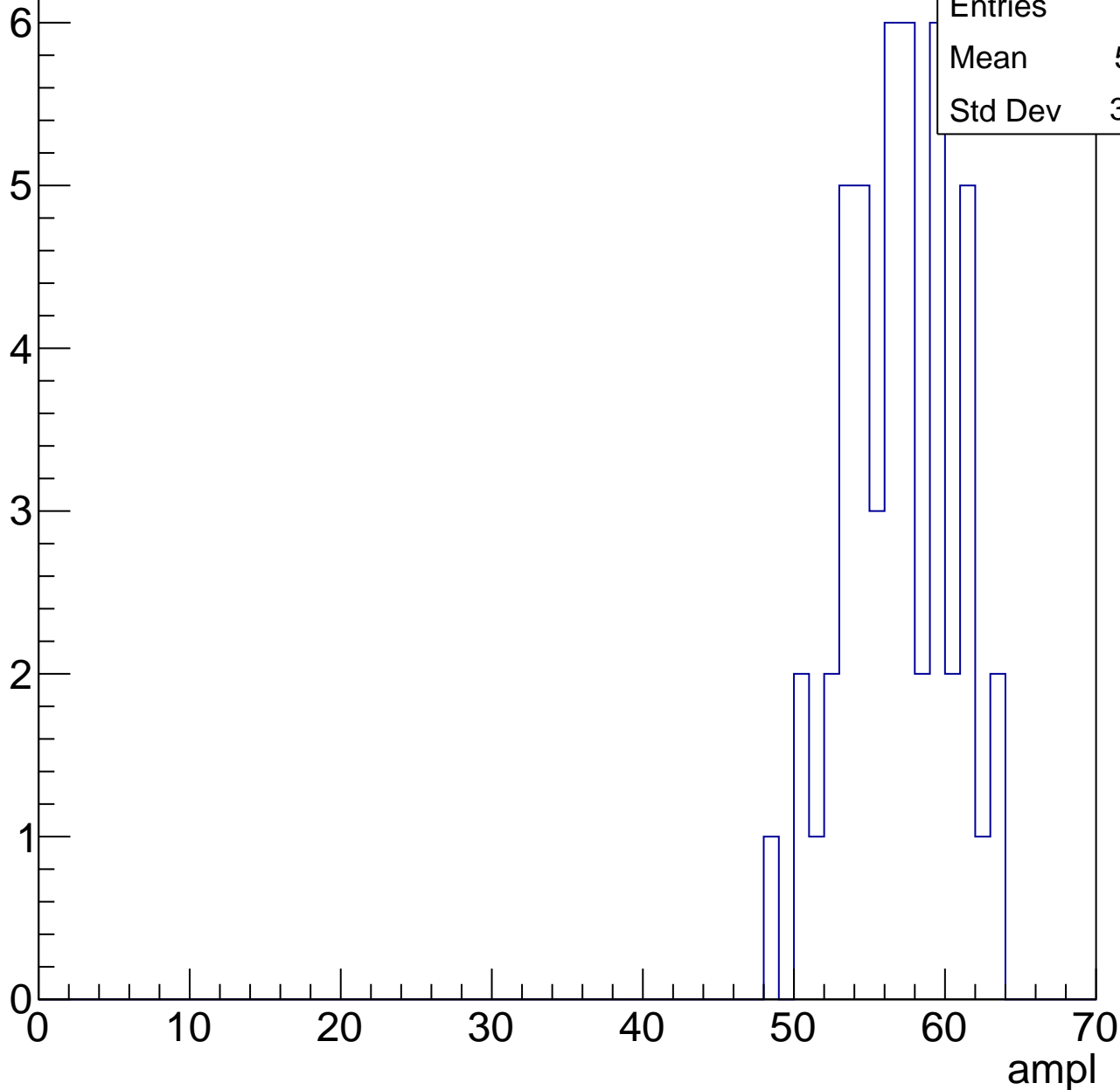


B0L001S, U18-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

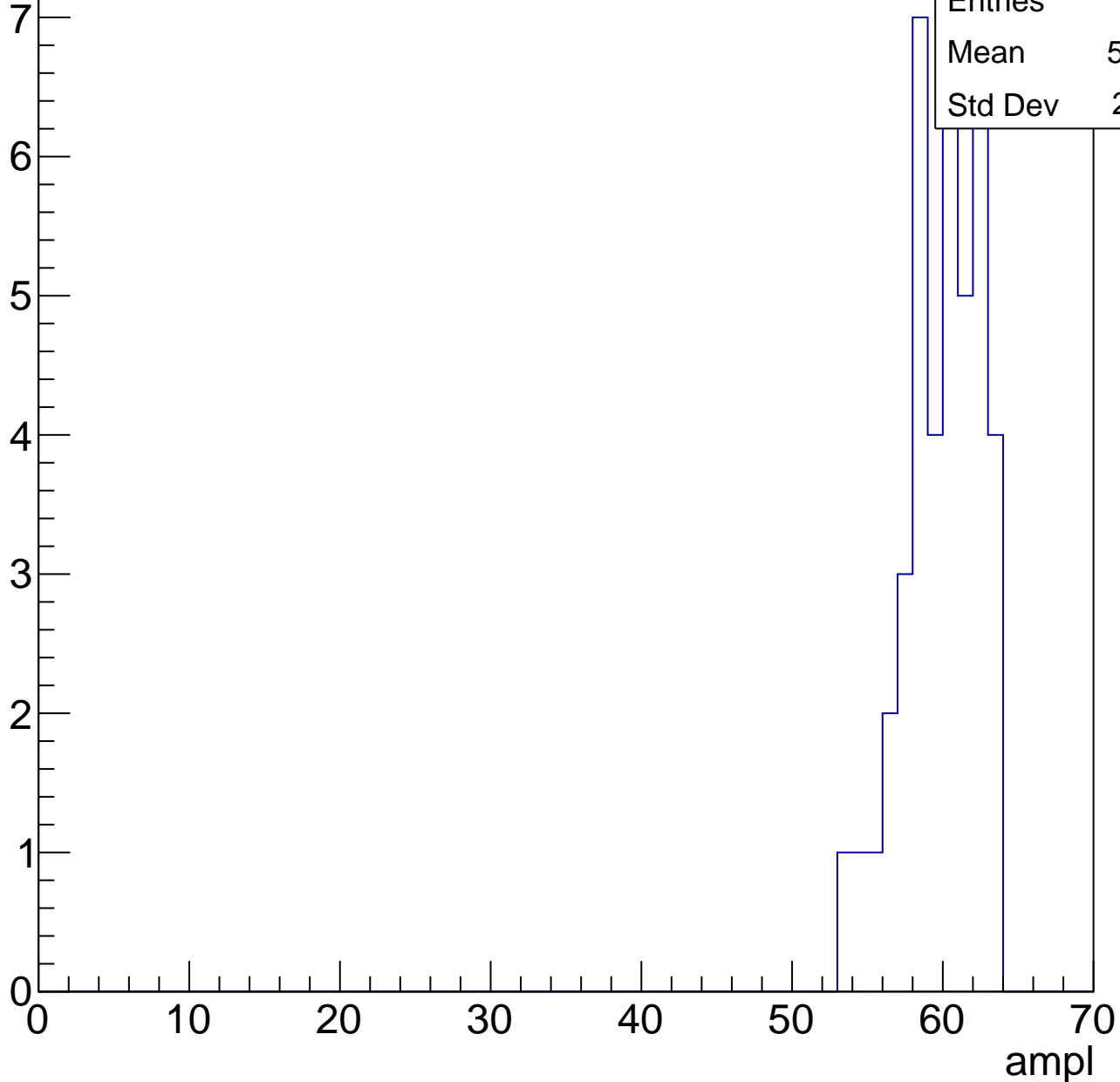
Entries	49
Mean	56.41
Std Dev	3.534



B0L001S, U18-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

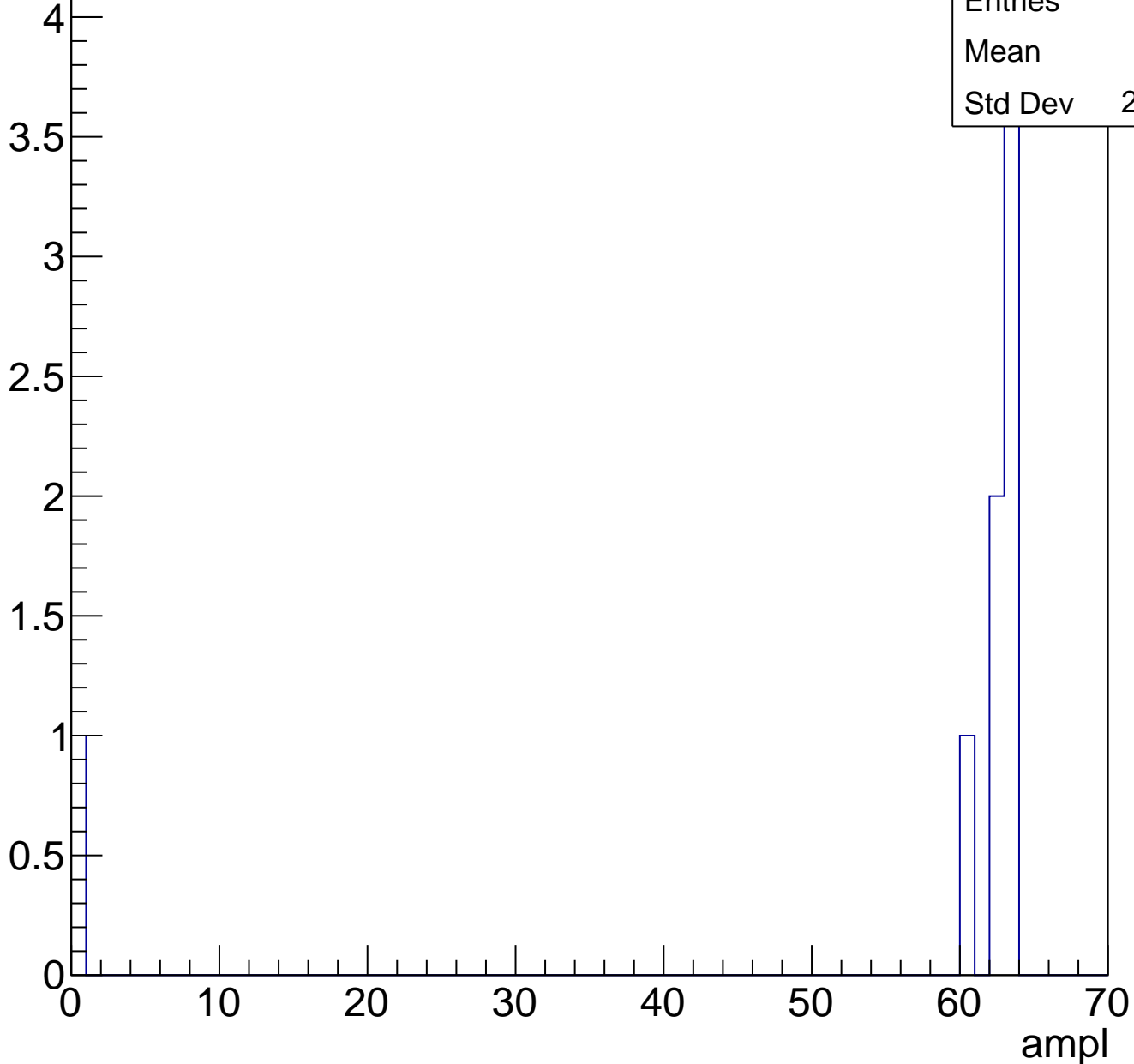
Entry



B0L001S, U18-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U18-ch123, adc0

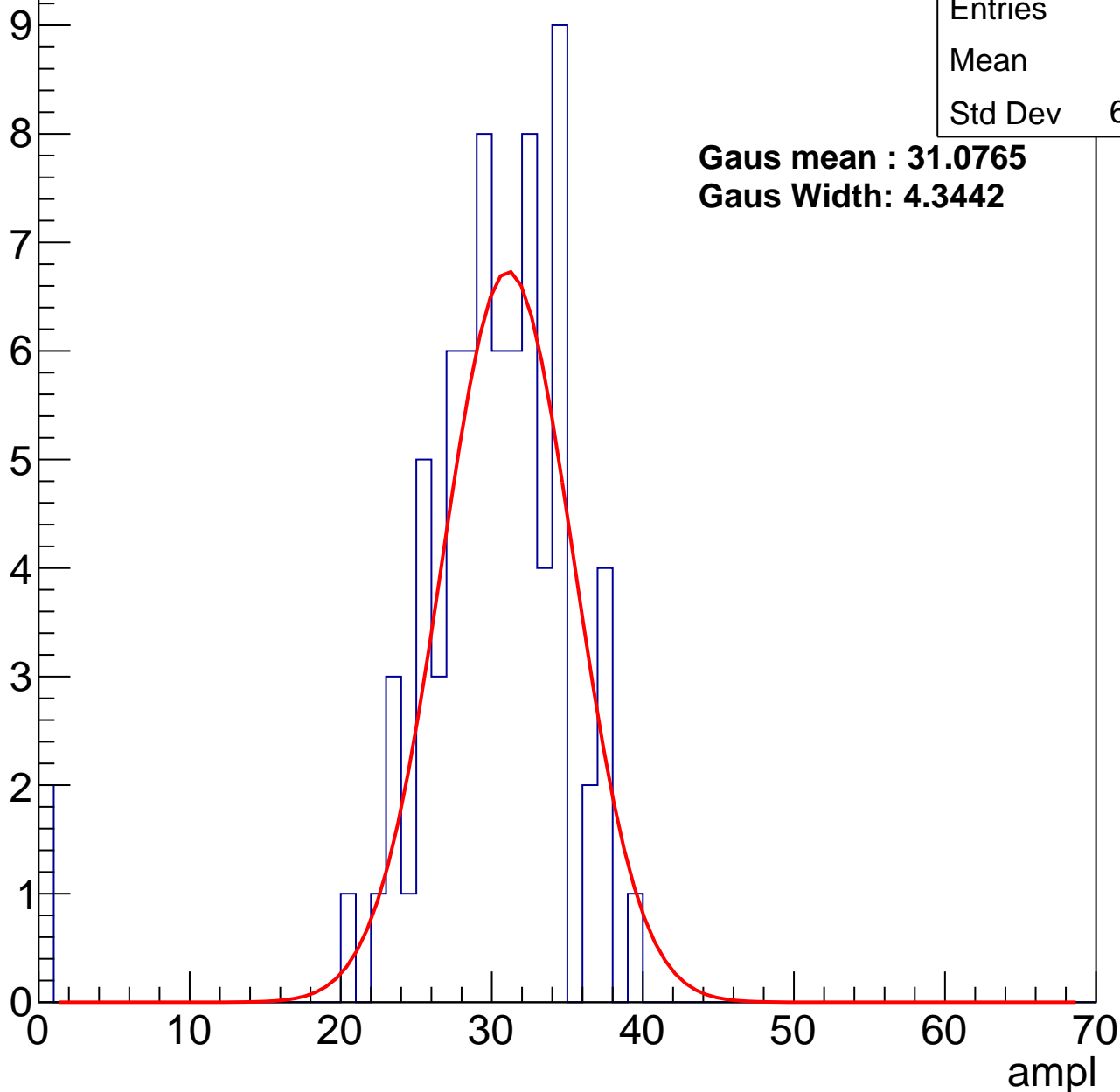
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	29.2
Std Dev	6.203

Gaus mean : 31.0765

Gaus Width: 4.3442



B0L001S, U18-ch123, adc1

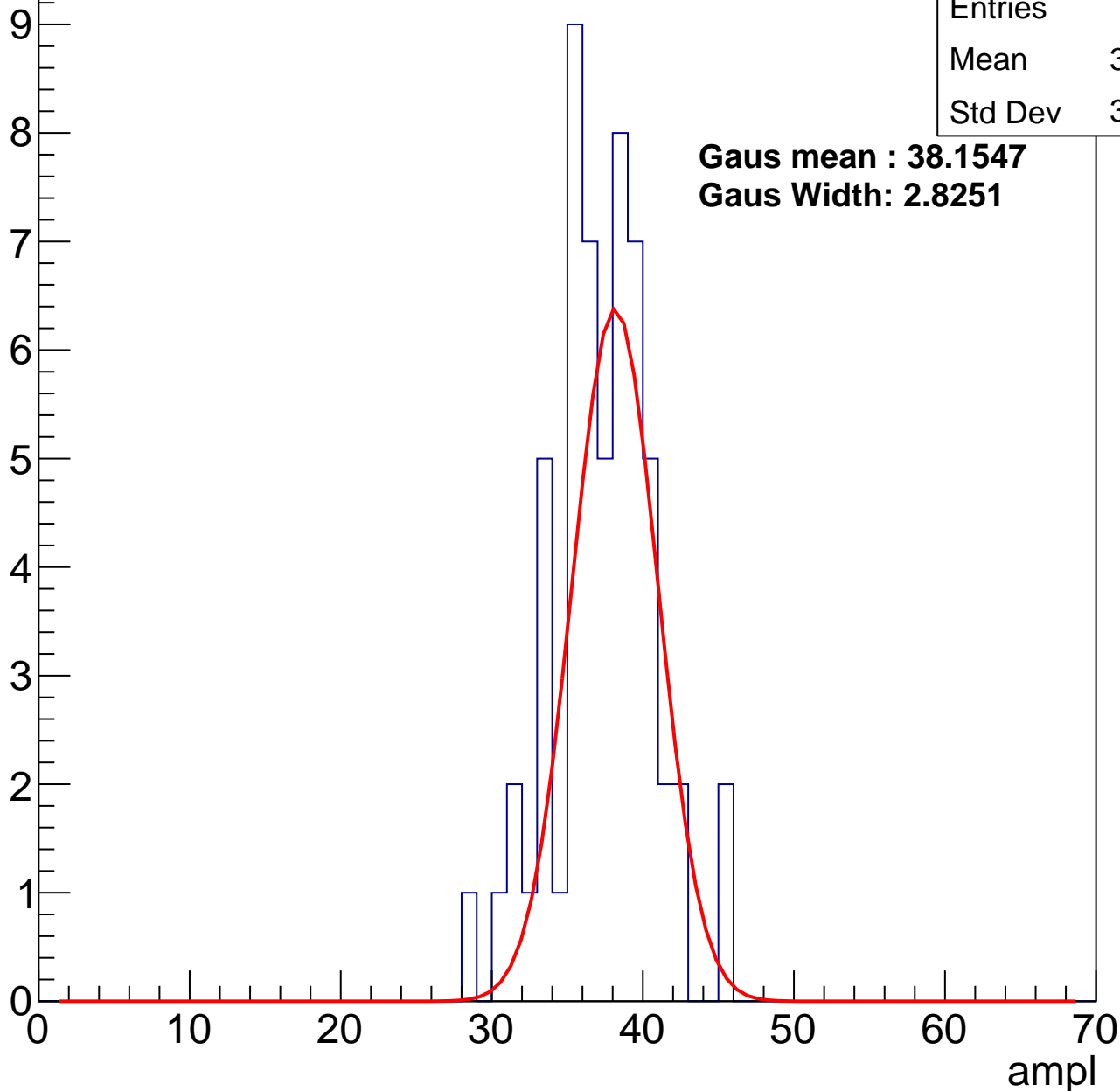
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	36.83
Std Dev	3.343

Gaus mean : 38.1547

Gaus Width: 2.8251



B0L001S, U18-ch123, adc2

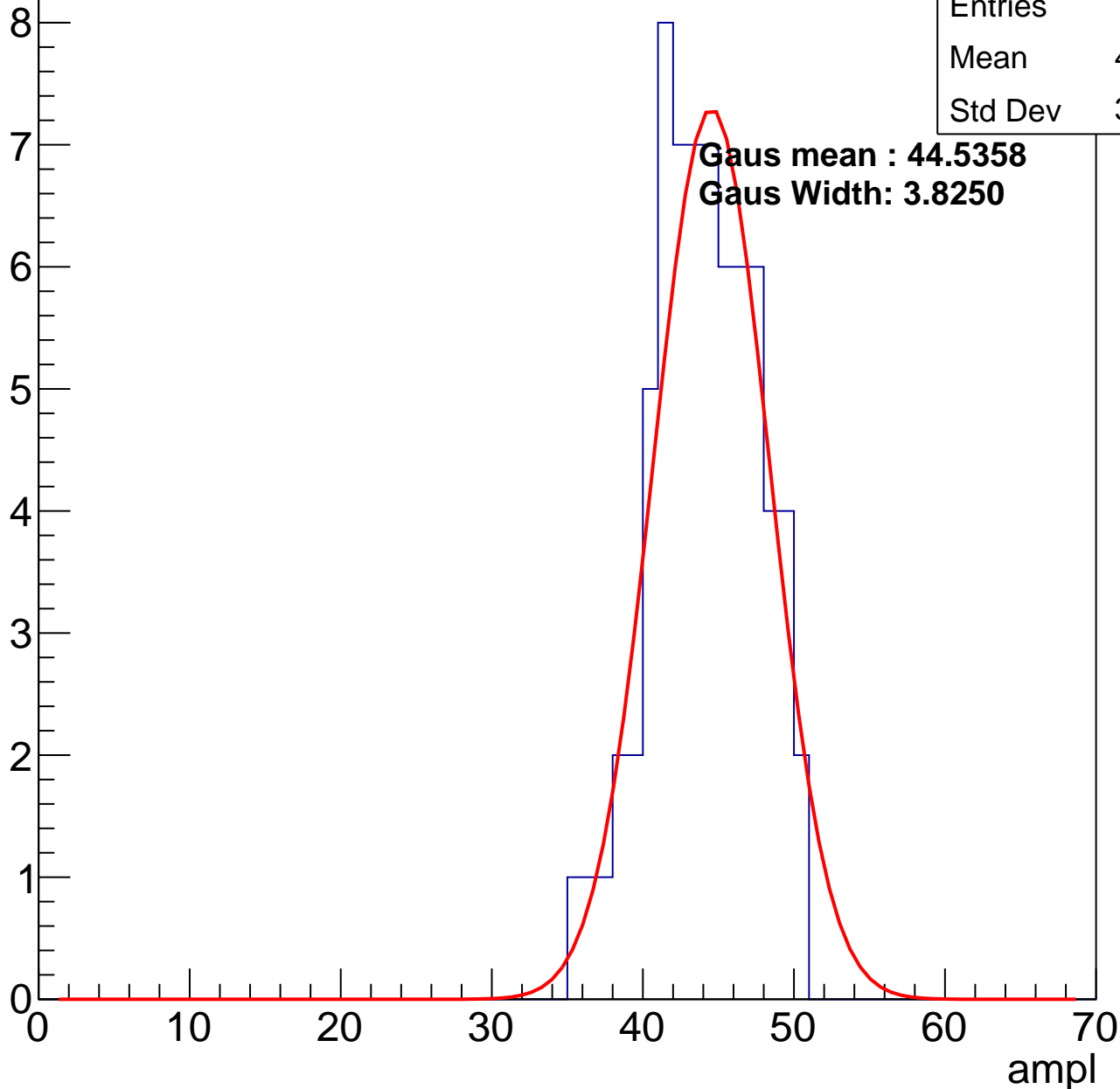
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.61
Std Dev	3.431

Gaus mean : 44.5358

Gaus Width: 3.8250

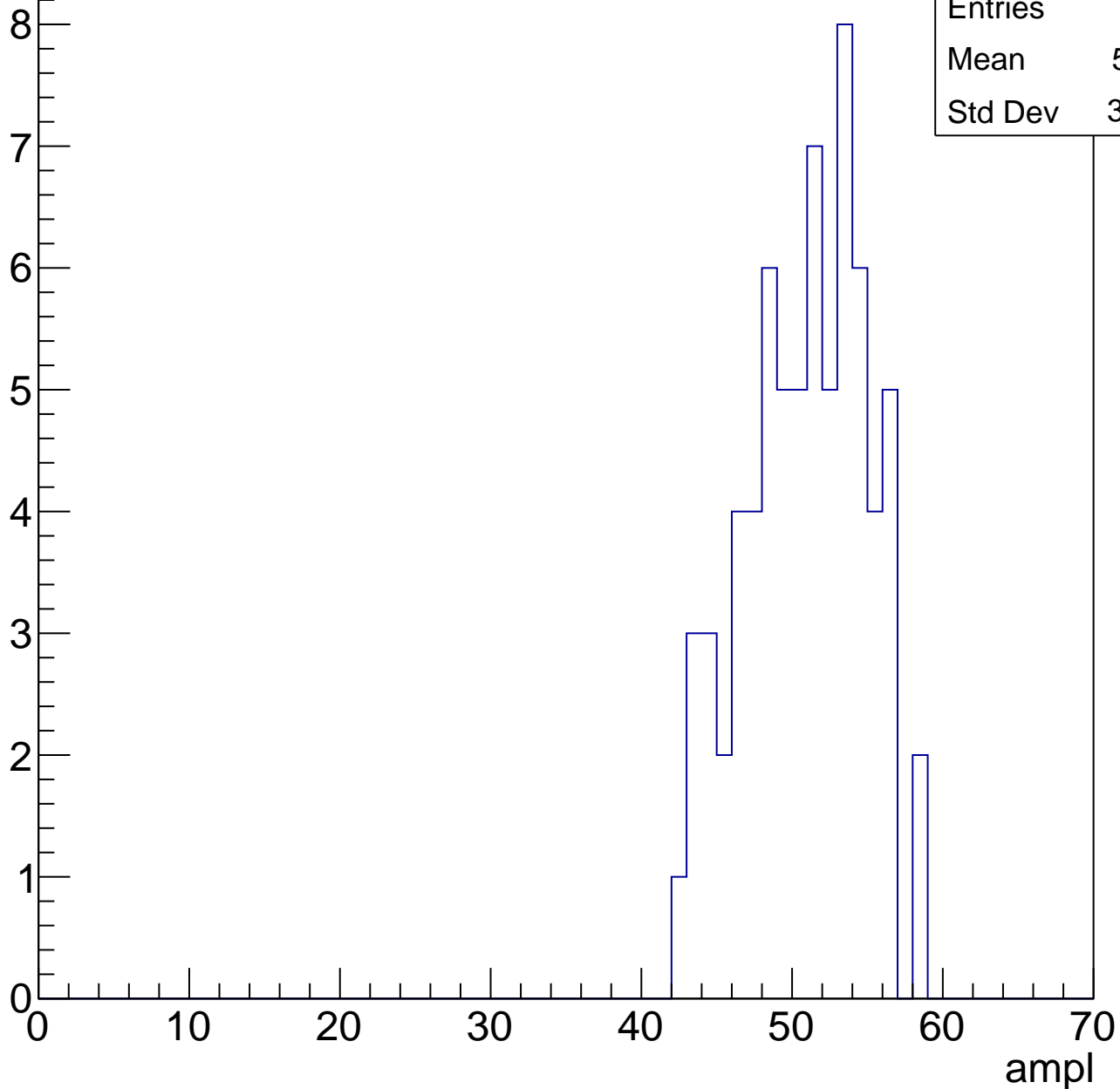


B0L001S, U18-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.41
Std Dev	3.944

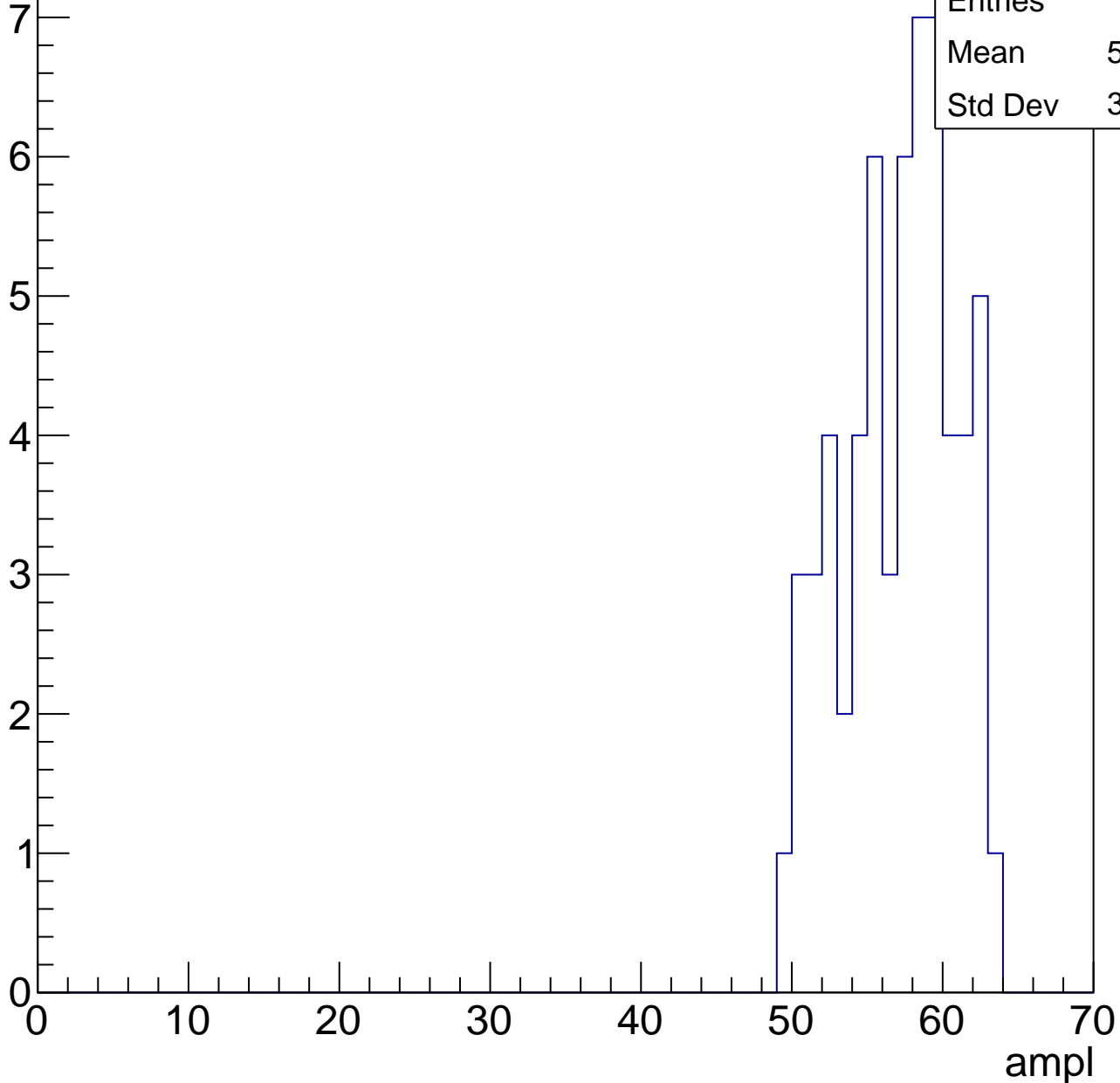


B0L001S, U18-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.63
Std Dev	3.656

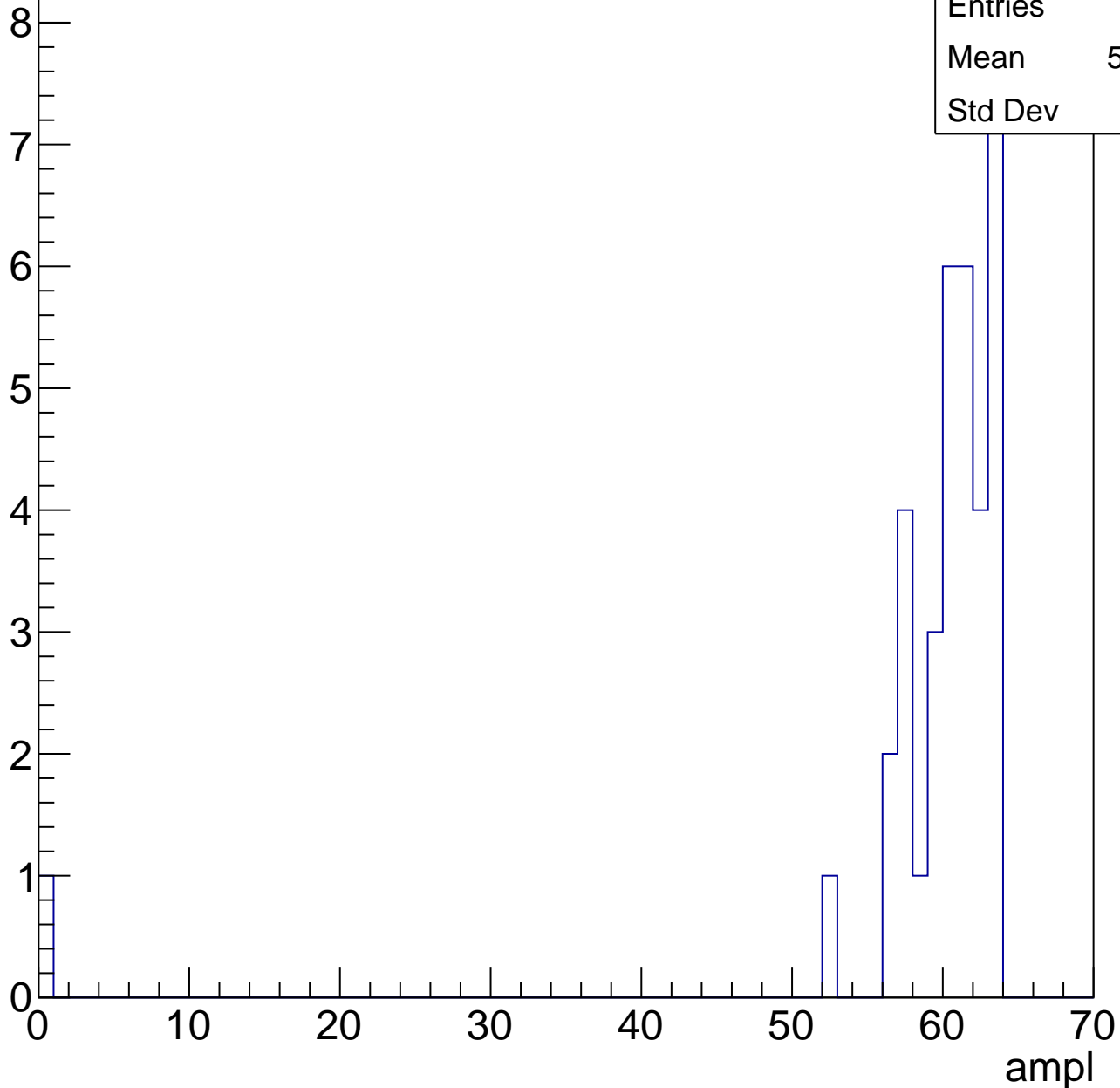


B0L001S, U18-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.47
Std Dev	10.2



B0L001S, U18-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U18-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch124, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	29.89
Std Dev	7.12

Gaus mean : 31.9835

Gaus Width: 3.7591

Entry

10
8
6
4
2
0

ampl

0

10

20

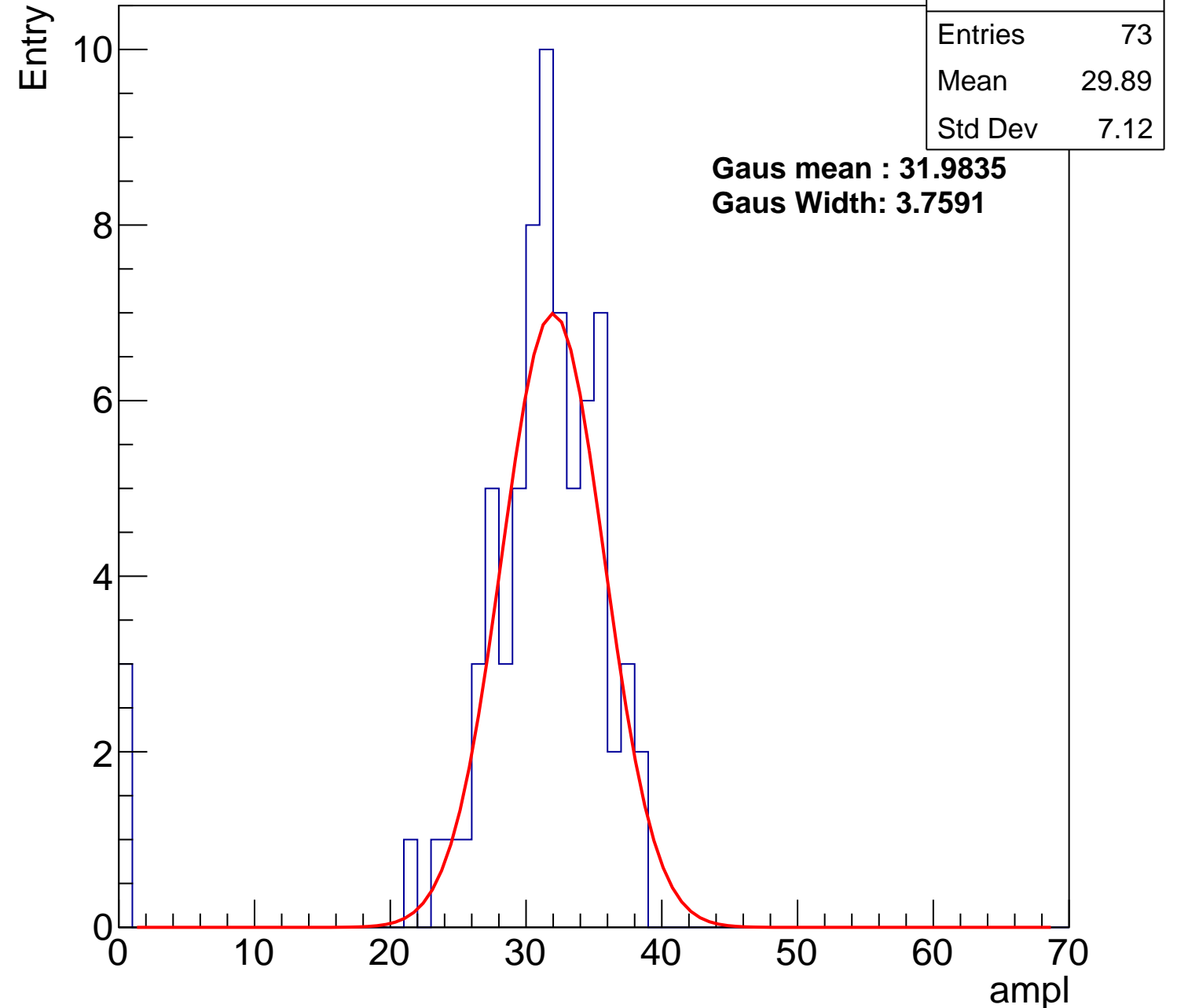
30

40

50

60

70



B0L001S, U18-ch124, adc1

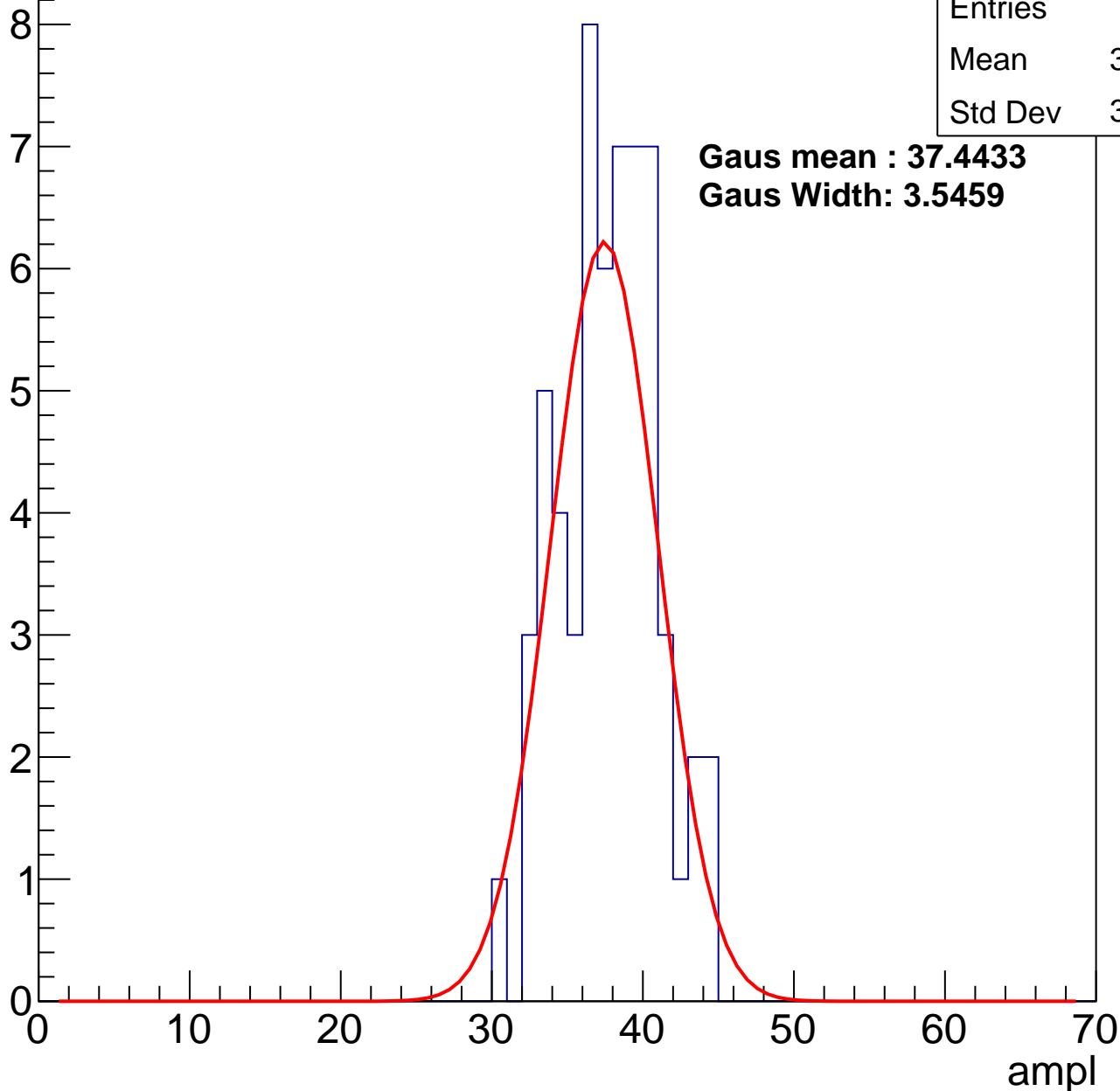
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.29
Std Dev	3.179

Gaus mean : 37.4433

Gaus Width: 3.5459



B0L001S, U18-ch124, adc2

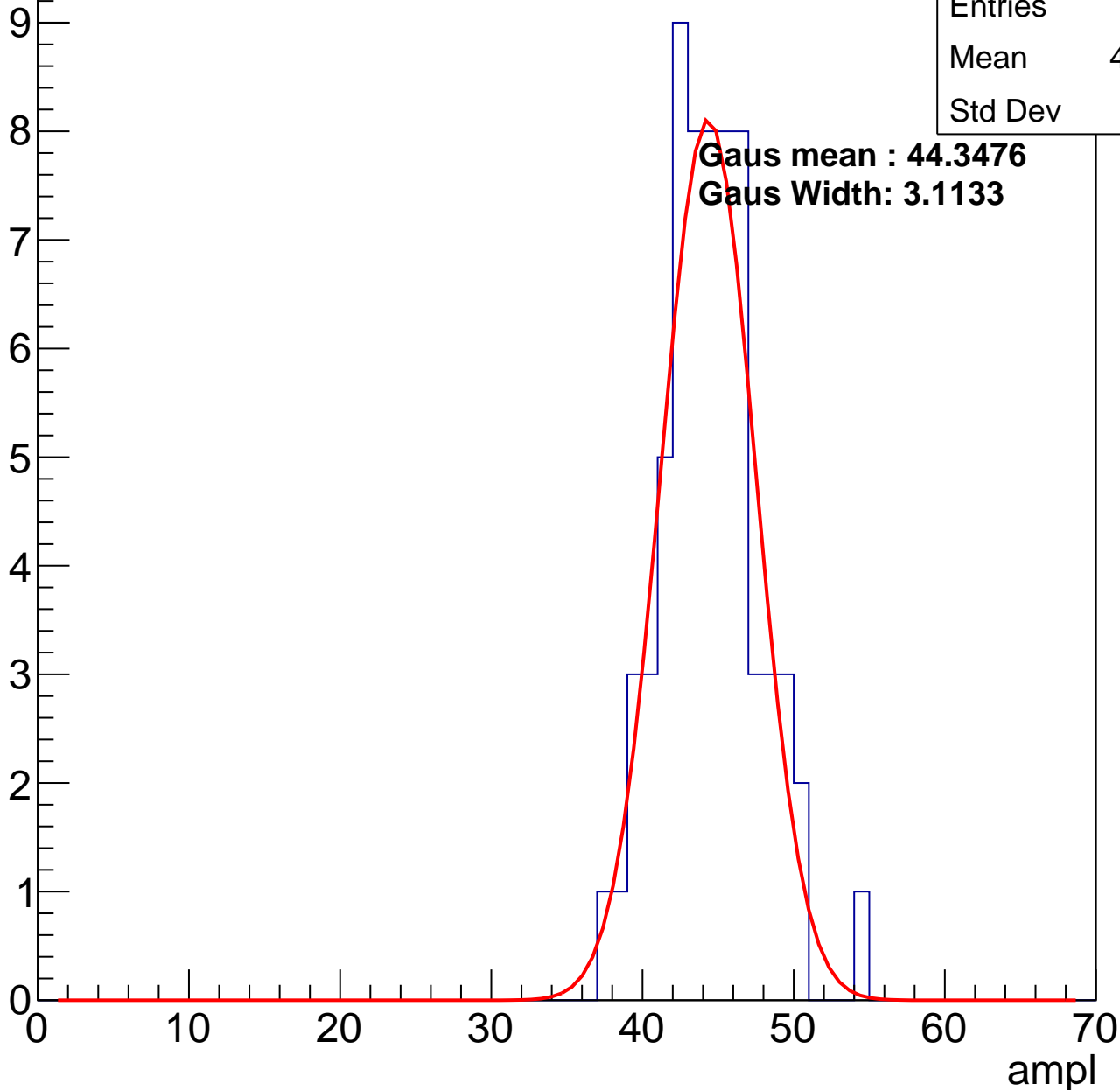
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.02
Std Dev	3.16

Gaus mean : 44.3476

Gaus Width: 3.1133

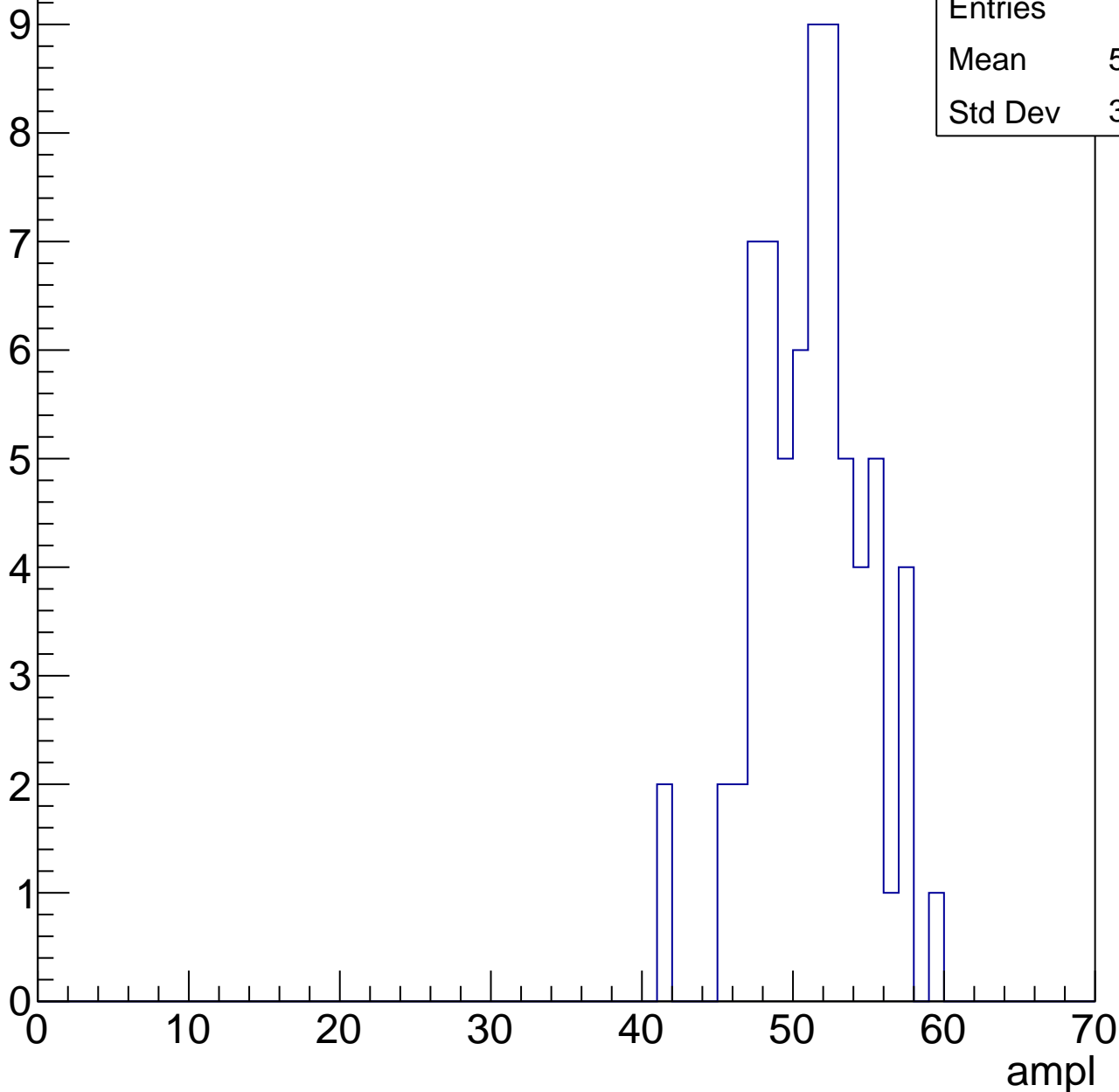


B0L001S, U18-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.72
Std Dev	3.603

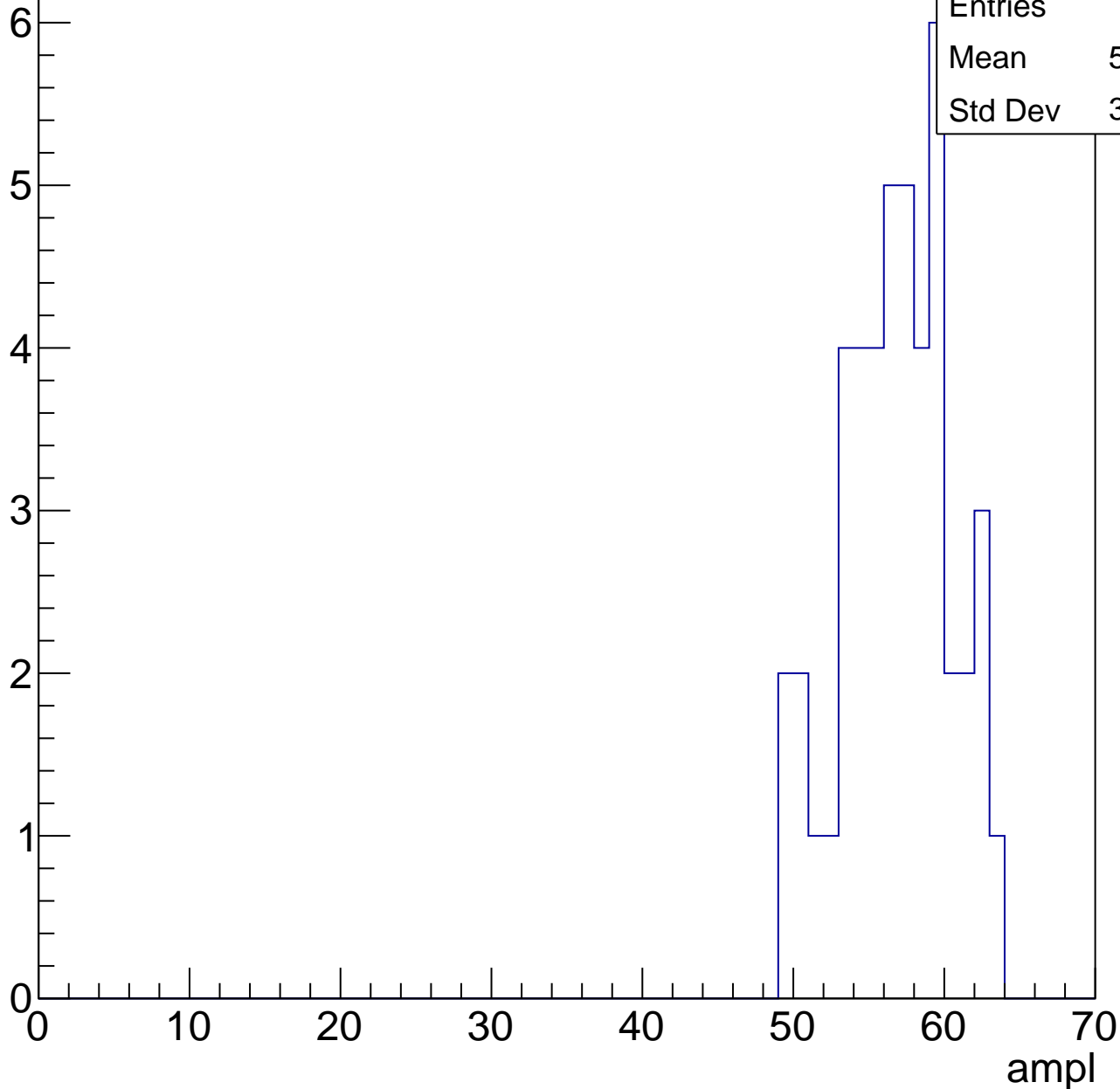


B0L001S, U18-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	56.33
Std Dev	3.539

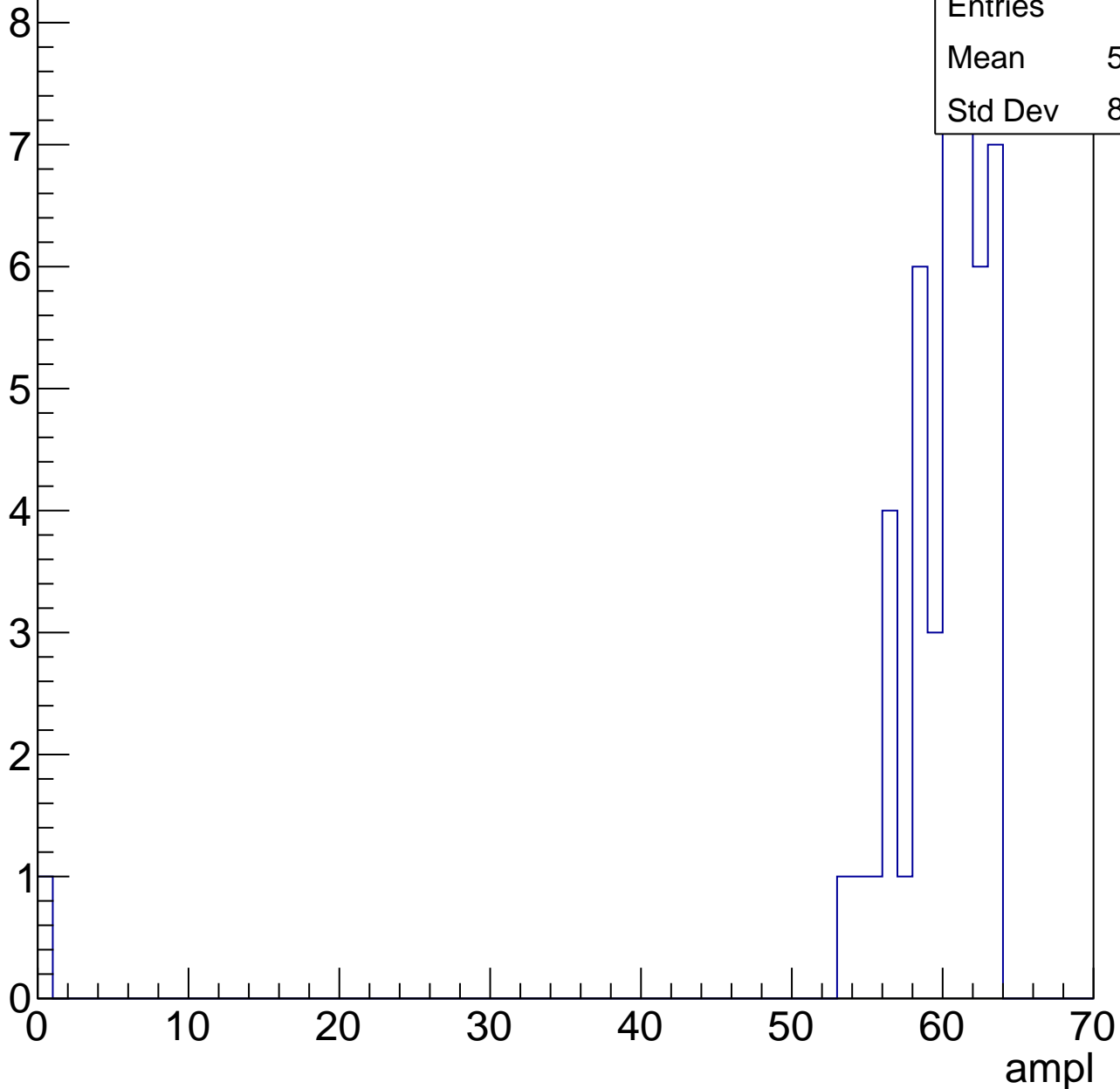


B0L001S, U18-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	58.49
Std Dev	8.989



B0L001S, U18-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U18-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U18-ch125, adc0

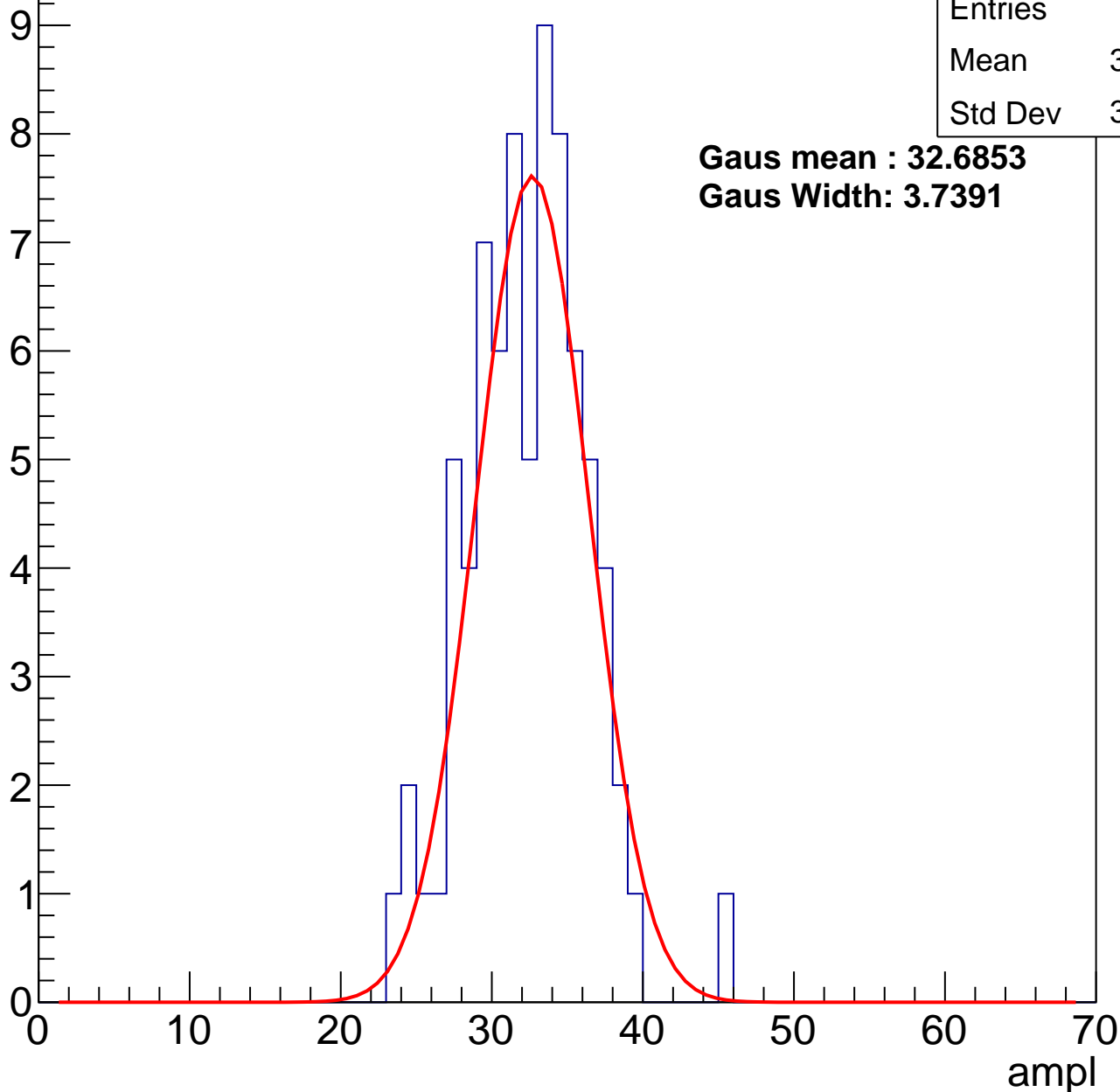
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	31.93
Std Dev	3.878

Gaus mean : 32.6853

Gaus Width: 3.7391



B0L001S, U18-ch125, adc1

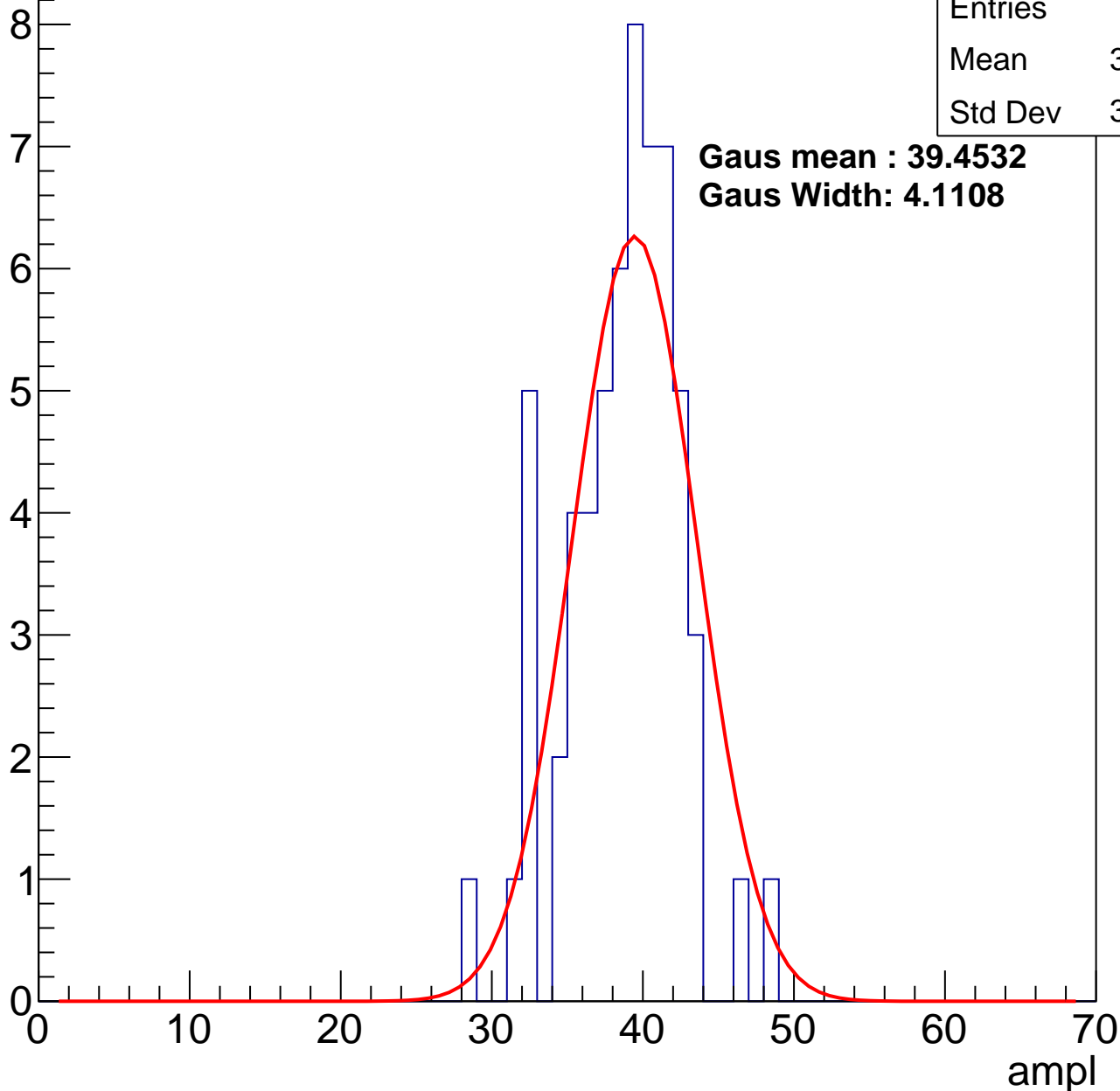
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.27
Std Dev	3.723

Gaus mean : 39.4532

Gaus Width: 4.1108



B0L001S, U18-ch125, adc2

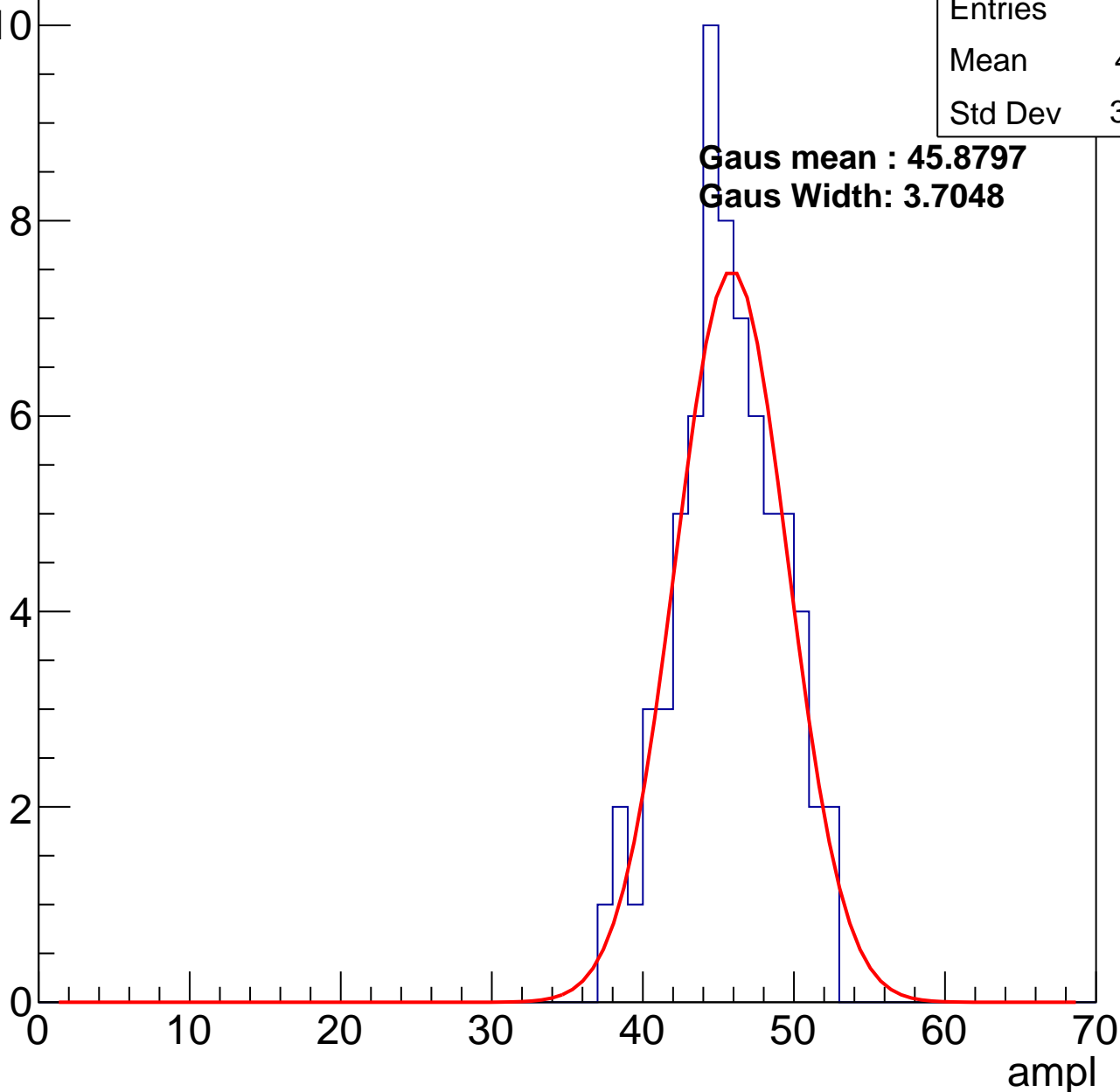
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	45.11
Std Dev	3.433

Gaus mean : 45.8797

Gaus Width: 3.7048

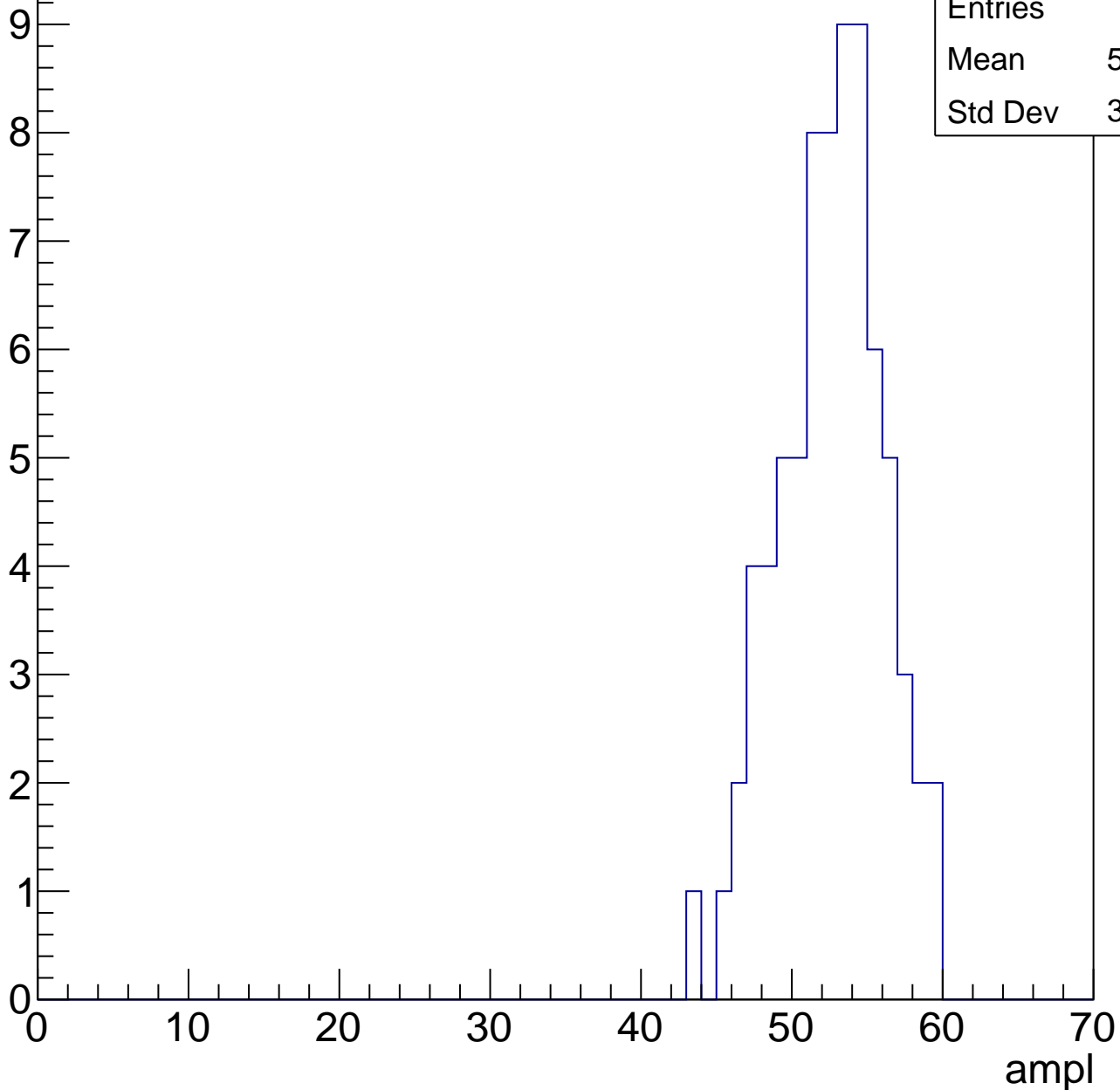


B0L001S, U18-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

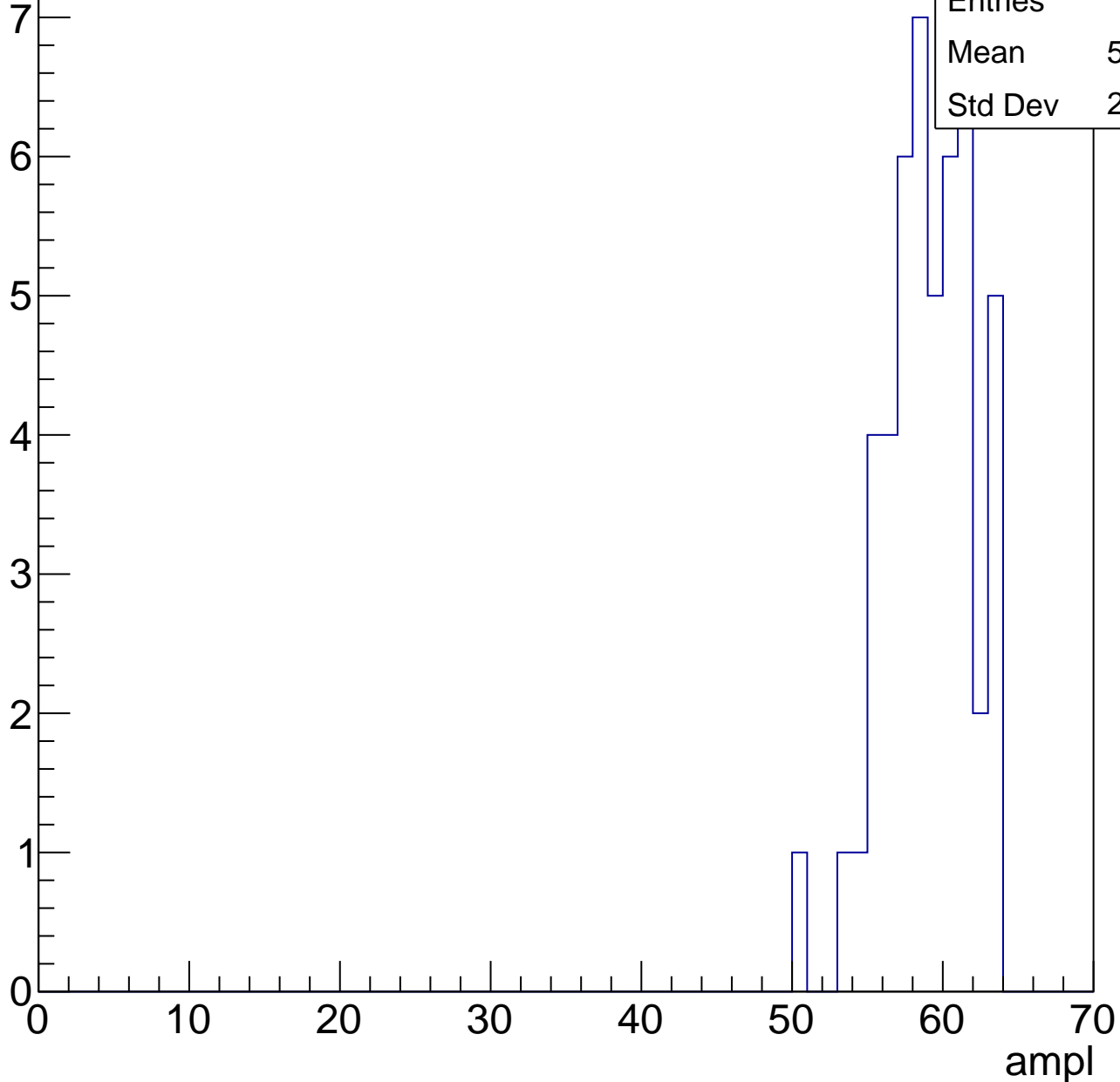
Entries	74
Mean	52.12
Std Dev	3.413



B0L001S, U18-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

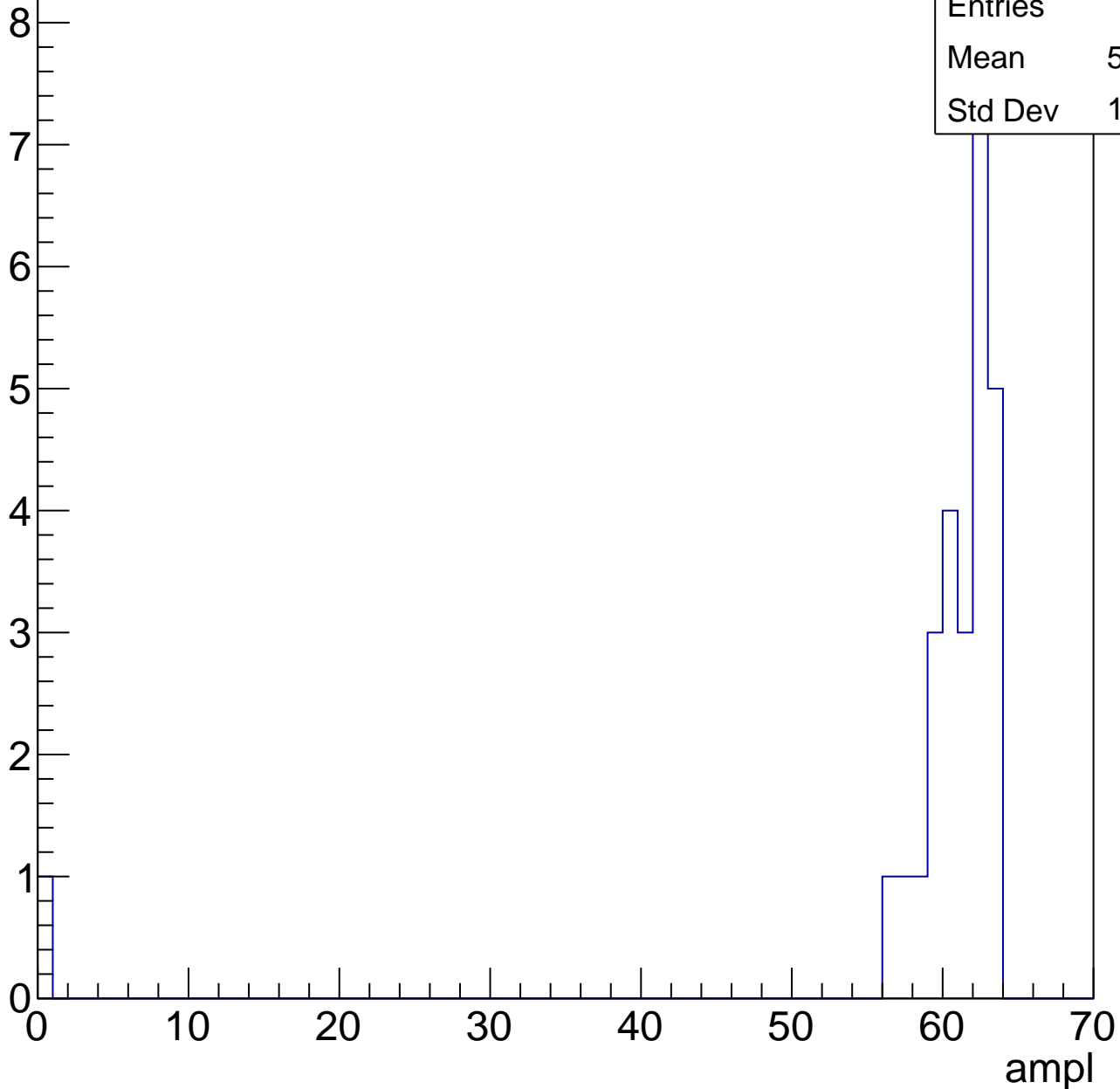


B0L001S, U18-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.59
Std Dev	11.64



B0L001S, U18-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U18-ch126, adc0

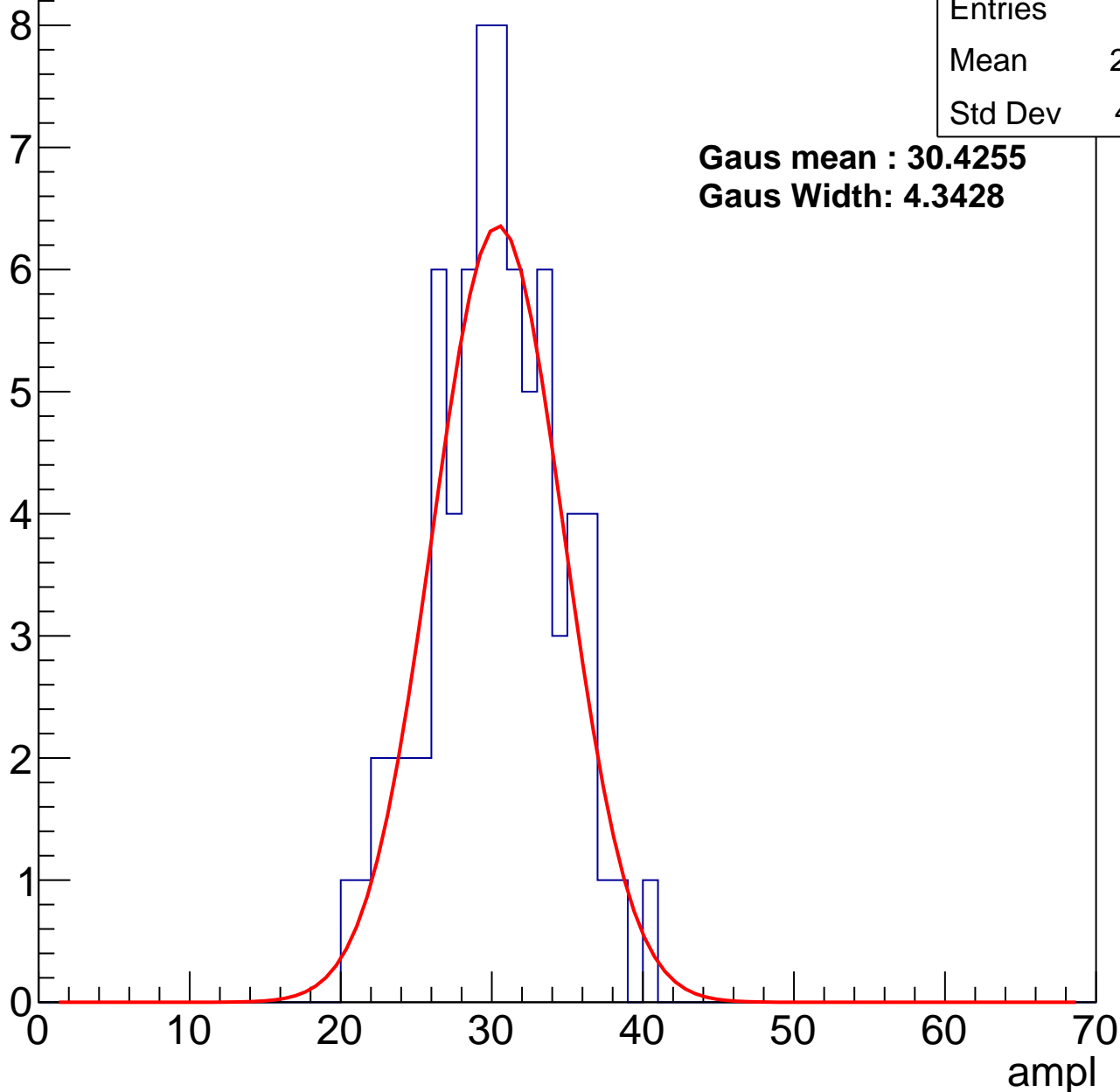
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.84
Std Dev	4.181

Gaus mean : 30.4255

Gaus Width: 4.3428



B0L001S, U18-ch126, adc1

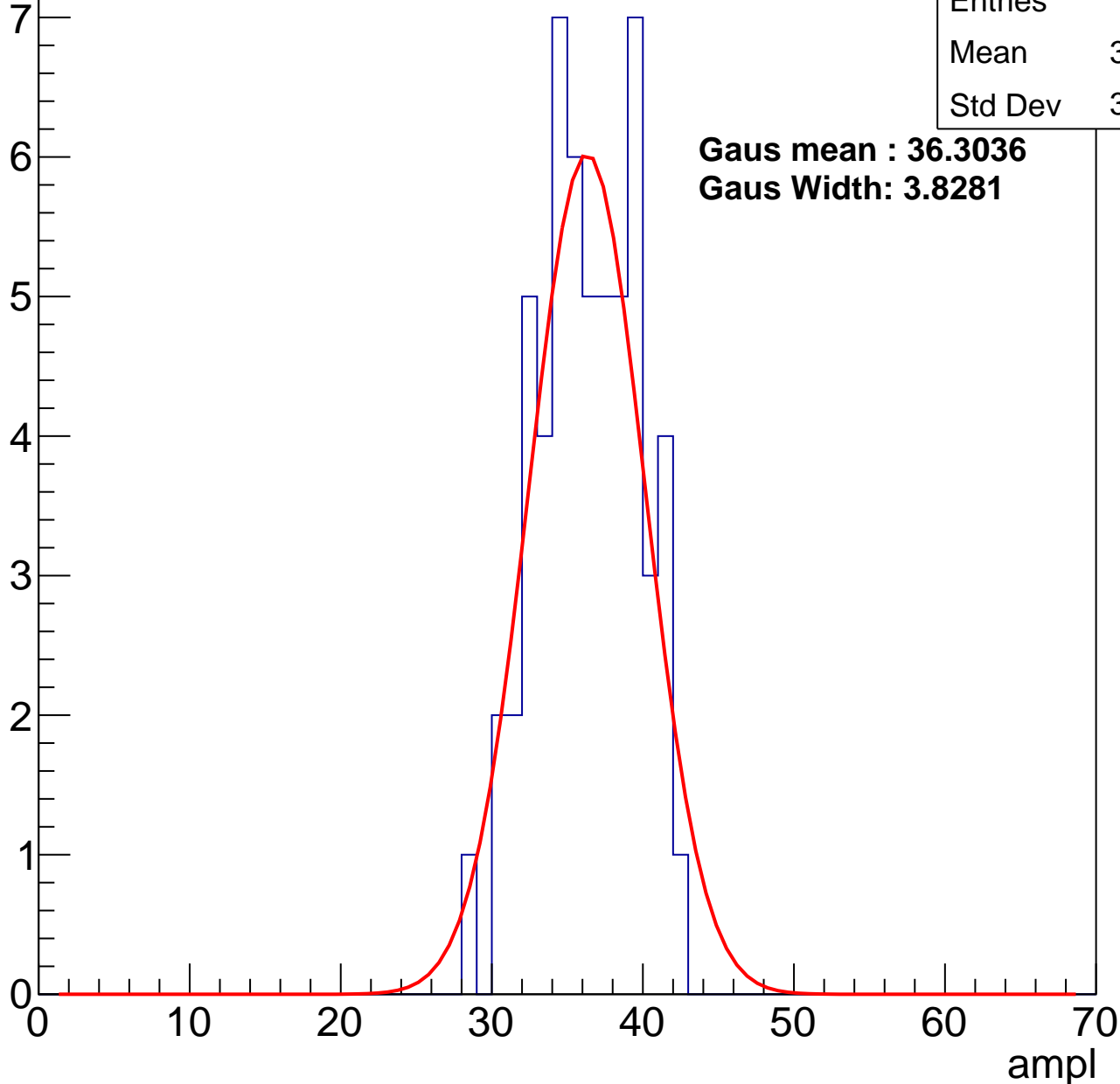
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	35.86
Std Dev	3.263

Gaus mean : 36.3036

Gaus Width: 3.8281



B0L001S, U18-ch126, adc2

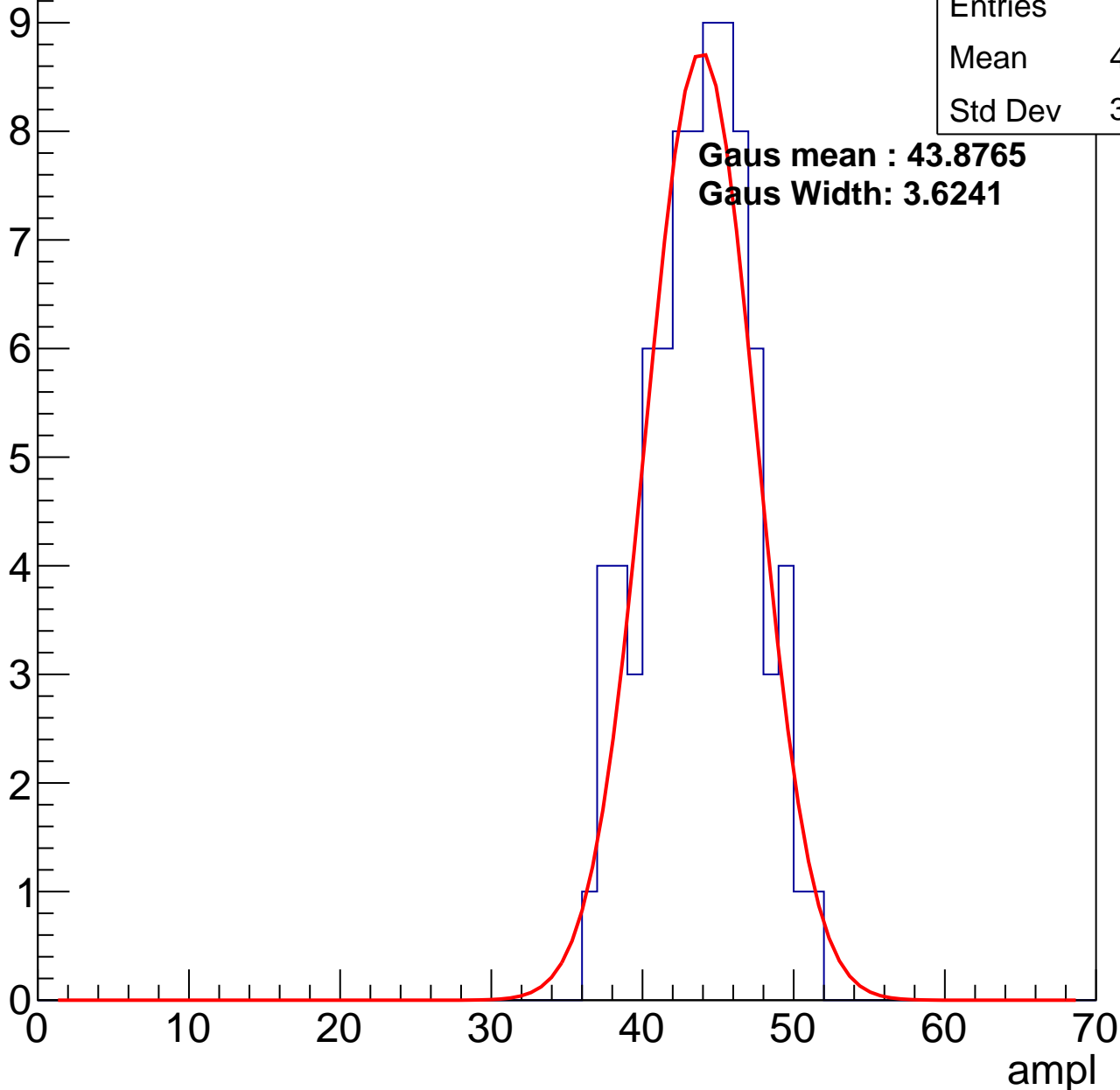
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	43.35
Std Dev	3.447

Gaus mean : 43.8765

Gaus Width: 3.6241

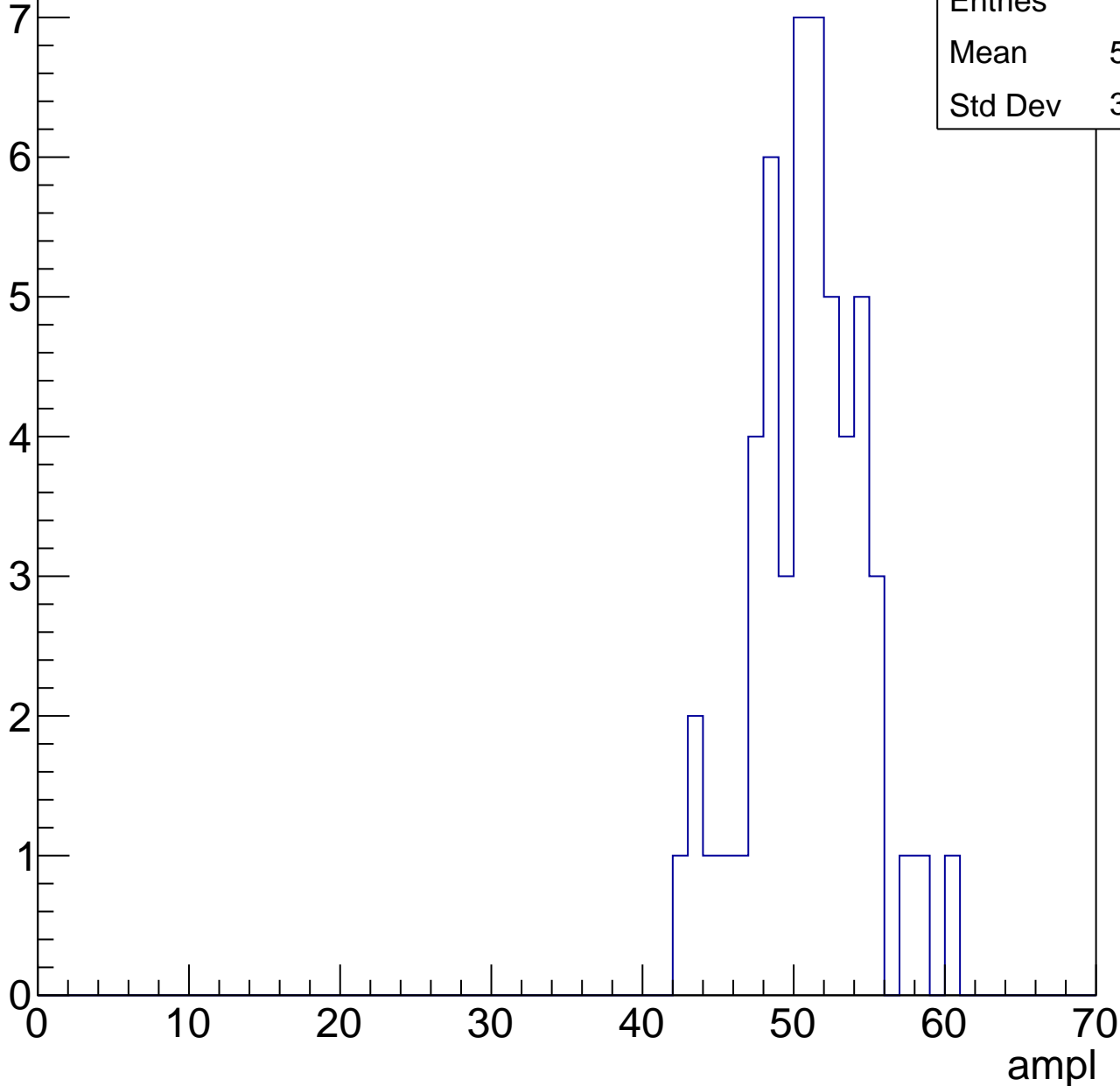


B0L001S, U18-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.47
Std Dev	3.689

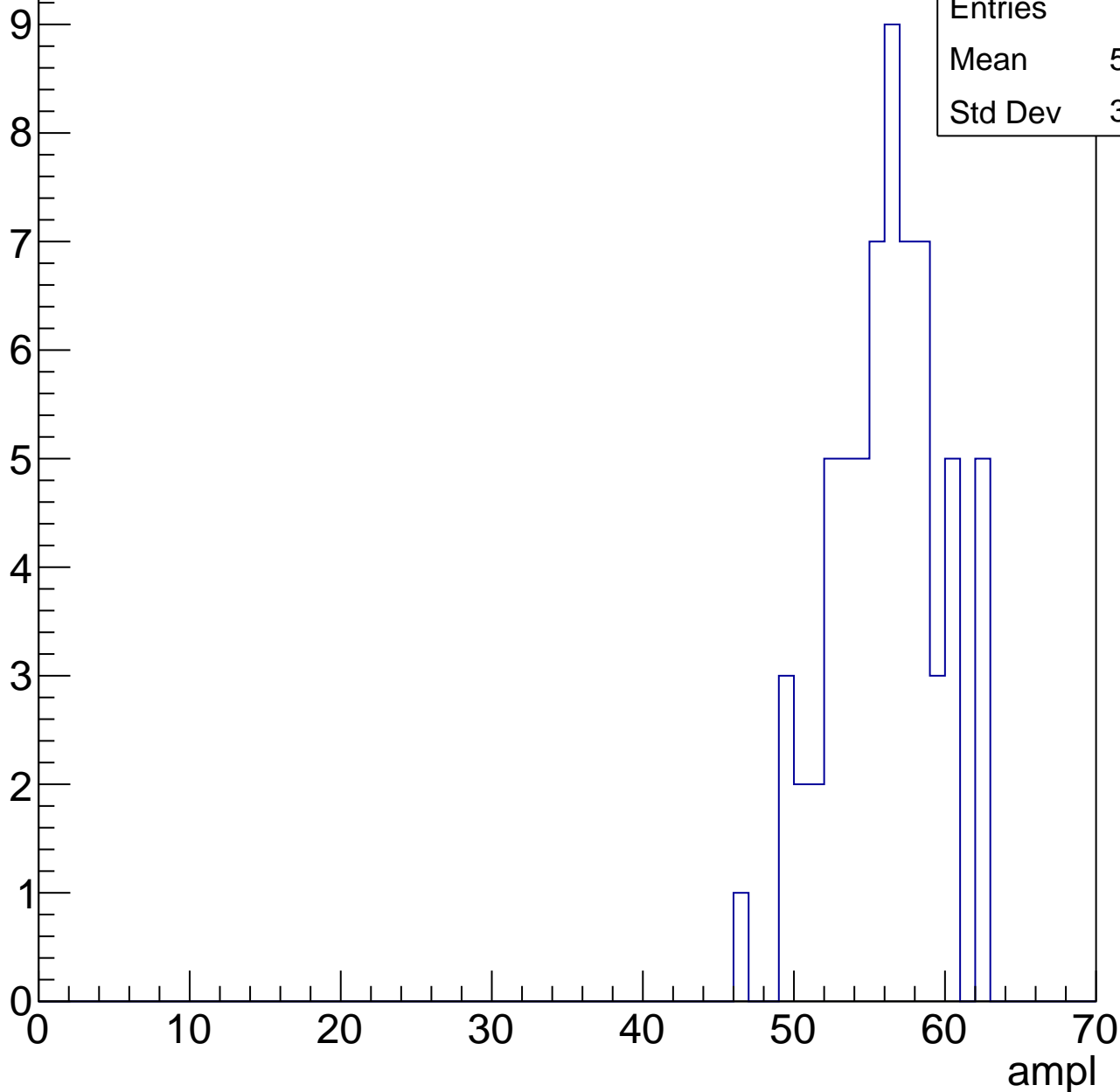


B0L001S, U18-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	55.62
Std Dev	3.554

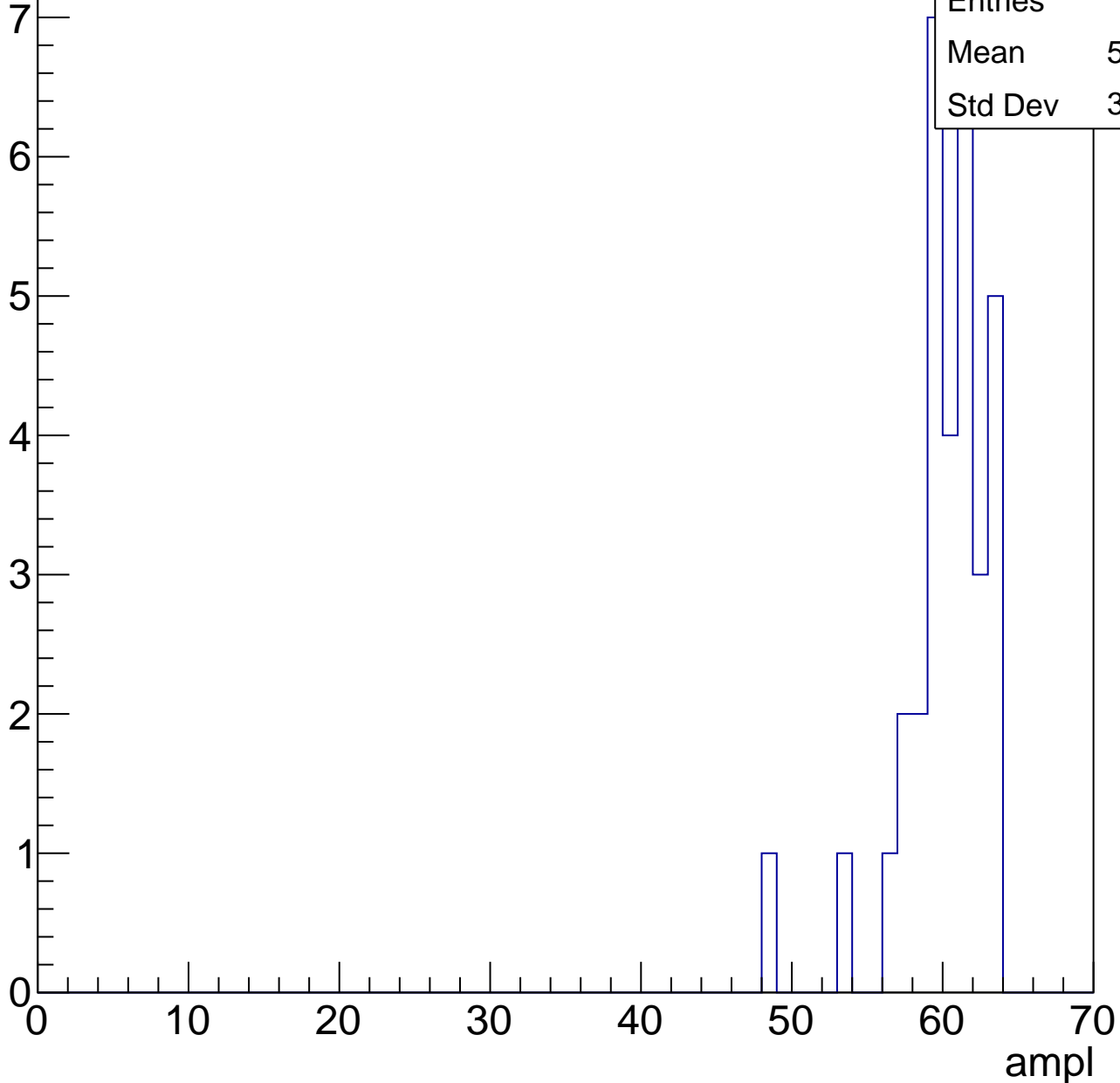


B0L001S, U18-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.64
Std Dev	3.023

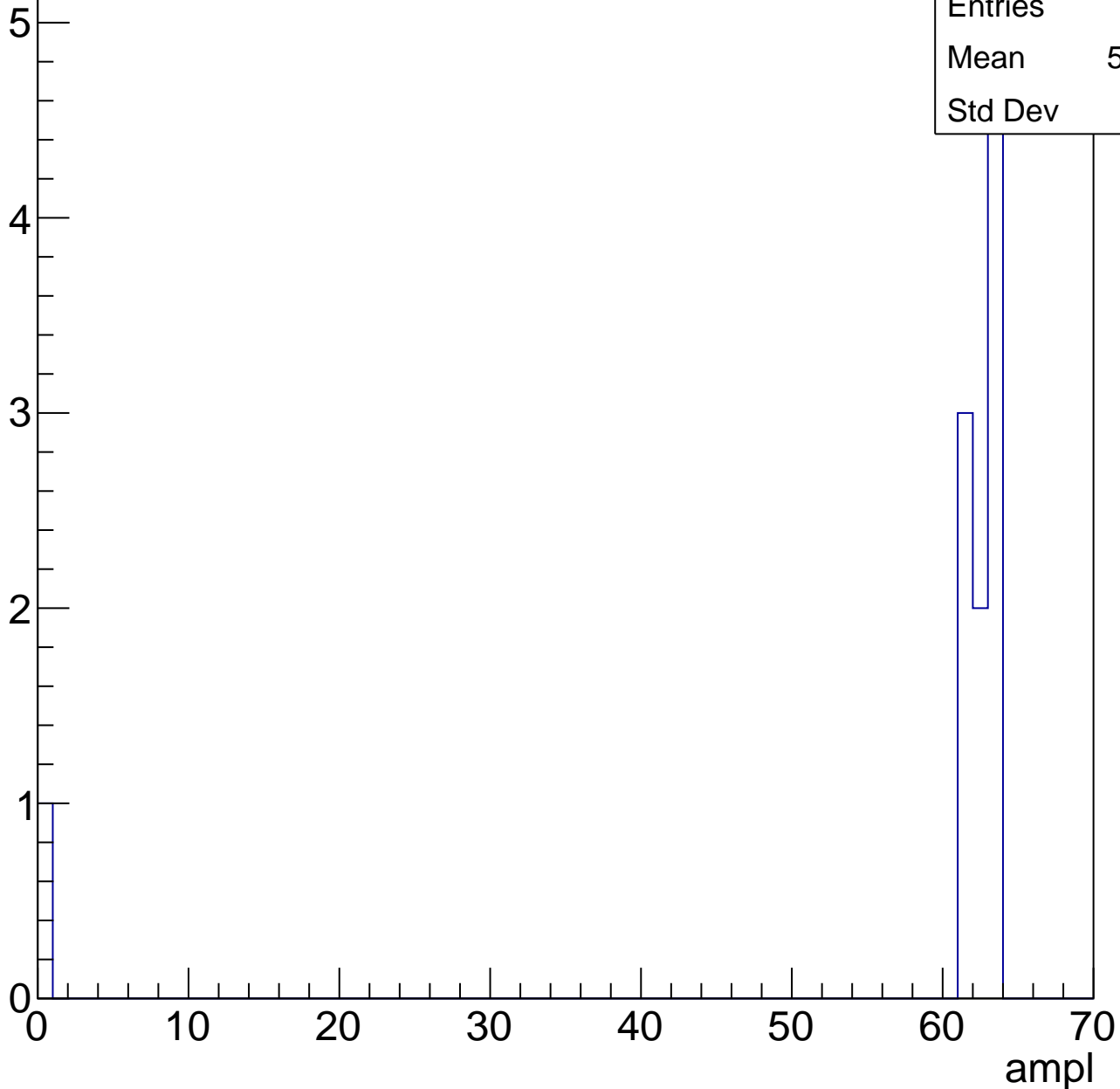


B0L001S, U18-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

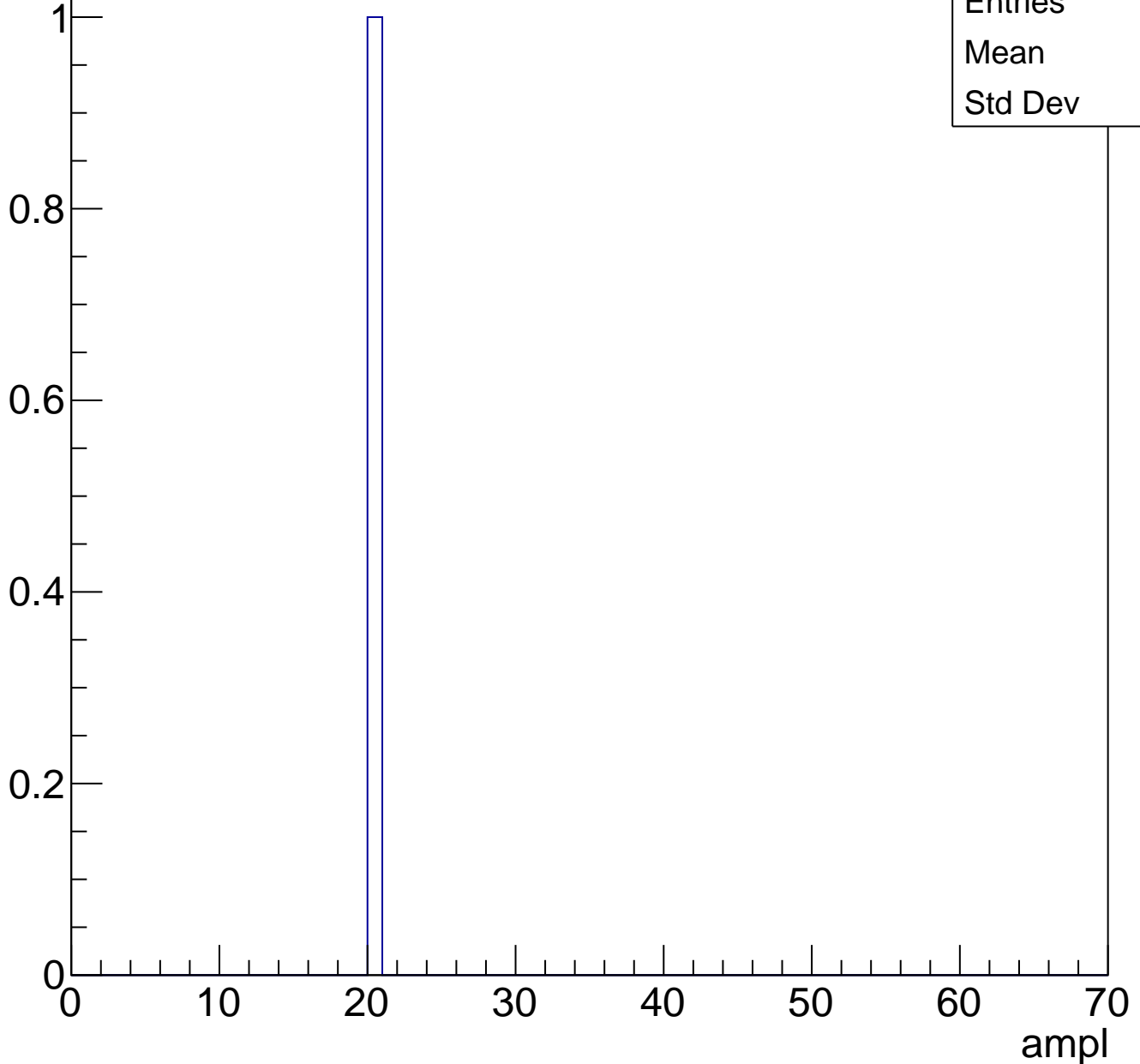
Entries	11
Mean	56.55
Std Dev	17.9



B0L001S, U18-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	20
Std Dev	0

B0L001S, U18-ch127, adc0

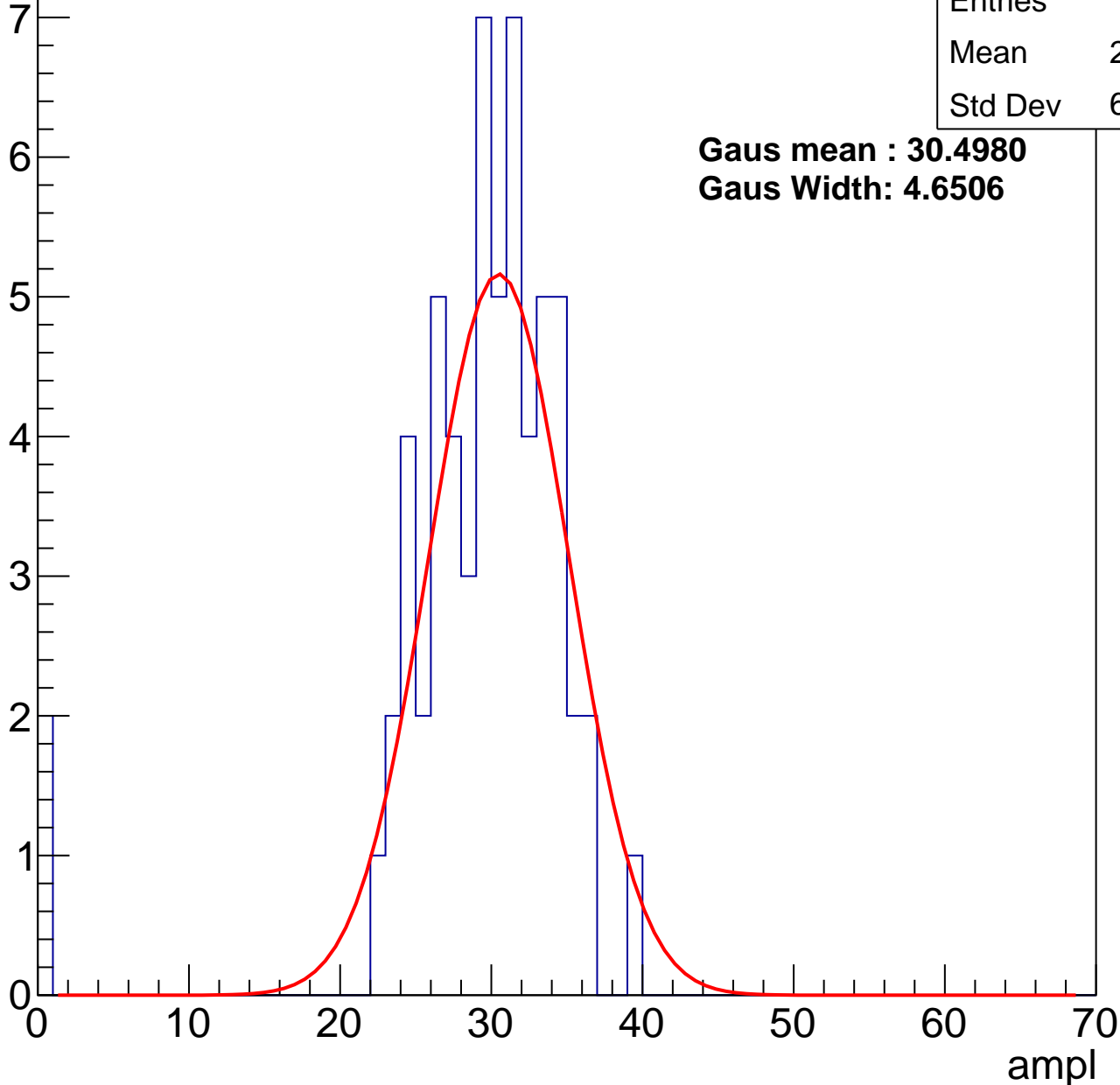
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.69
Std Dev	6.449

Gaus mean : 30.4980

Gaus Width: 4.6506



B0L001S, U18-ch127, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	93
Mean	37.43
Std Dev	3.939

Gaus mean : 37.7586

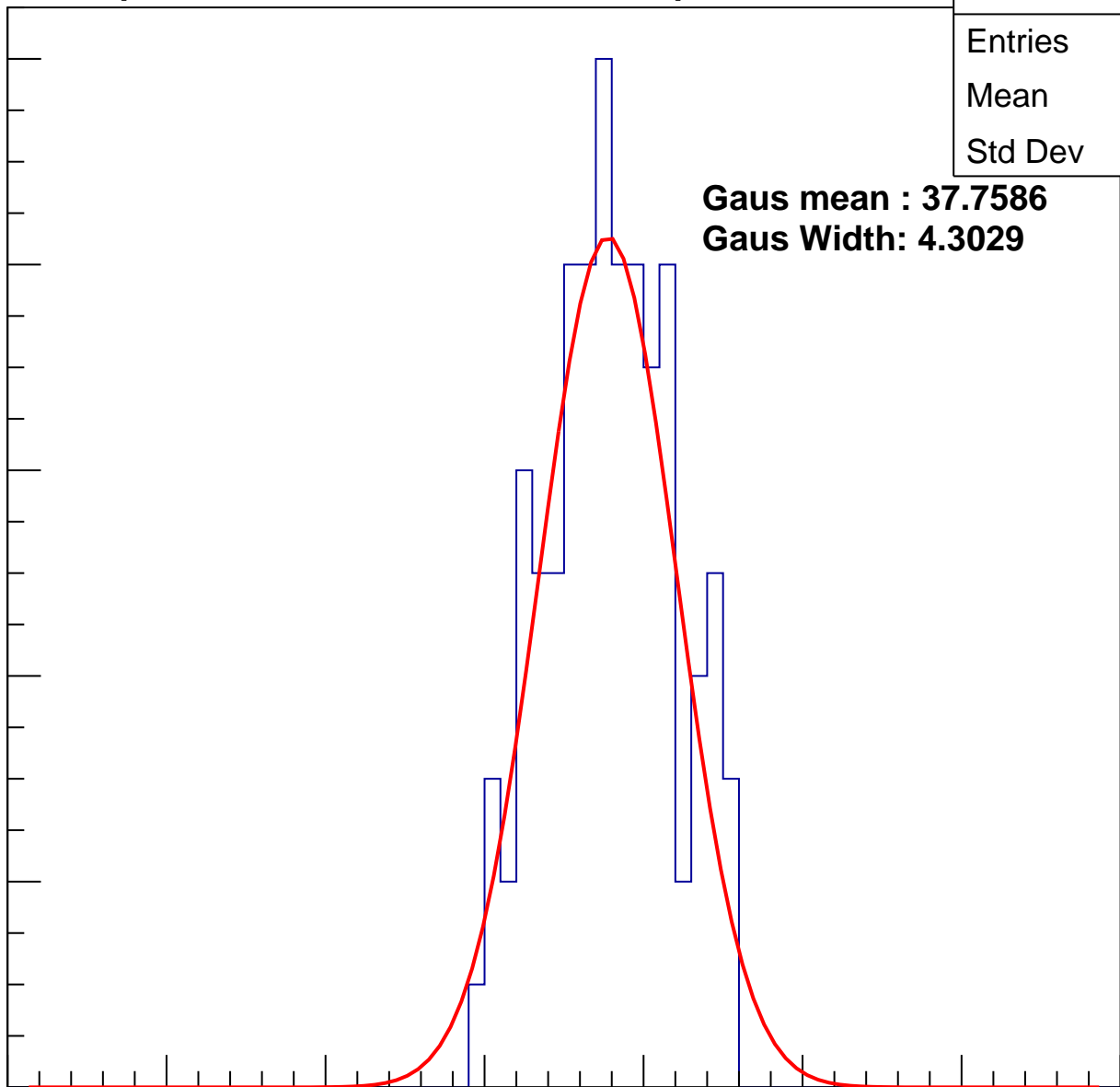
Gaus Width: 4.3029

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U18-ch127, adc2

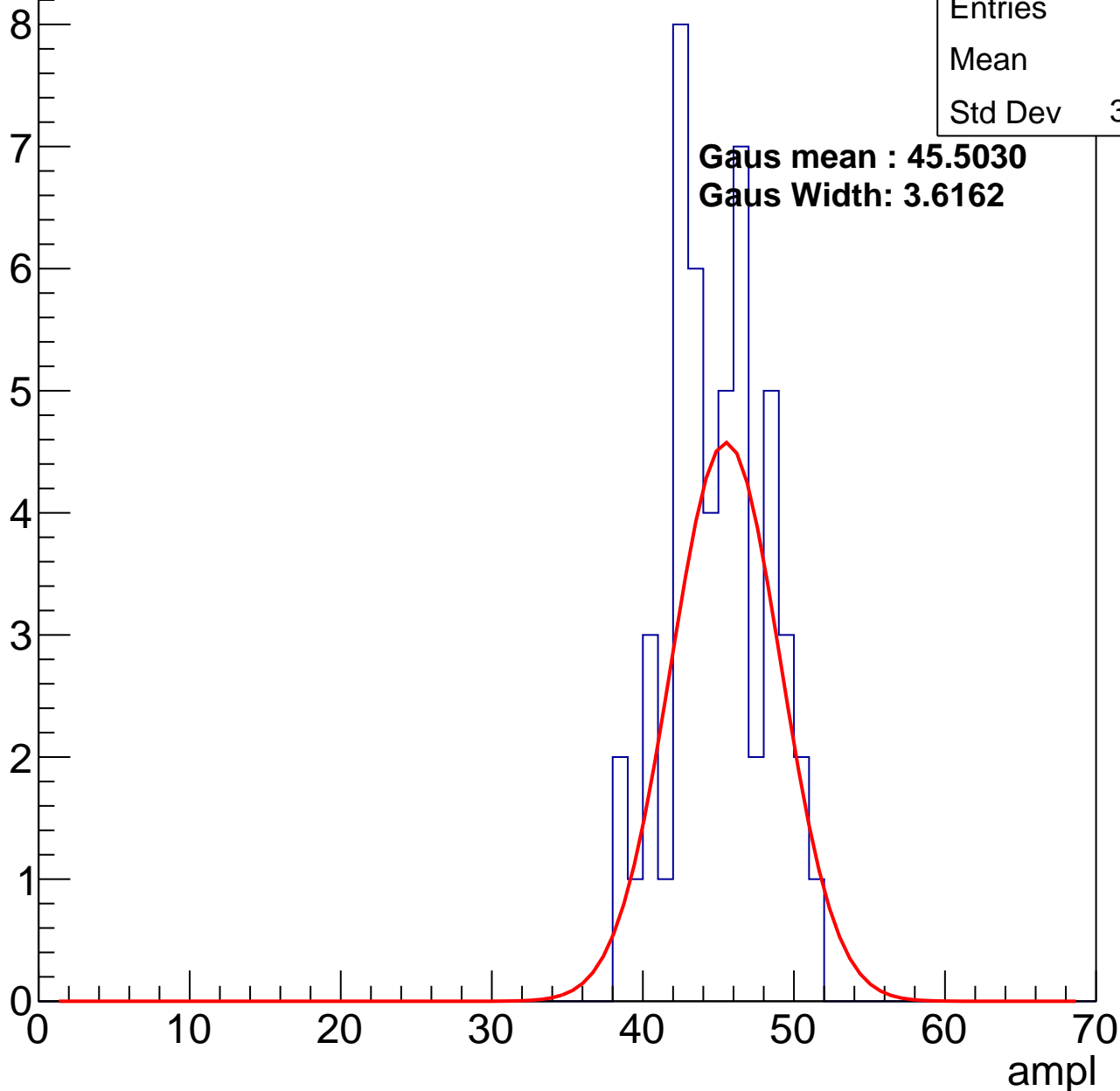
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	44.5
Std Dev	3.183

Gaus mean : 45.5030

Gaus Width: 3.6162

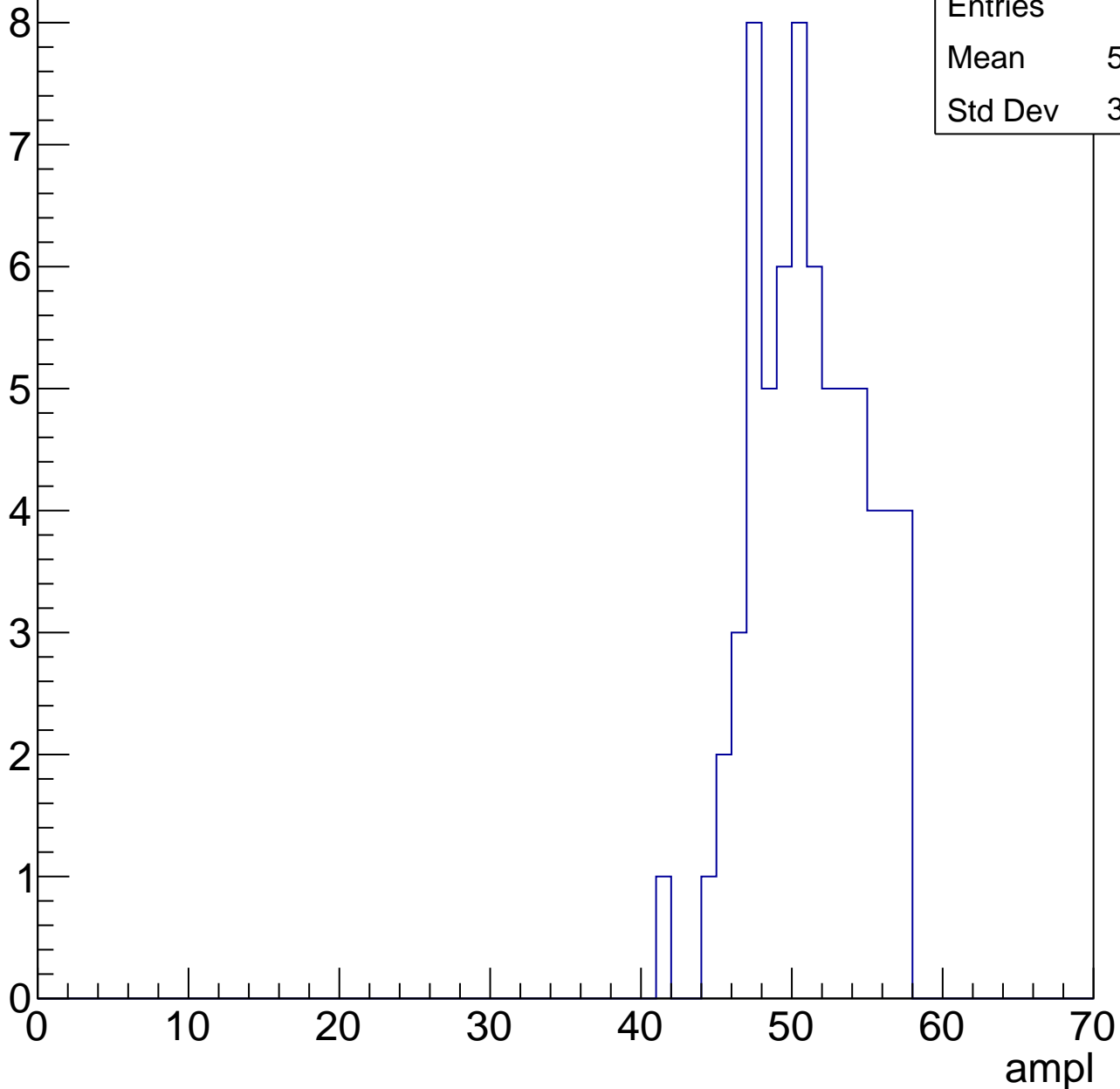


B0L001S, U18-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.69
Std Dev	3.608

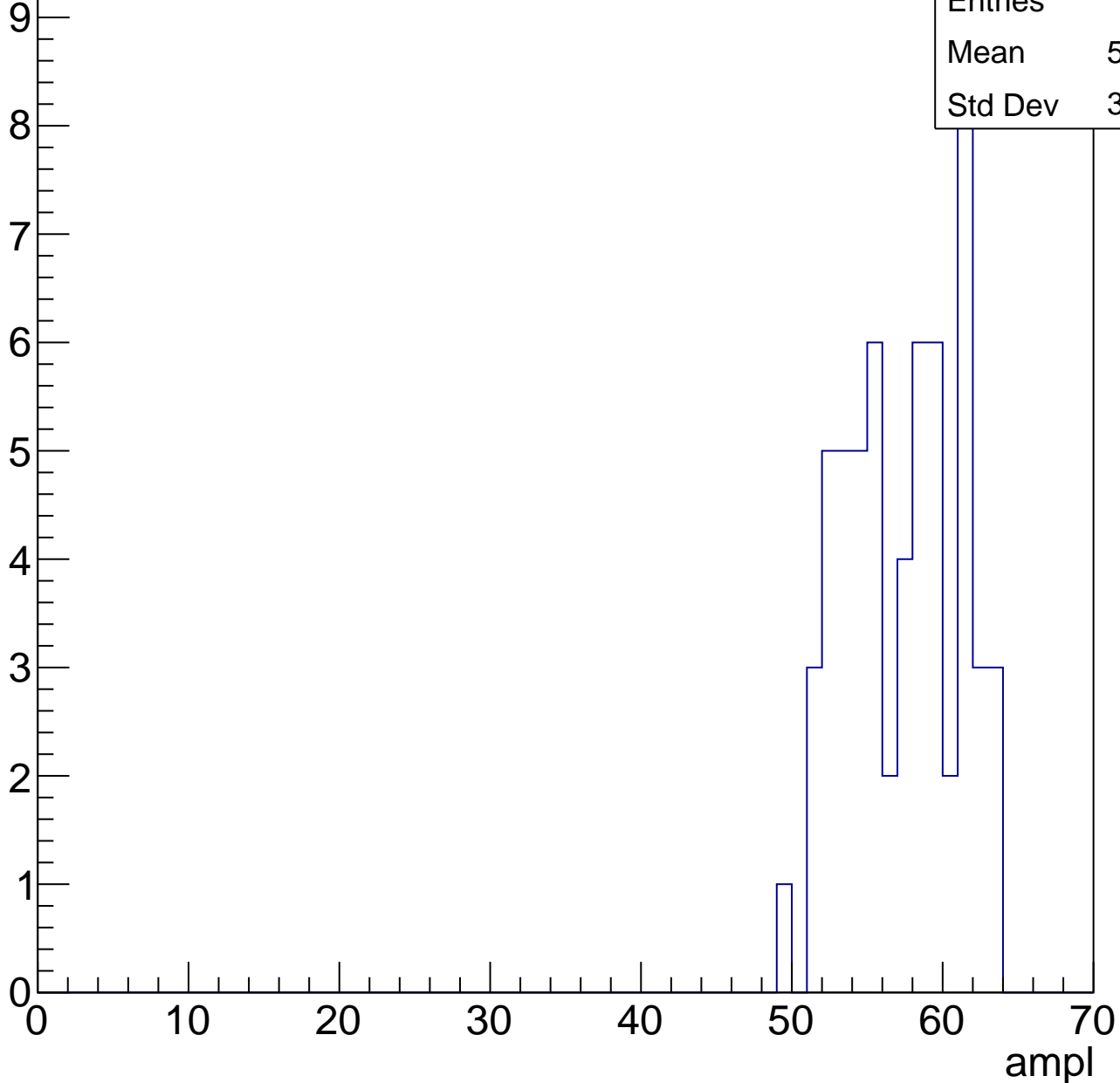


B0L001S, U18-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.88
Std Dev	3.702

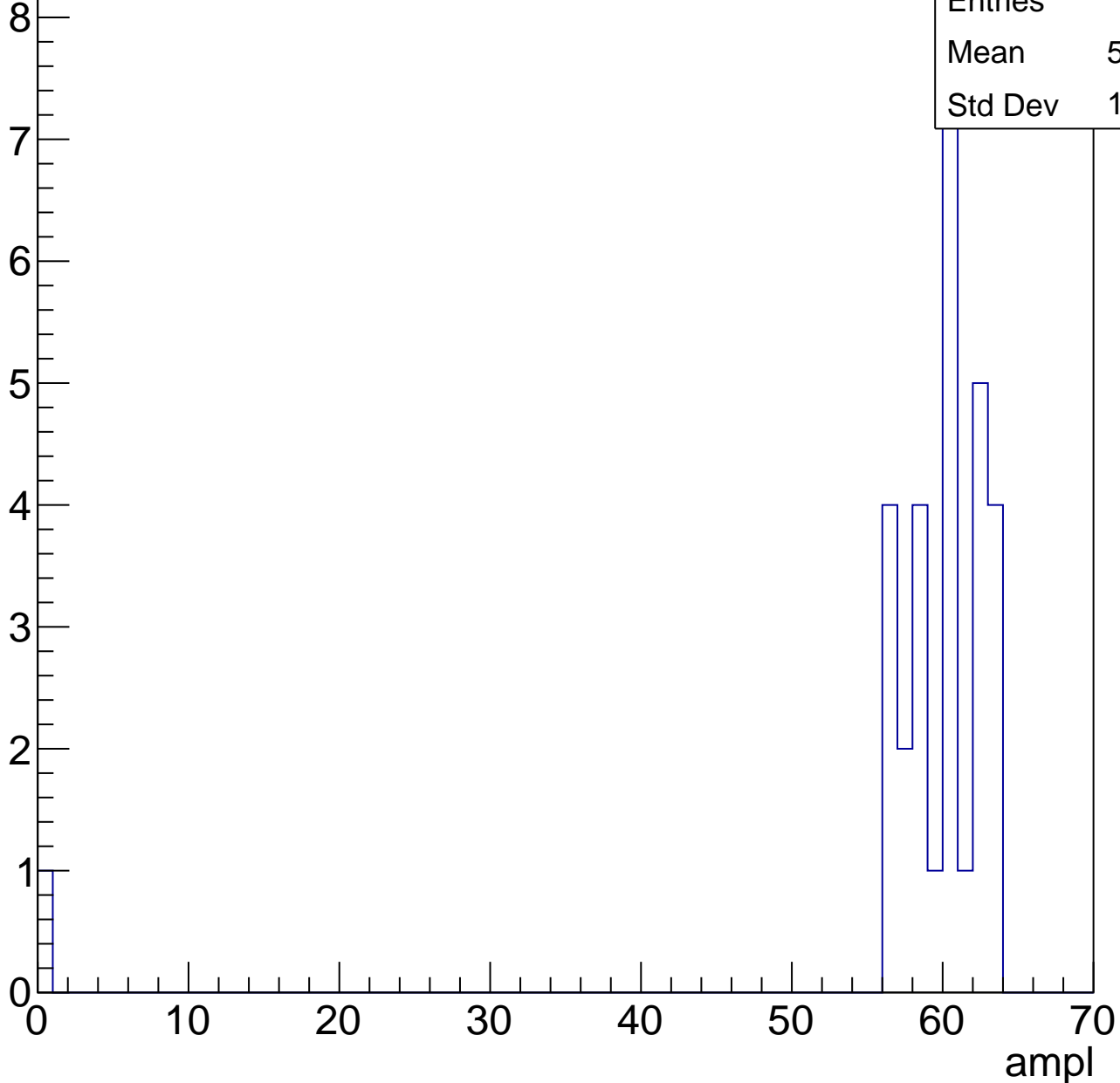


B0L001S, U18-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	57.73
Std Dev	10.96



B0L001S, U18-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.25

Std Dev

1.785

B0L001S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

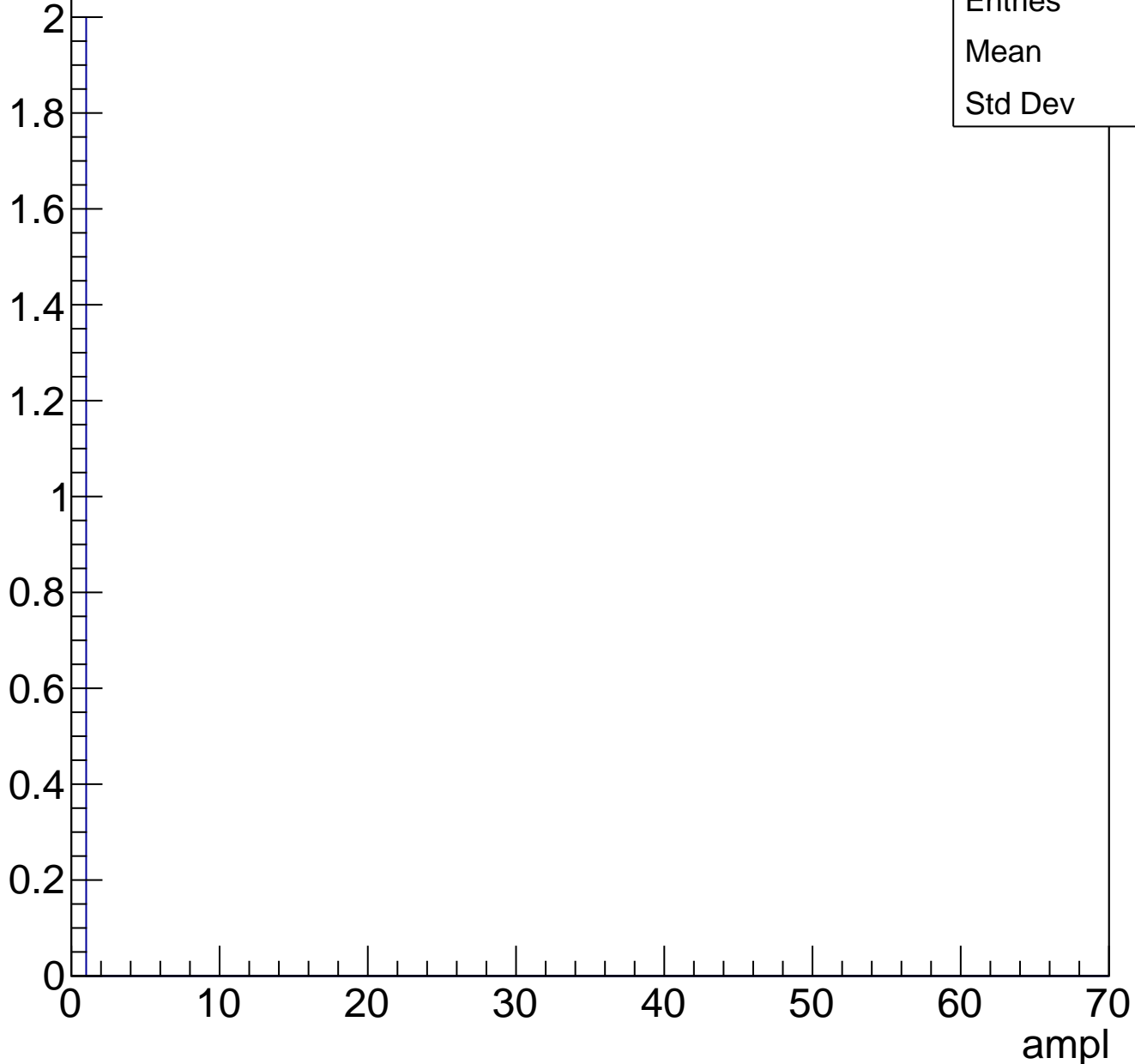
Entry



B0L001S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0