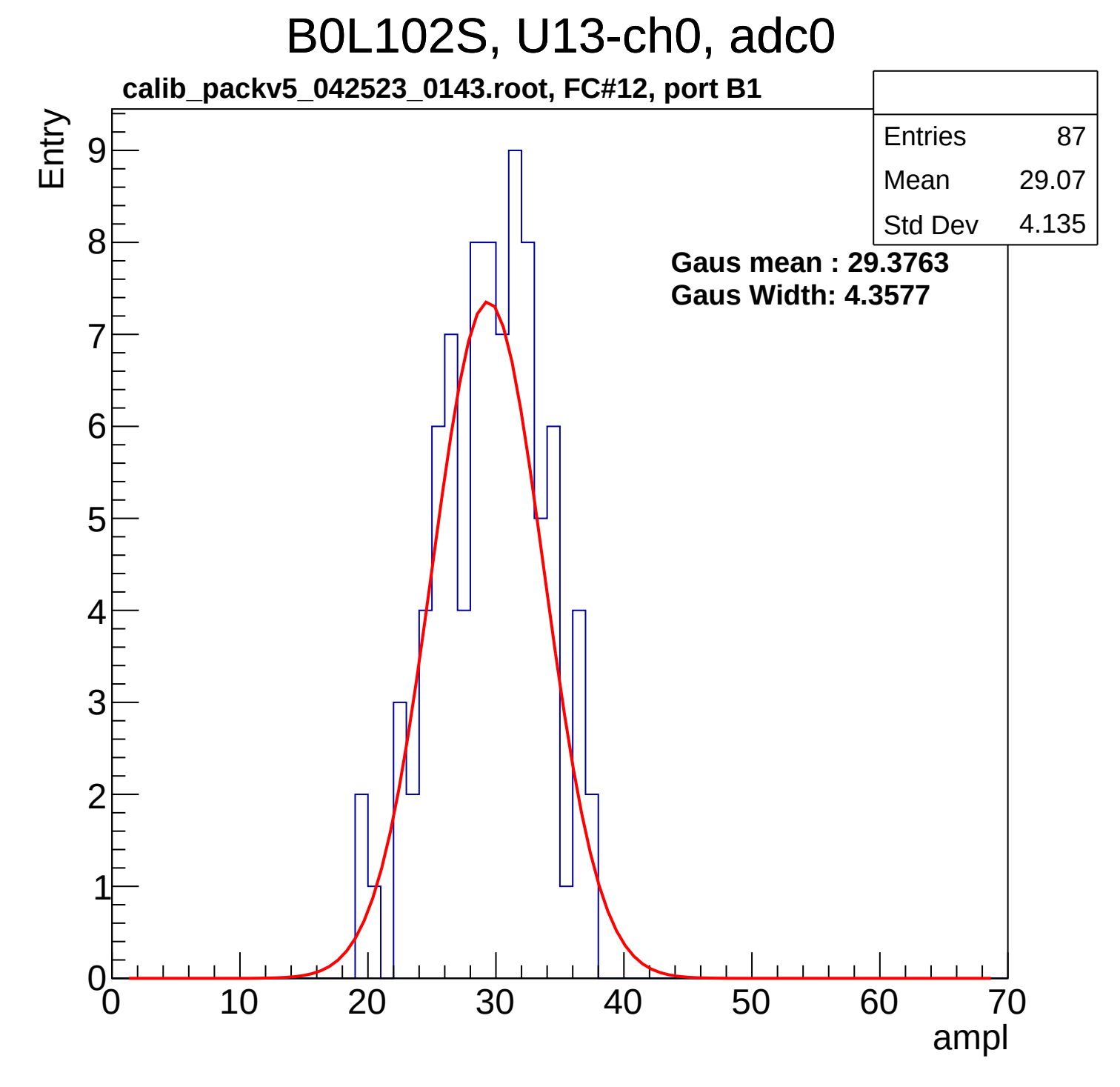
B0L102S, U13-ch0, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 29.07
Std Dev 4.135
Gaus mean : 29.3763
Gaus Width: 4.3577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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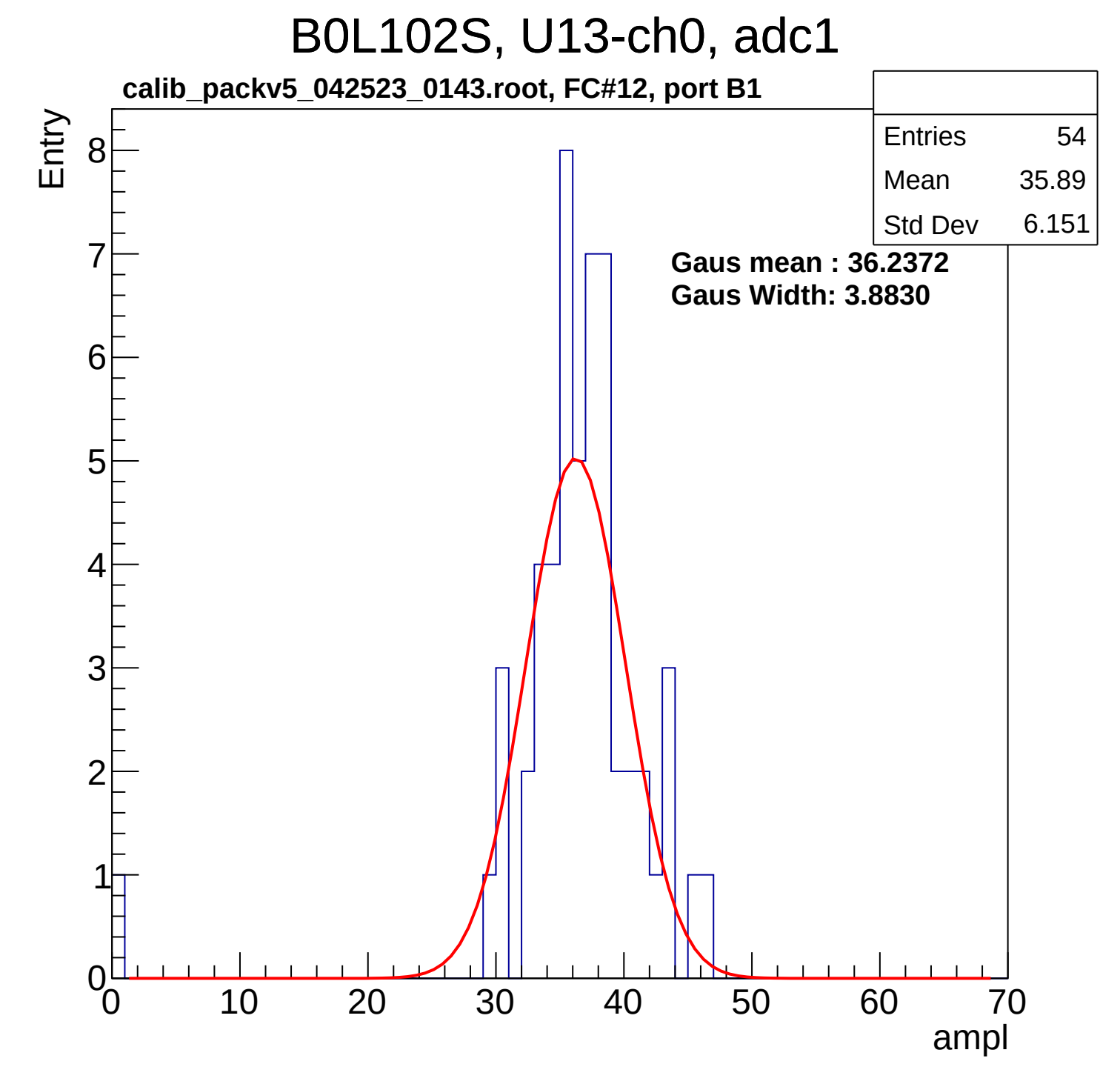
B0L102S, U13-ch0, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 35.89
Std Dev 6.151
Gaus mean : 36.2372
Gaus Width: 3.8830
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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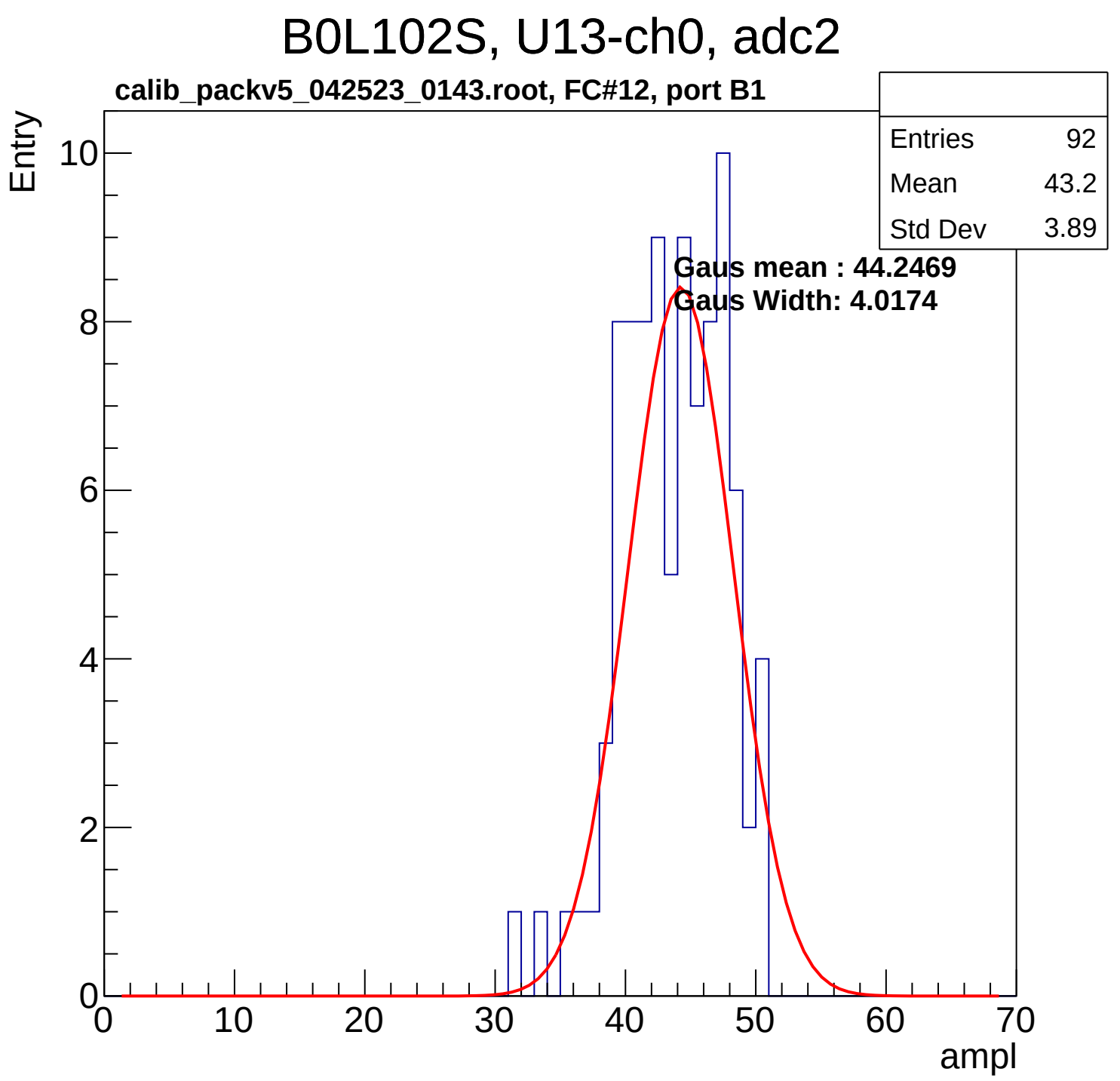
B0L102S, U13-ch0, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 92
Mean 43.2
Std Dev 3.89
Gaus mean : 44.2469
Gaus Width: 4.0174
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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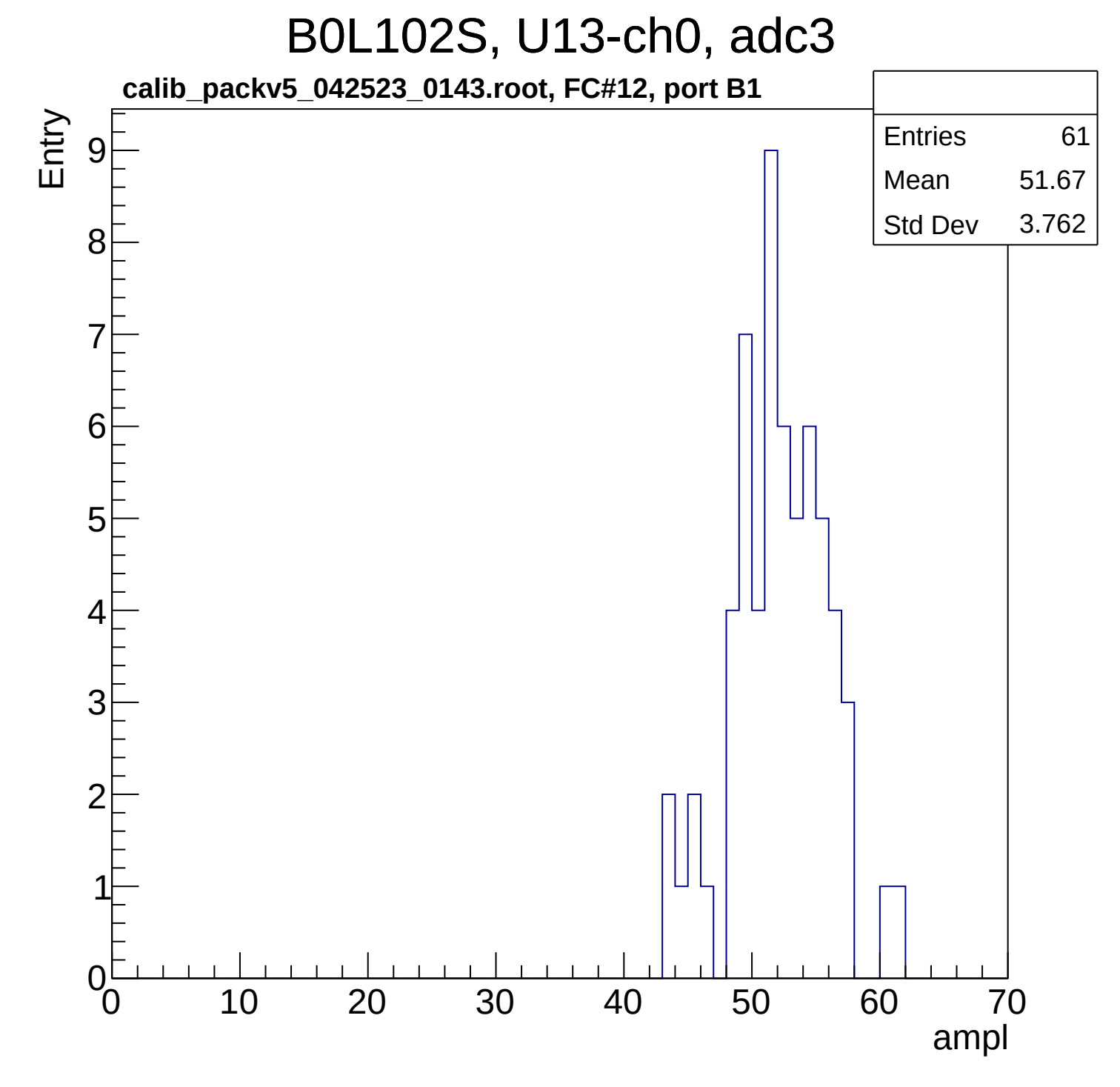
B0L102S, U13-ch0, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 51.67
Std Dev 3.762
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
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40
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60
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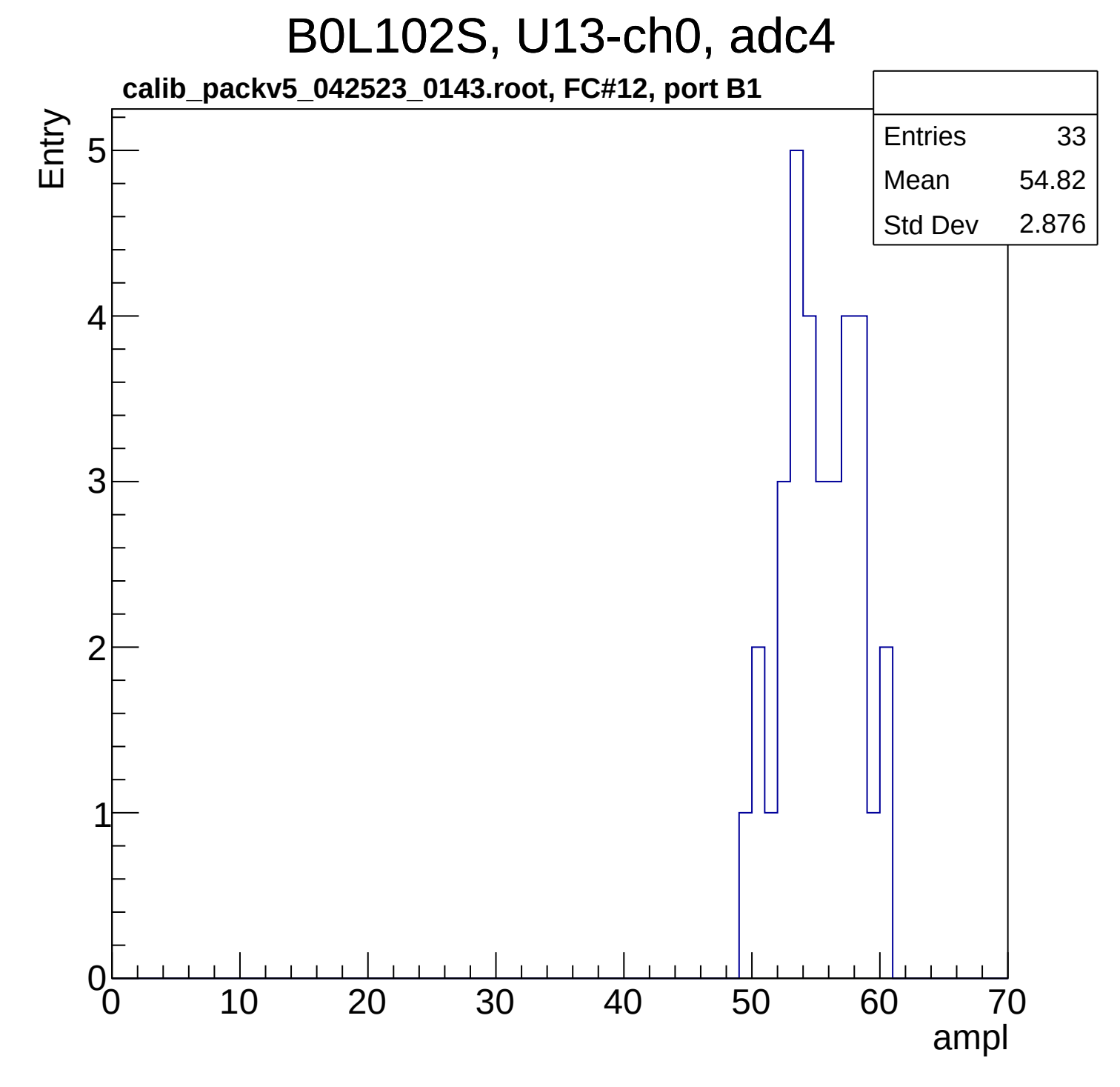
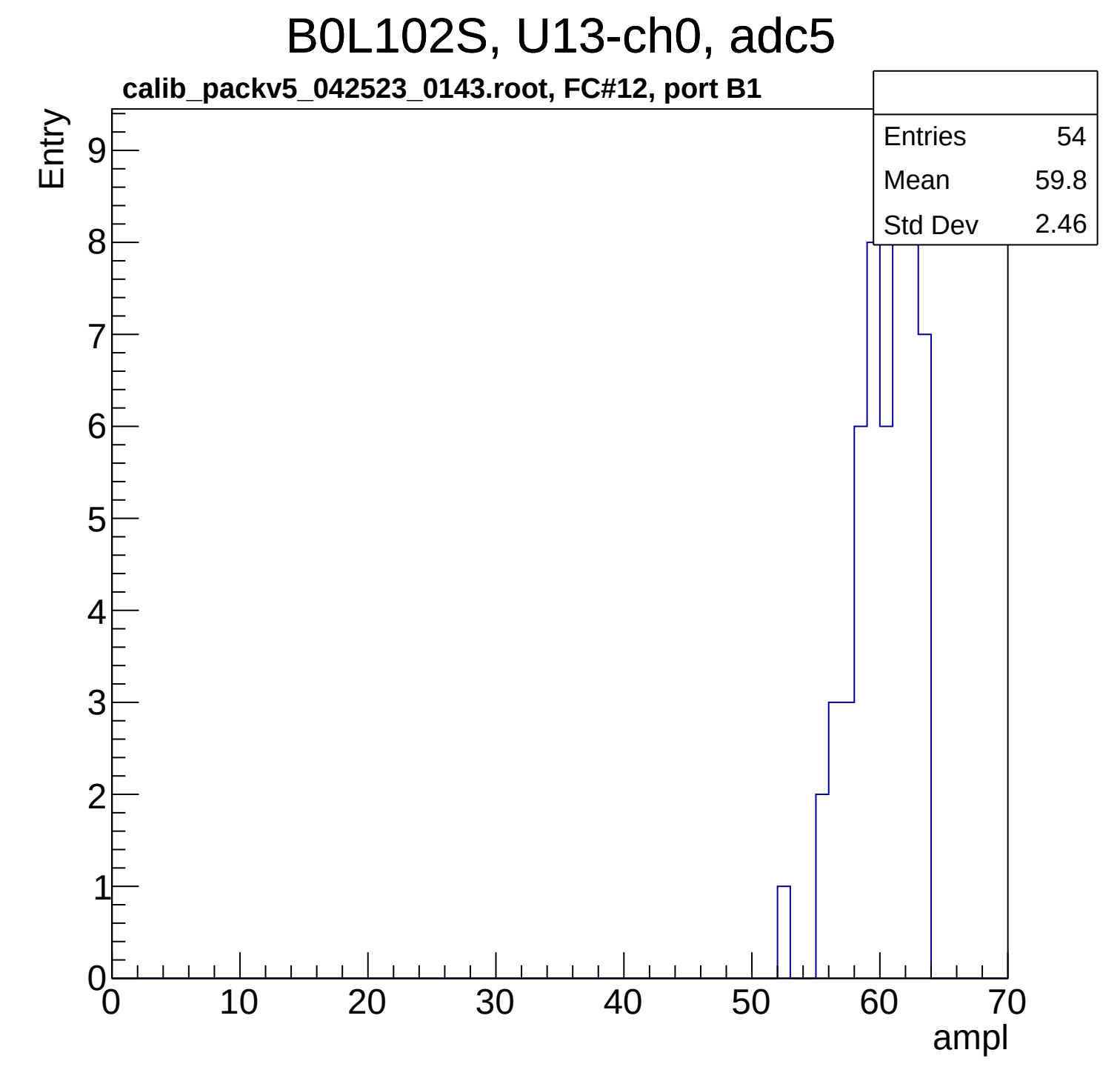

B0L102S, U13-ch0, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 54.82
Std Dev 2.876
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch0, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 59.8
Std Dev 2.46
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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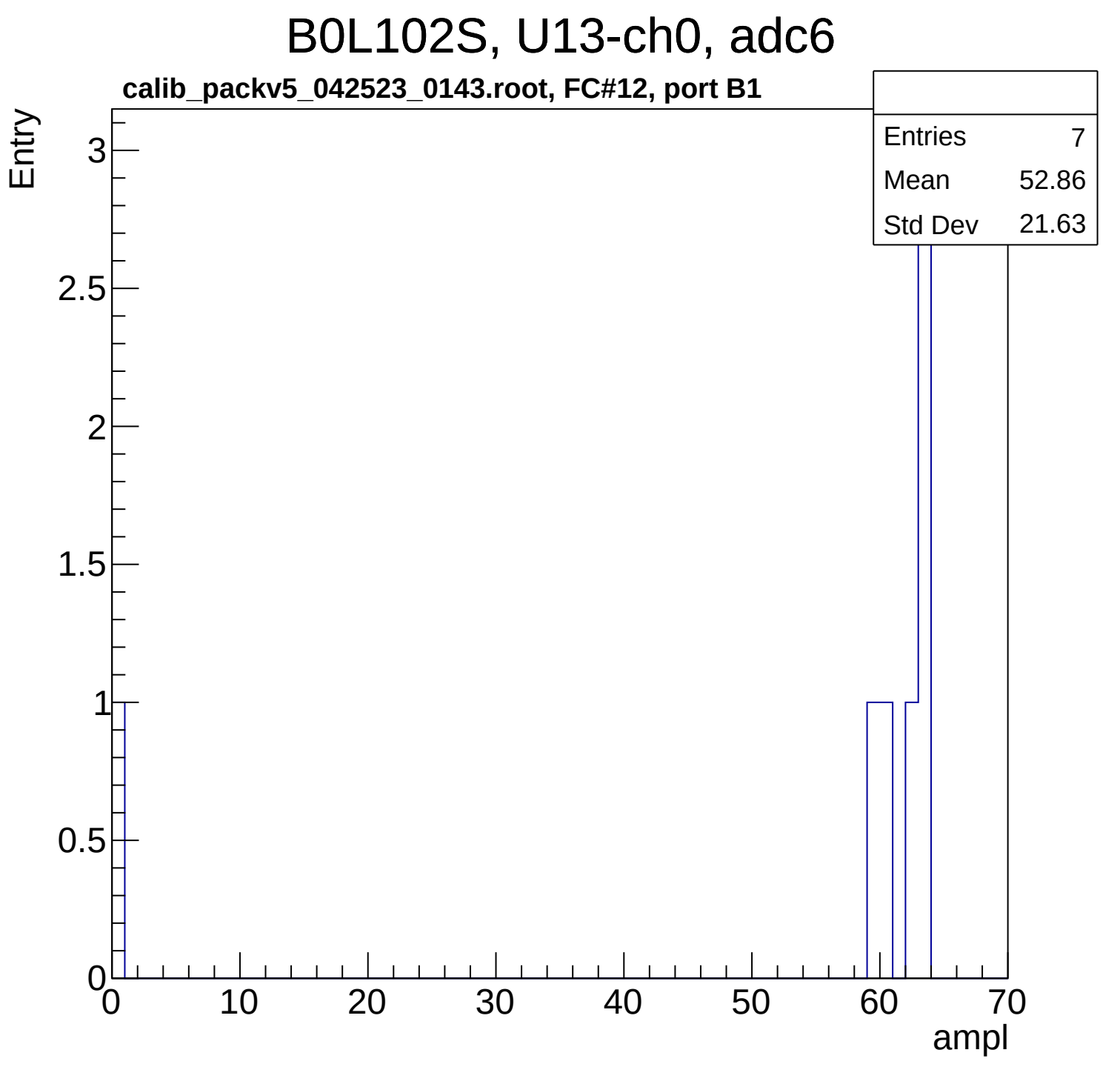
B0L102S, U13-ch0, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 52.86
Std Dev 21.63
Entry
ampl
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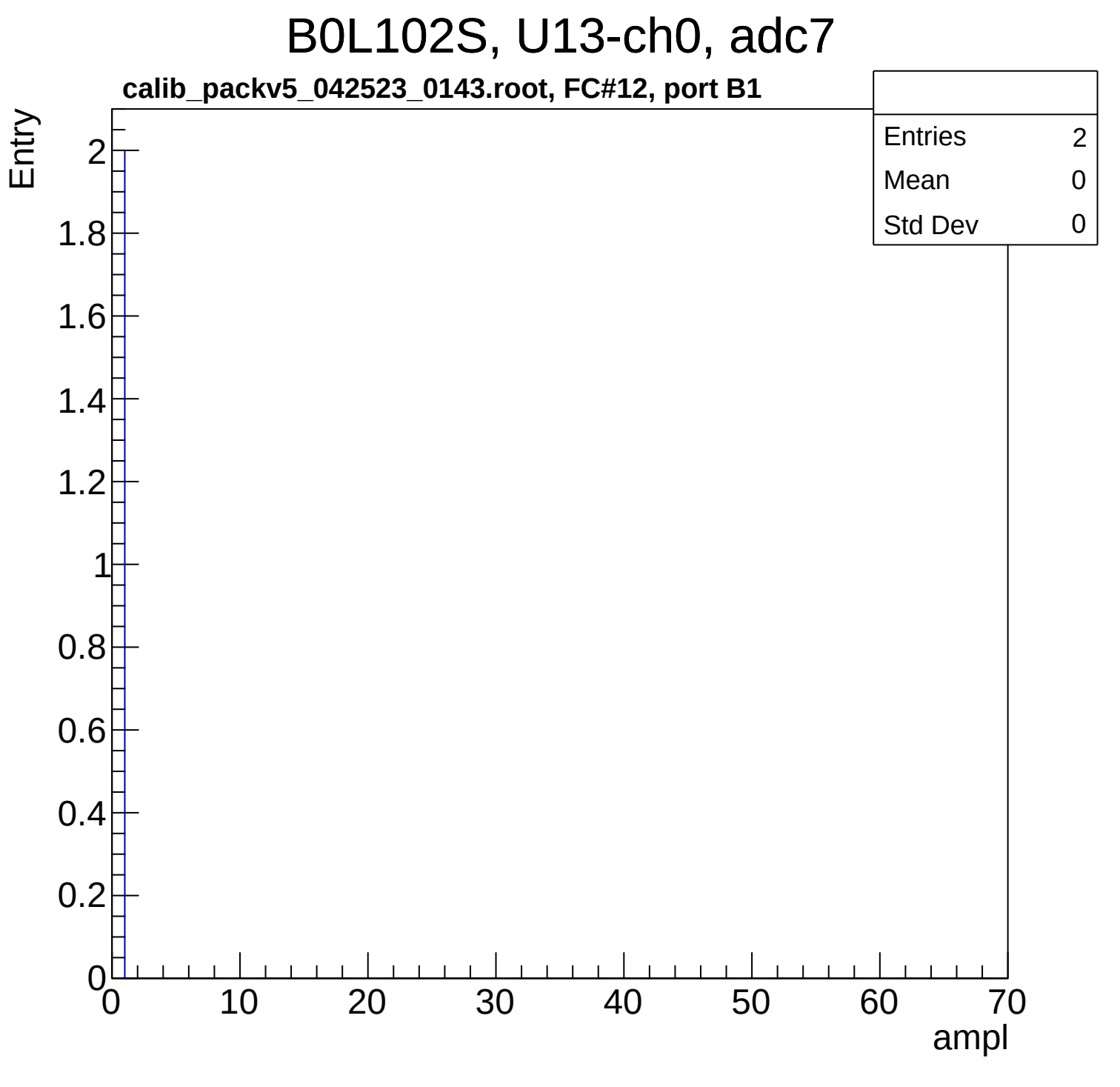
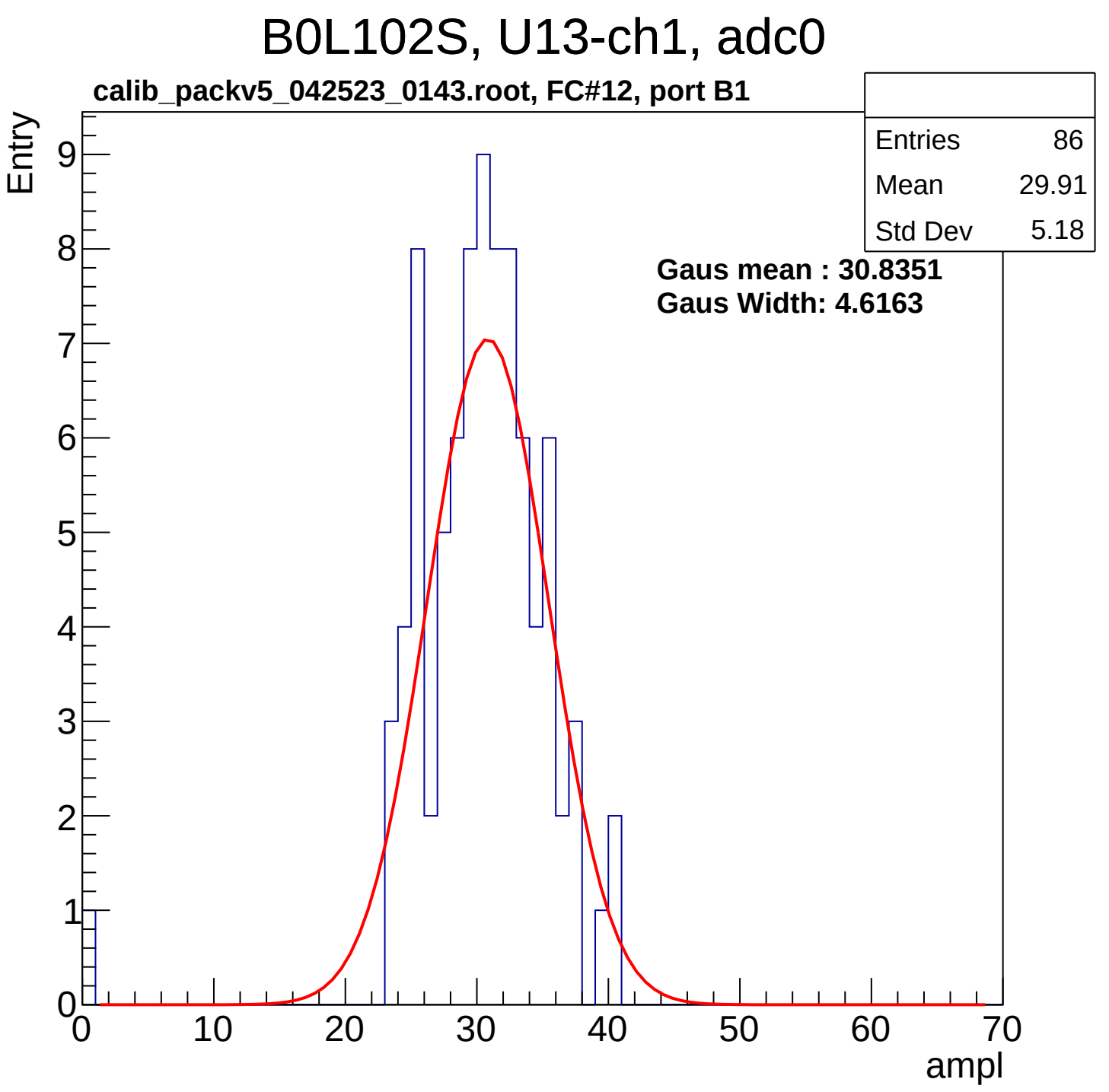
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch0, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch1, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 29.91
Std Dev 5.18
Gaus mean : 30.8351
Gaus Width: 4.6163
Entry
ampl
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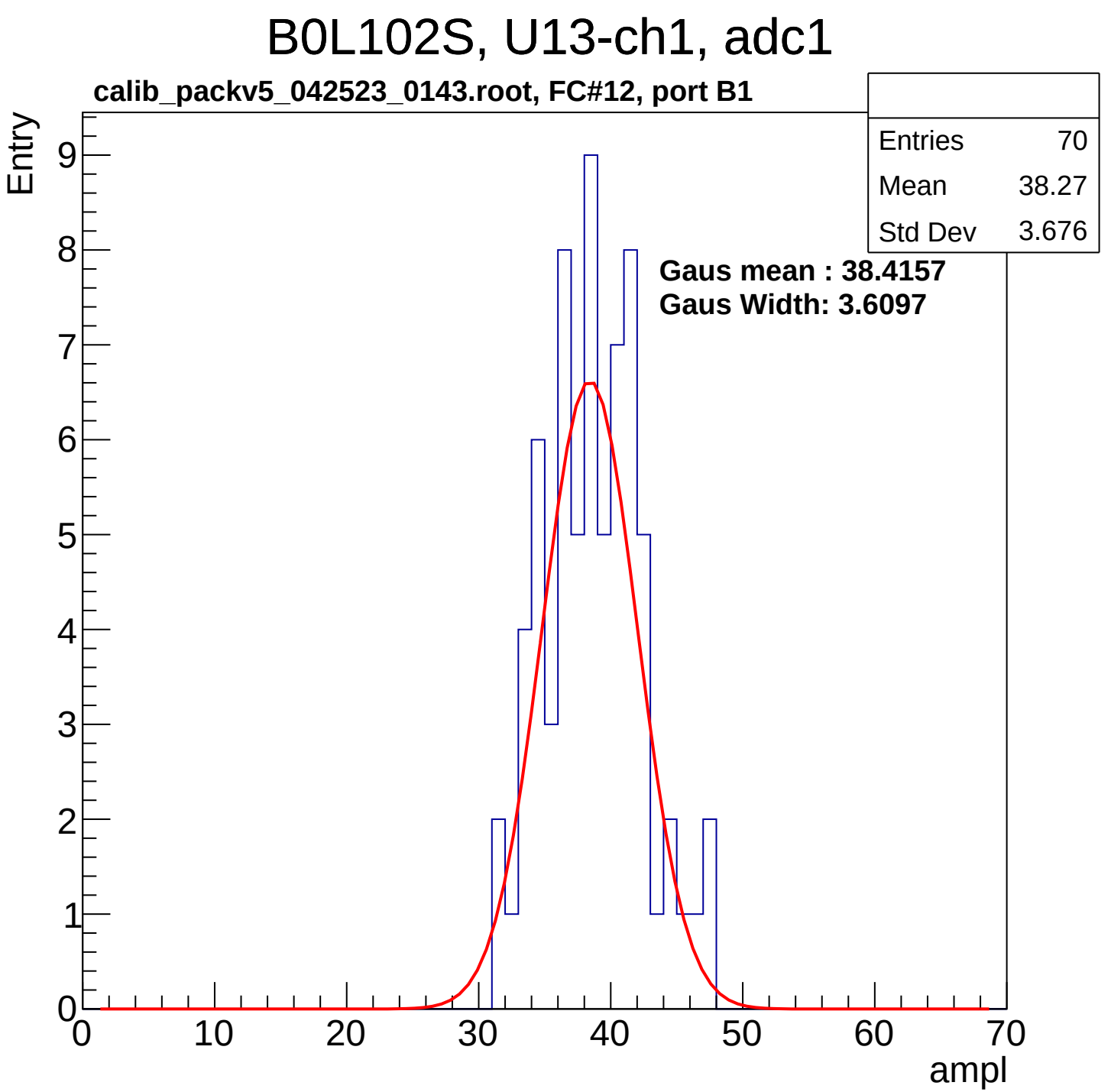
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch1, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 38.27
Std Dev 3.676
Gaus mean : 38.4157
Gaus Width: 3.6097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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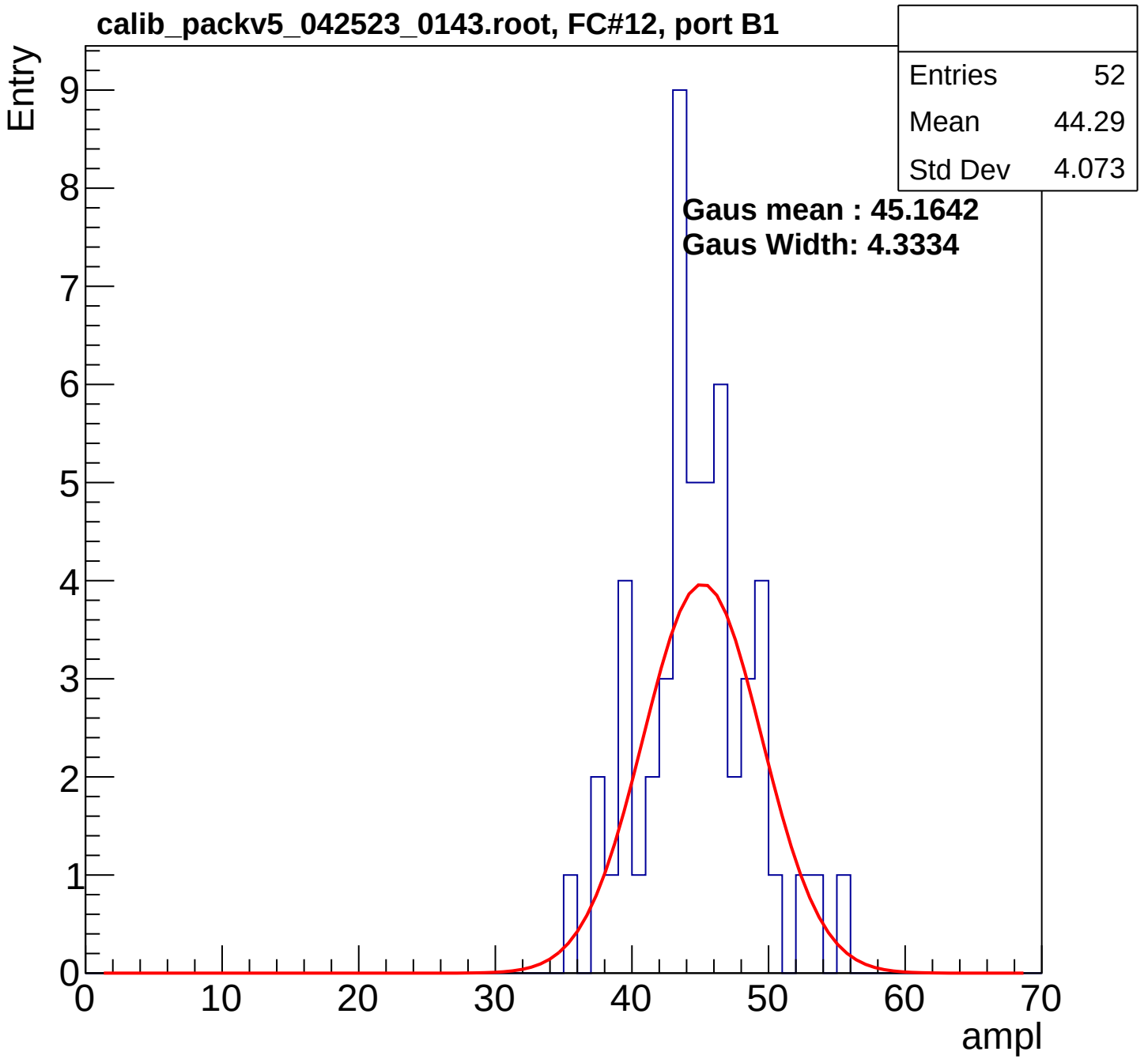
B0L102S, U13-ch1, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 44.29
Std Dev 4.073
Gaus mean : 45.1642
Gaus Width: 4.3334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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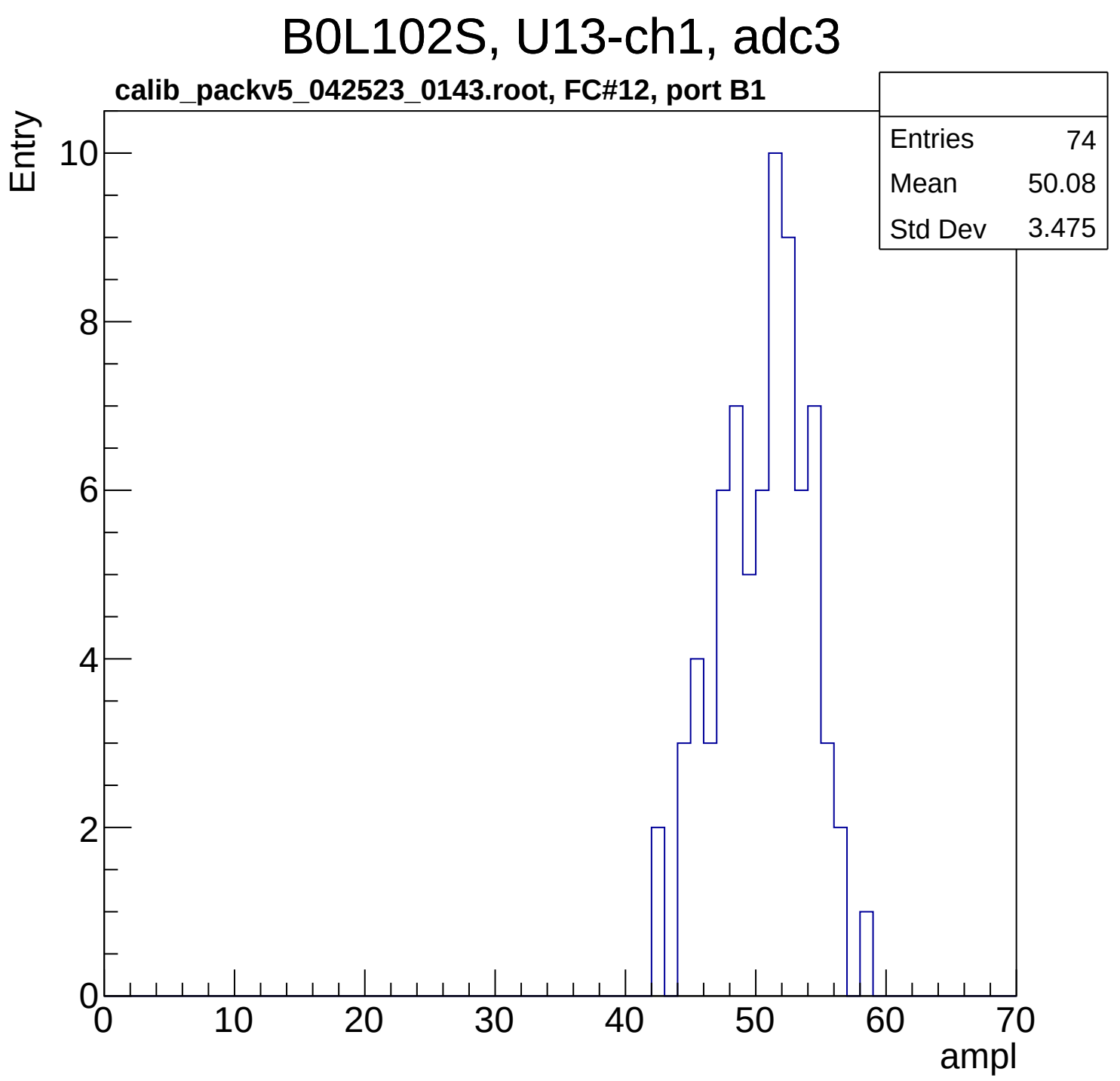
B0L102S, U13-ch1, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 50.08
Std Dev 3.475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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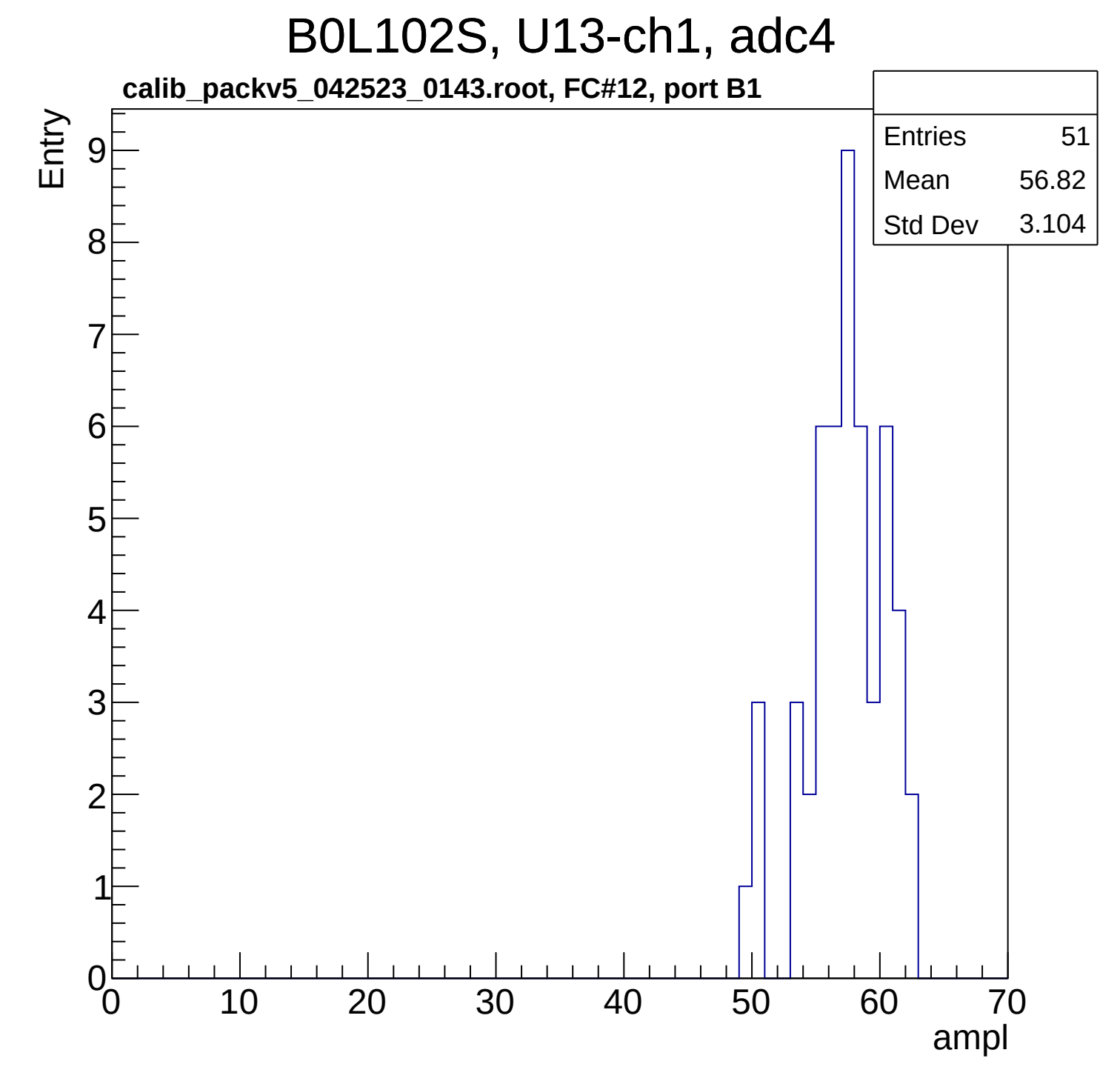
B0L102S, U13-ch1, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 56.82
Std Dev 3.104
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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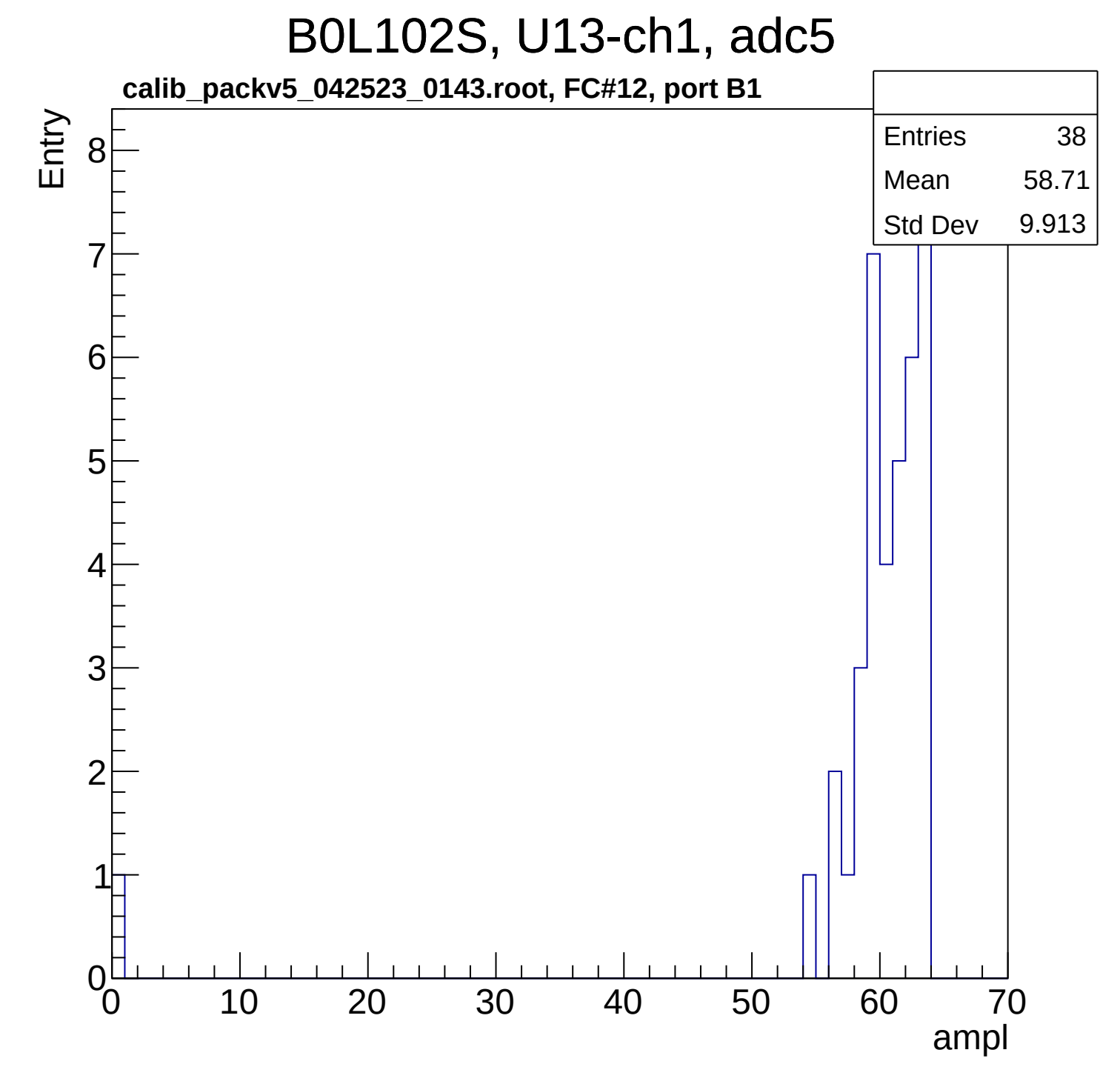
B0L102S, U13-ch1, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.71
Std Dev 9.913
Entry
ampl
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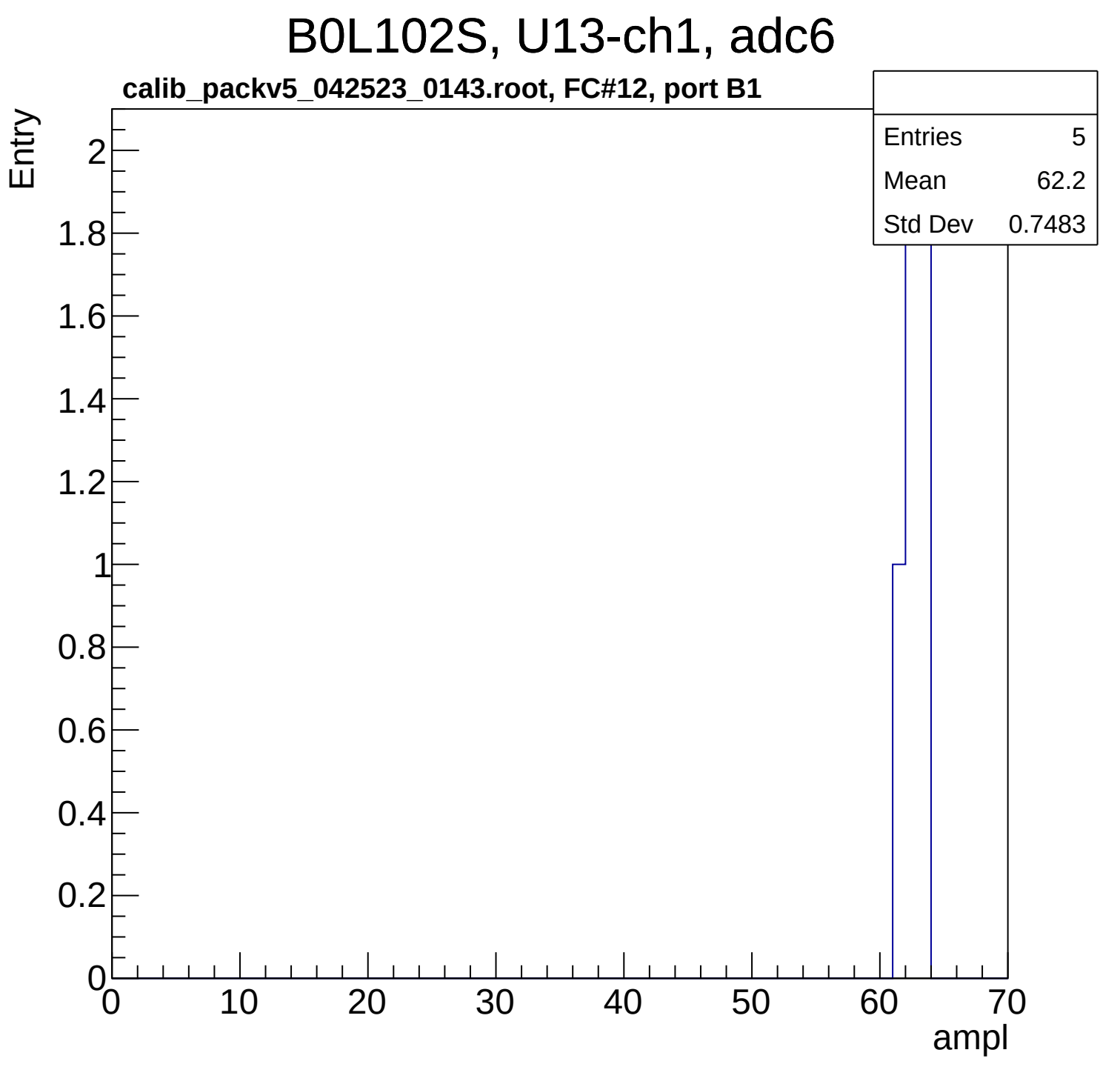
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch1, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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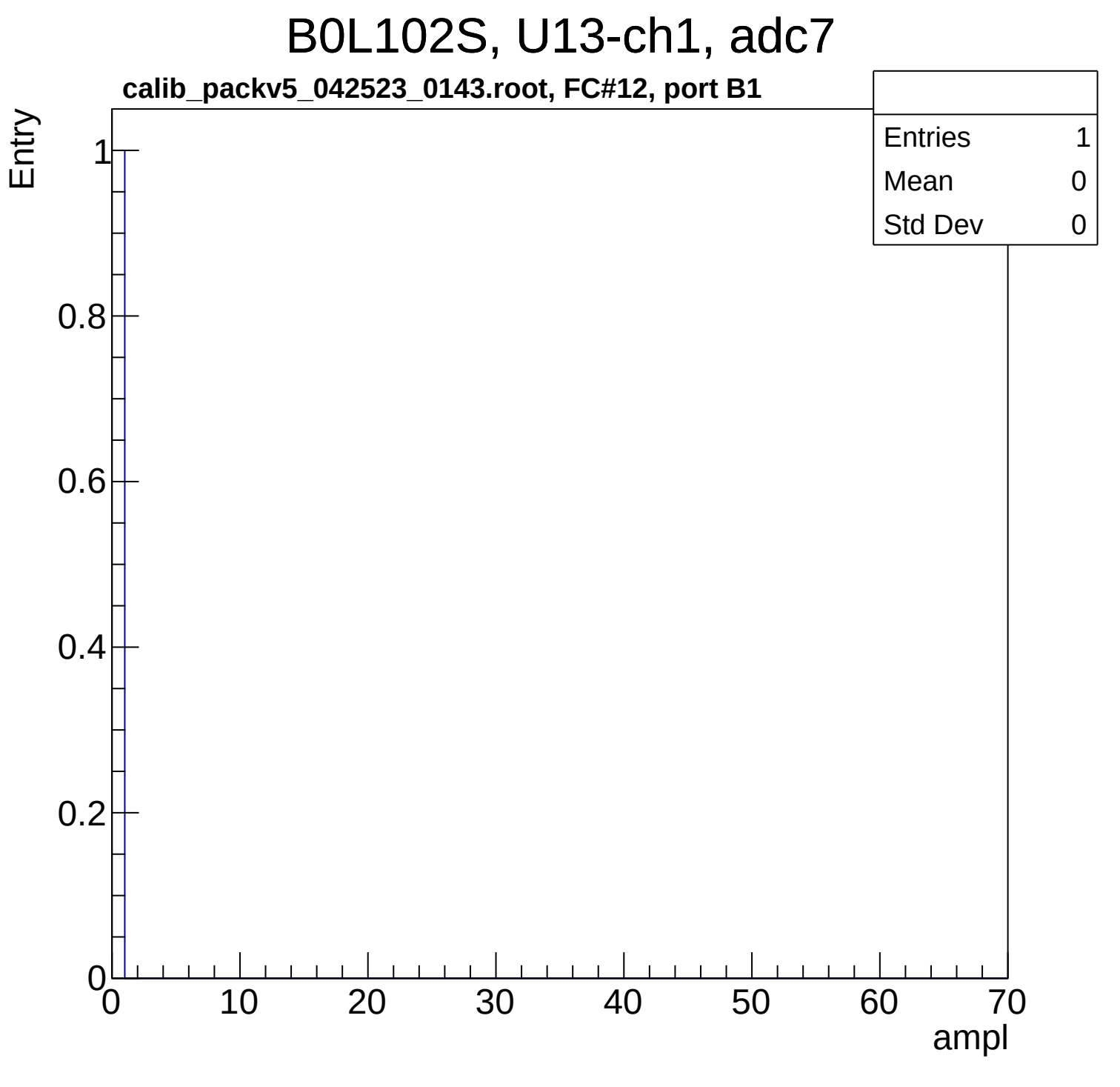
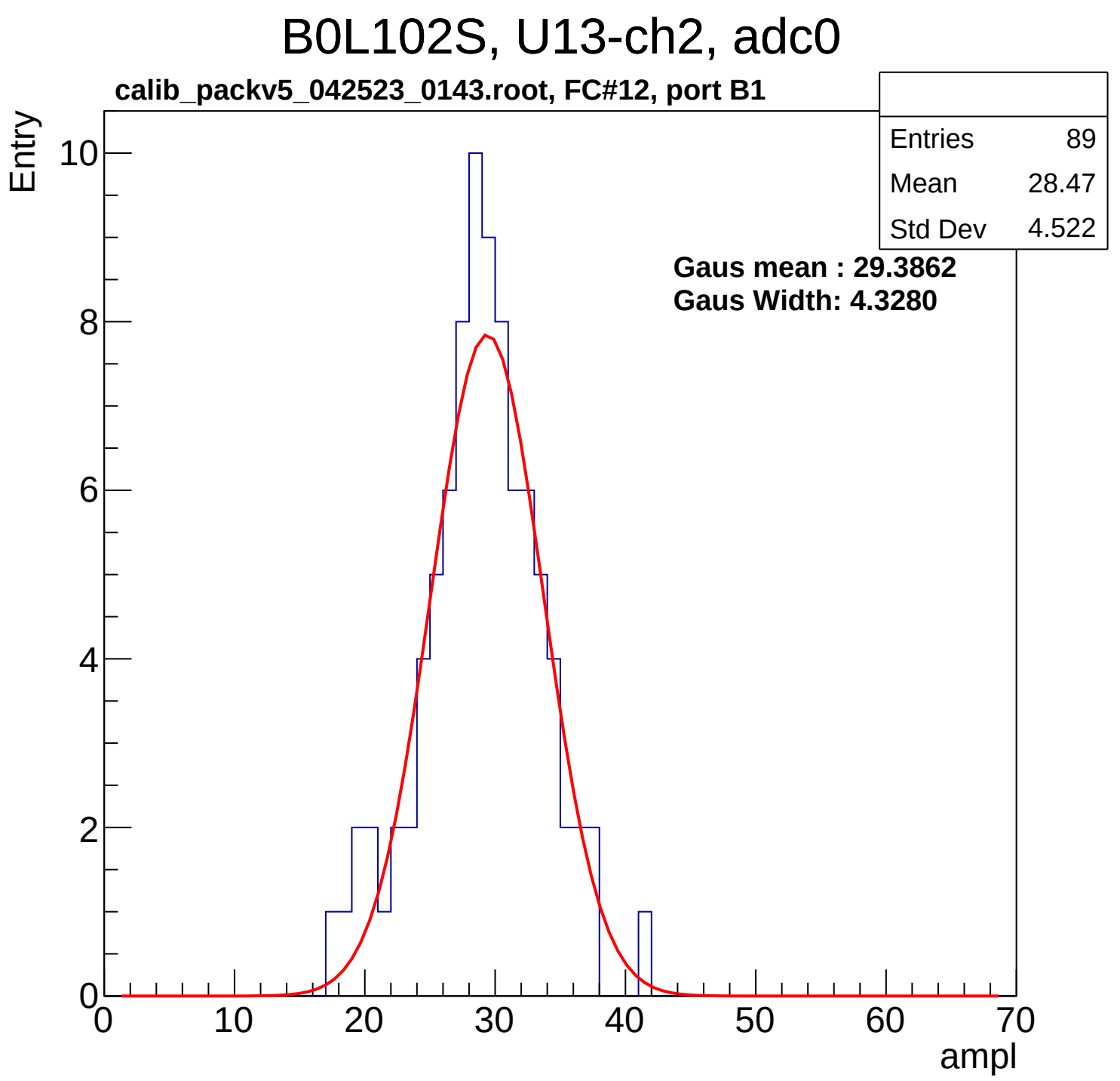

B0L102S, U13-ch1, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch2, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 28.47
Std Dev 4.522
Gaus mean : 29.3862
Gaus Width: 4.3280
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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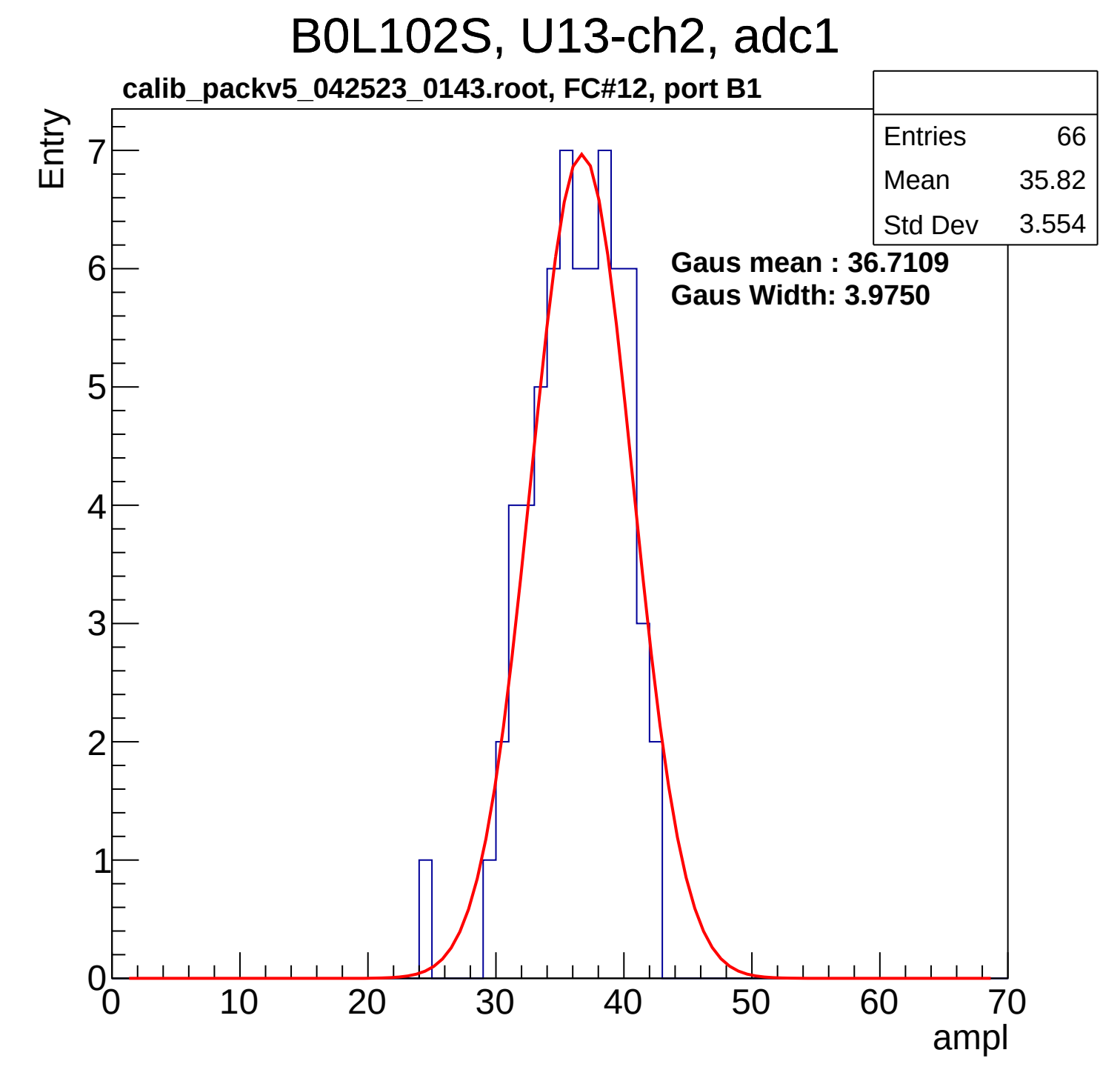
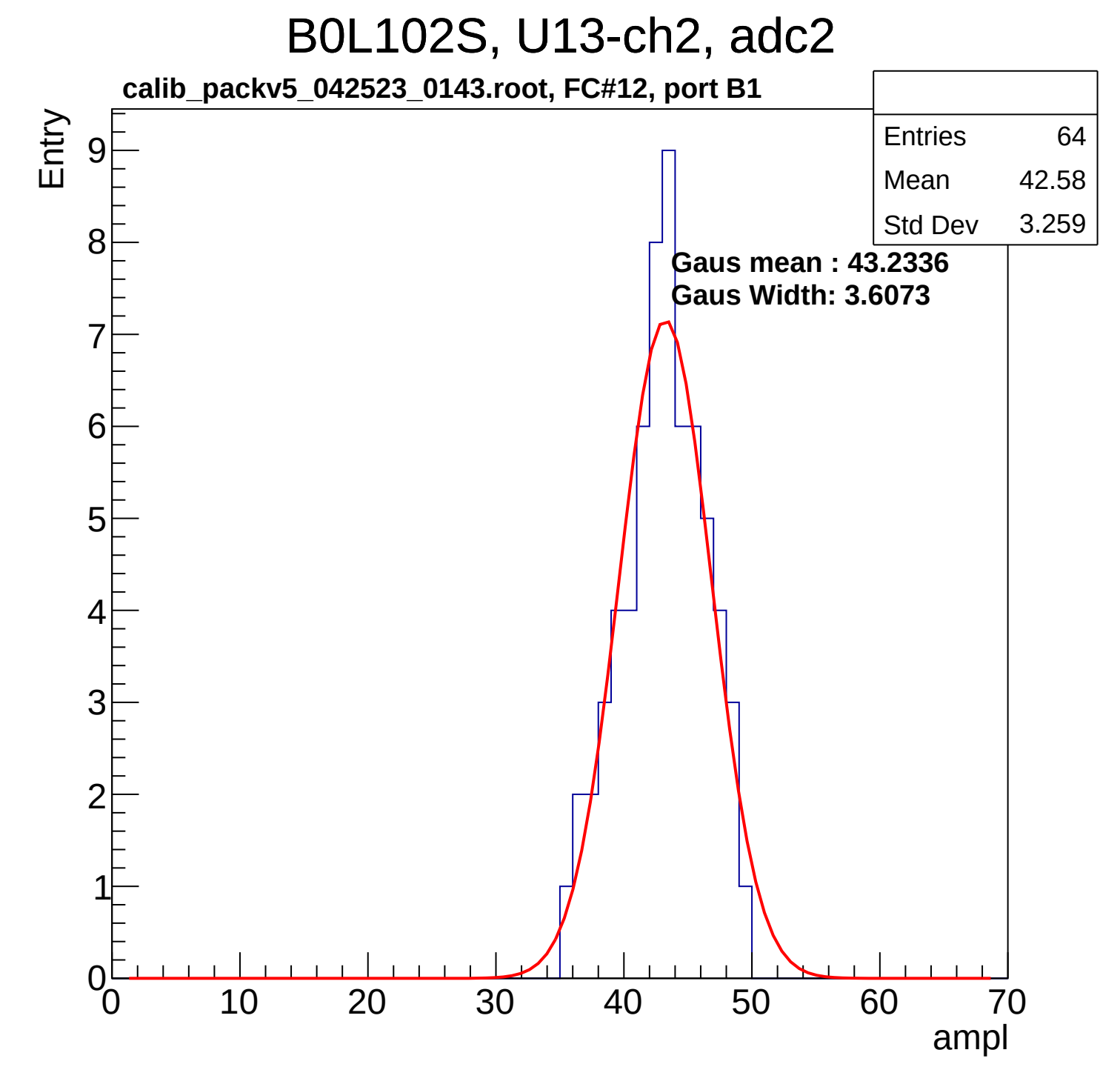

B0L102S, U13-ch2, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 35.82
Std Dev 3.554
Gaus mean : 36.7109
Gaus Width: 3.9750
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch2, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.58
Std Dev 3.259
Gaus mean : 43.2336
Gaus Width: 3.6073
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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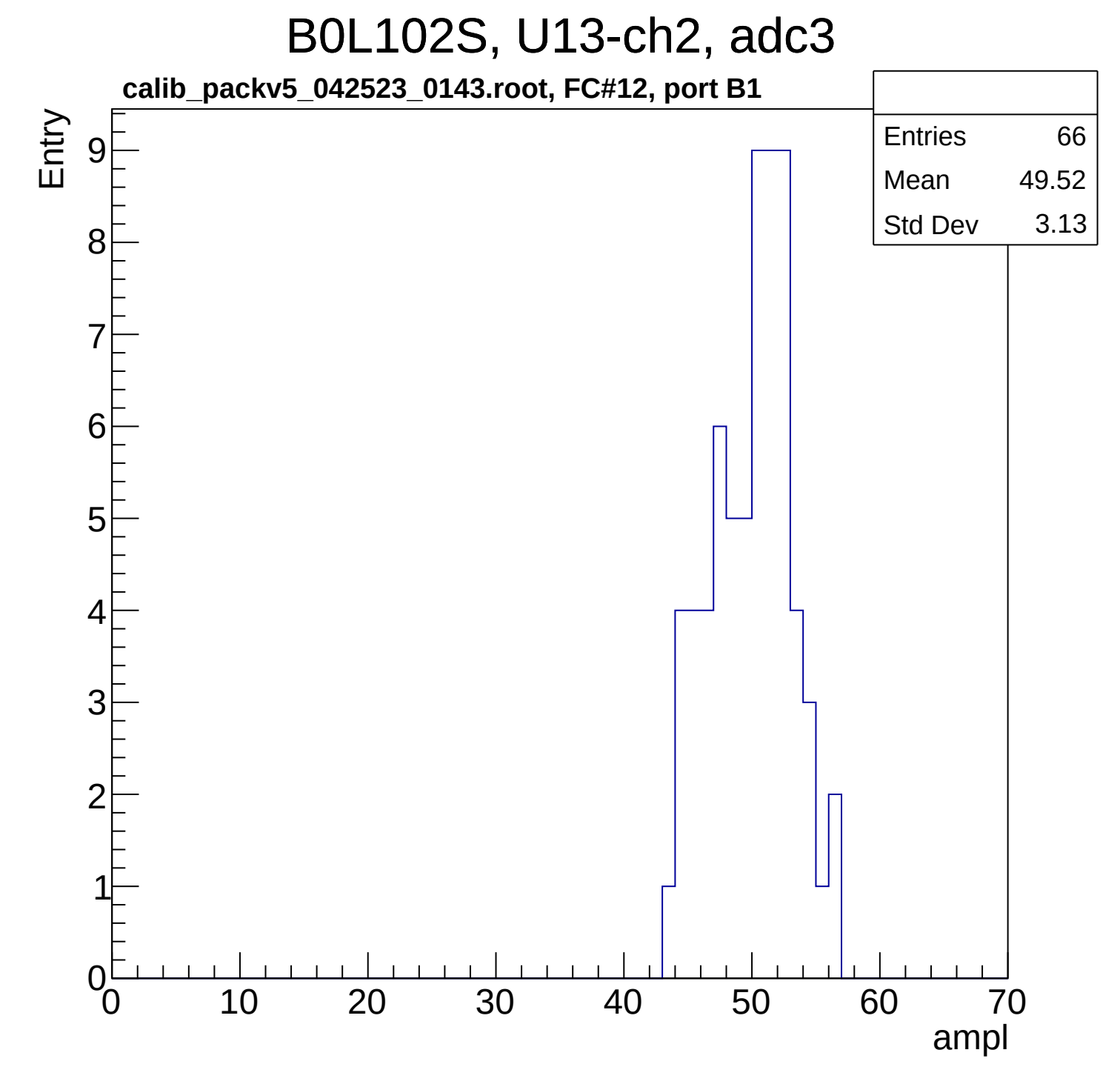
B0L102S, U13-ch2, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.52
Std Dev 3.13
Entry
ampl
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3
4
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7
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0
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60
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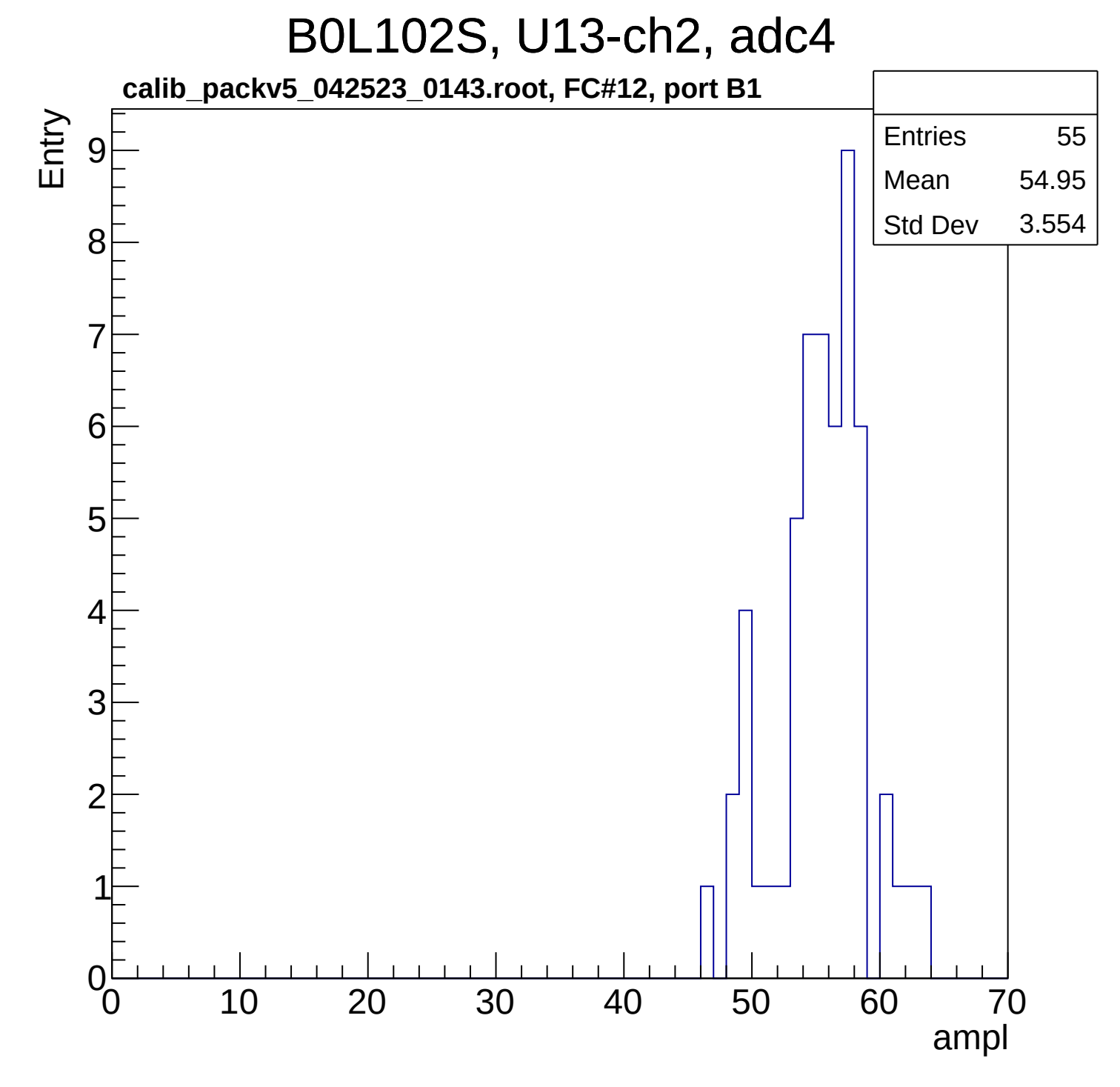
B0L102S, U13-ch2, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.95
Std Dev 3.554
Entry
ampl
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7
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0
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60
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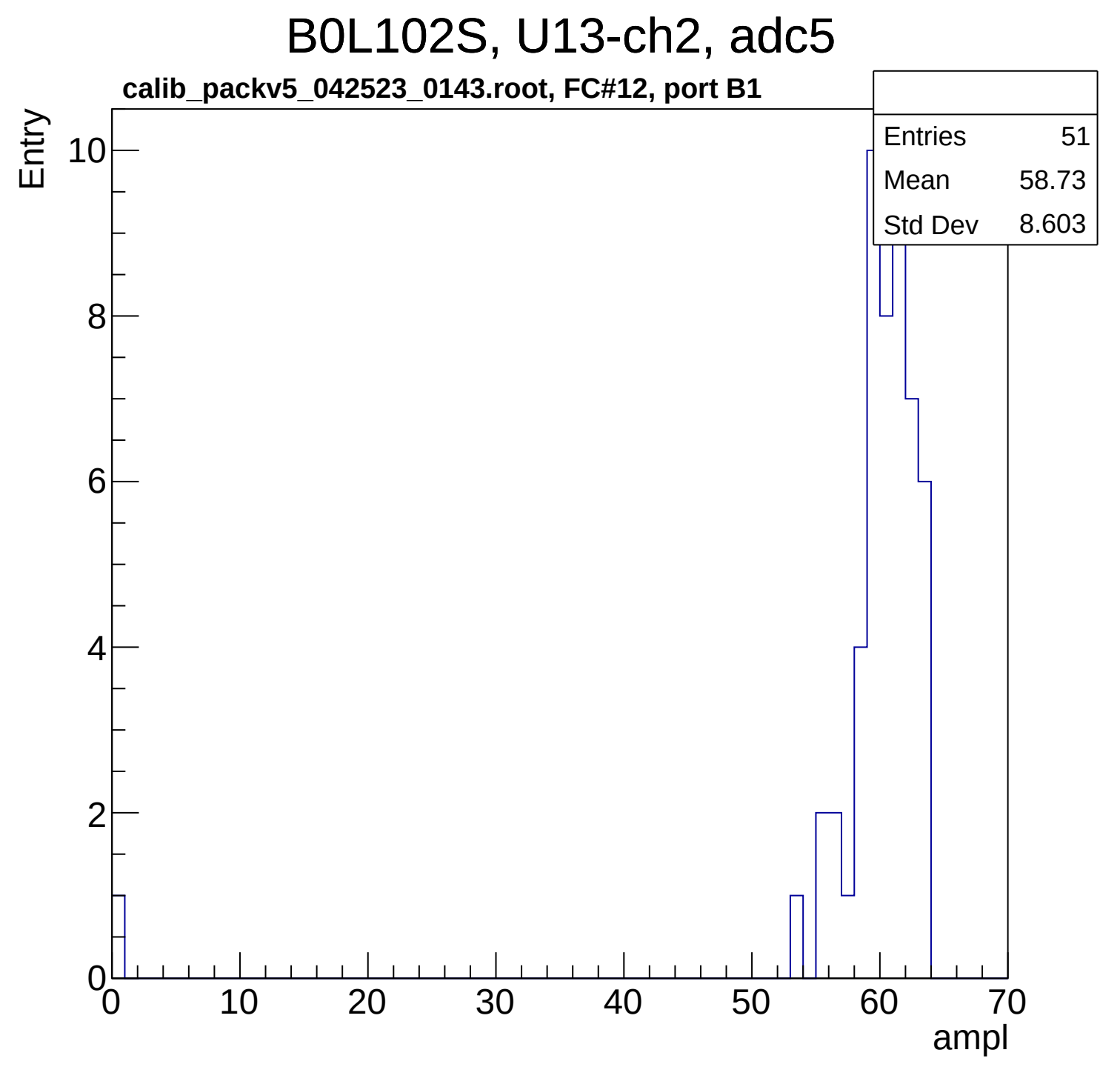
B0L102S, U13-ch2, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 58.73
Std Dev 8.603
Entry
ampl
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60
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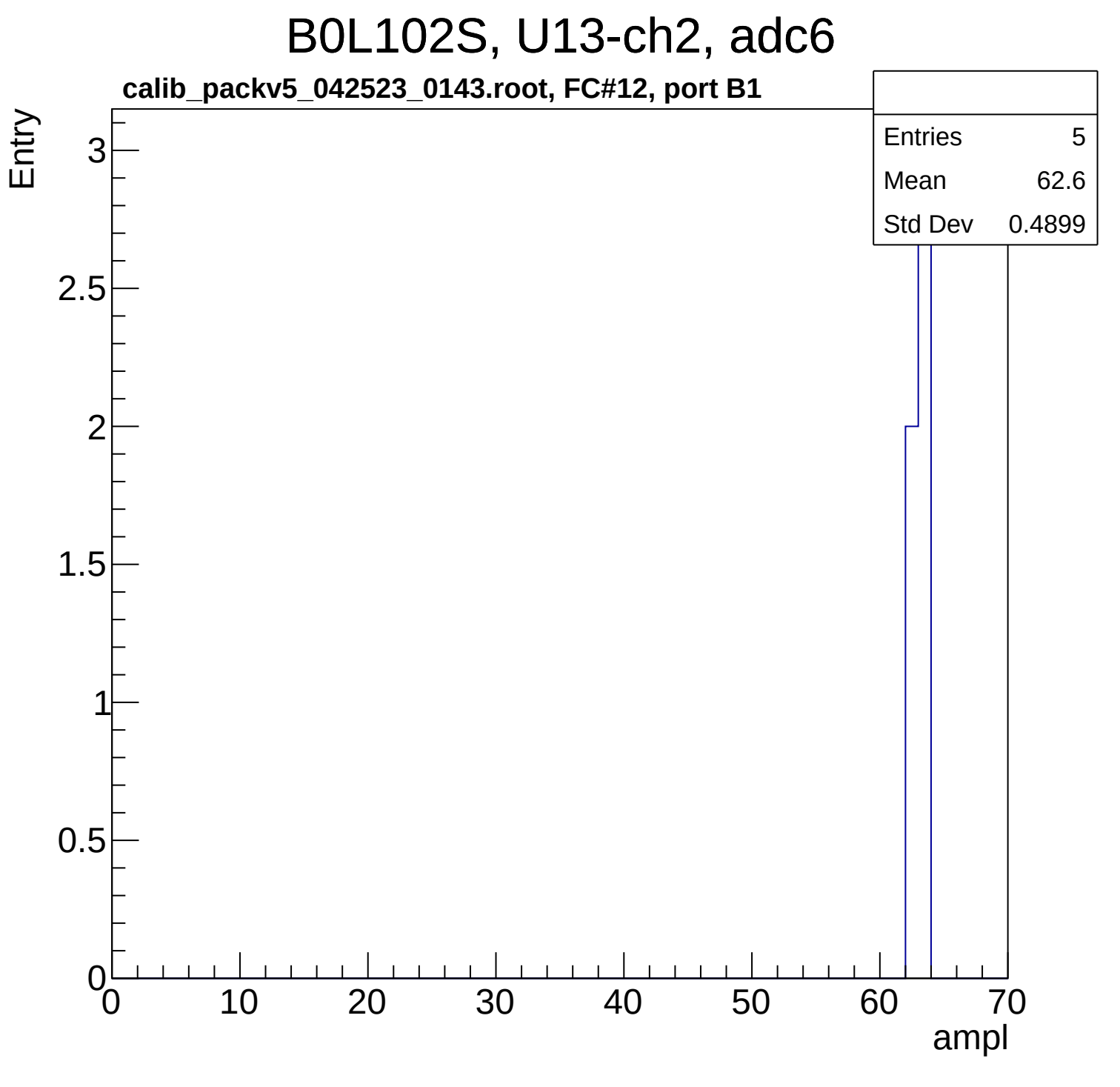
B0L102S, U13-ch2, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
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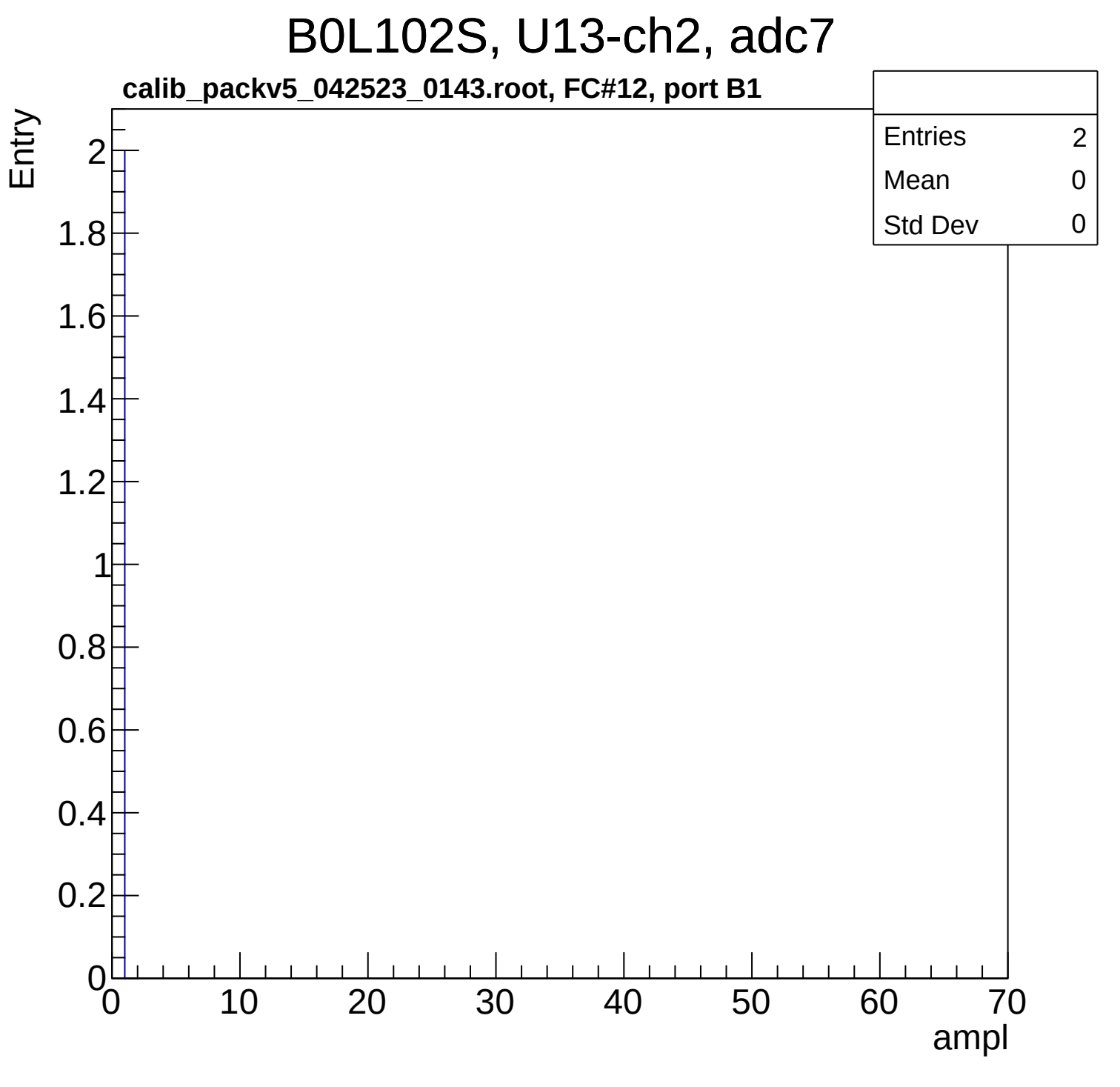
40
50
60
70


B0L102S, U13-ch2, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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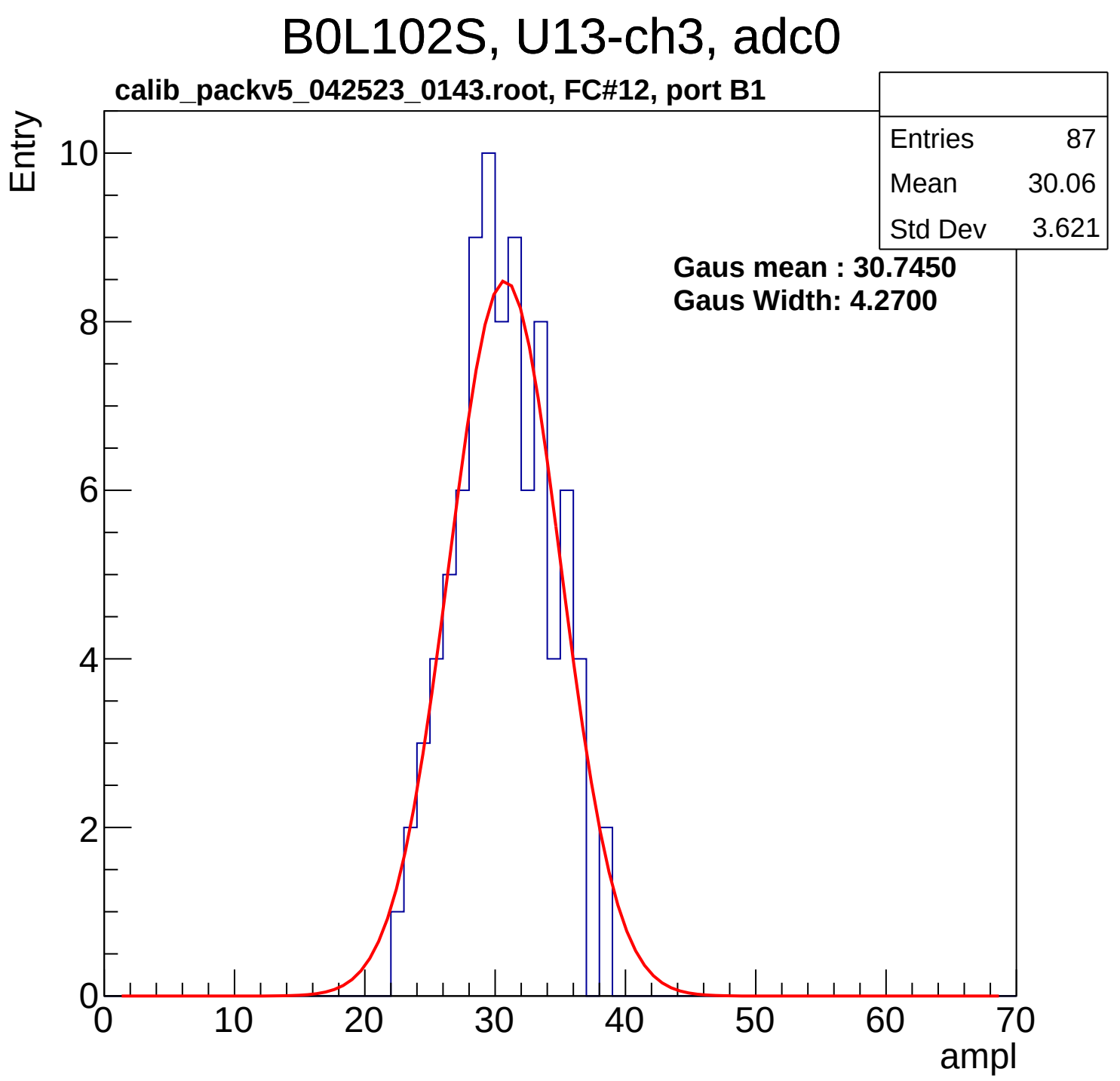
B0L102S, U13-ch3, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 30.06
Std Dev 3.621
Gaus mean : 30.7450
Gaus Width: 4.2700
Entry
ampl
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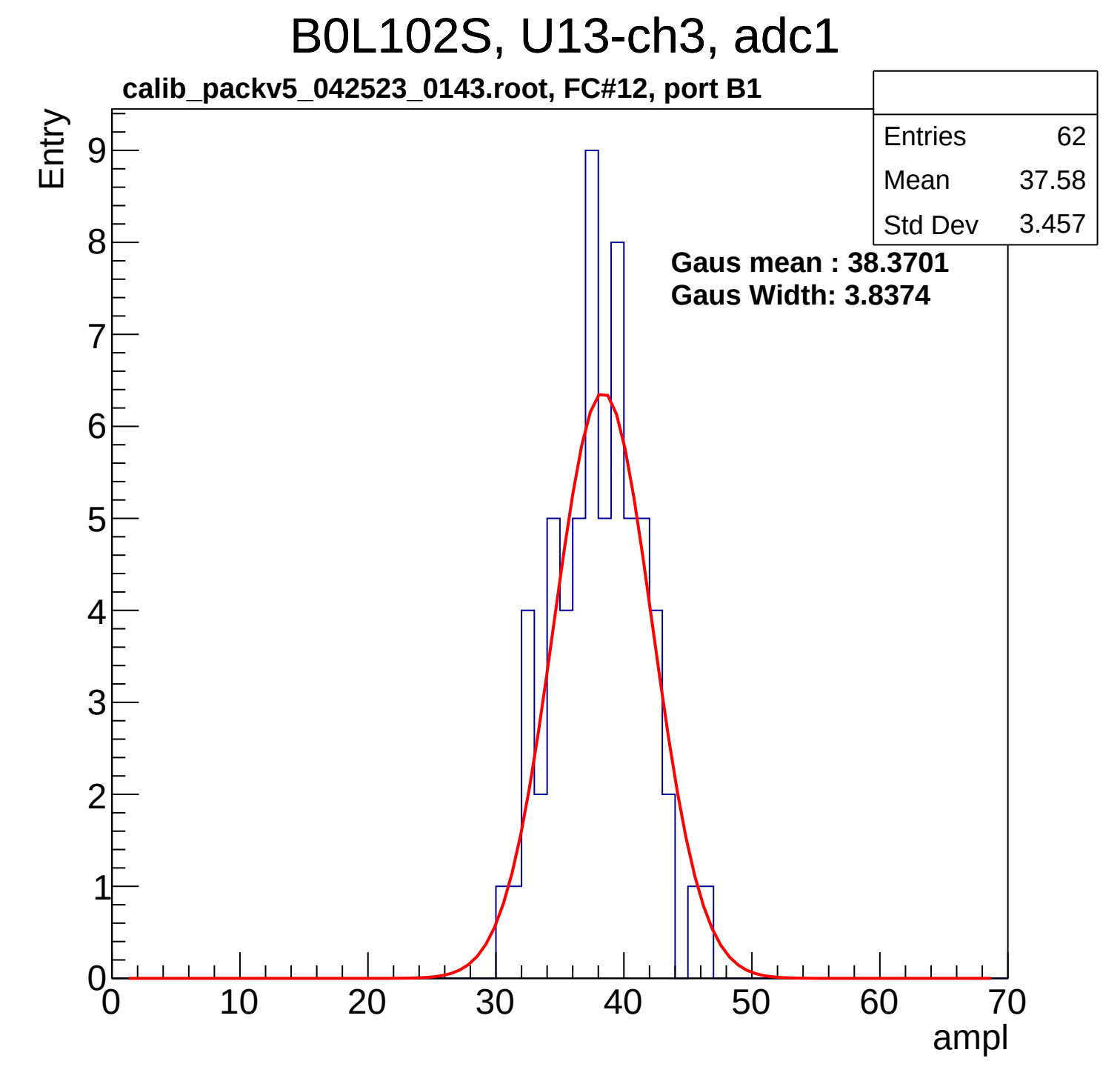
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch3, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 37.58
Std Dev 3.457
Gaus mean : 38.3701
Gaus Width: 3.8374
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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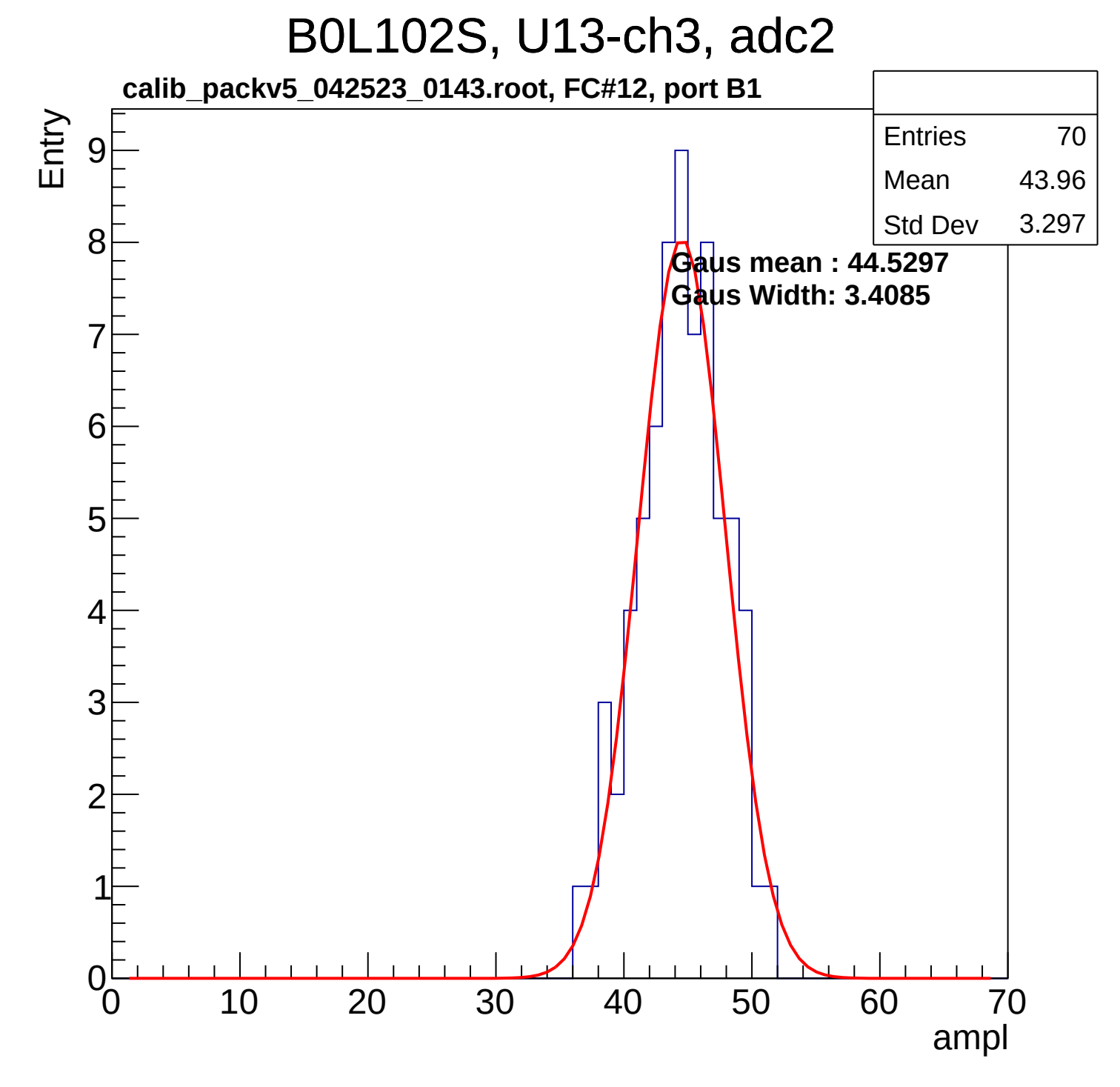
B0L102S, U13-ch3, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.96
Std Dev 3.297
Gaus mean : 44.5297
Gaus Width: 3.4085
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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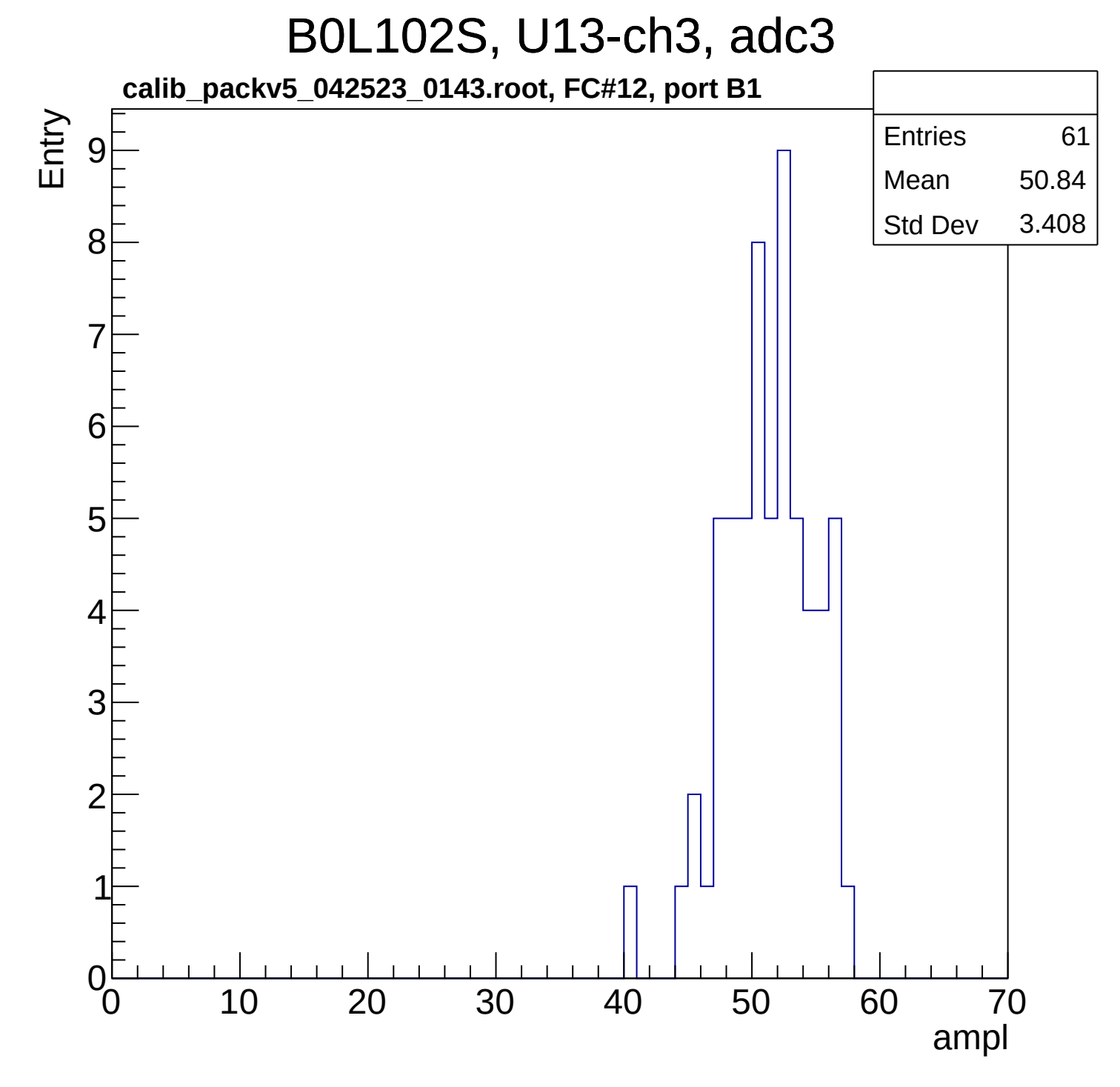
B0L102S, U13-ch3, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 50.84
Std Dev 3.408
Entry
ampl
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3
4
5
6
7
8
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0
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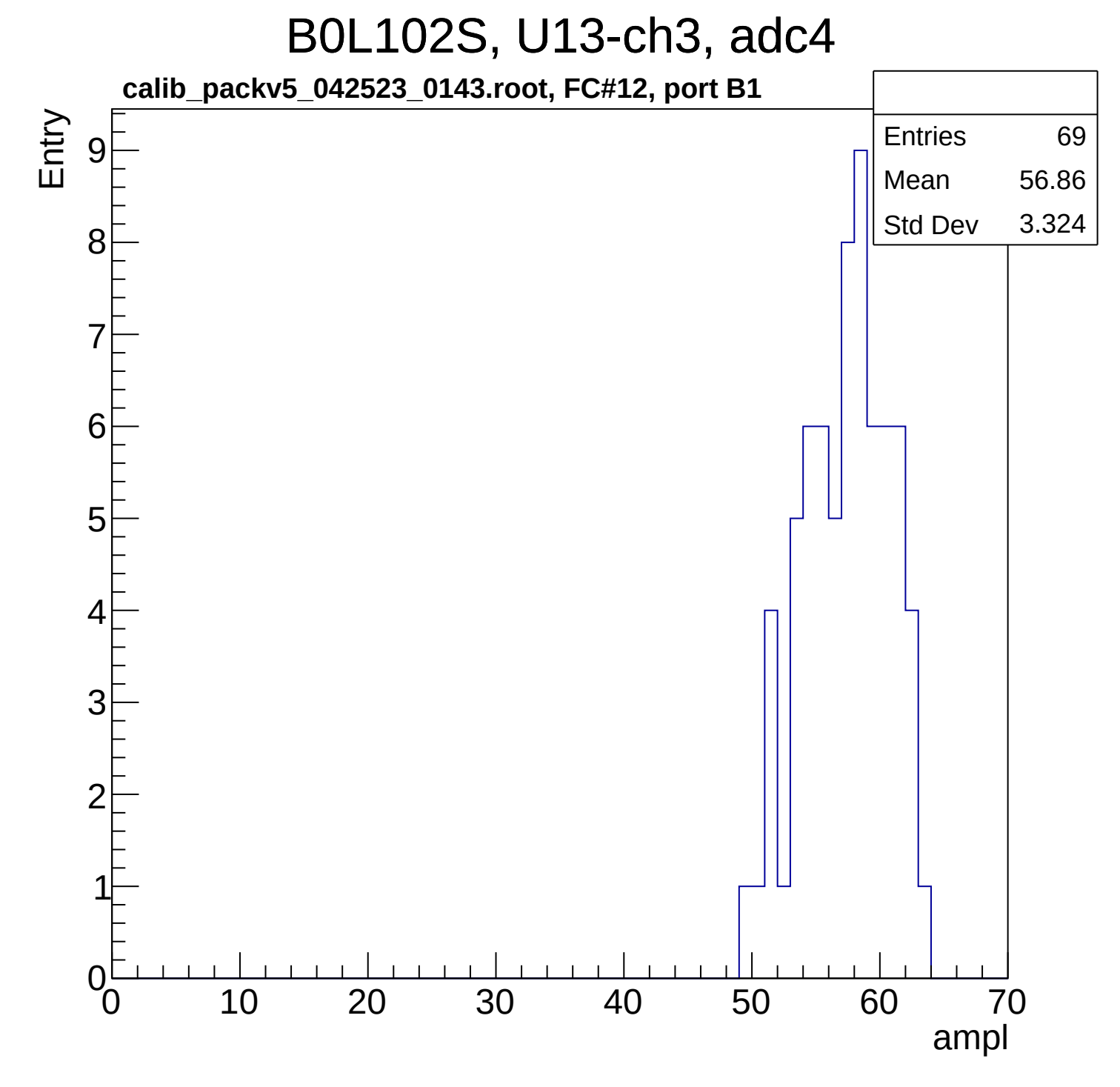
B0L102S, U13-ch3, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 56.86
Std Dev 3.324
Entry
ampl
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2
3
4
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20
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40
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60
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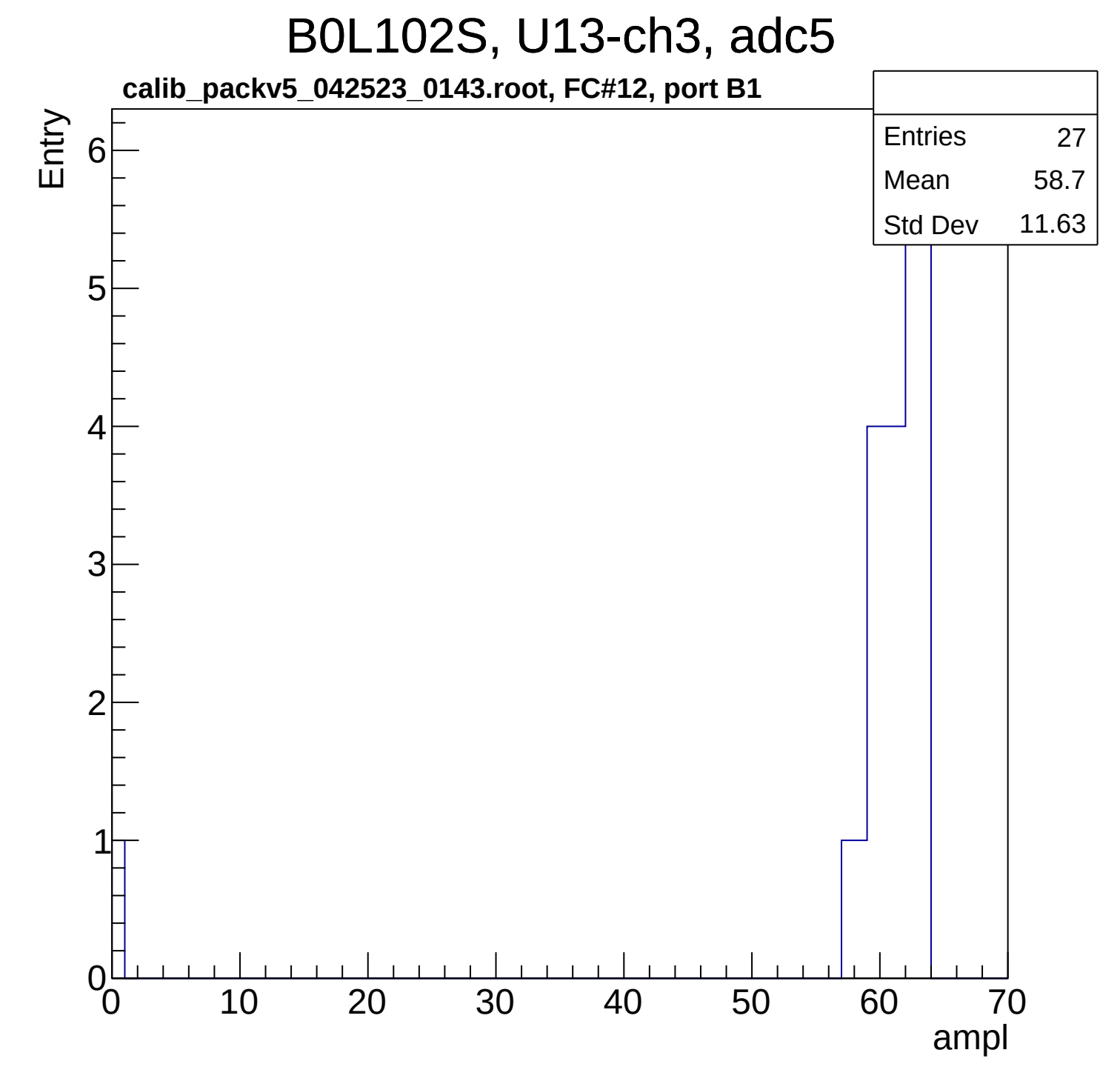
B0L102S, U13-ch3, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.7
Std Dev 11.63
Entry
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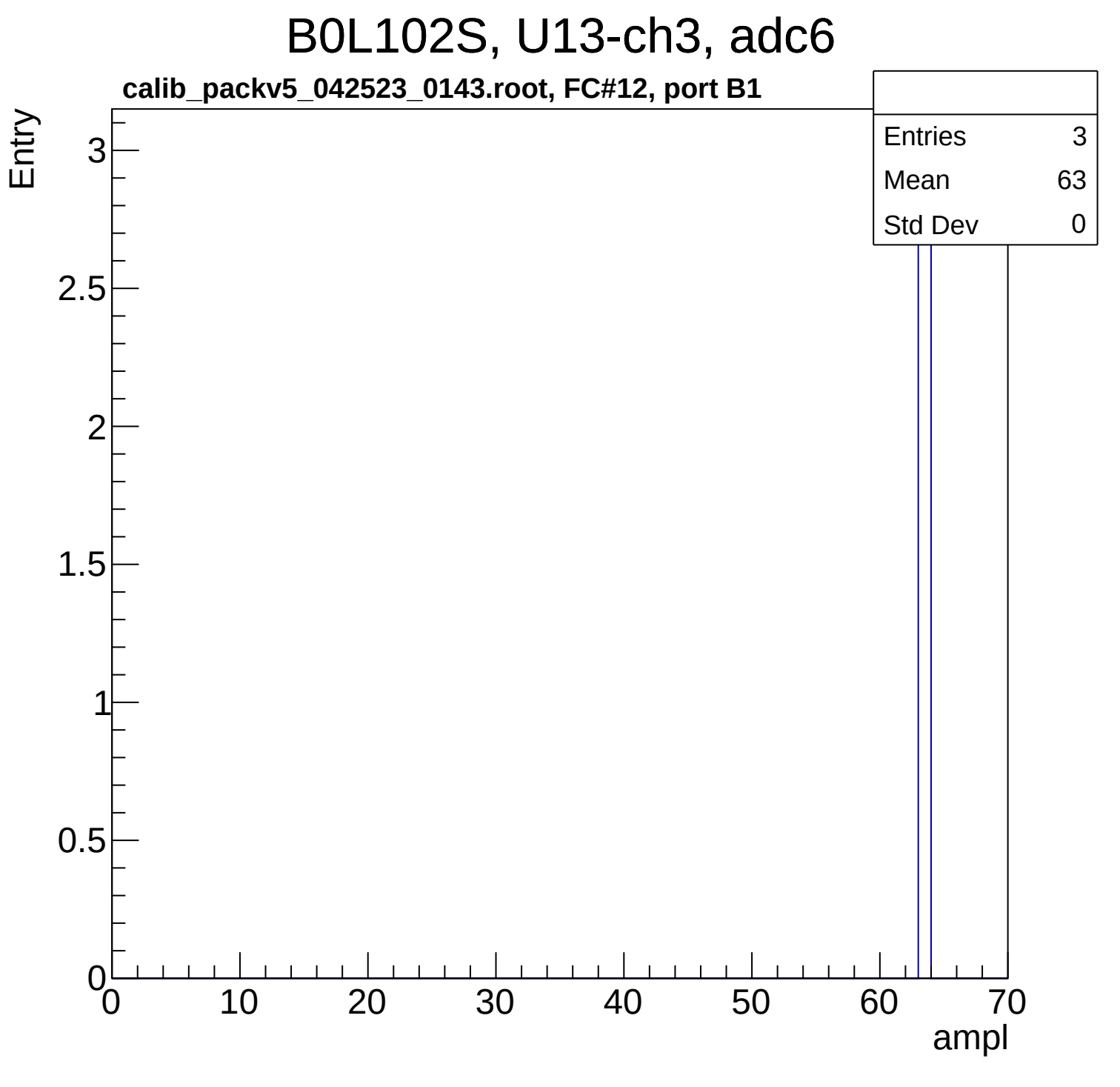
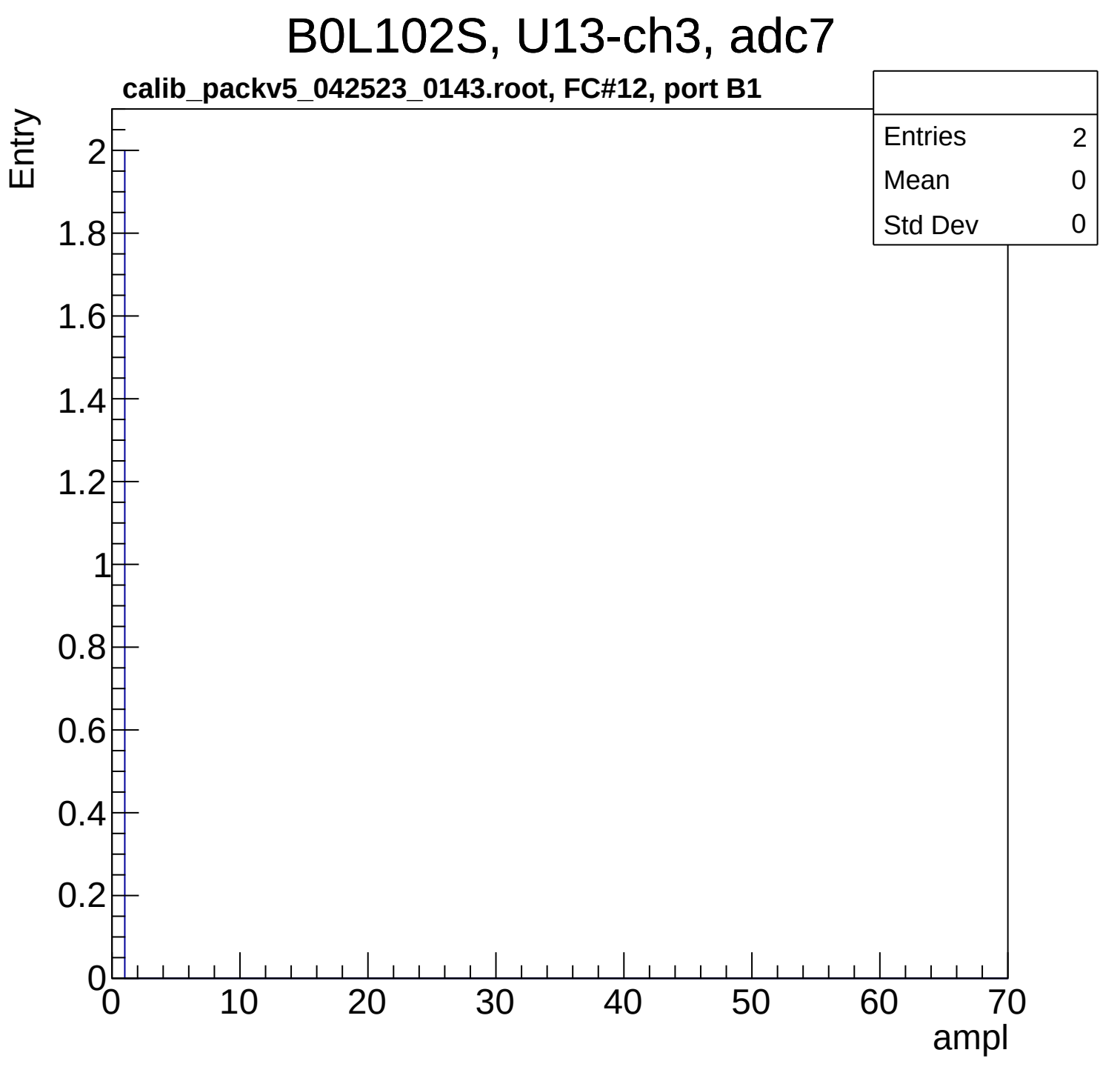
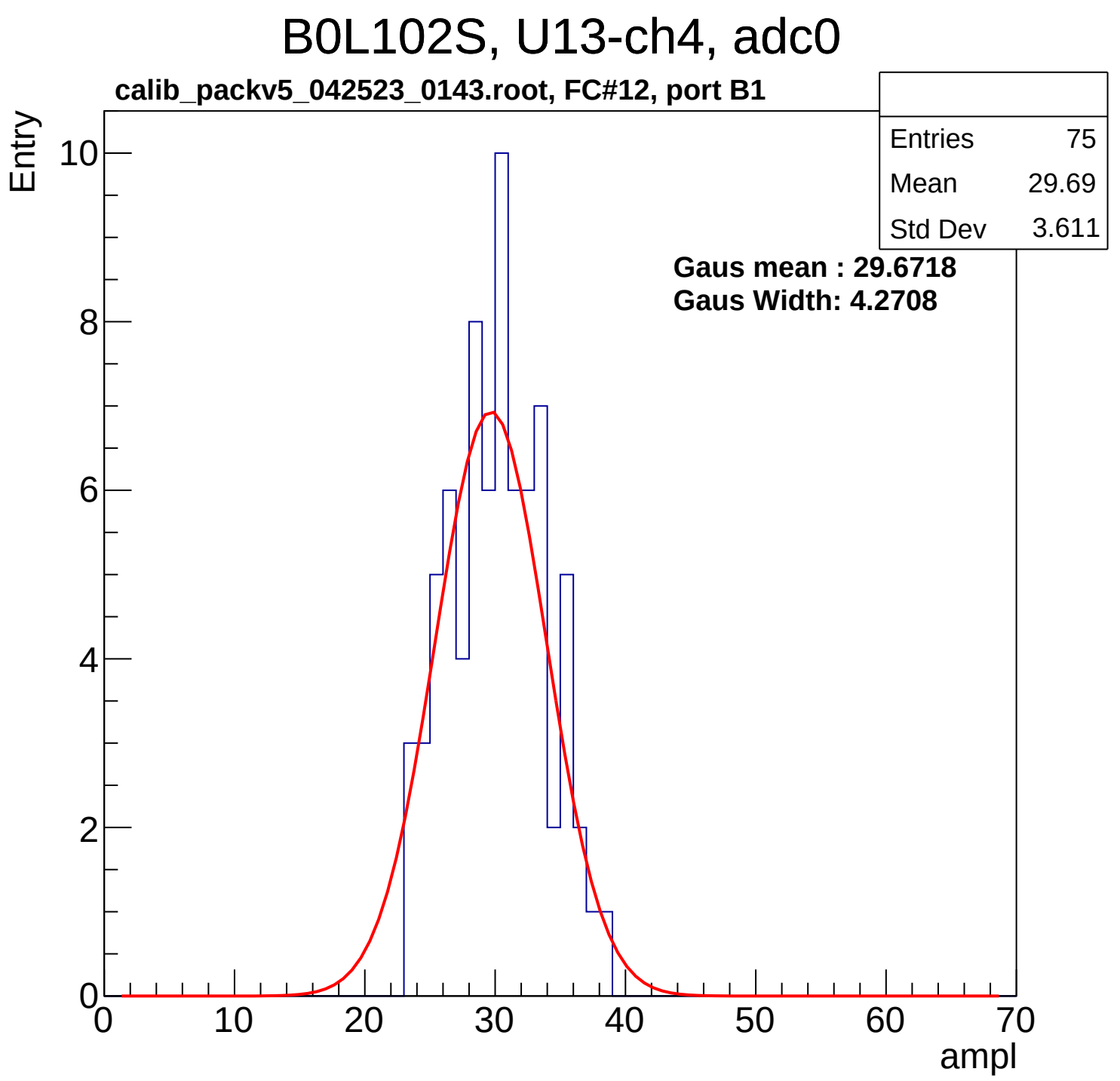
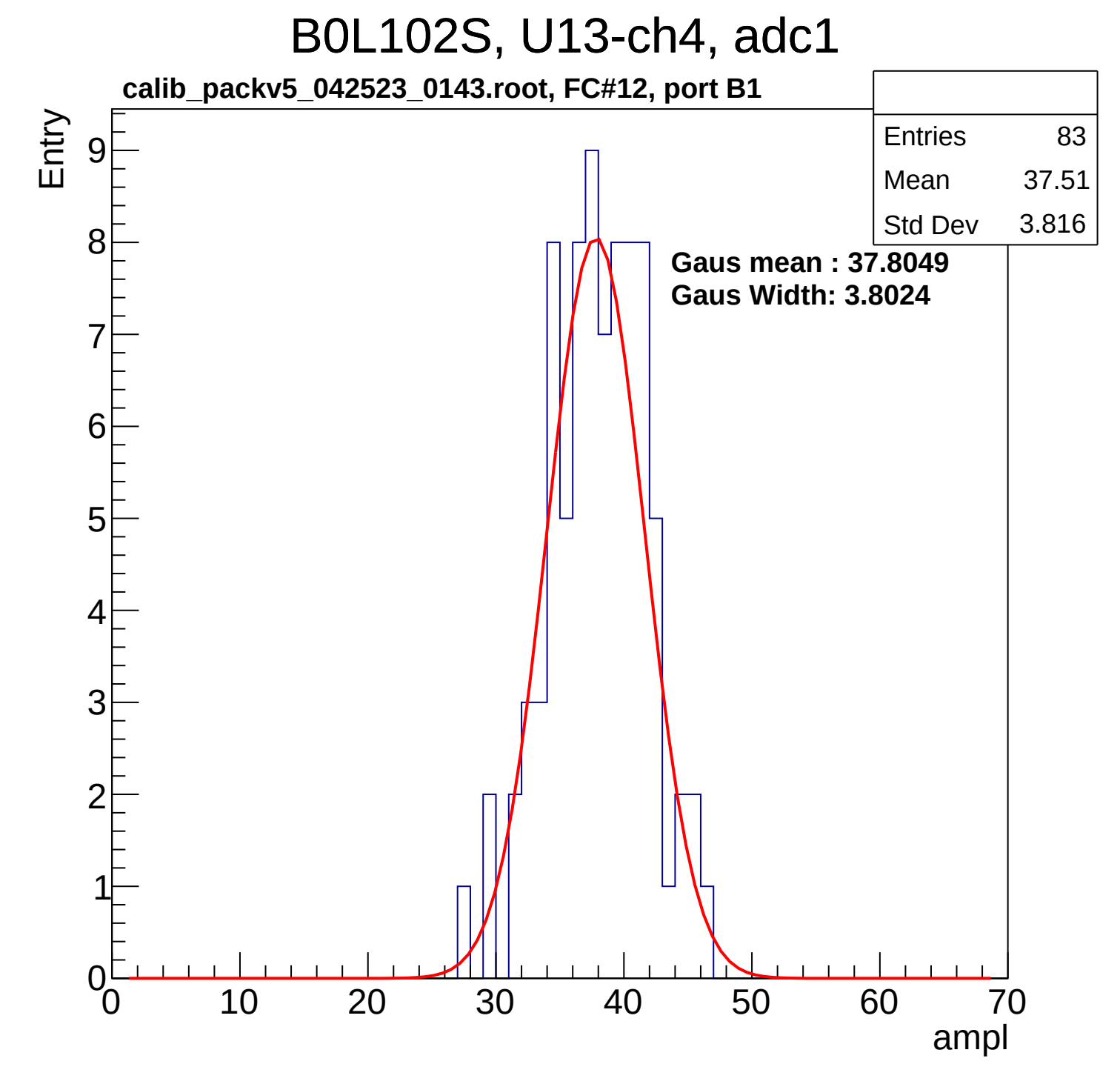
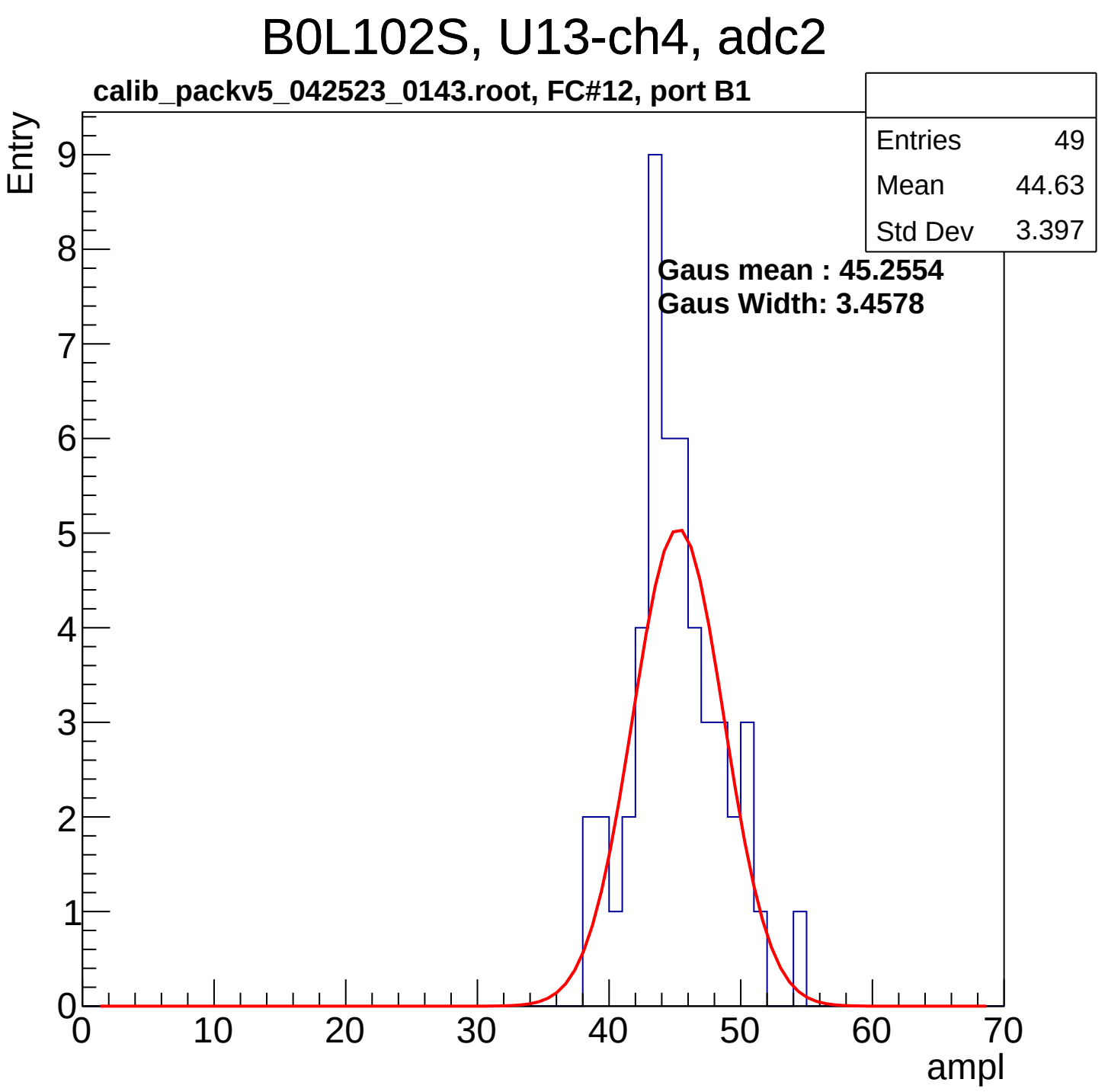
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

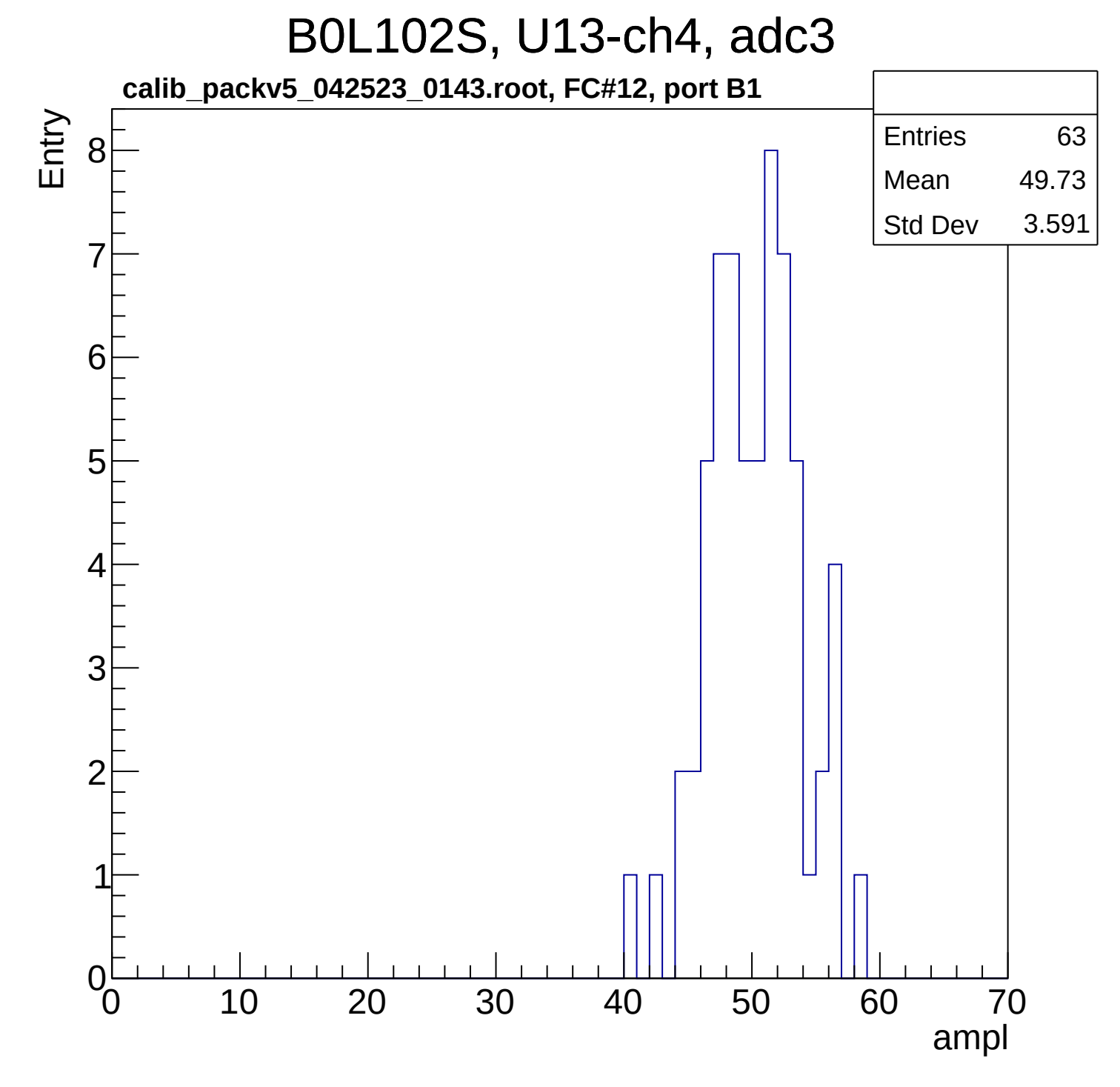

B0L102S, U13-ch3, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch3, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.69
Std Dev 3.611
Gaus mean : 29.6718
Gaus Width: 4.2708
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 37.51
Std Dev 3.816
Gaus mean : 37.8049
Gaus Width: 3.8024
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch4, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 44.63
Std Dev 3.397
Gaus mean : 45.2554
Gaus Width: 3.4578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.73
Std Dev 3.591
Entry
ampl
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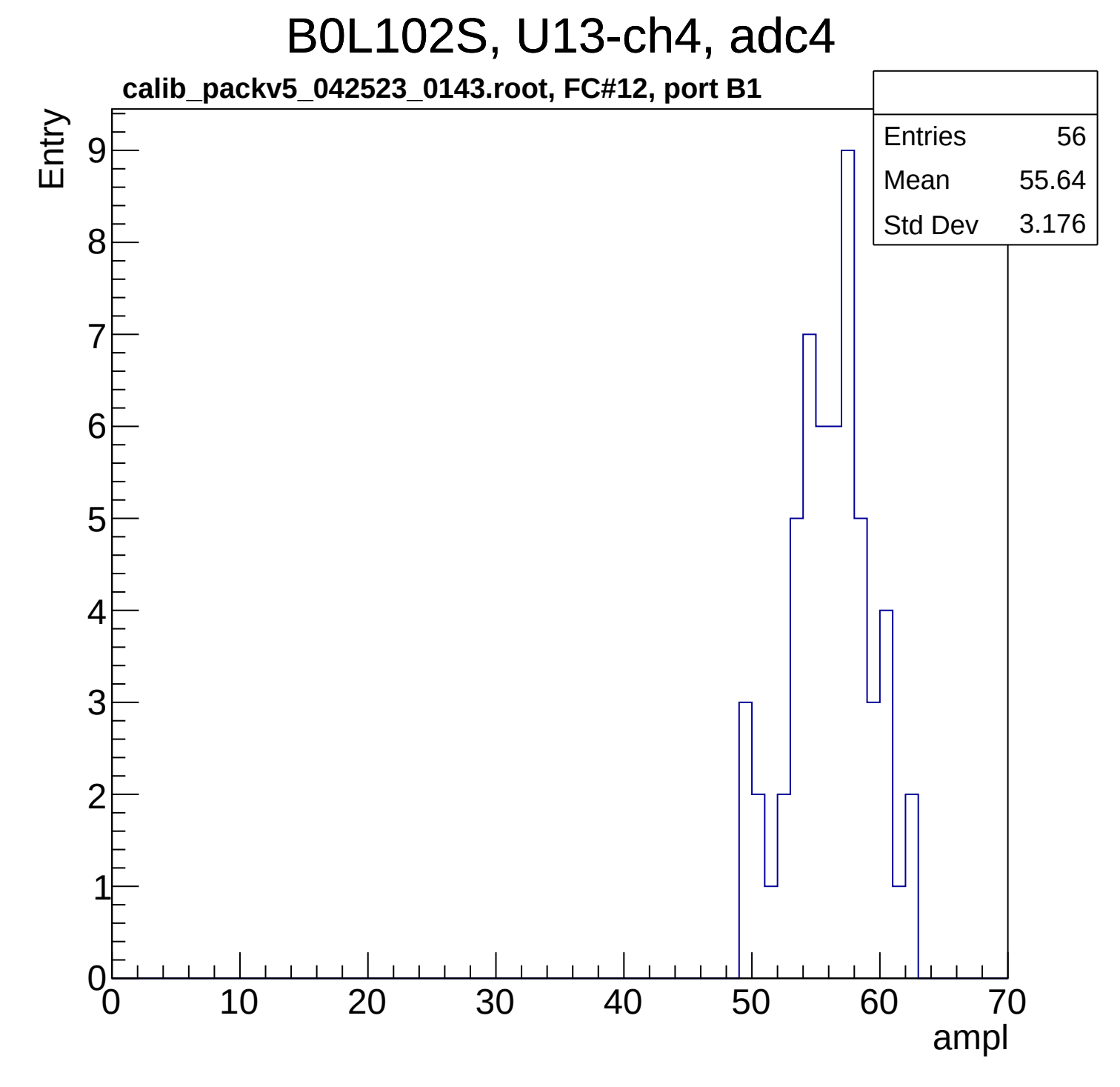
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.64
Std Dev 3.176
Entry
ampl
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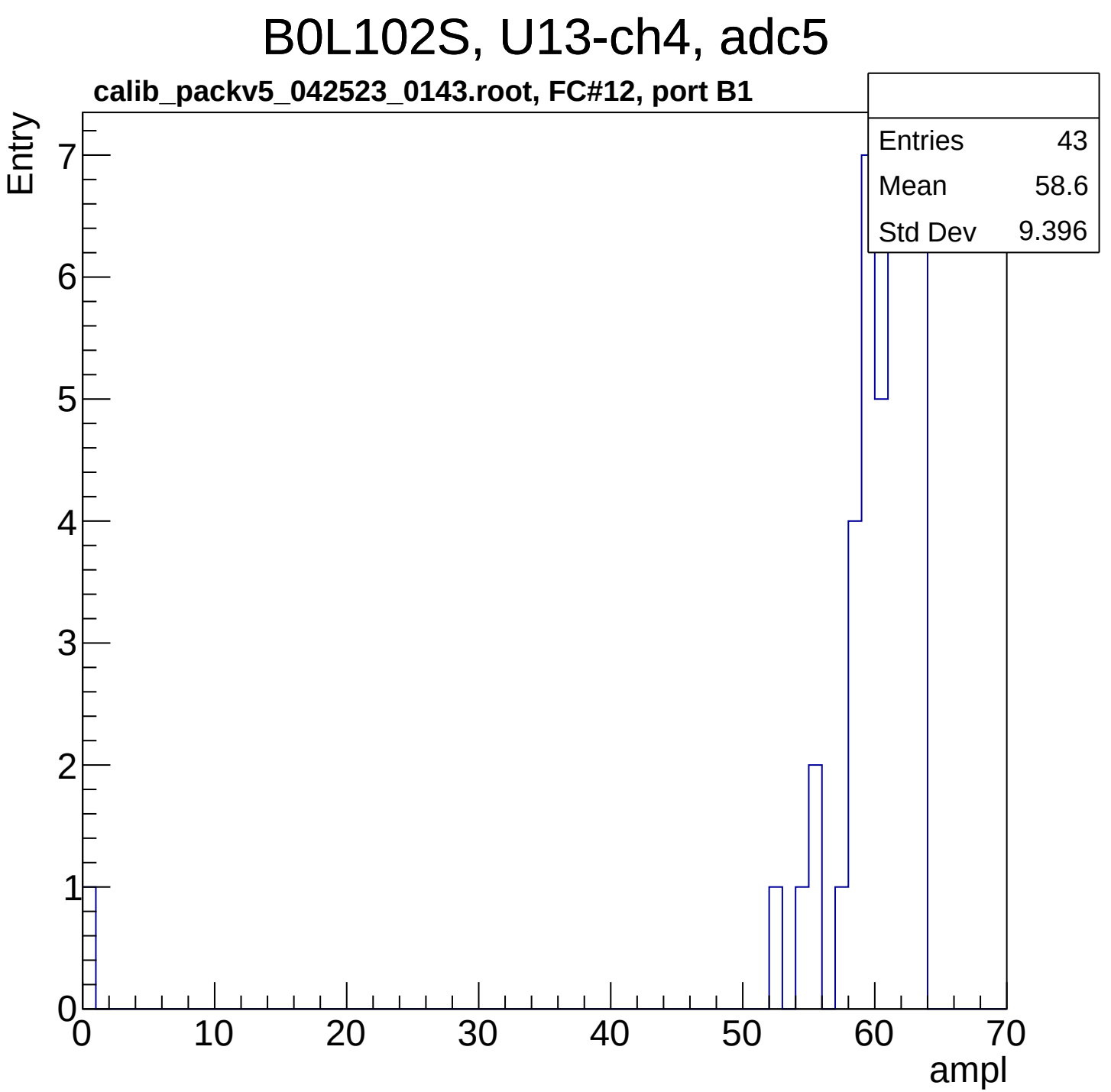
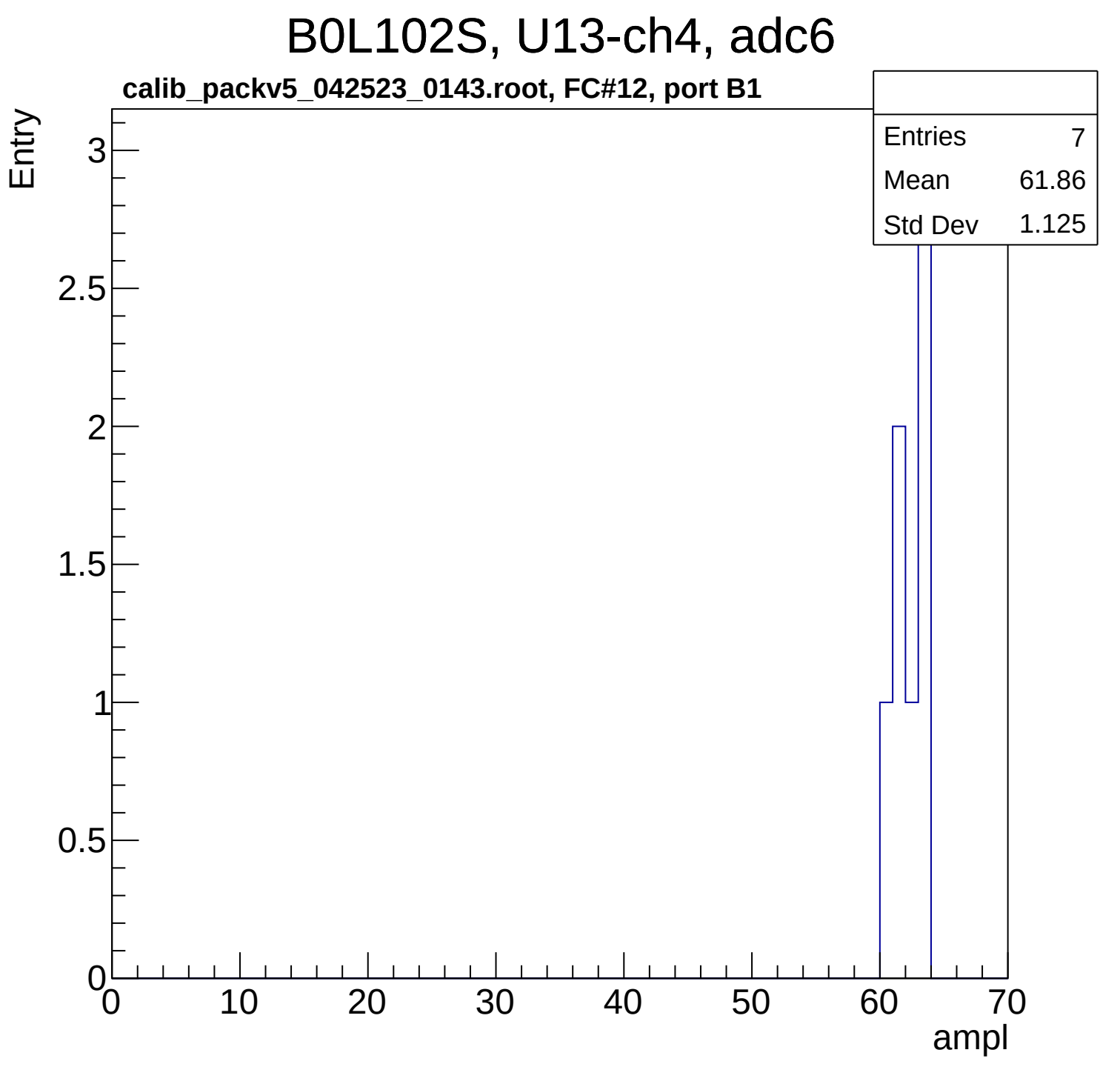
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.6
Std Dev 9.396
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch4, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.86
Std Dev 1.125
Entry
ampl
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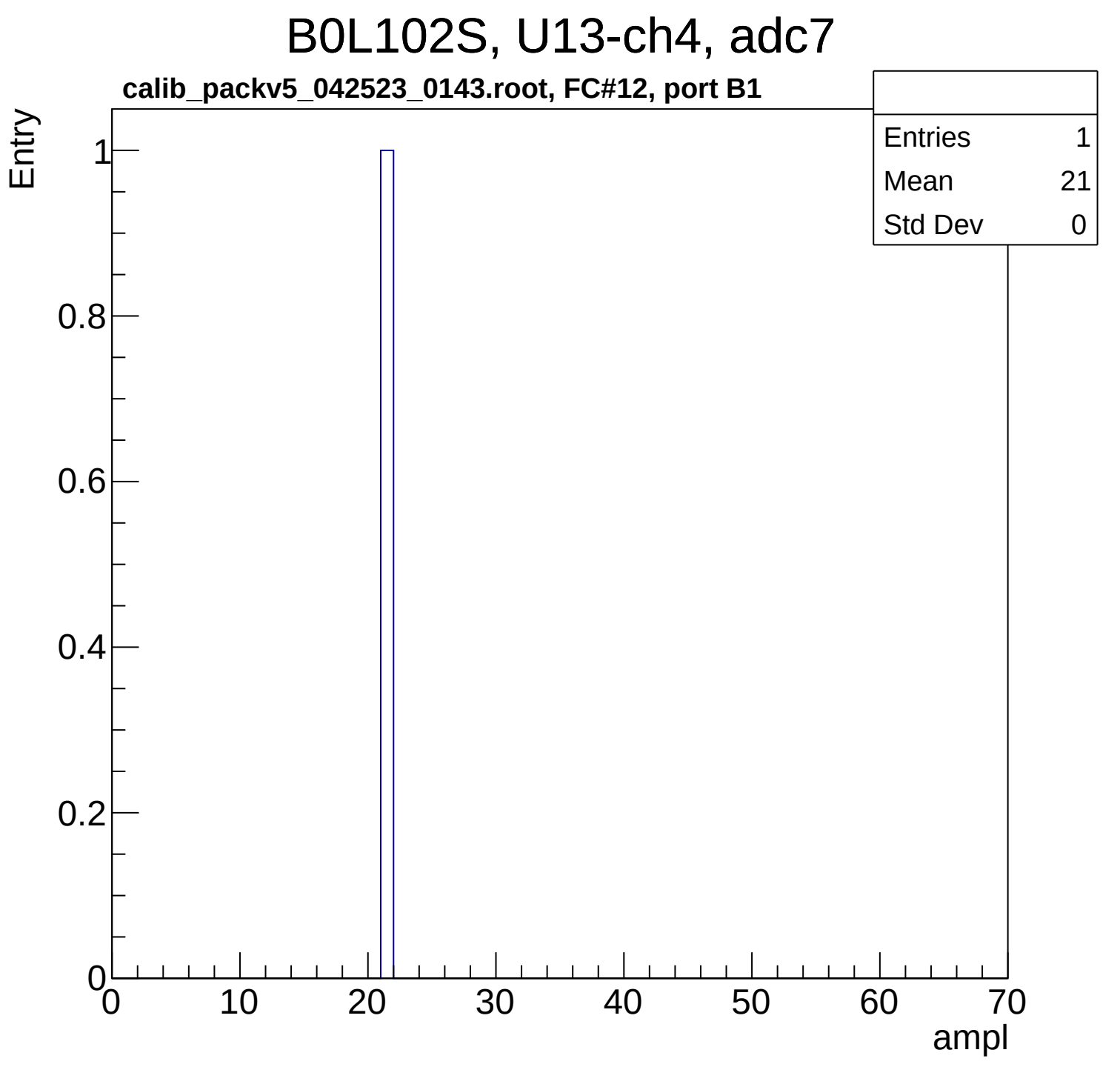
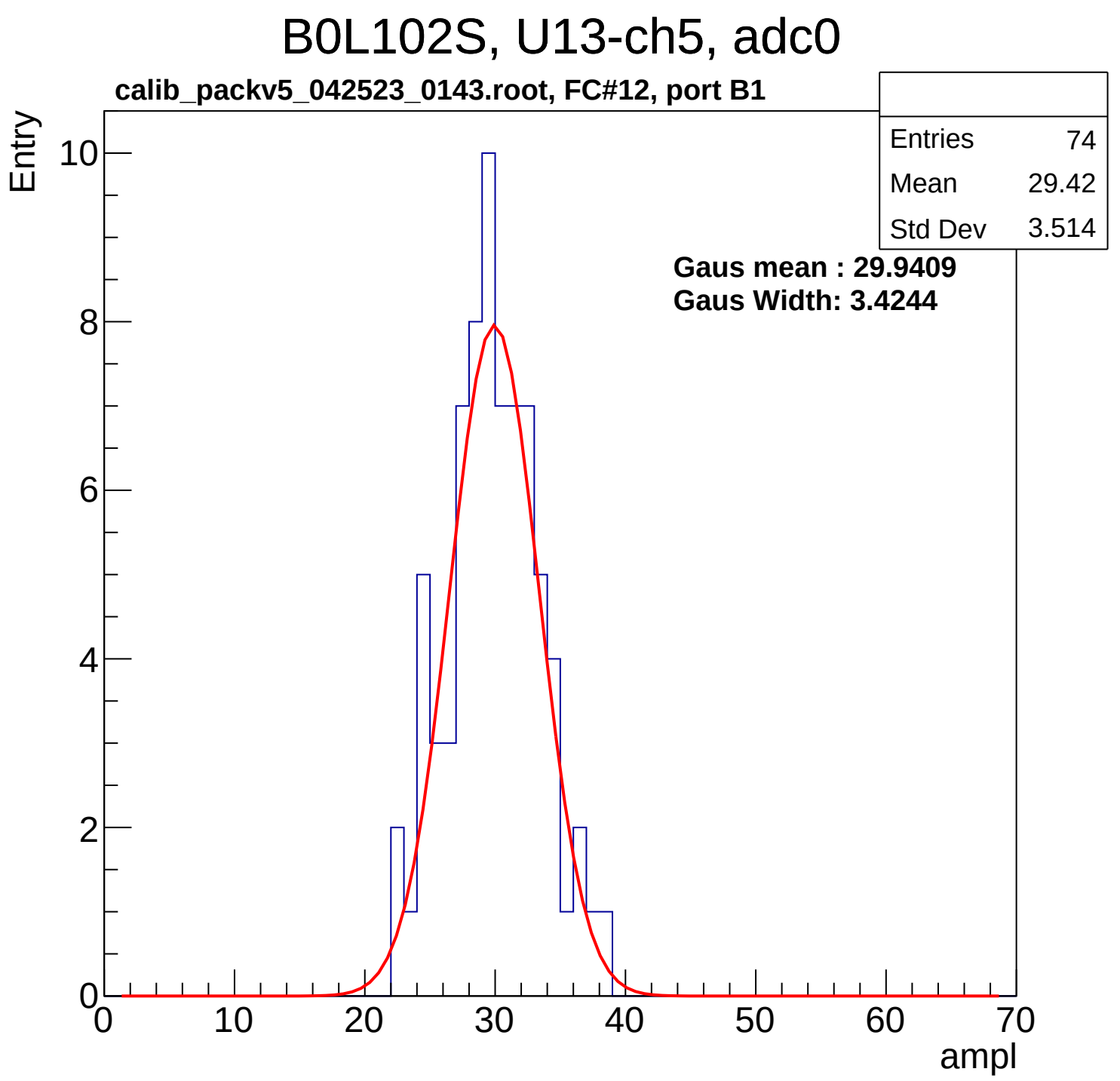
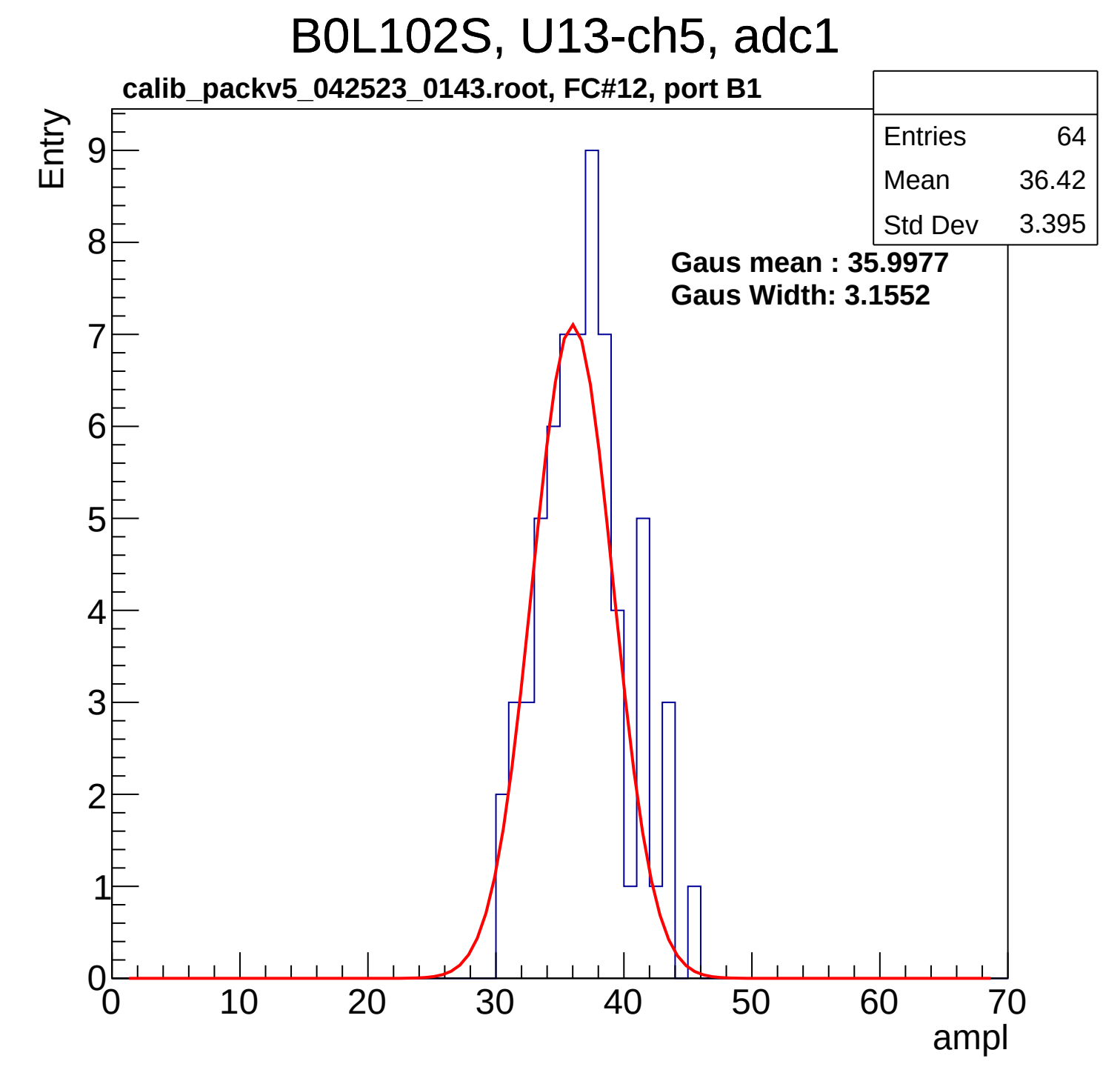
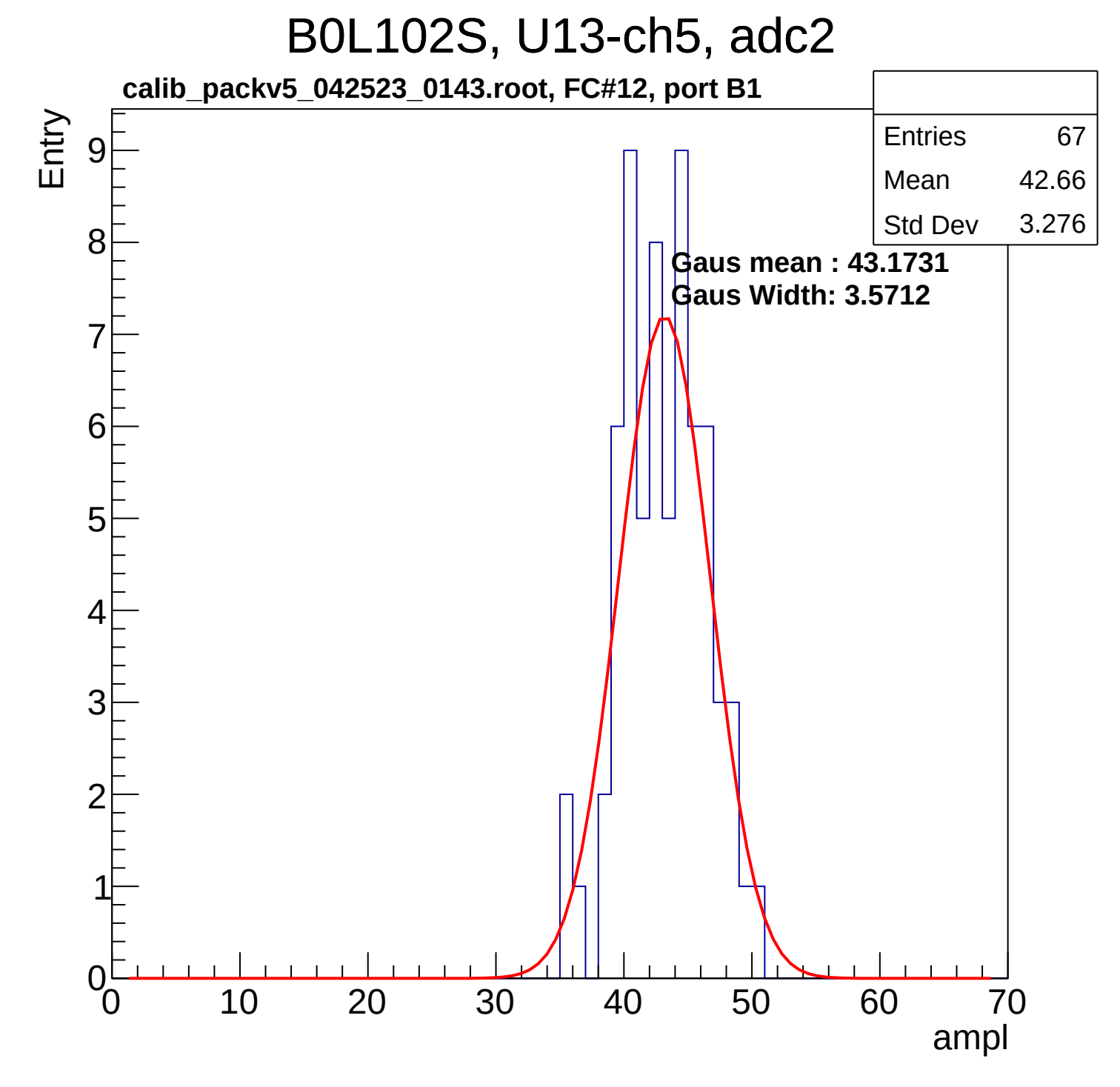
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch4, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch5, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 29.42
Std Dev 3.514
Gaus mean : 29.9409
Gaus Width: 3.4244
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch5, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.42
Std Dev 3.395
Gaus mean : 35.9977
Gaus Width: 3.1552
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch5, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.66
Std Dev 3.276
Gaus mean : 43.1731
Gaus Width: 3.5712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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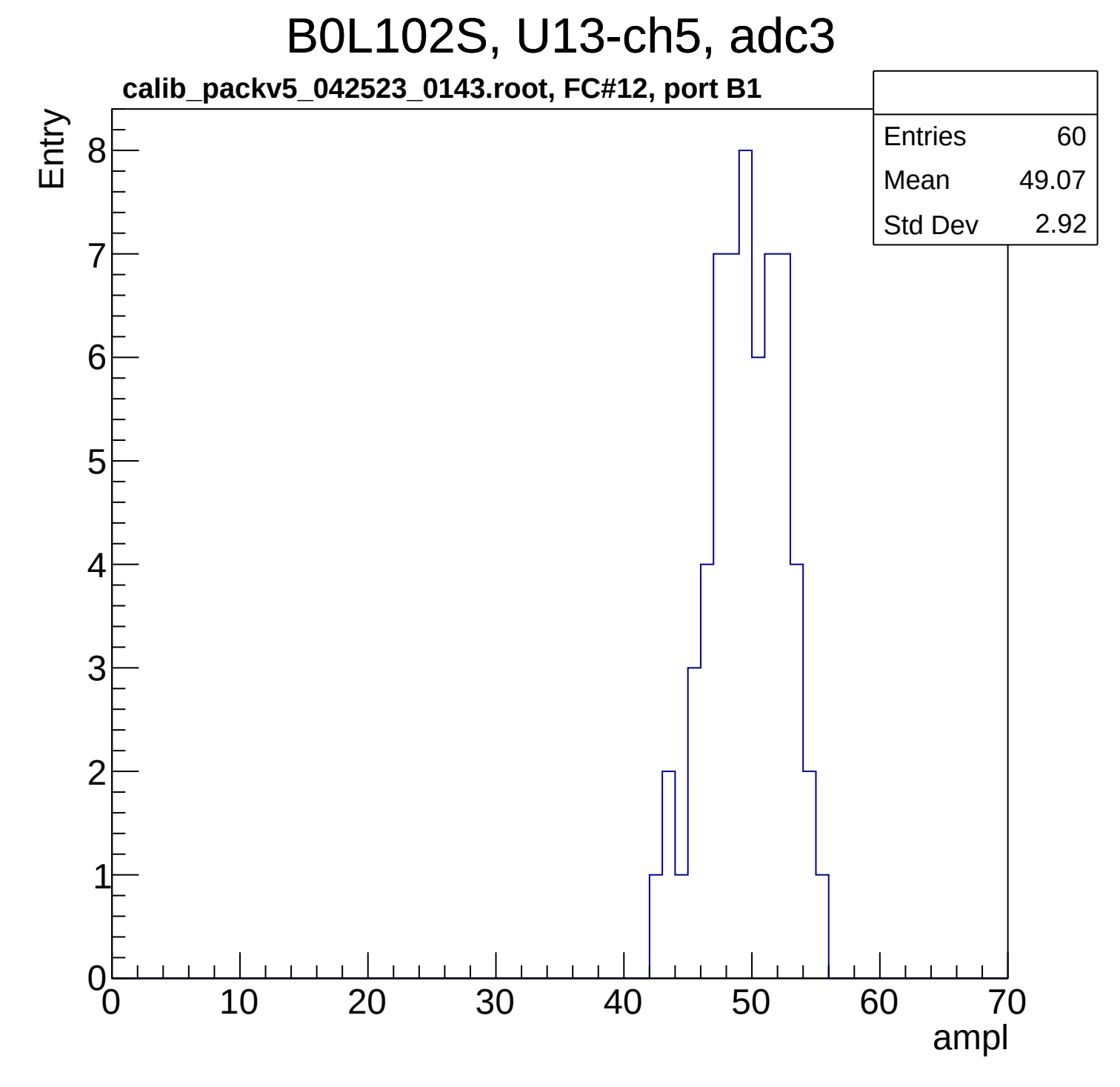
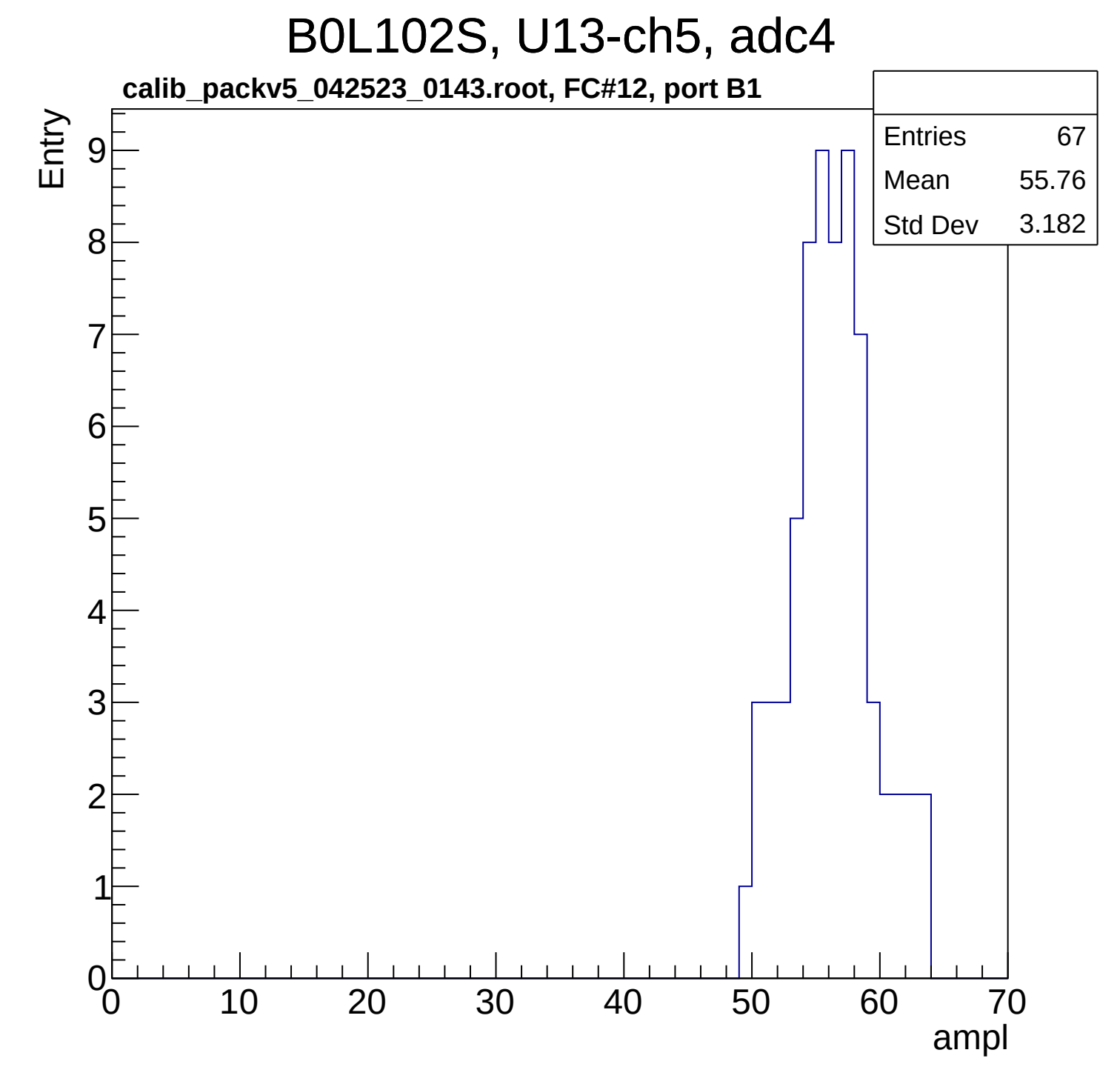

B0L102S, U13-ch5, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 49.07
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch5, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.76
Std Dev 3.182
Entry
ampl
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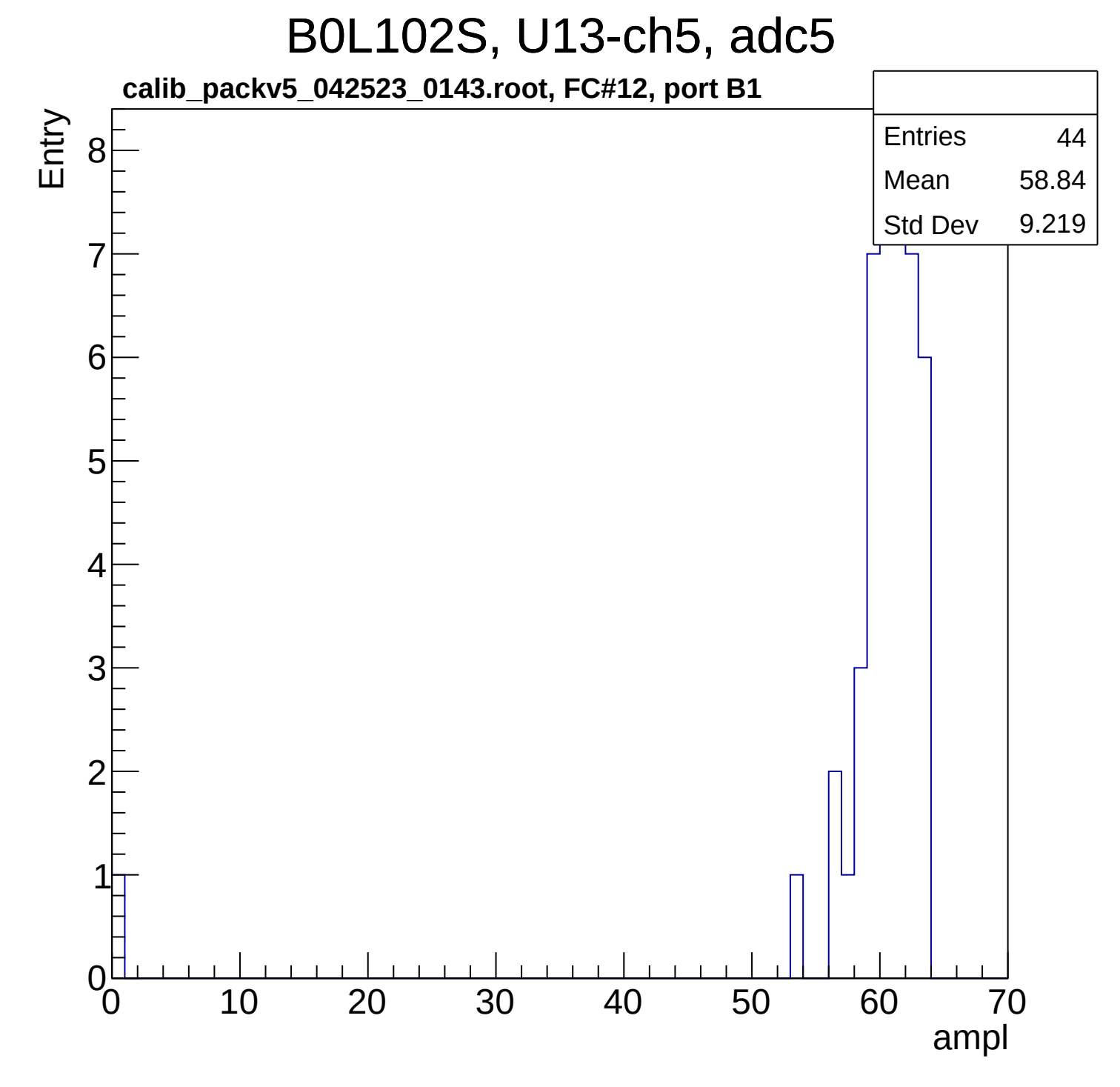
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch5, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.84
Std Dev 9.219
Entry
ampl
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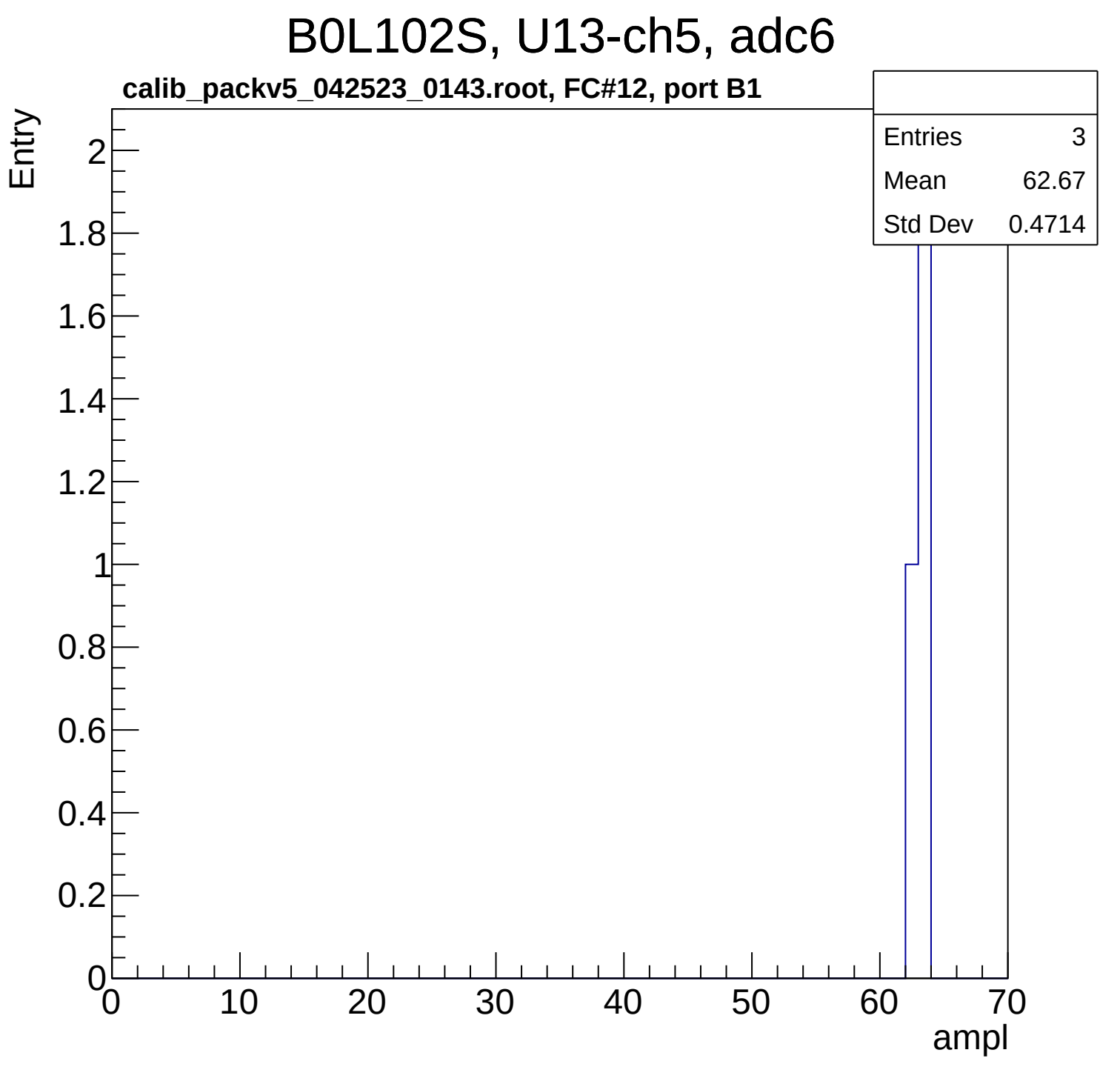
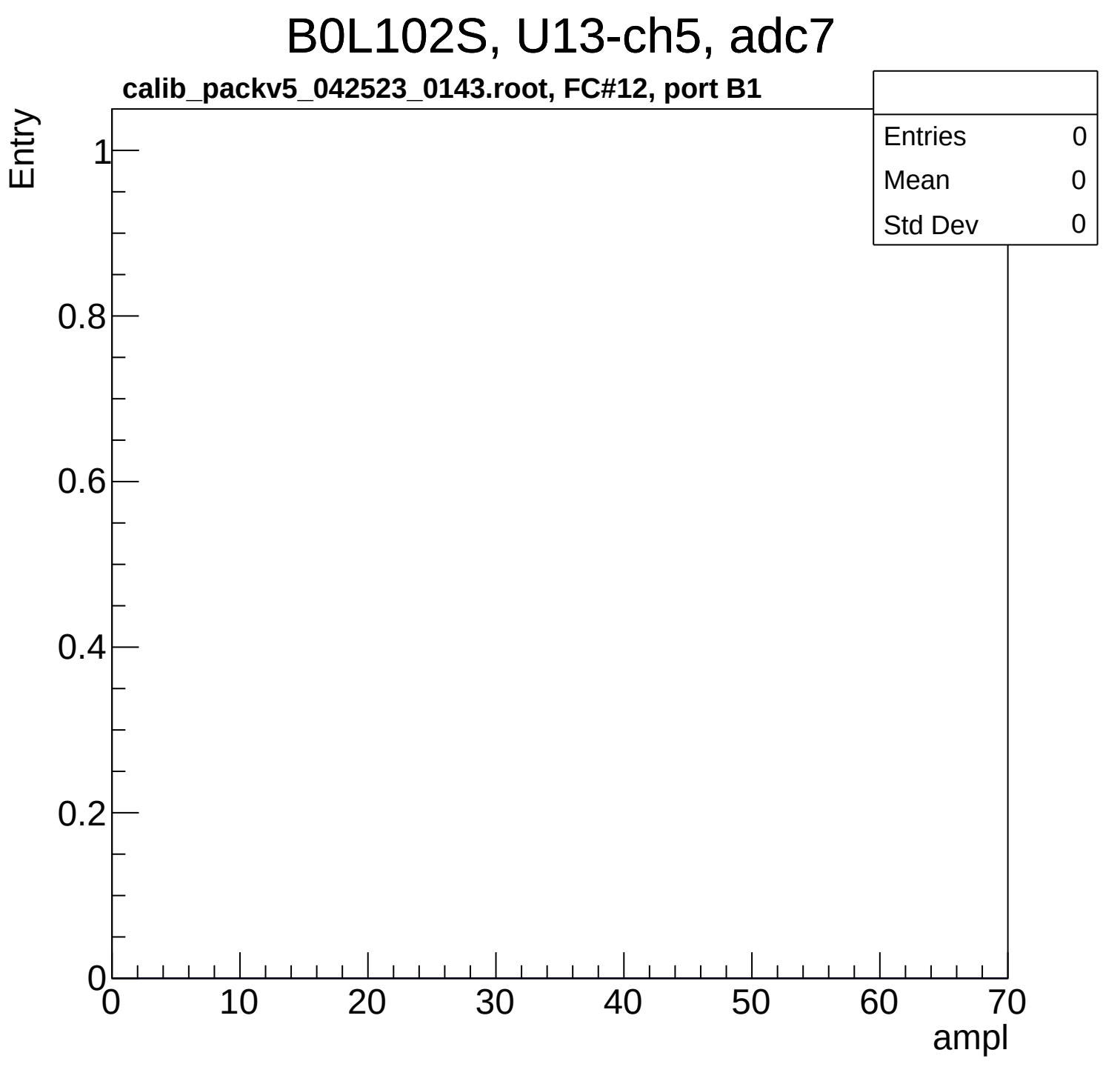
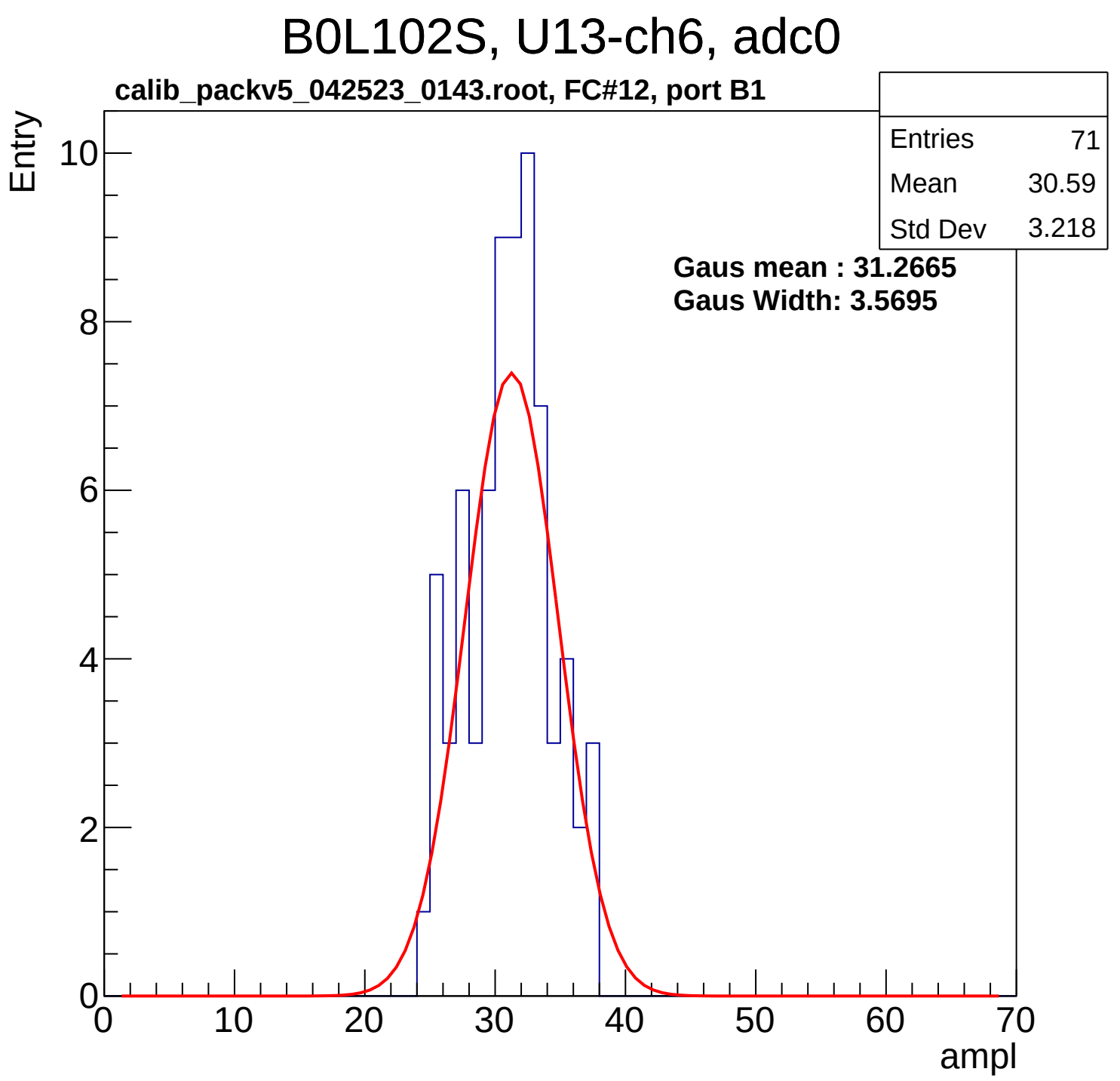
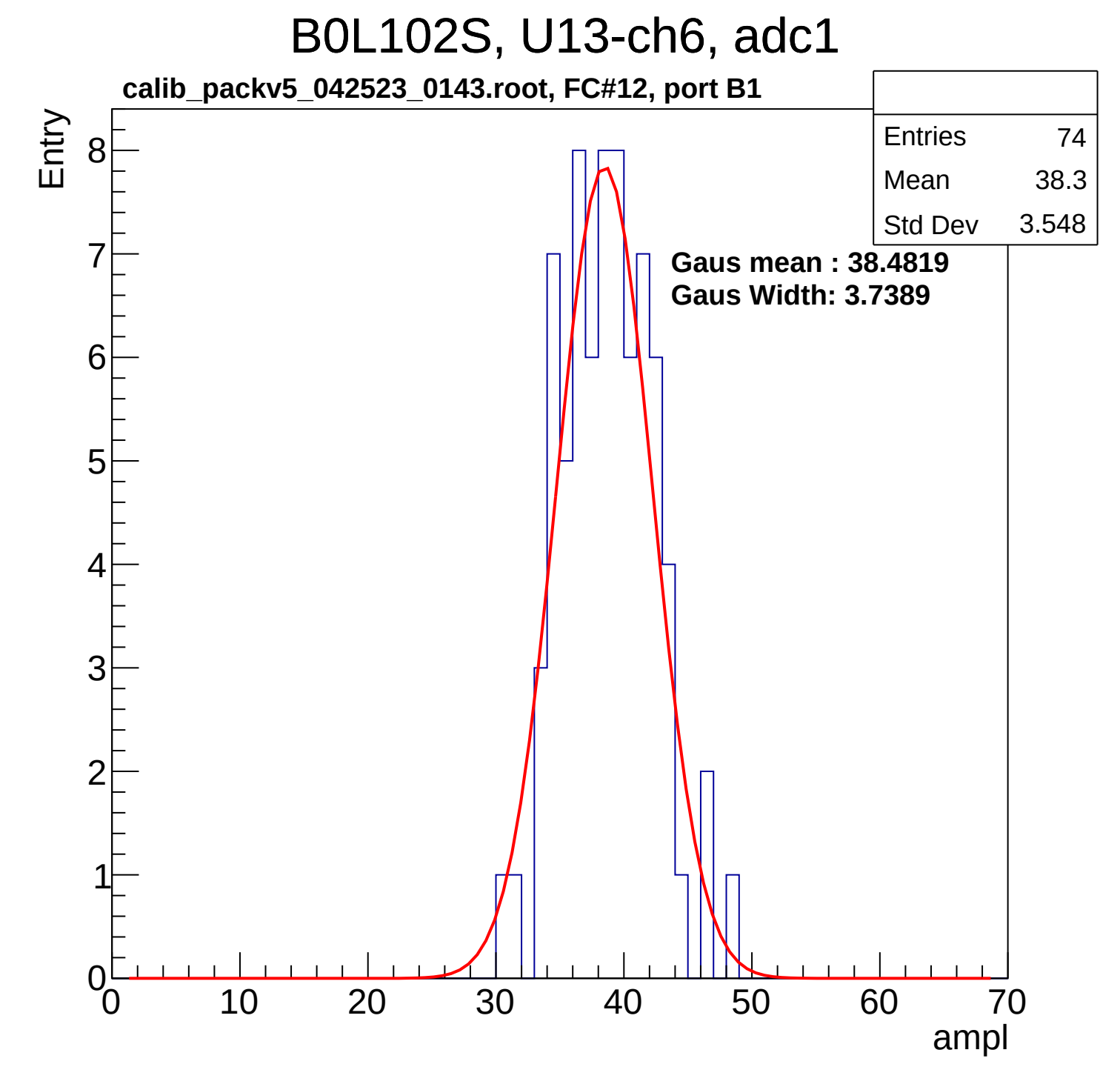
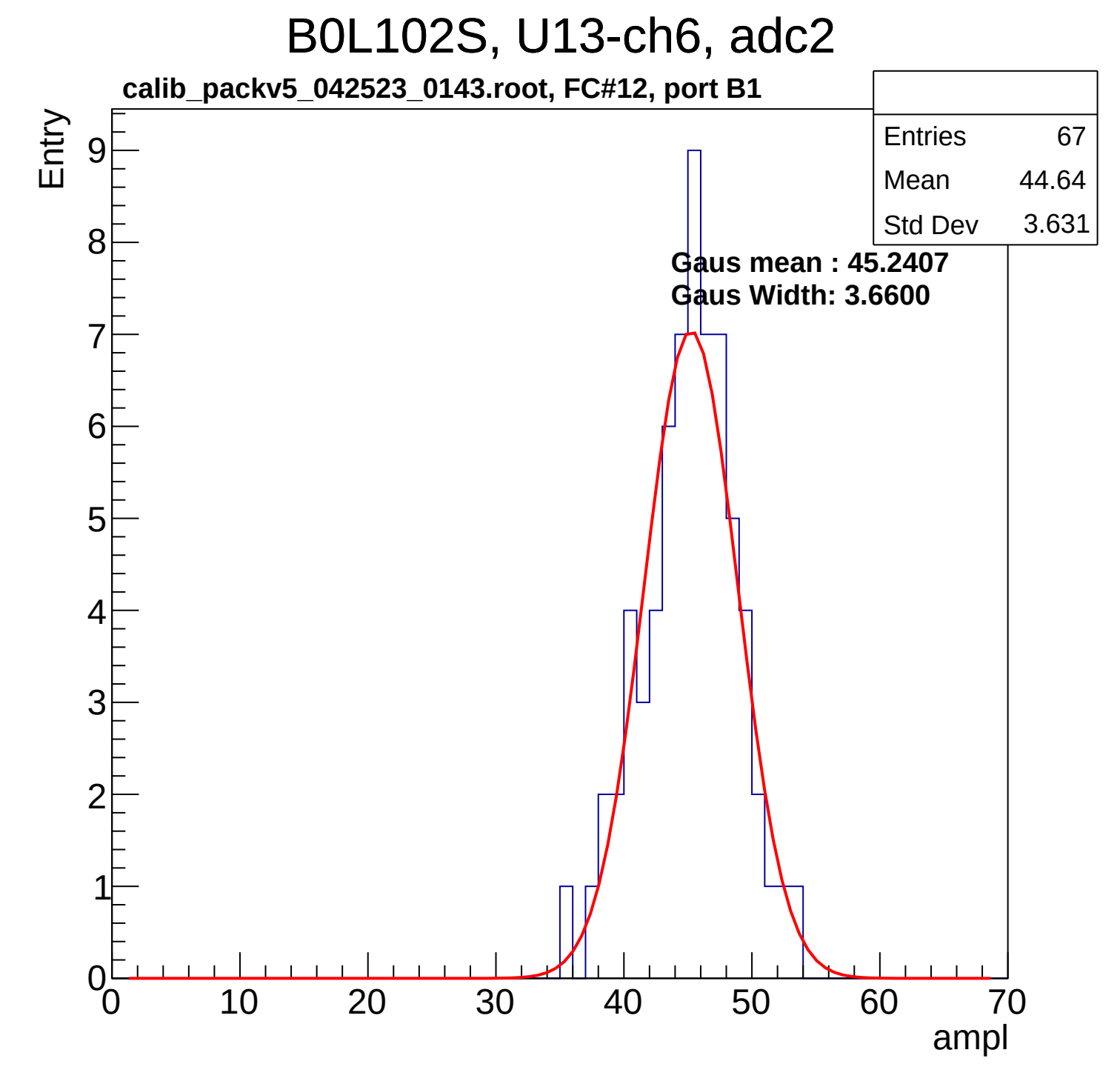
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch5, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch5, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch6, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 30.59
Std Dev 3.218
Gaus mean : 31.2665
Gaus Width: 3.5695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch6, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 38.3
Std Dev 3.548
Gaus mean : 38.4819
Gaus Width: 3.7389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch6, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 44.64
Std Dev 3.631
Gaus mean : 45.2407
Gaus Width: 3.6600
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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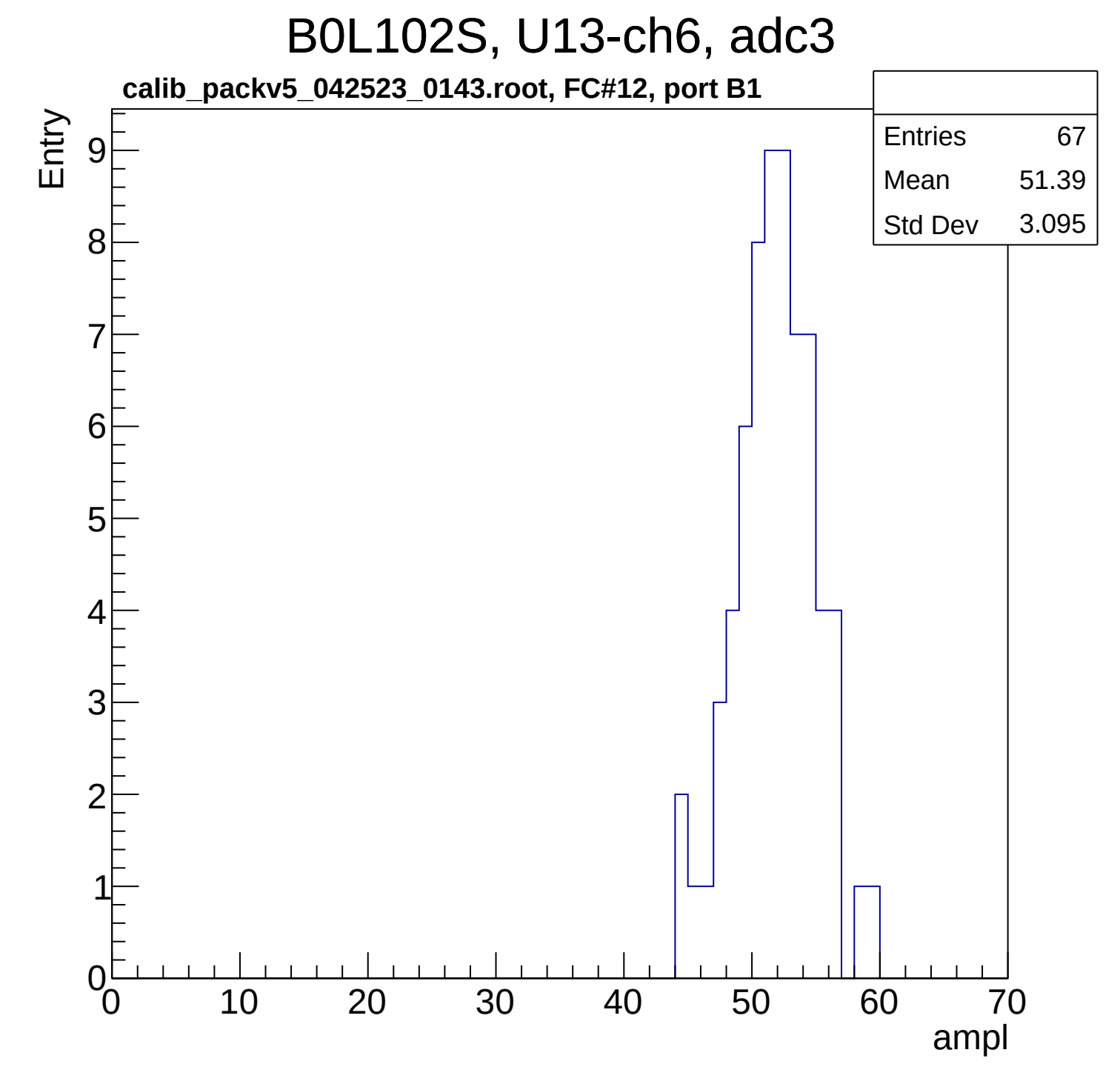
B0L102S, U13-ch6, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 51.39
Std Dev 3.095
Entry
ampl
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2
3
4
5
6
7
8
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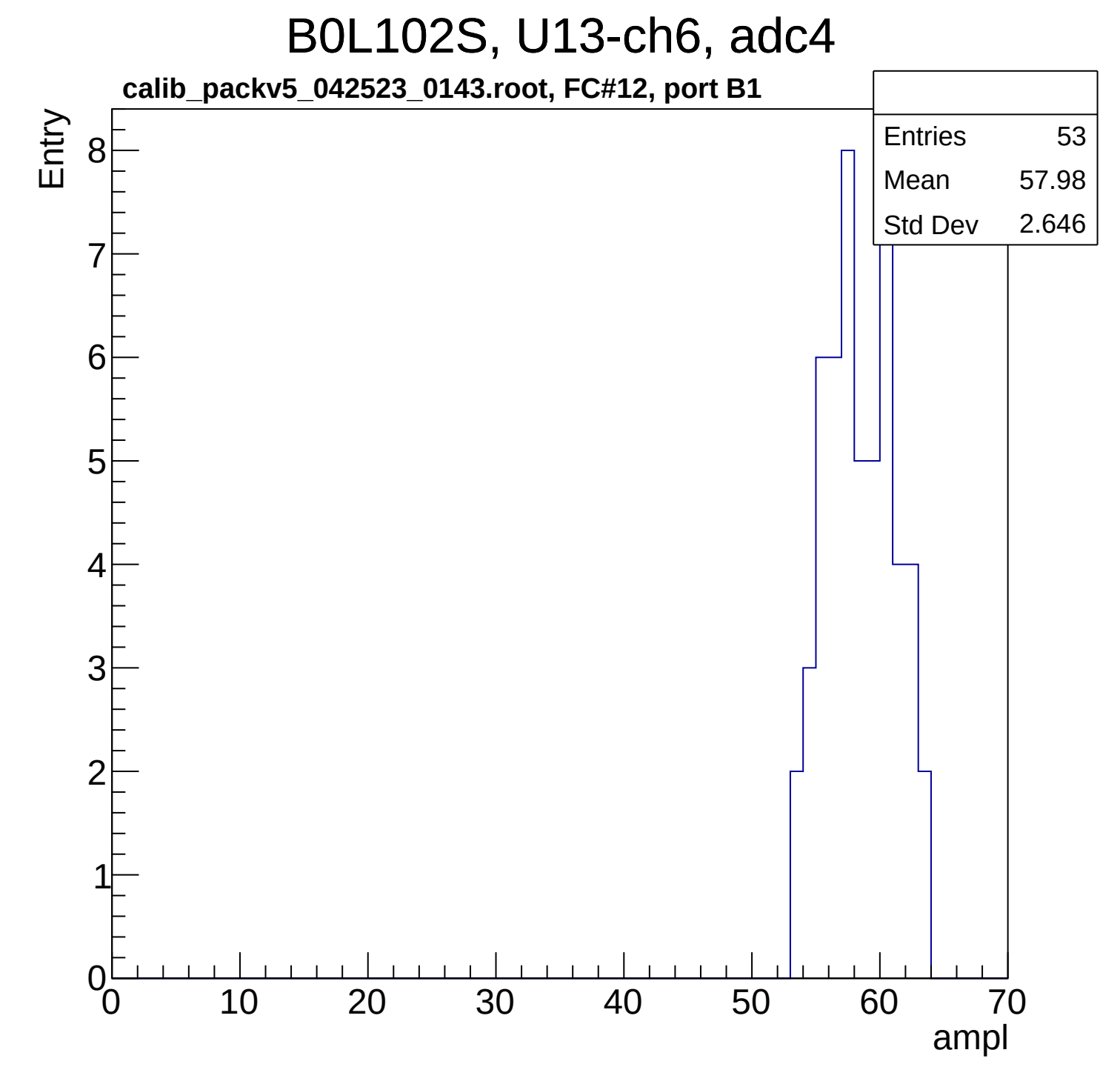
0
10
20
30
40
50
60
70


B0L102S, U13-ch6, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.98
Std Dev 2.646
Entry
ampl
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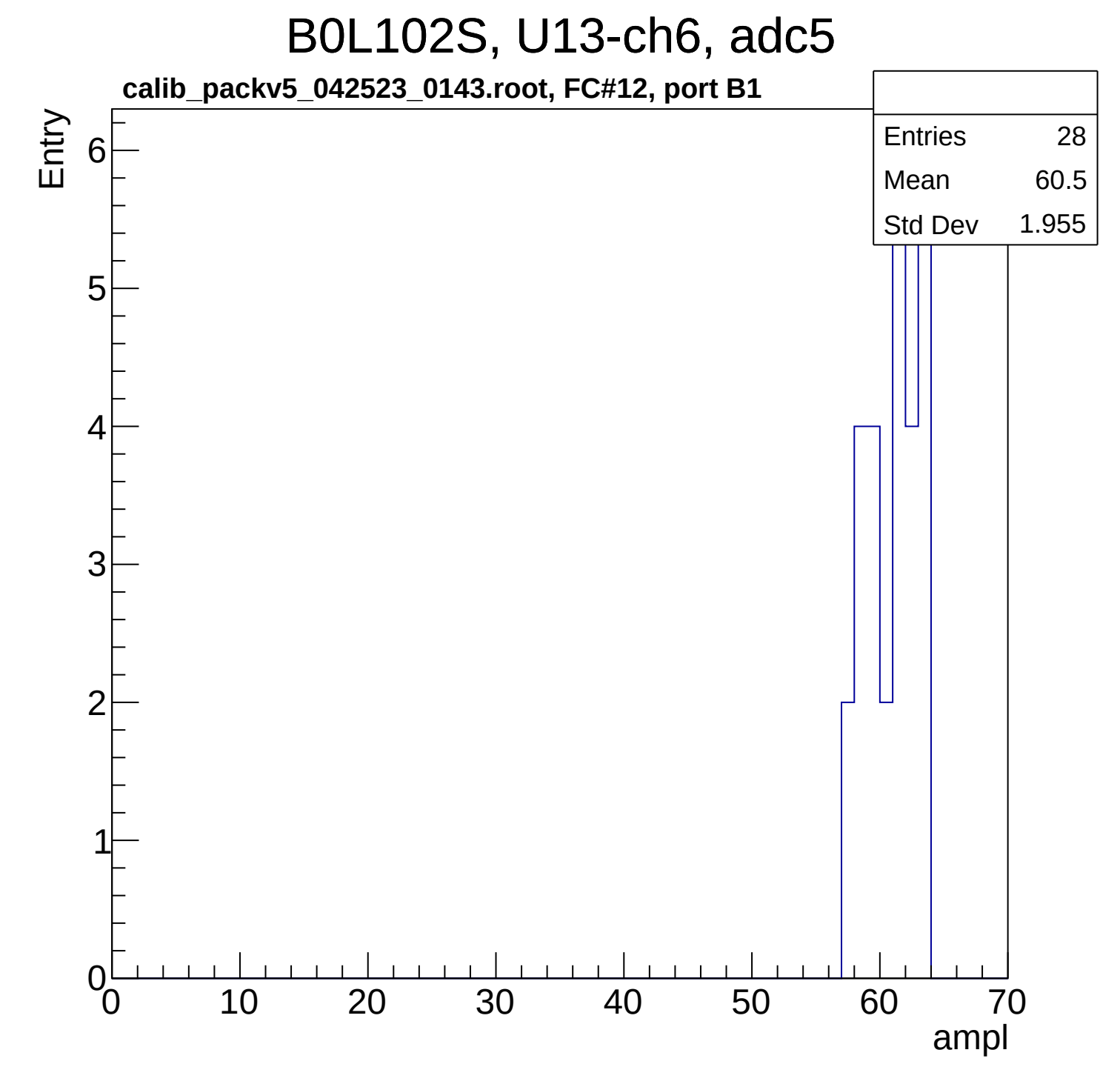
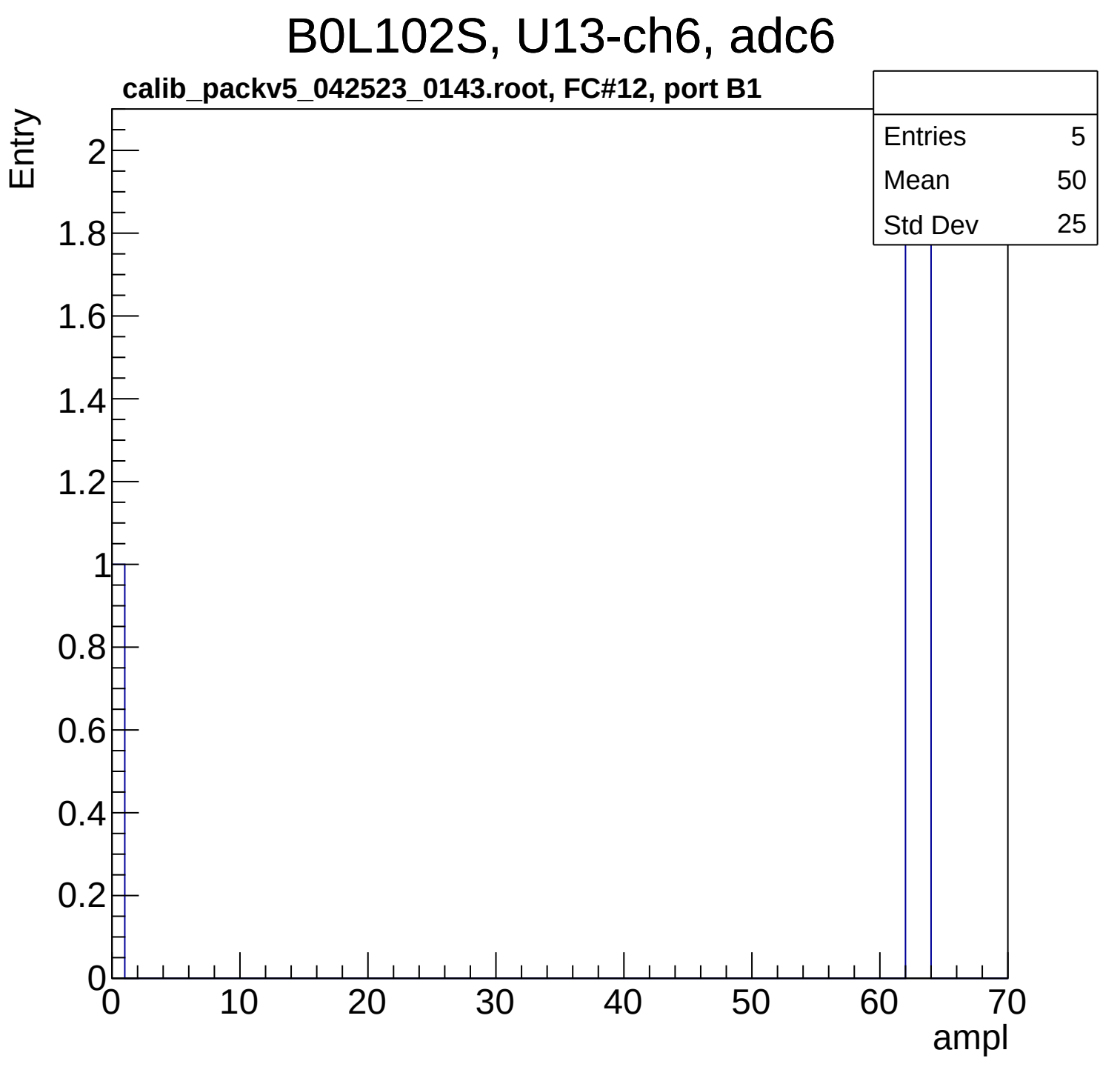
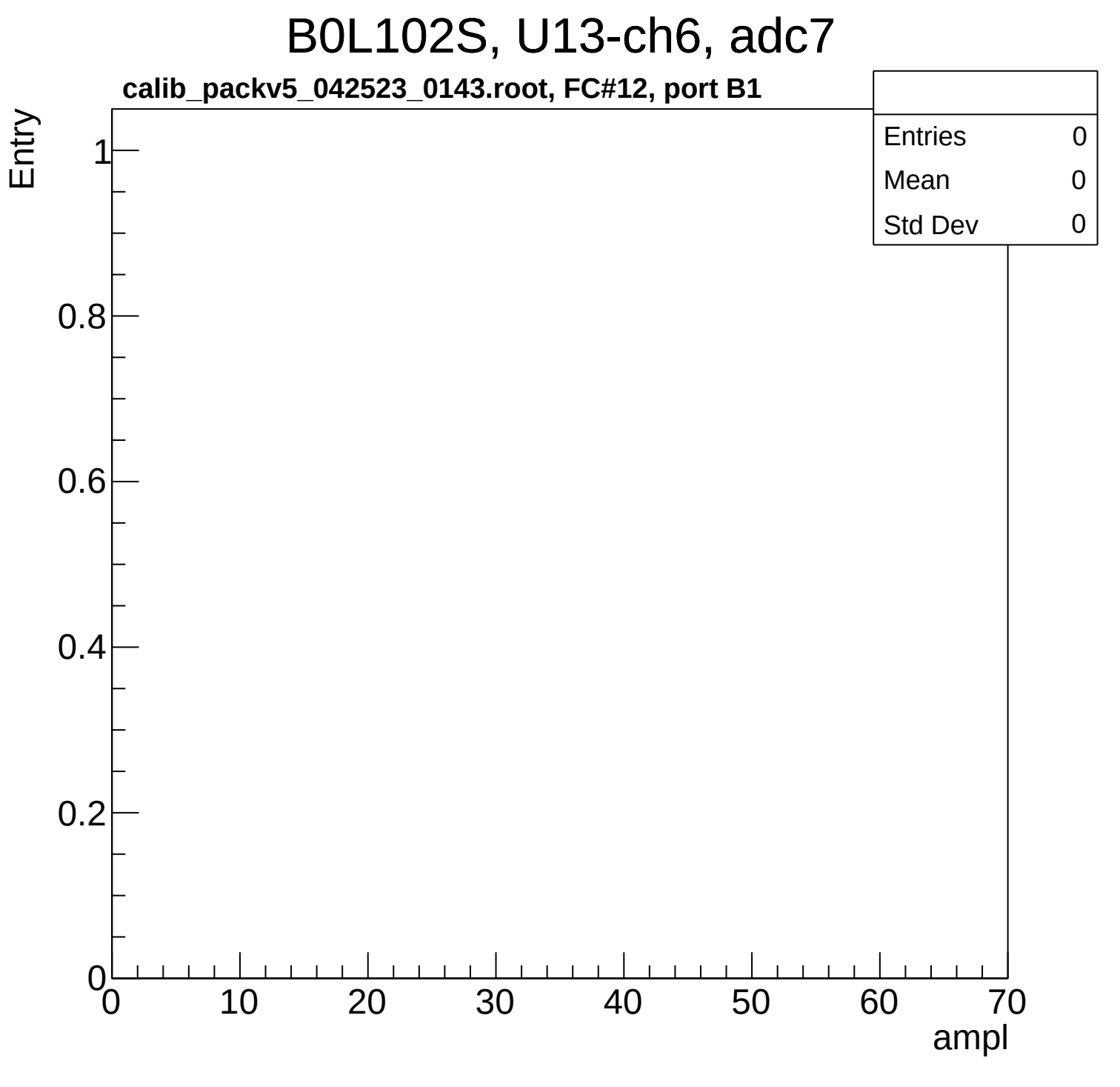
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch6, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 28
Mean 60.5
Std Dev 1.955
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch6, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch6, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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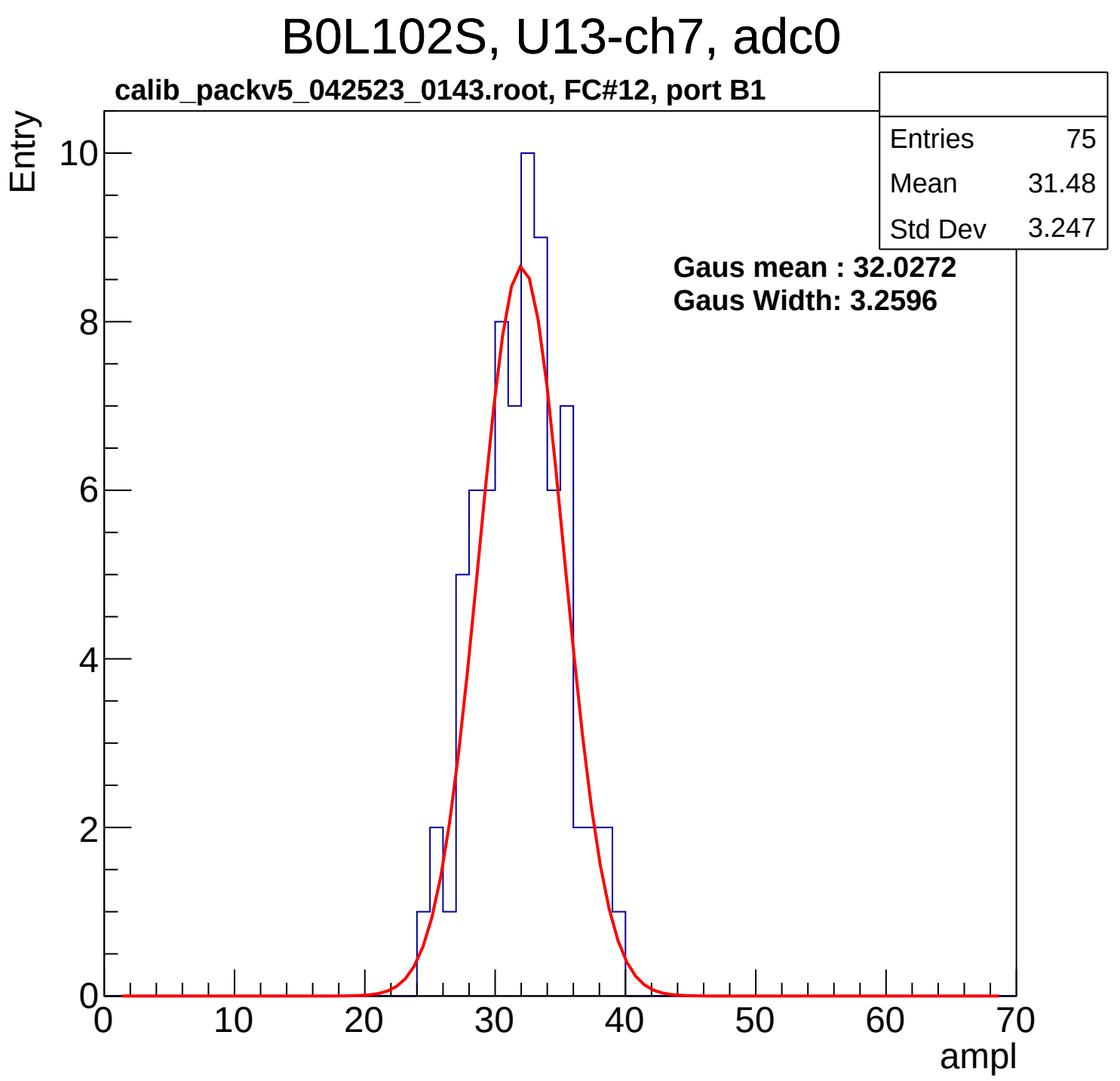
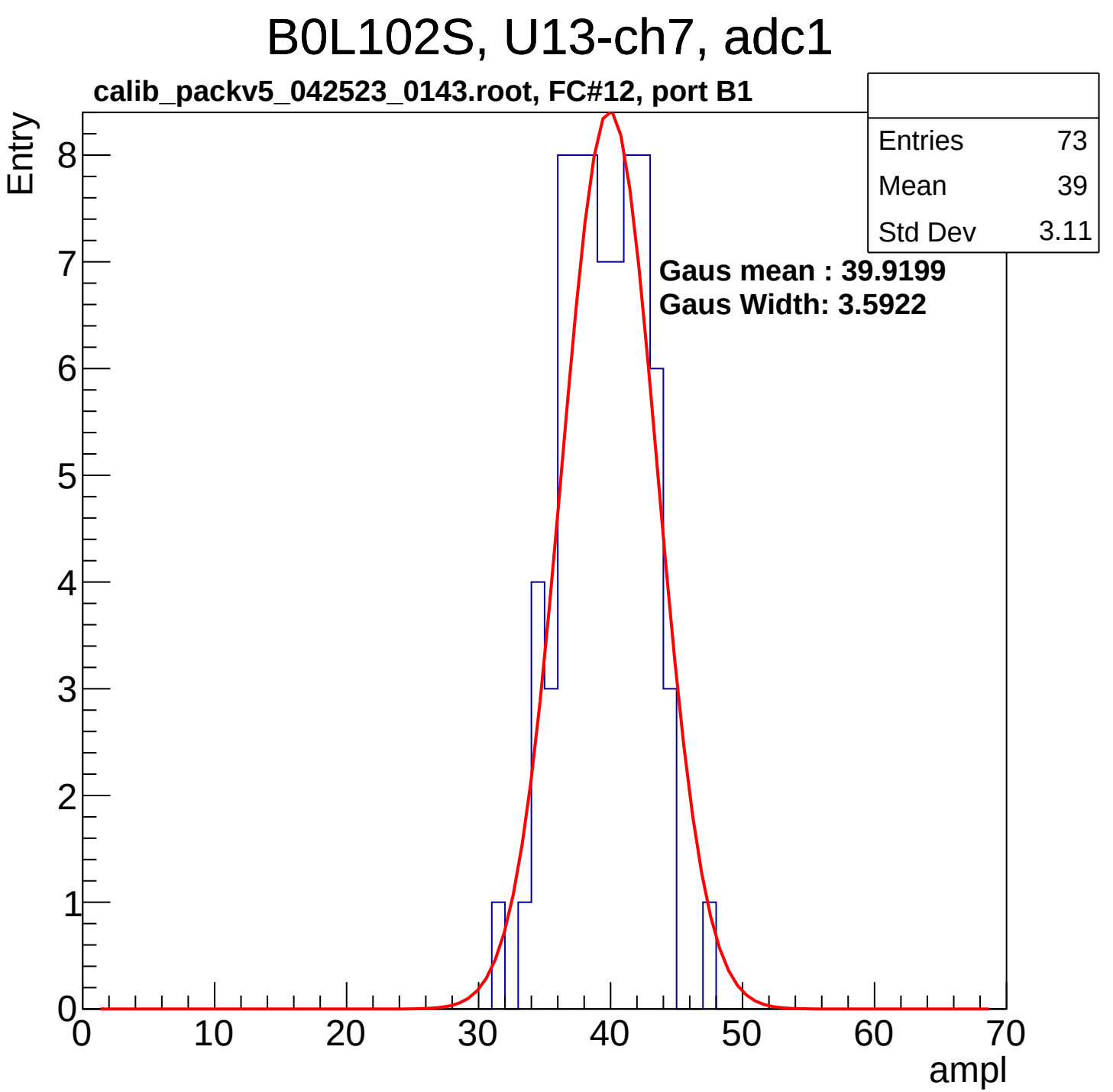
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch7, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 31.48
Std Dev 3.247
Gaus mean : 32.0272
Gaus Width: 3.2596
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch7, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 39
Std Dev 3.11
Gaus mean : 39.9199
Gaus Width: 3.5922
Entry
ampl
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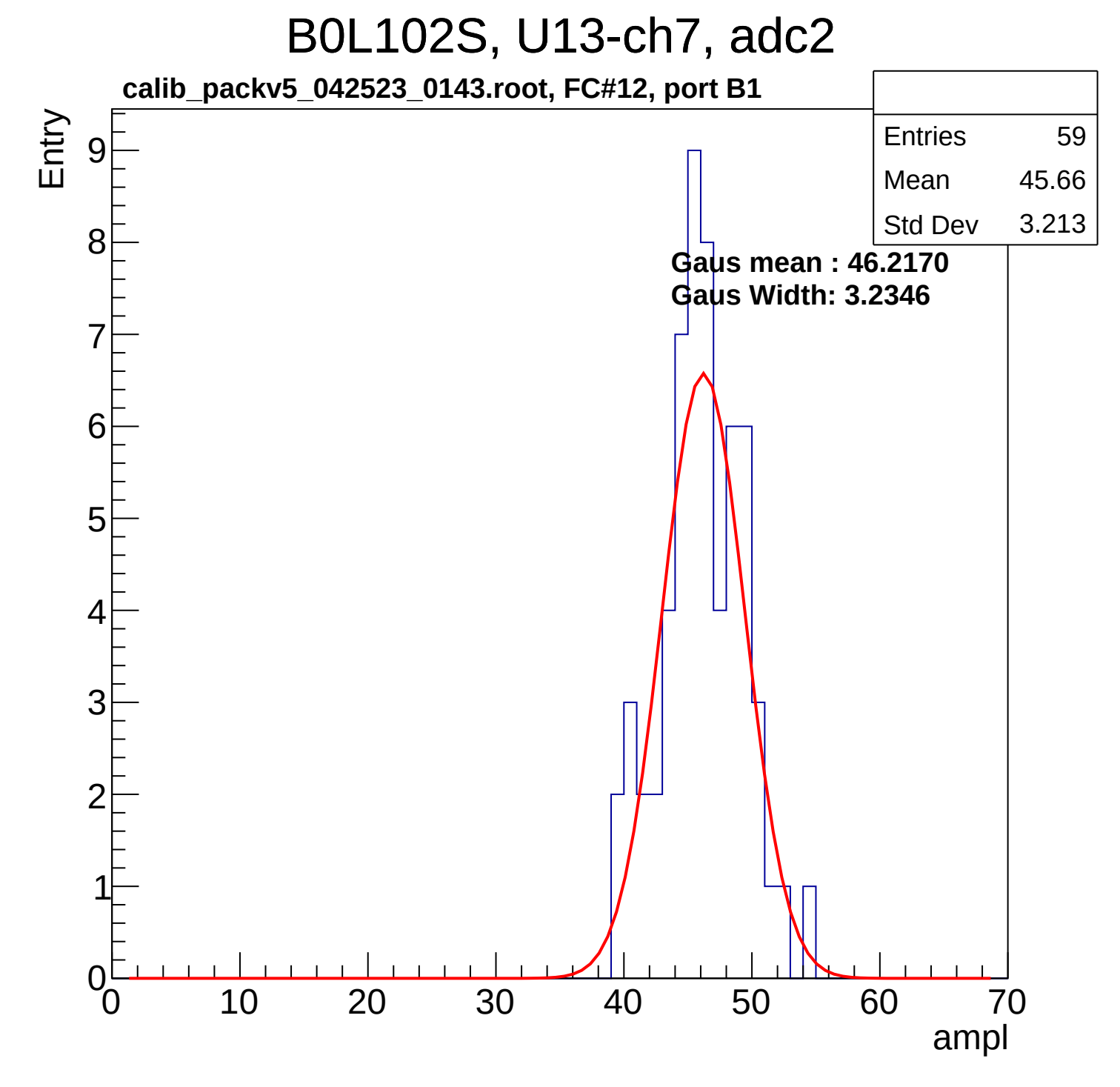
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch7, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 45.66
Std Dev 3.213
Gaus mean : 46.2170
Gaus Width: 3.2346
Entry
ampl
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2
3
4
5
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7
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0
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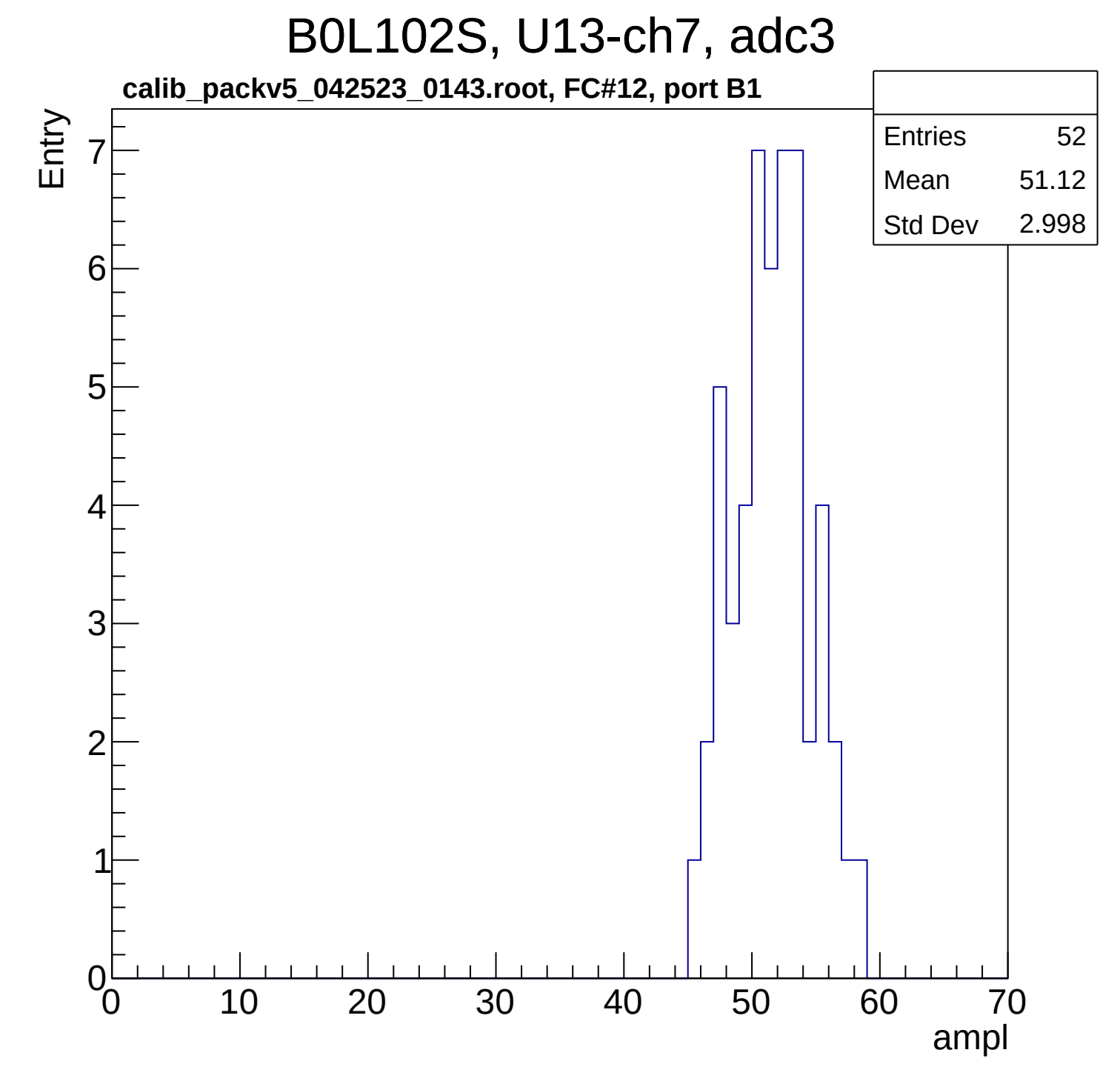
B0L102S, U13-ch7, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 51.12
Std Dev 2.998
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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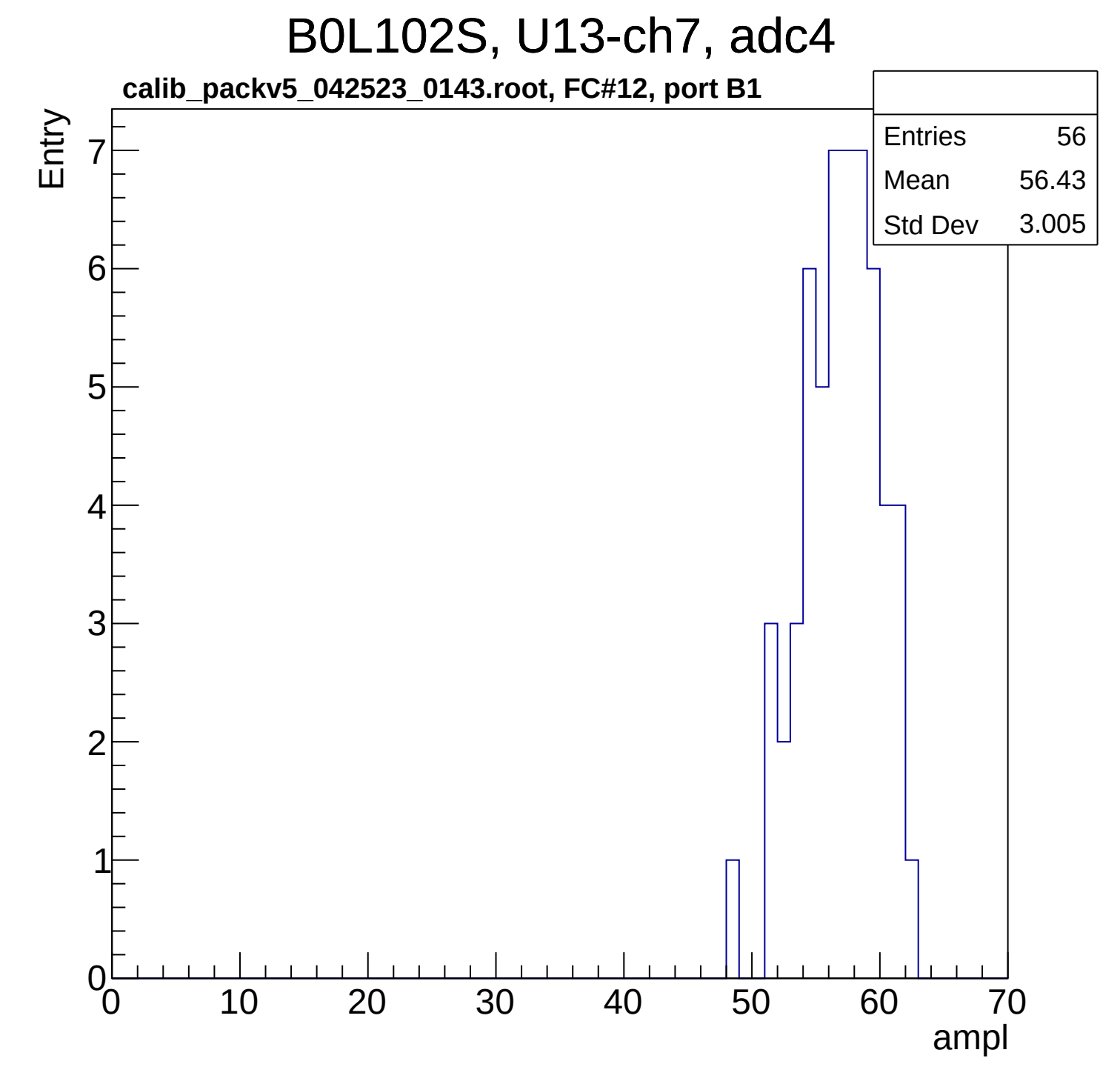
40
50
60
70


B0L102S, U13-ch7, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 56.43
Std Dev 3.005
Entry
ampl
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2
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4
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60
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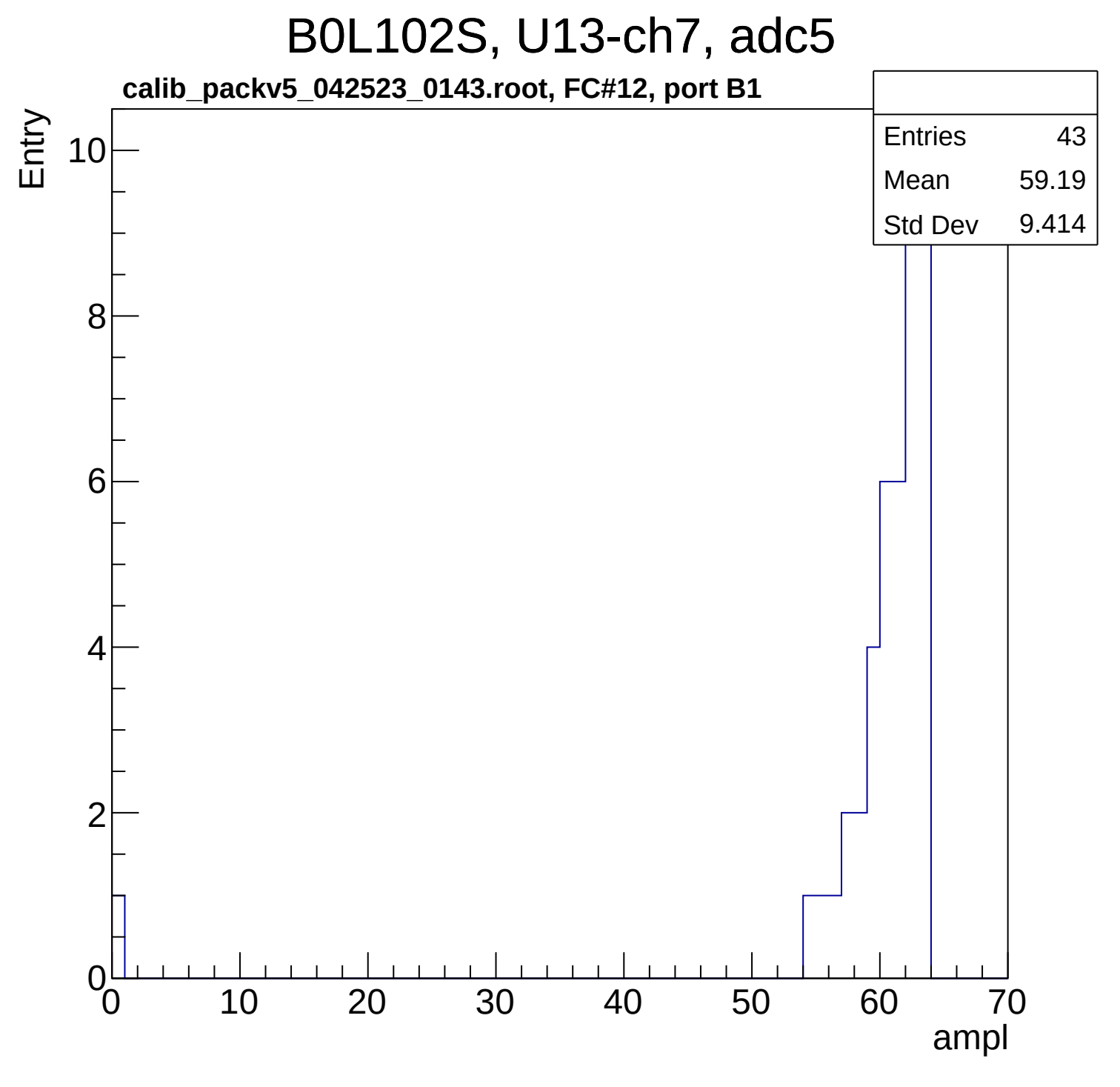
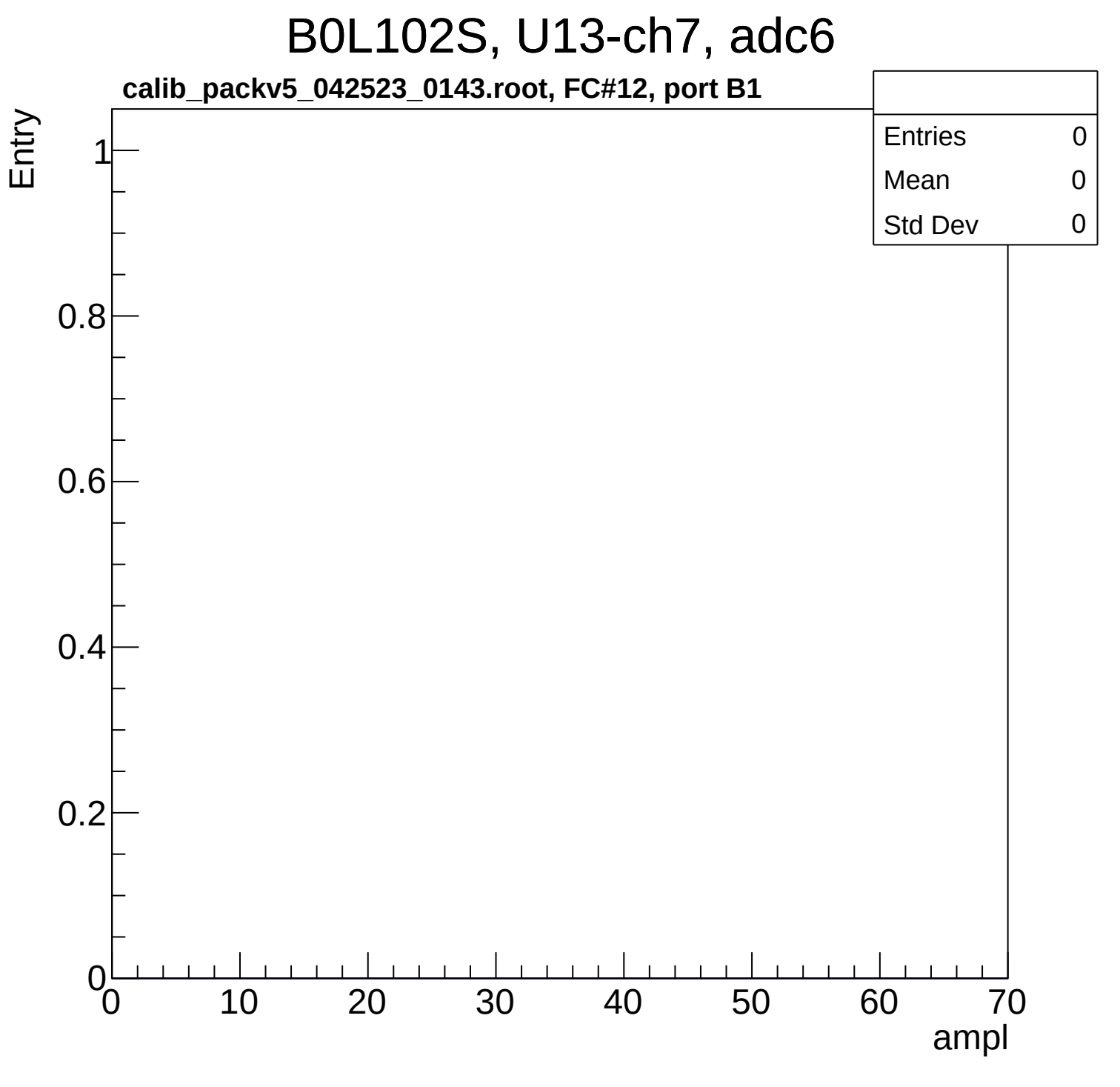
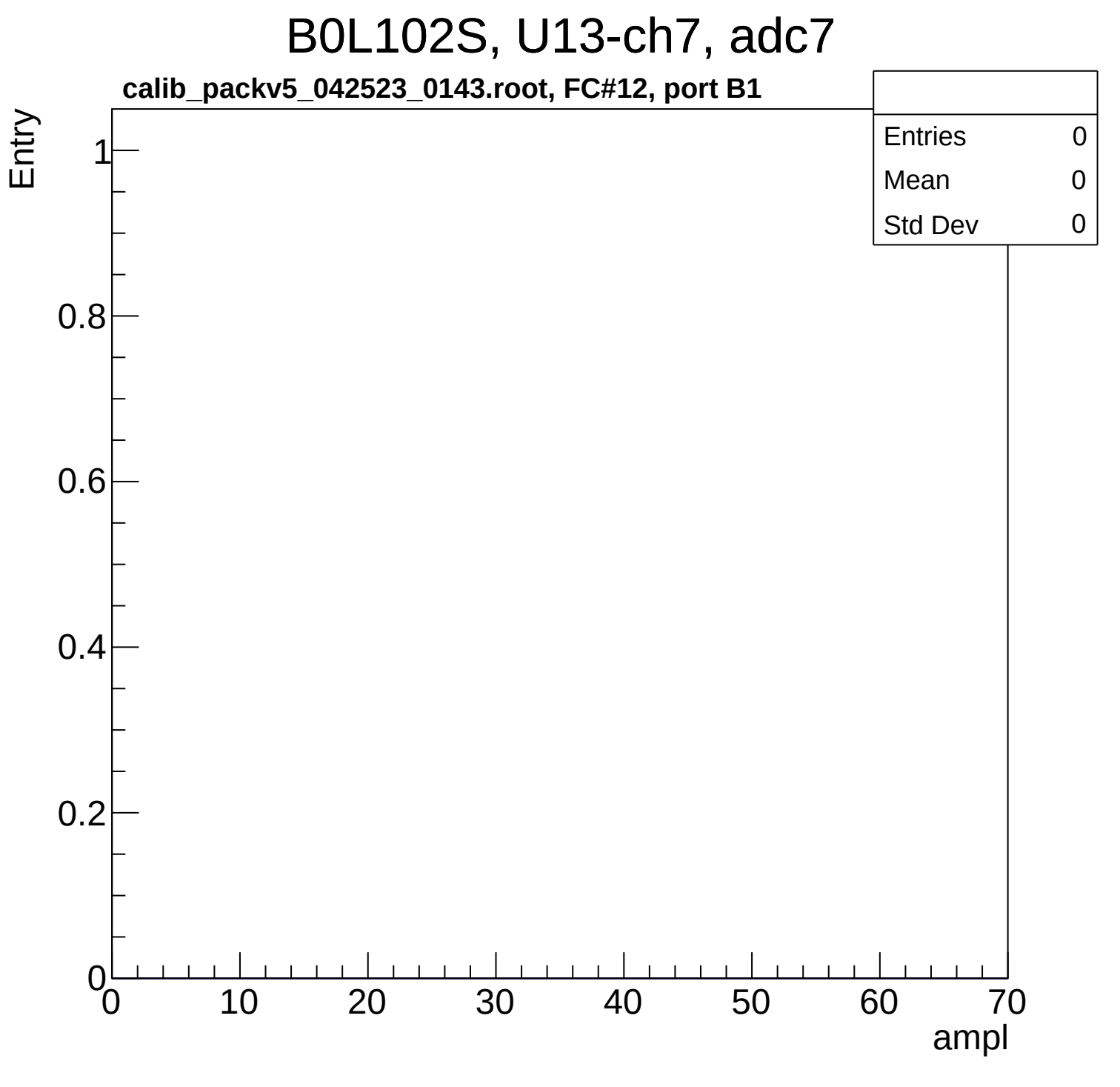

B0L102S, U13-ch7, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.19
Std Dev 9.414
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch7, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch7, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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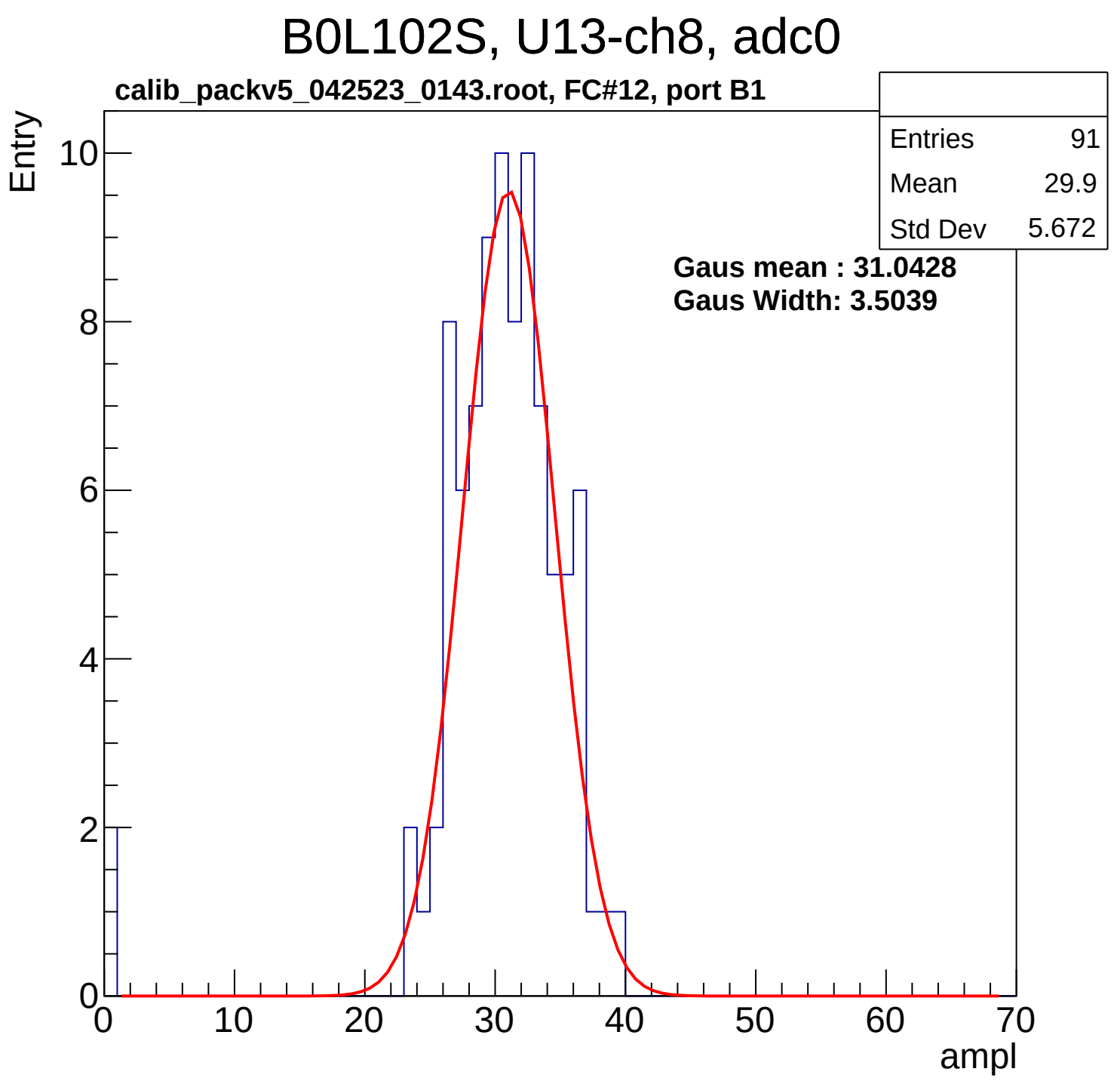
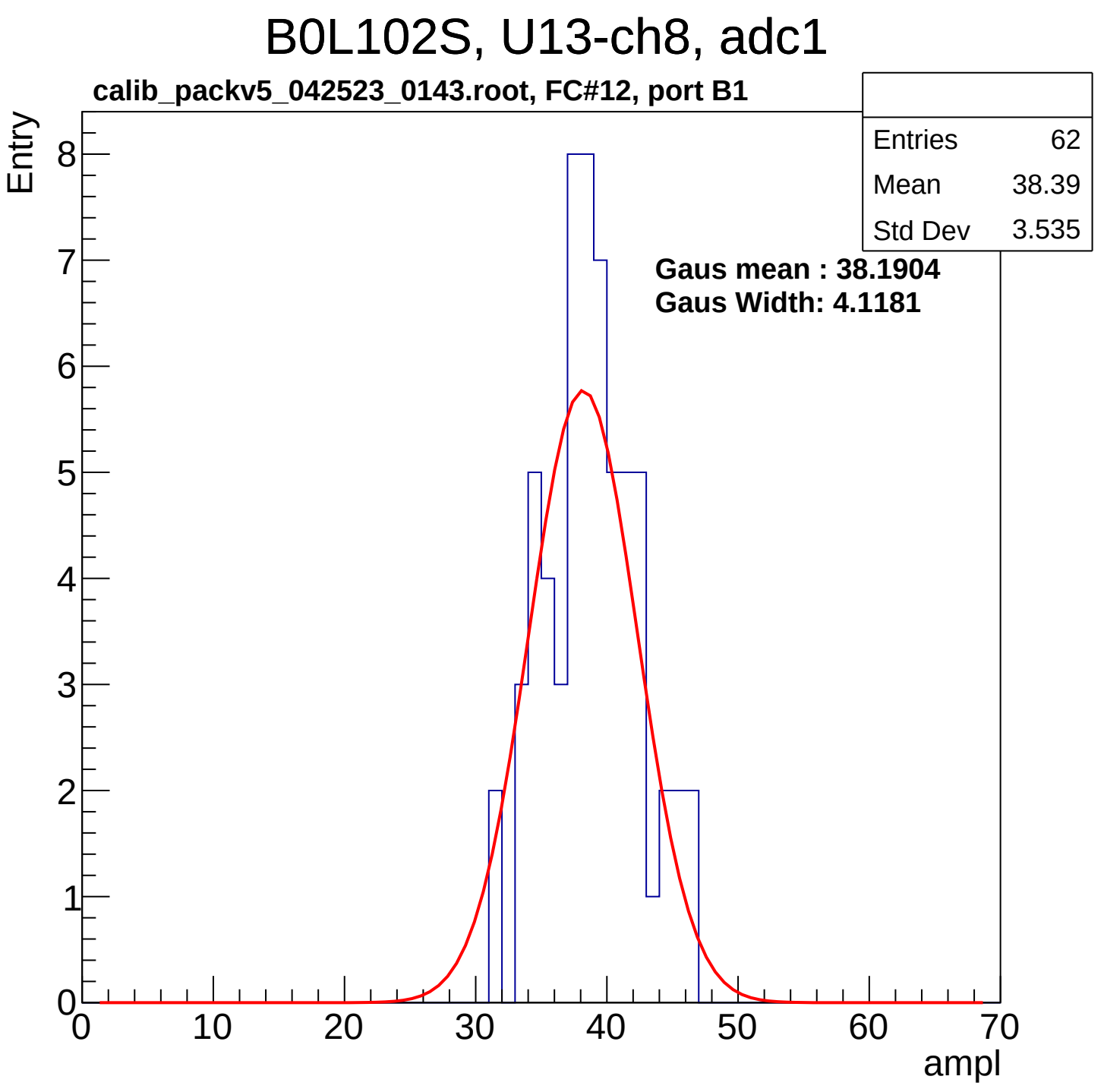
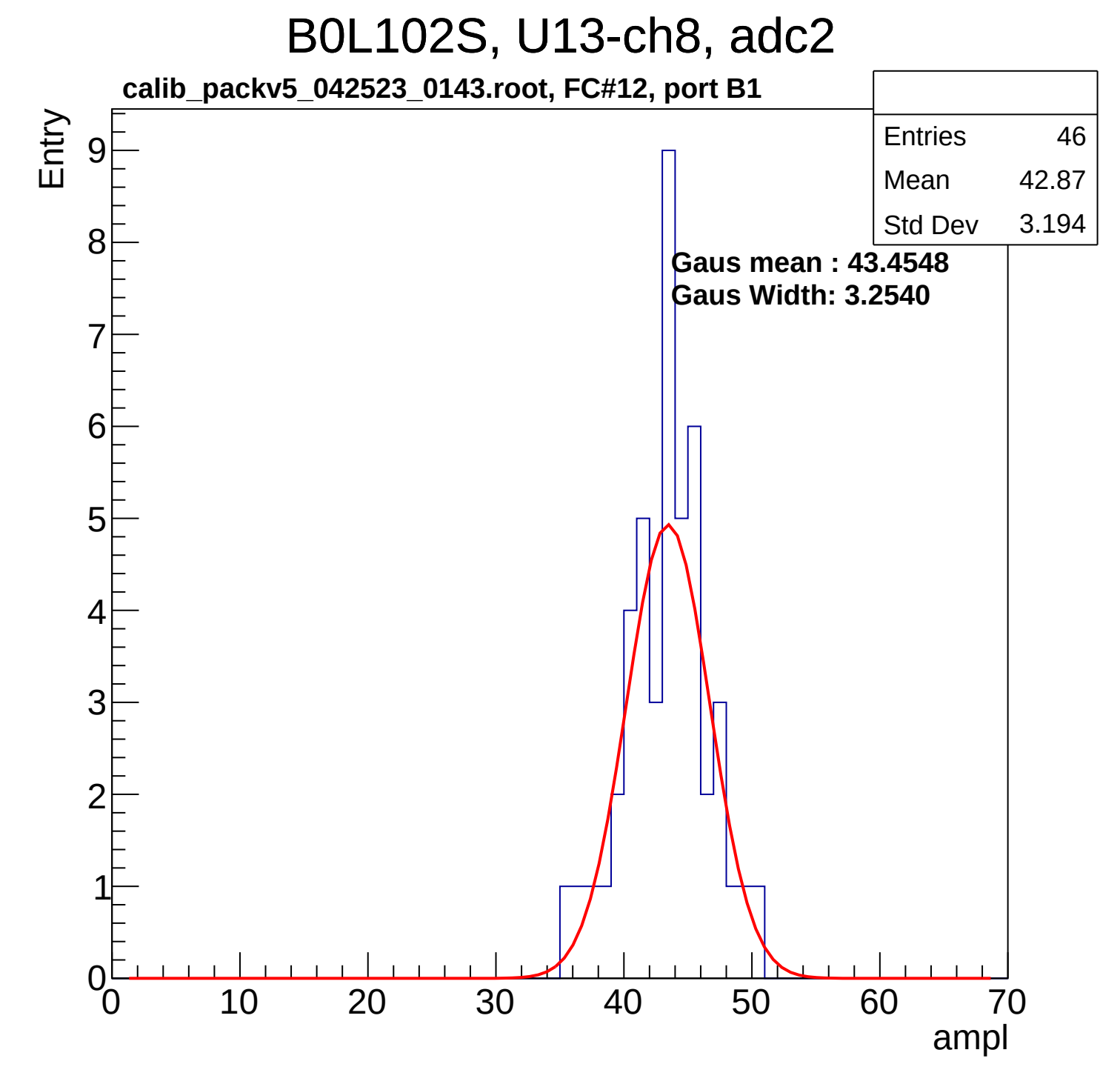
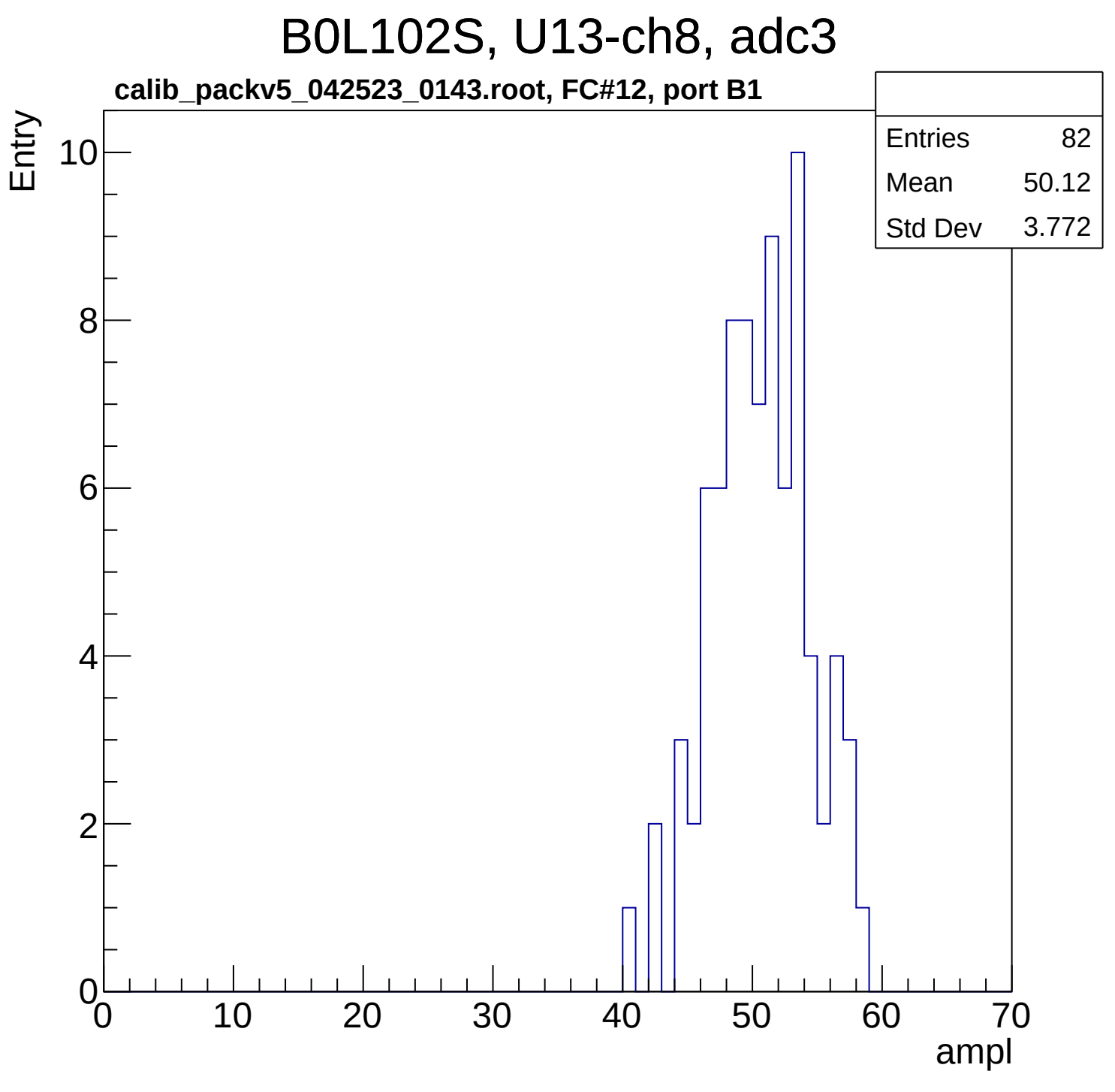
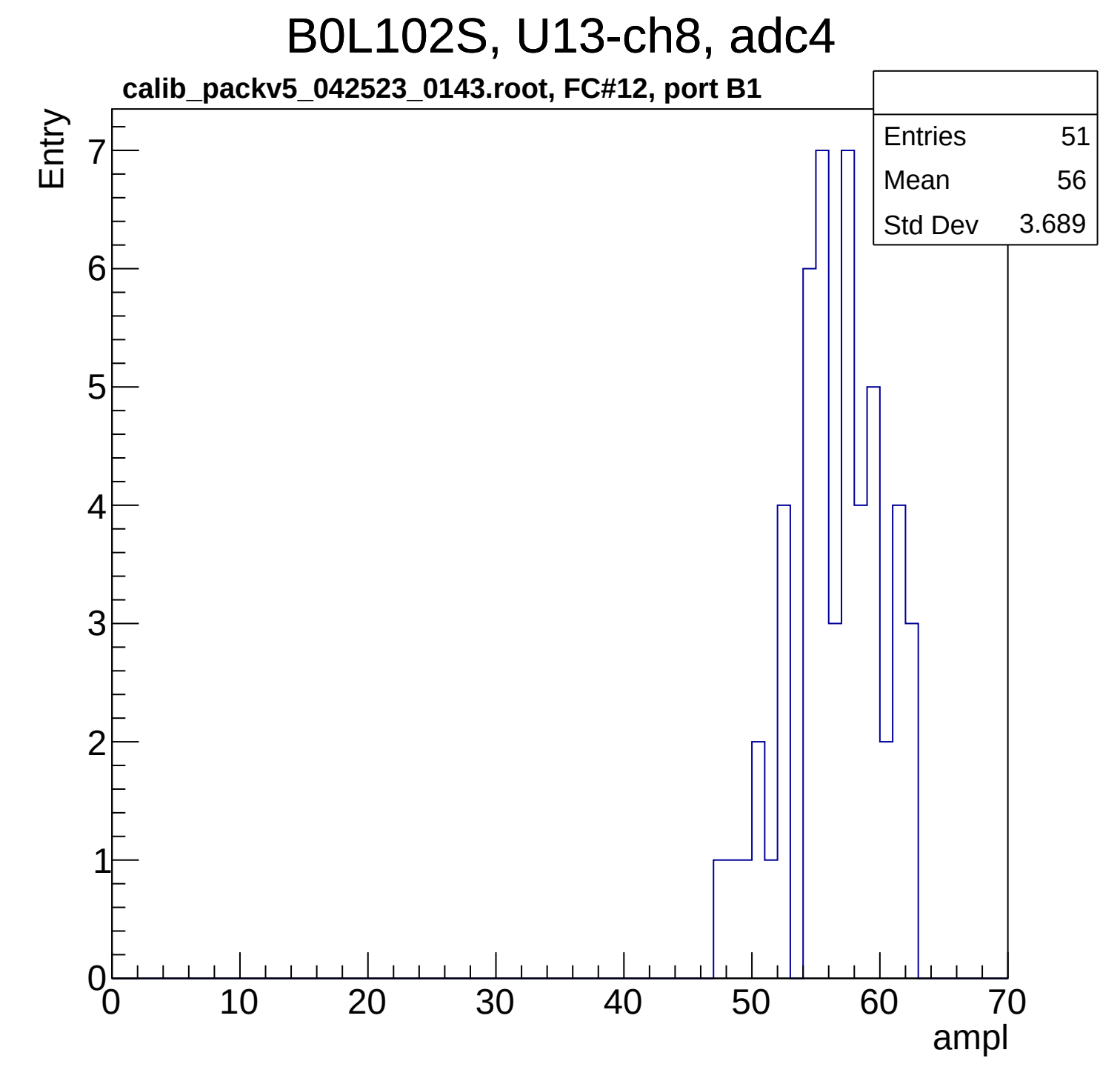
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

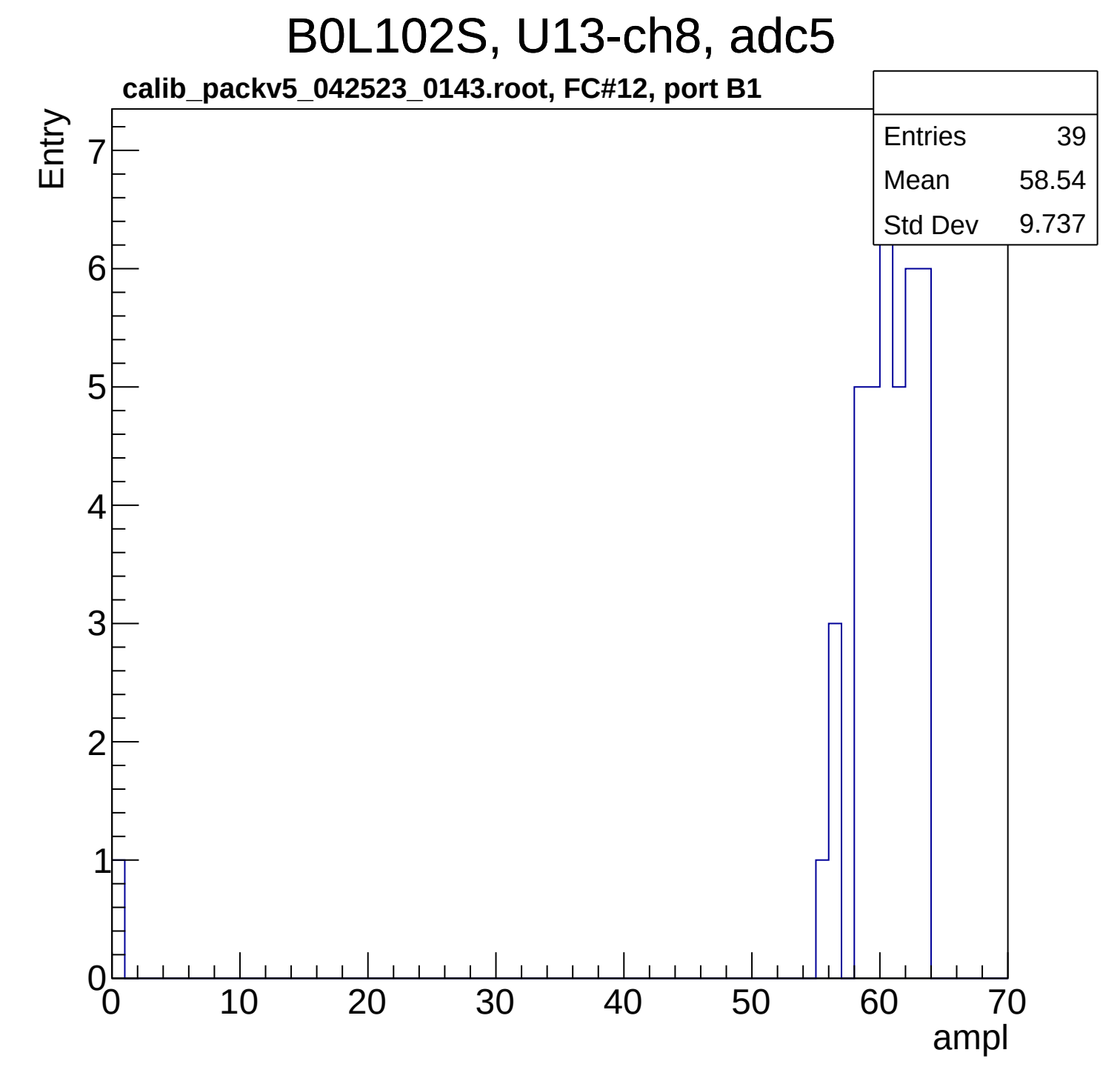

B0L102S, U13-ch8, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 91
Mean 29.9
Std Dev 5.672
Gaus mean : 31.0428
Gaus Width: 3.5039
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

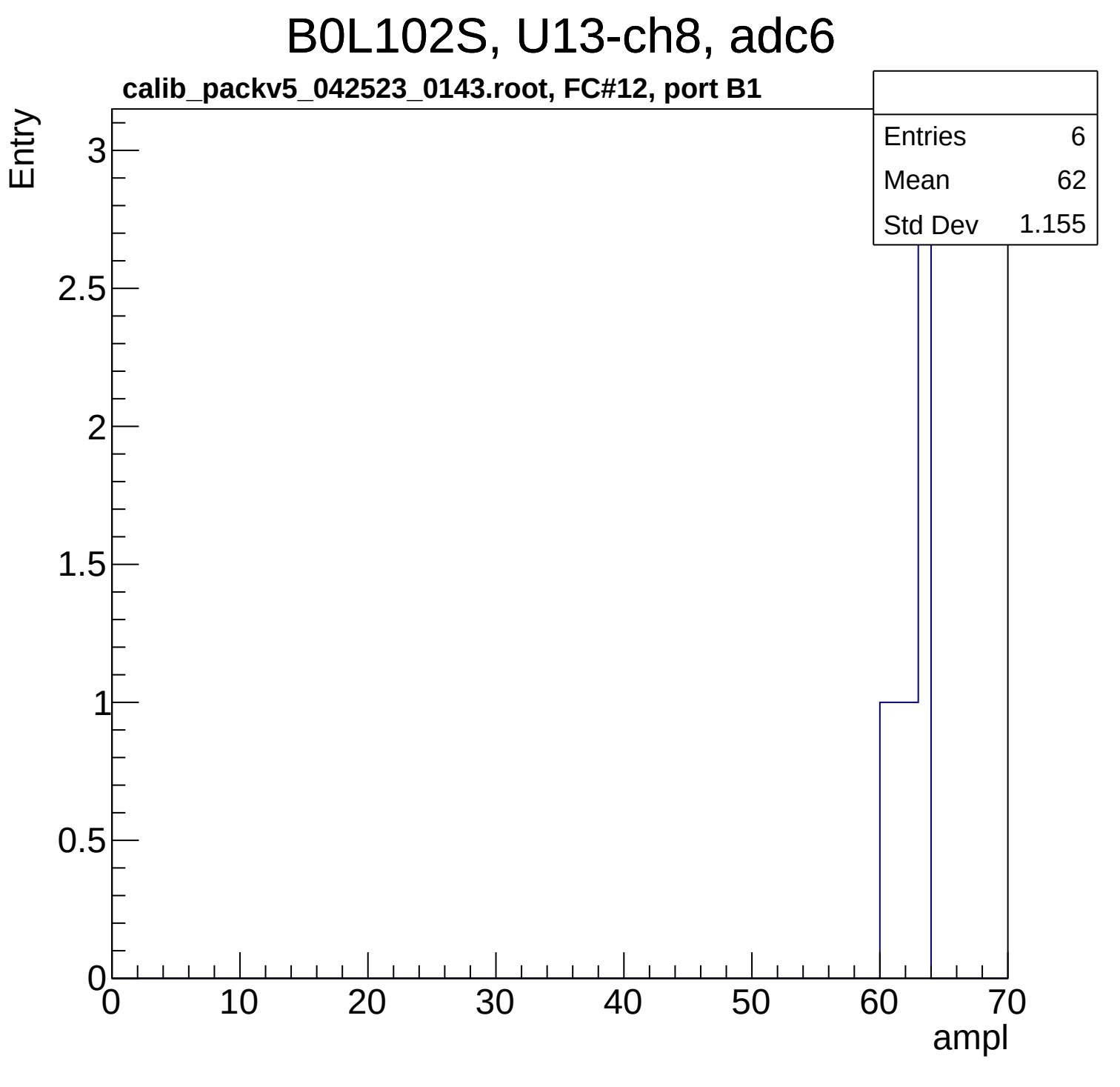

B0L102S, U13-ch8, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 38.39
Std Dev 3.535
Gaus mean : 38.1904
Gaus Width: 4.1181
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

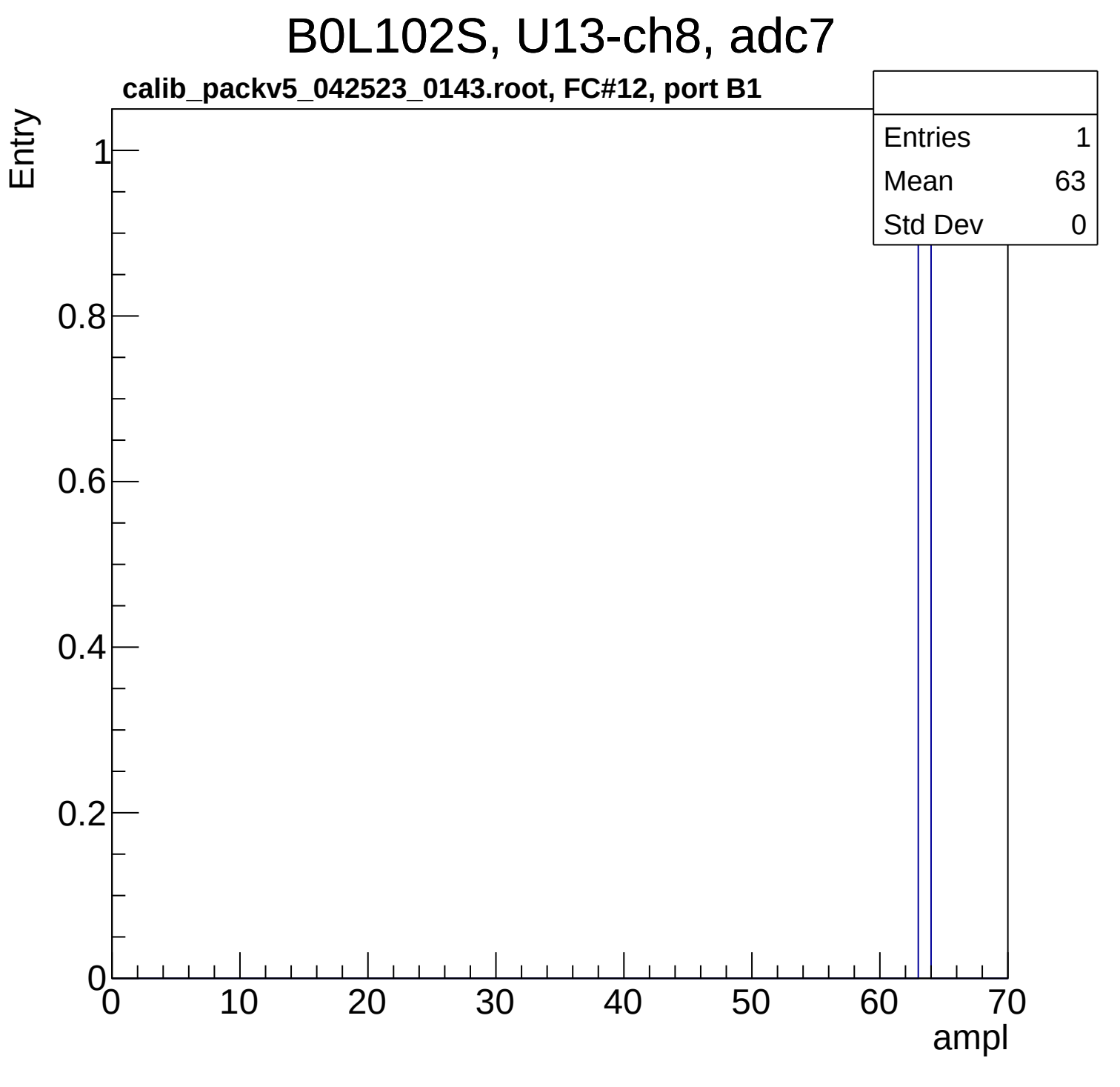

B0L102S, U13-ch8, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 42.87
Std Dev 3.194
Gaus mean : 43.4548
Gaus Width: 3.2540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

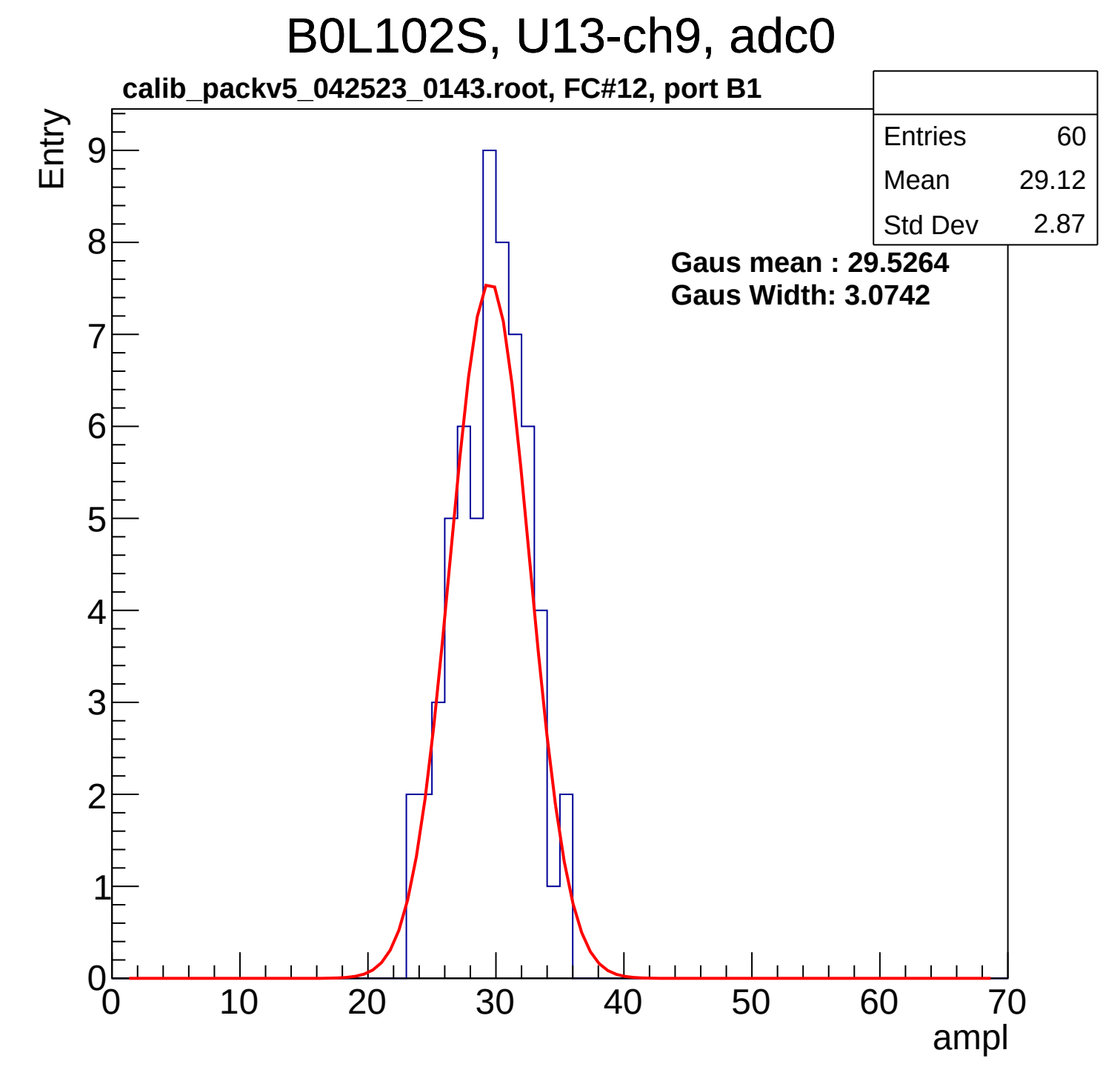

B0L102S, U13-ch8, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 50.12
Std Dev 3.772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

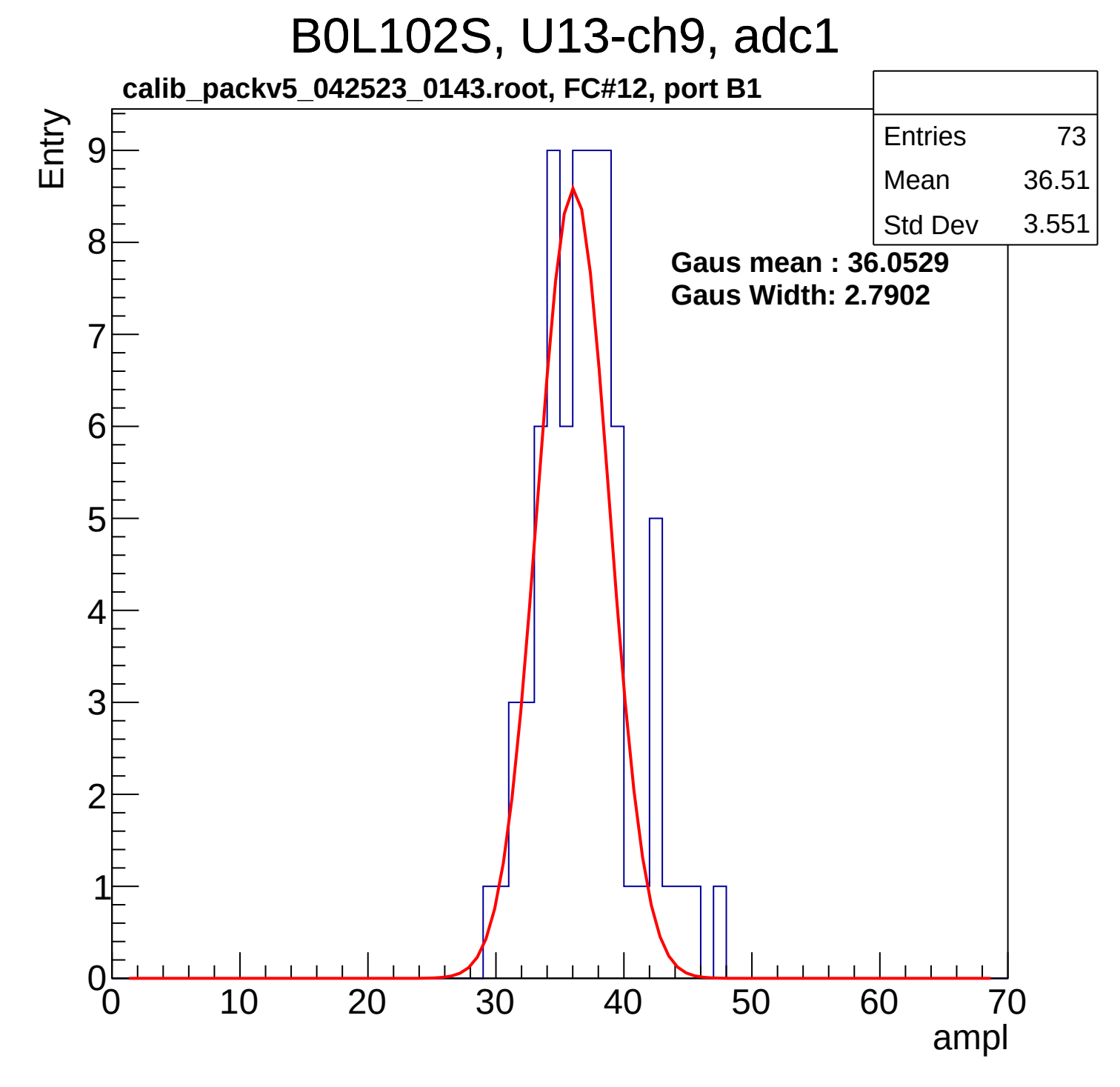

B0L102S, U13-ch8, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 56
Std Dev 3.689
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

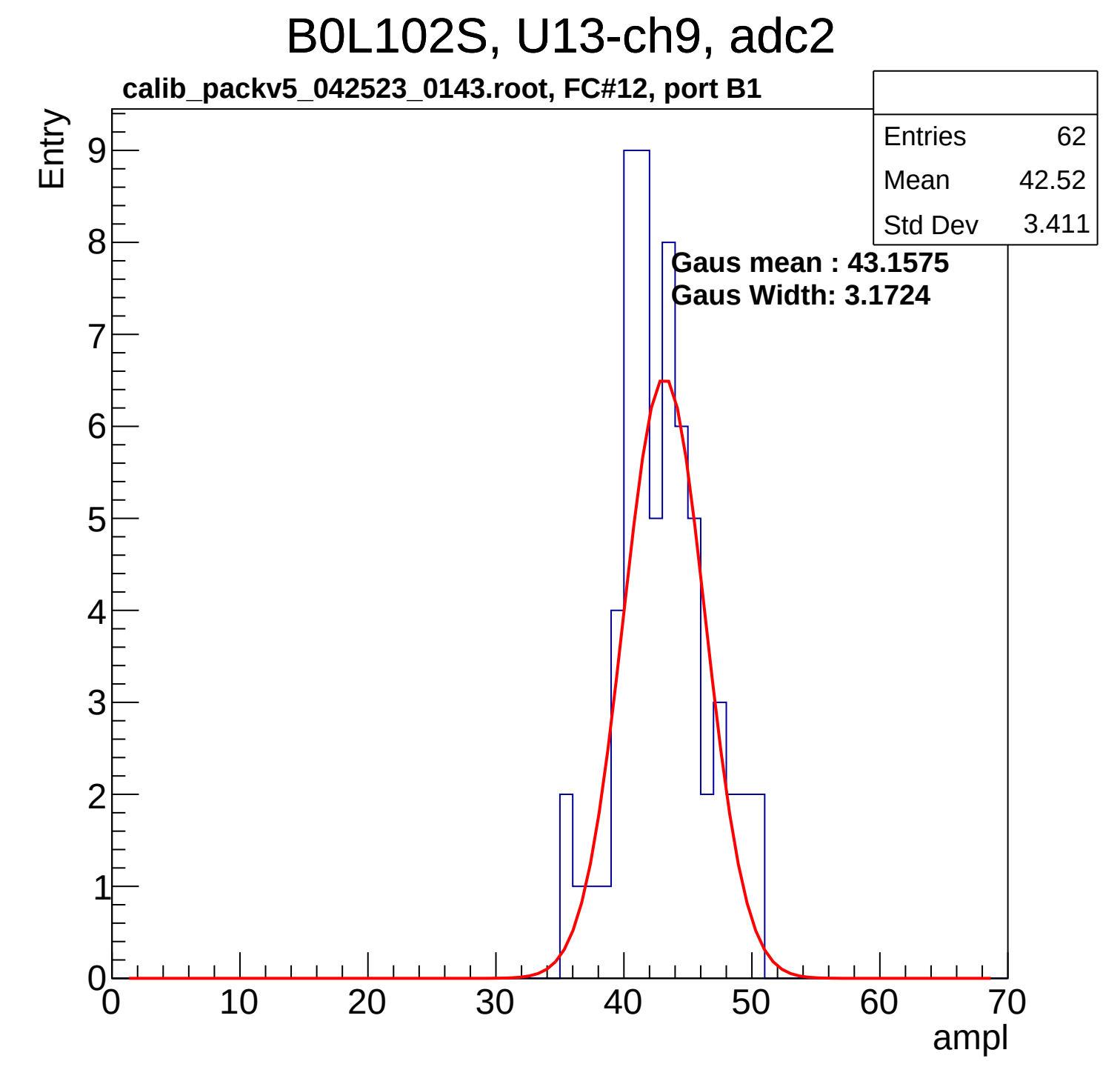

B0L102S, U13-ch8, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 58.54
Std Dev 9.737
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

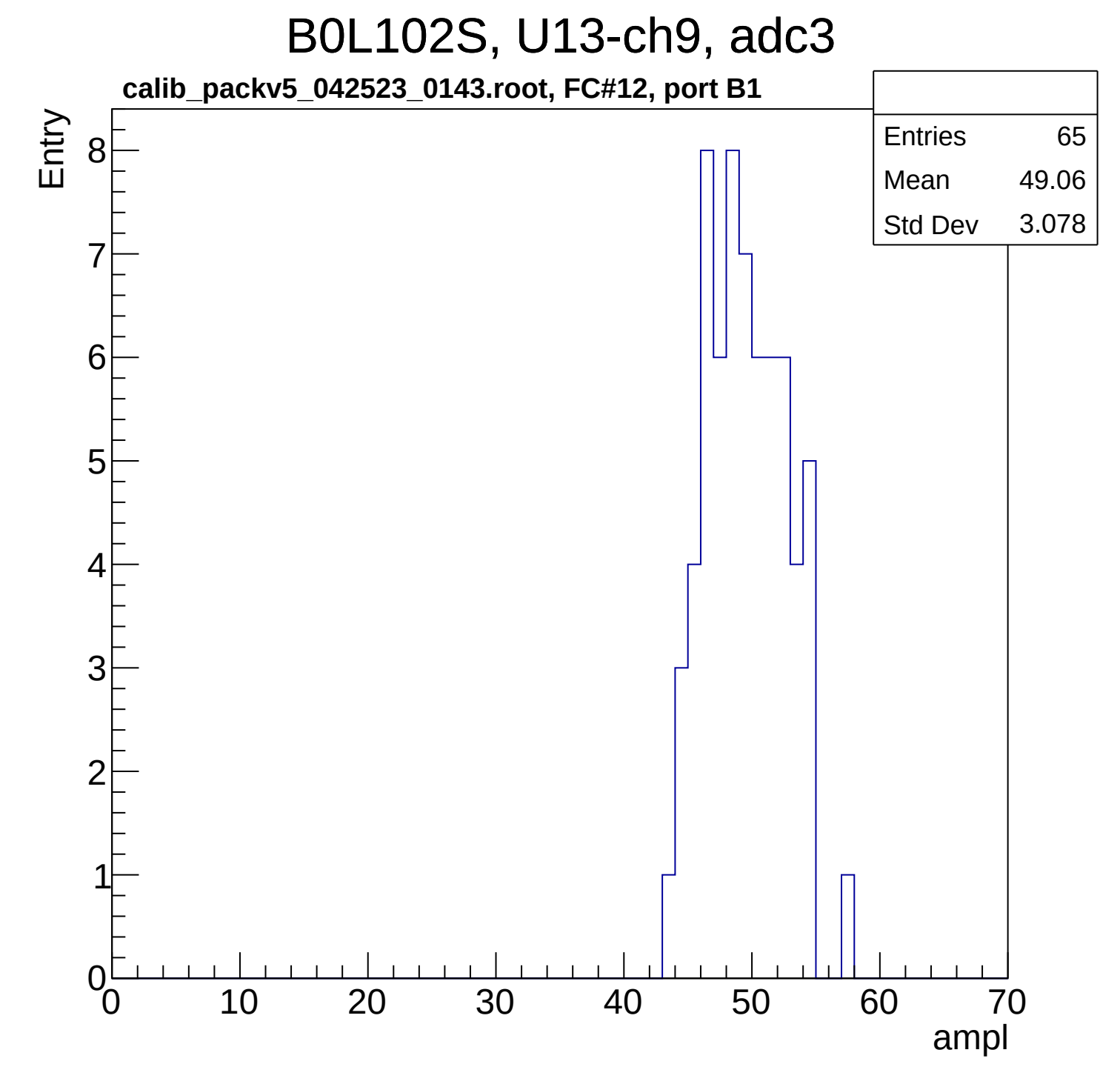

B0L102S, U13-ch8, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62
Std Dev 1.155
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch8, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch9, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 29.12
Std Dev 2.87
Gaus mean : 29.5264
Gaus Width: 3.0742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch9, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.51
Std Dev 3.551
Gaus mean : 36.0529
Gaus Width: 2.7902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch9, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.52
Std Dev 3.411
Gaus mean : 43.1575
Gaus Width: 3.1724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch9, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.06
Std Dev 3.078
Entry
ampl
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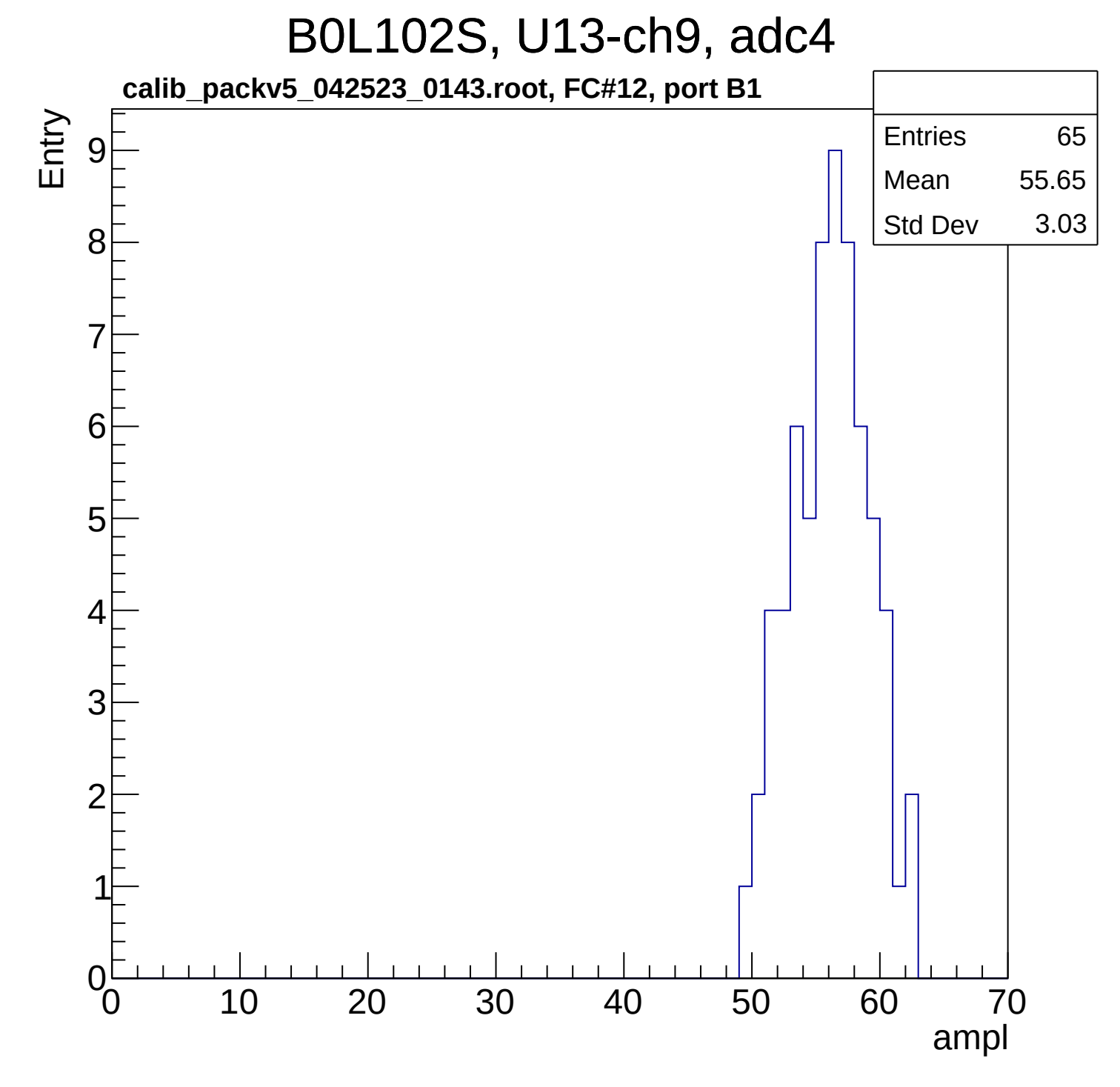
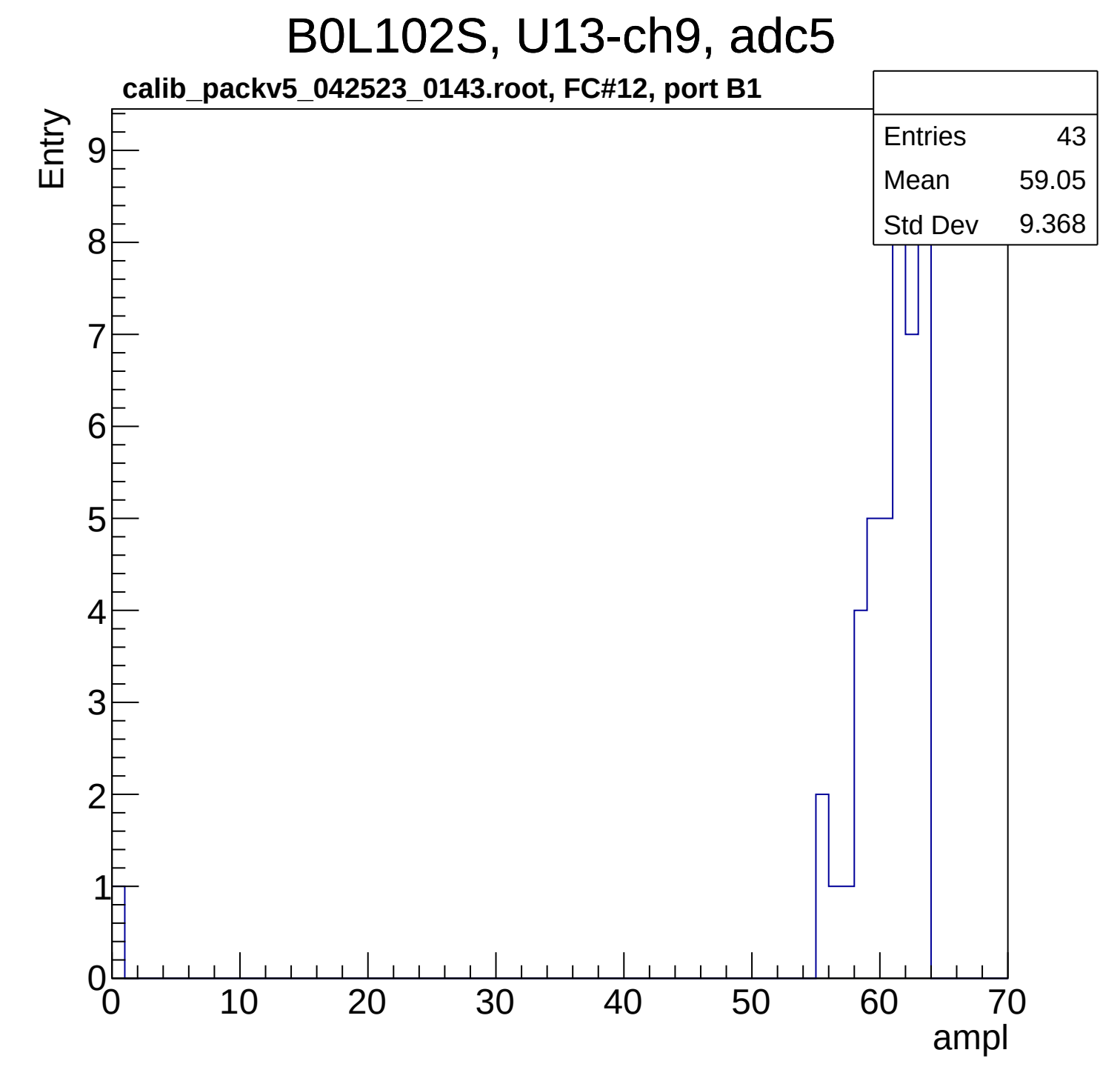
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch9, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 55.65
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch9, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.05
Std Dev 9.368
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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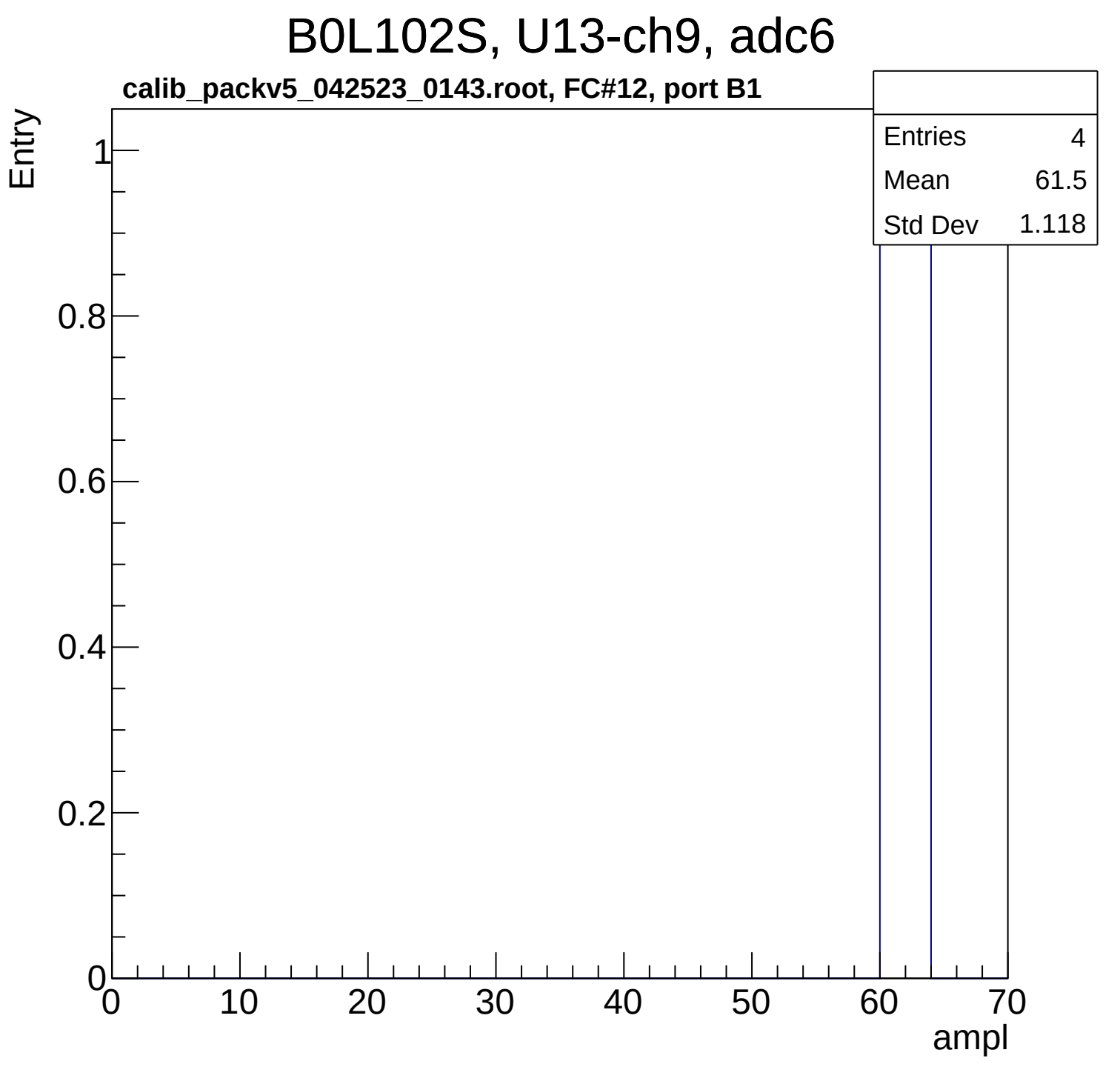
B0L102S, U13-ch9, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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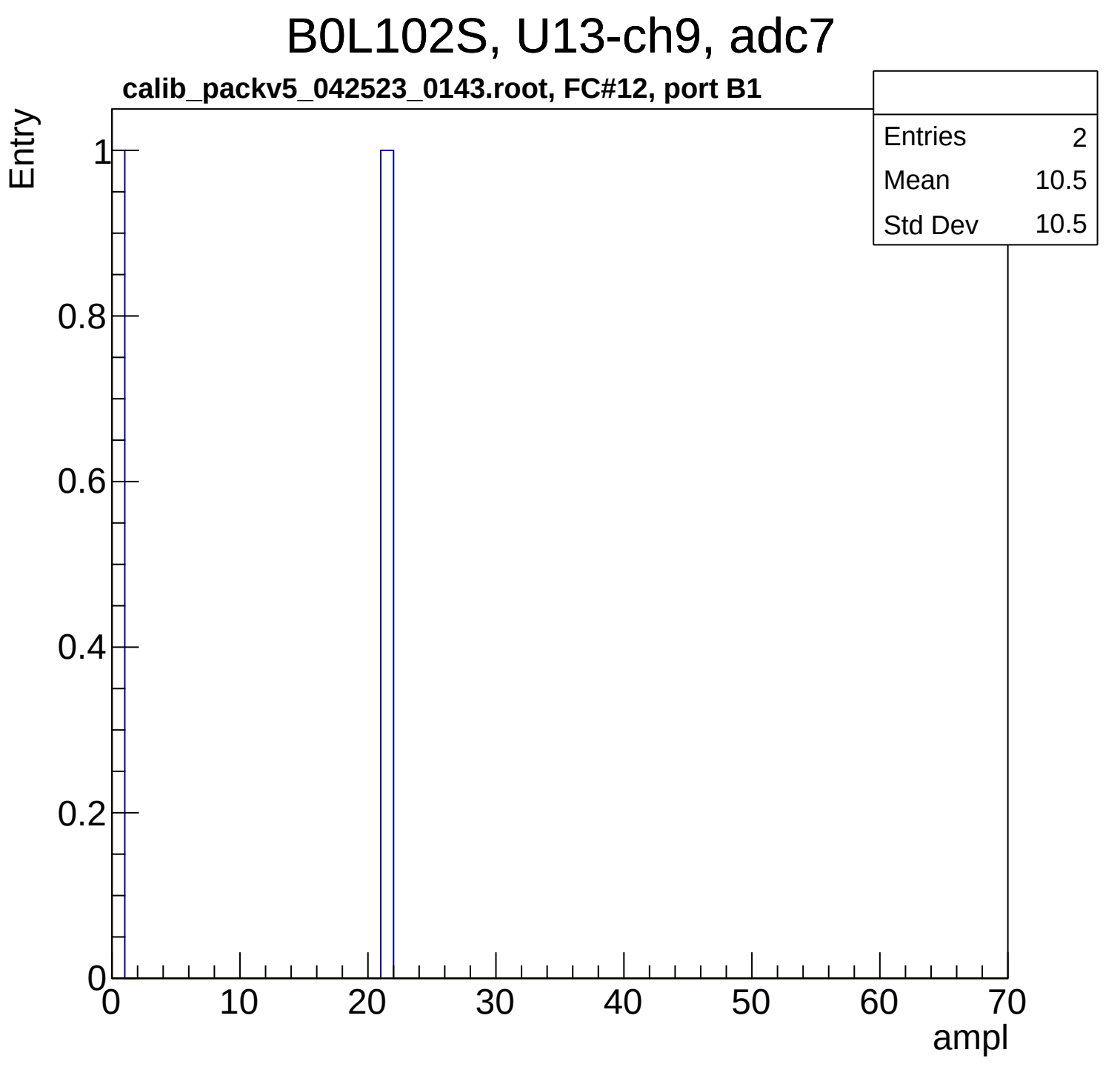
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch9, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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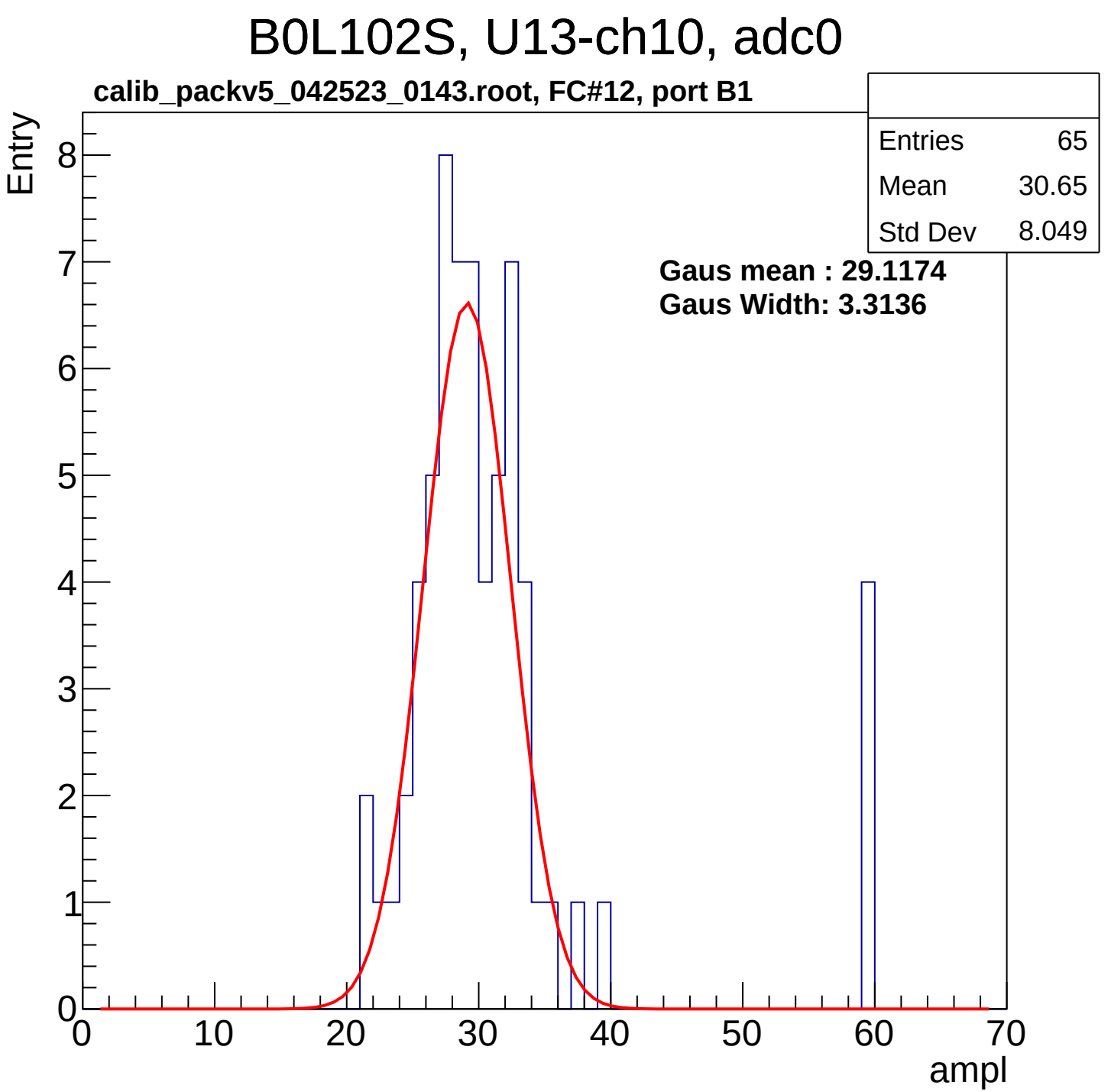
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch10, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 30.65
Std Dev 8.049
Gaus mean : 29.1174
Gaus Width: 3.3136
Entry
ampl
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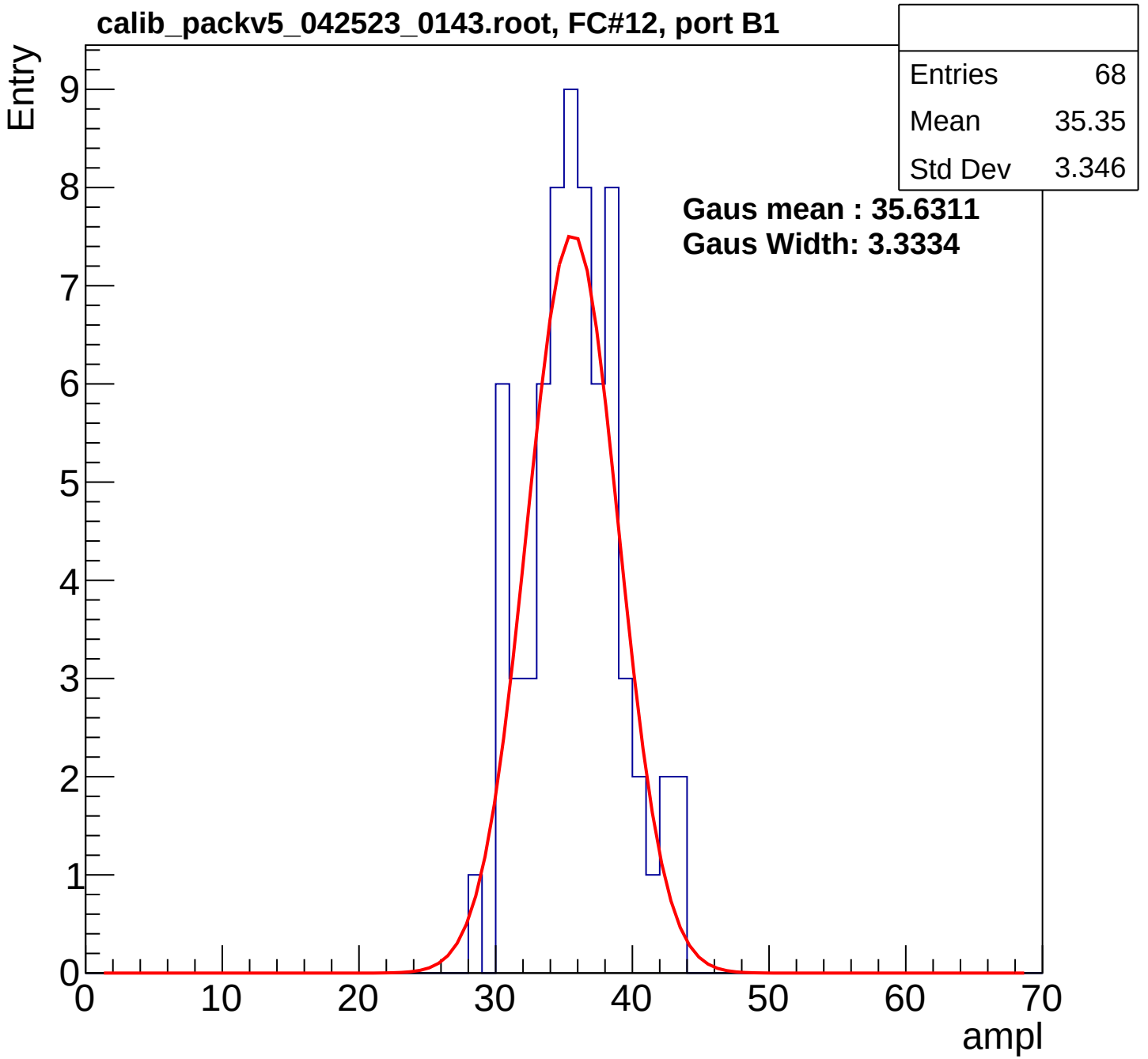
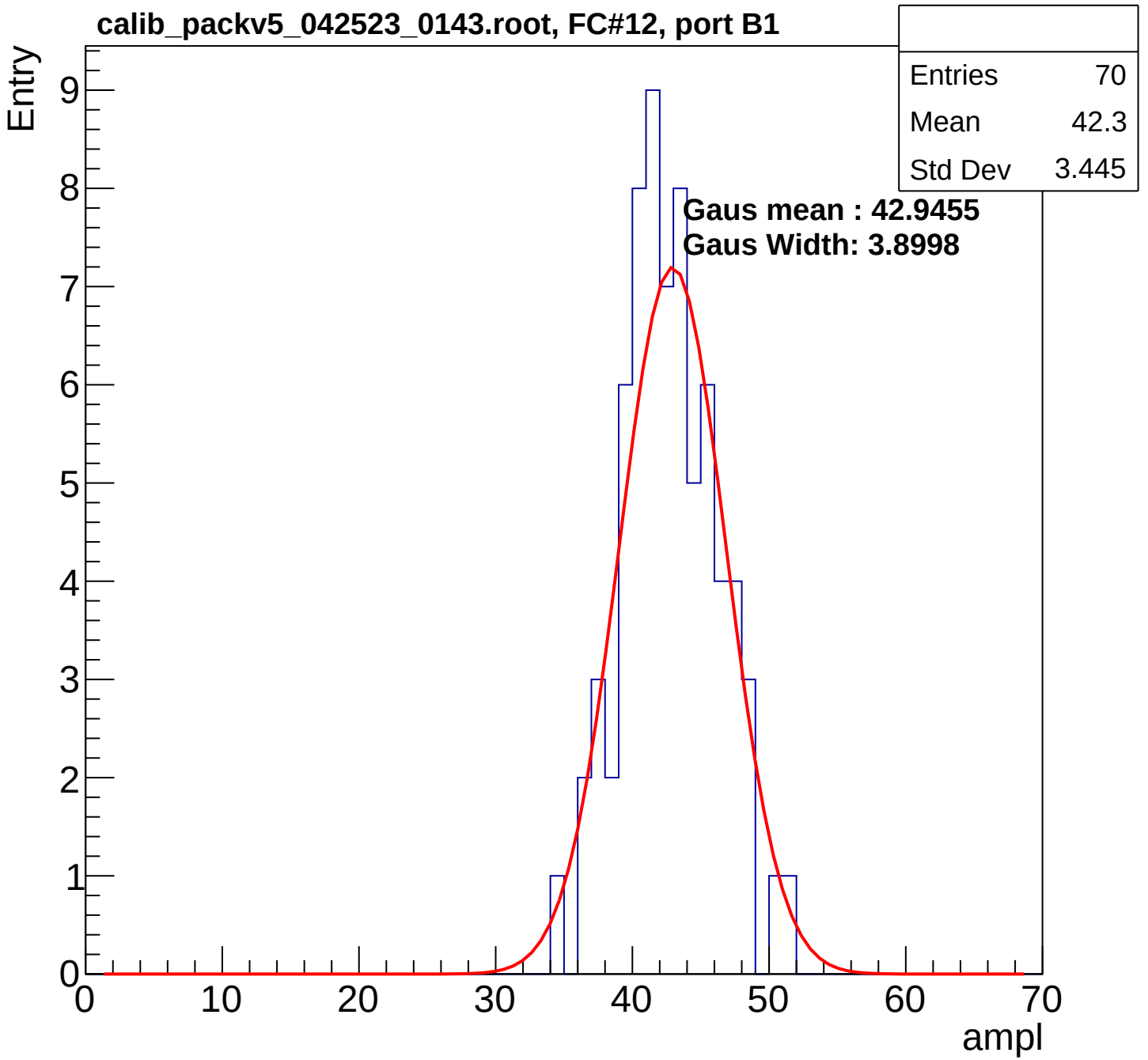
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch10, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.35
Std Dev 3.346
Gaus mean : 35.6311
Gaus Width: 3.3334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch10, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 42.3
Std Dev 3.445
Gaus mean : 42.9455
Gaus Width: 3.8998
Entry
ampl
0
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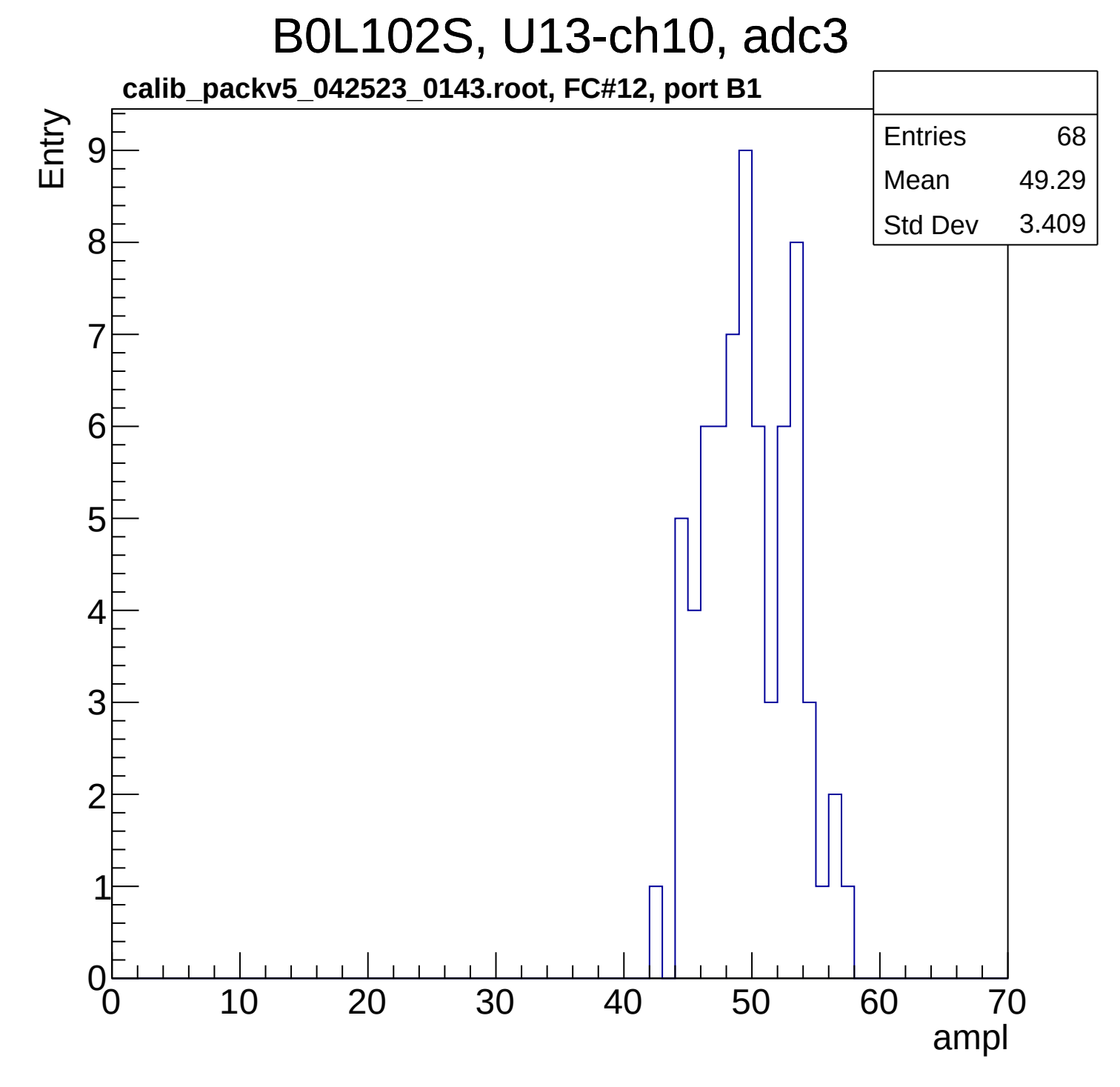
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch10, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.29
Std Dev 3.409
Entry
ampl
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4
5
6
7
8
9
0
10
20
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40
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60
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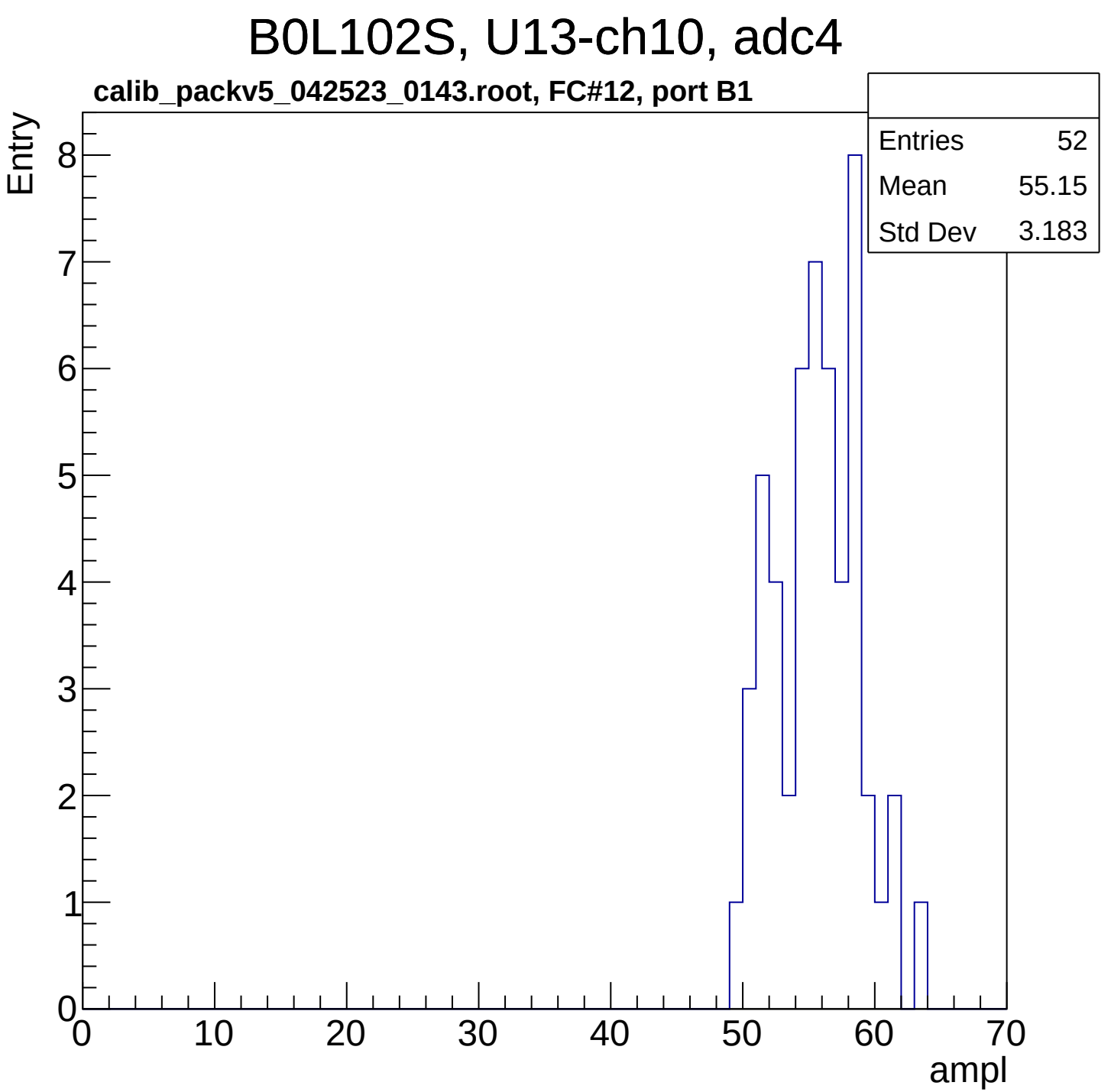
B0L102S, U13-ch10, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.15
Std Dev 3.183
Entry
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6
7
8
0
10
20
30
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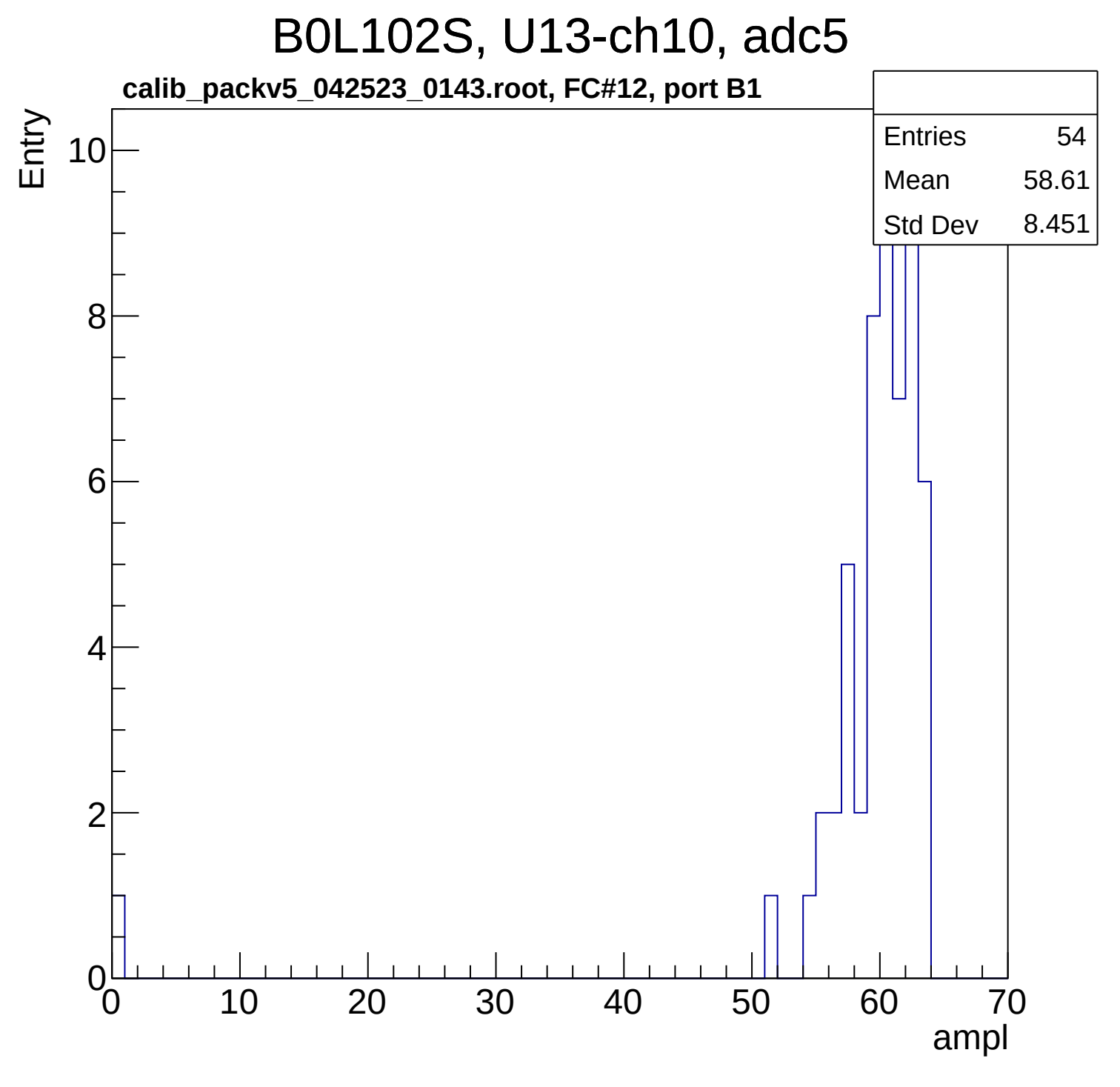
50
60
70
ampl


B0L102S, U13-ch10, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.61
Std Dev 8.451
Entry
ampl
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2
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60
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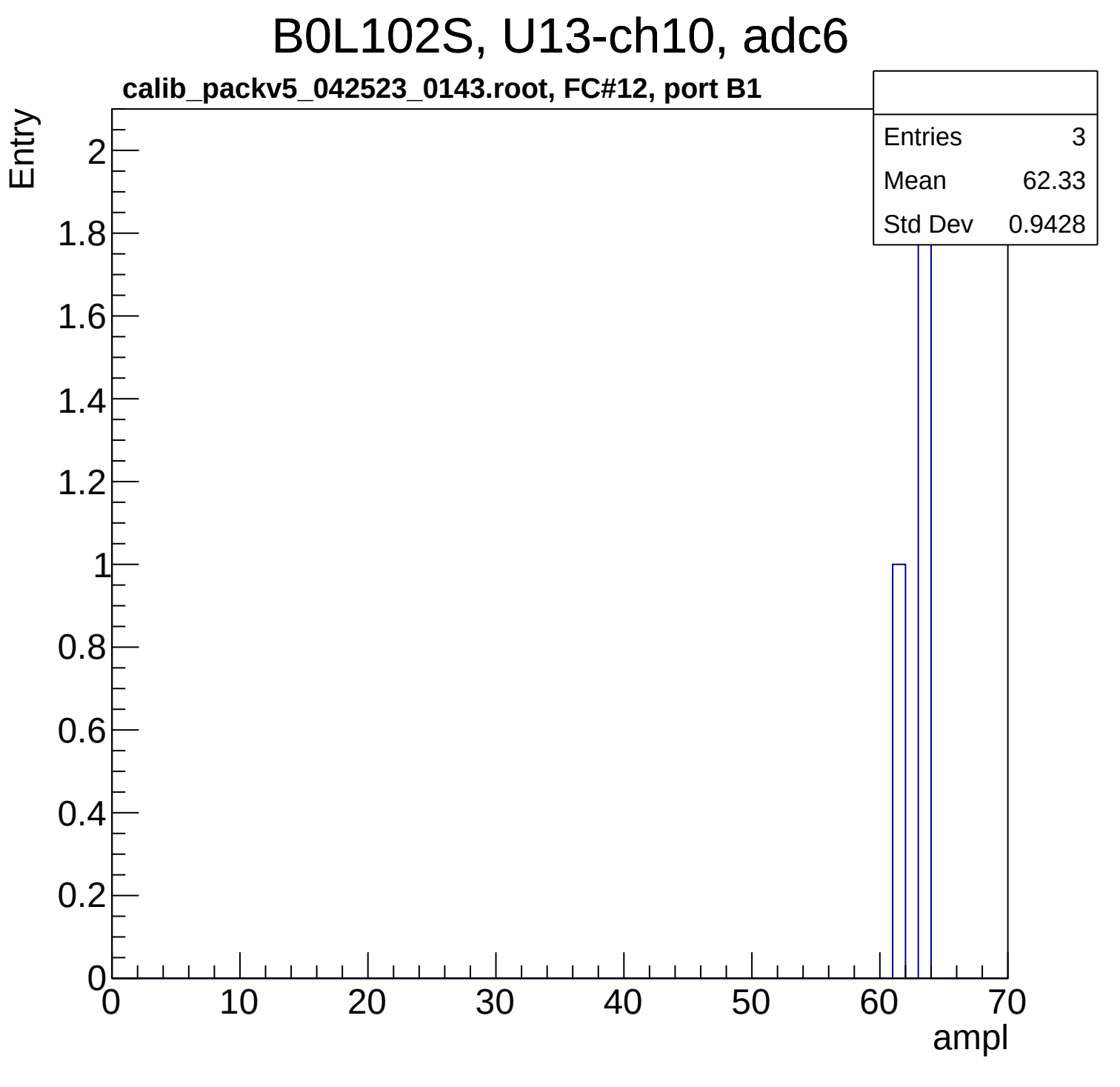
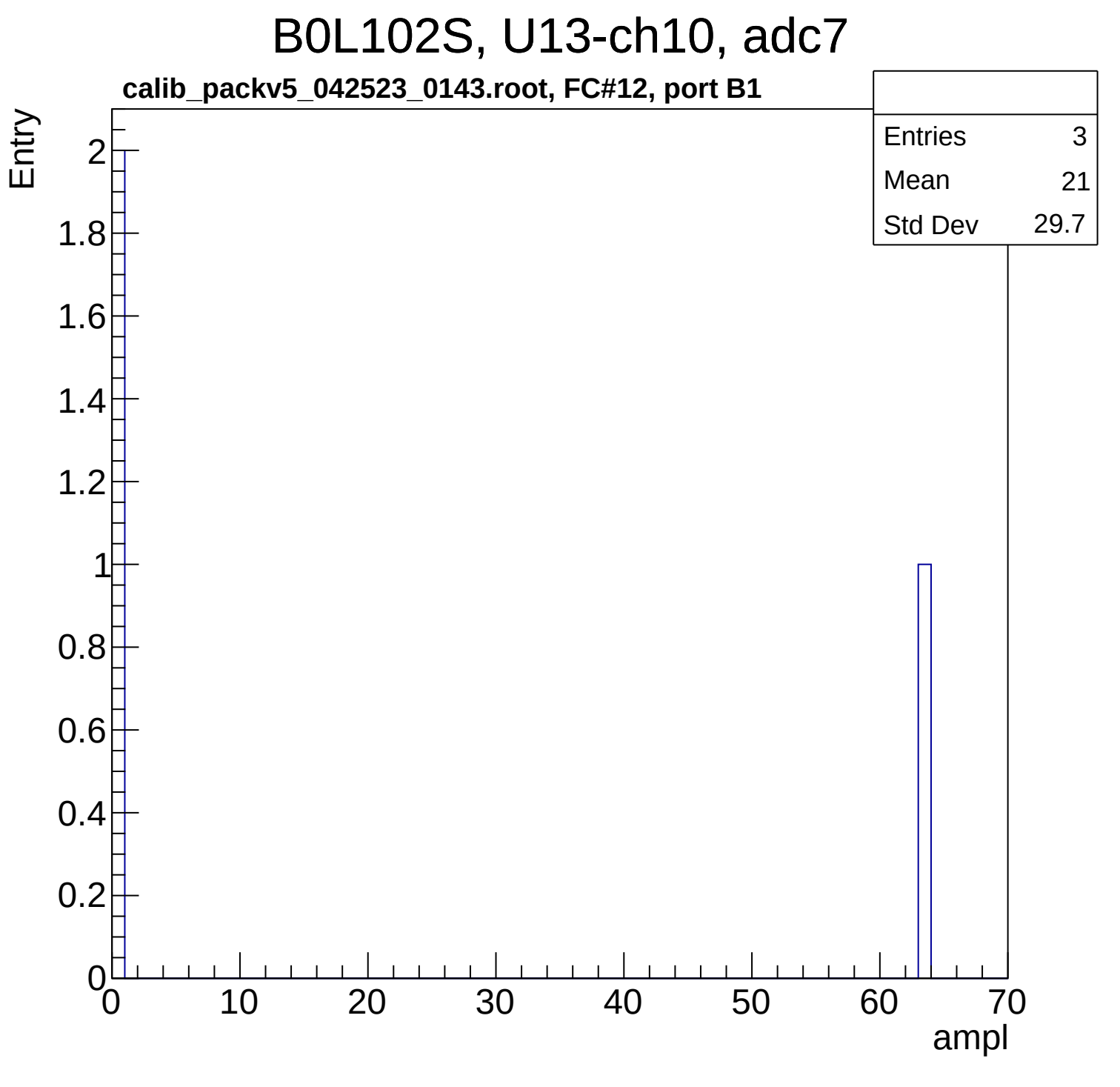

B0L102S, U13-ch10, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch10, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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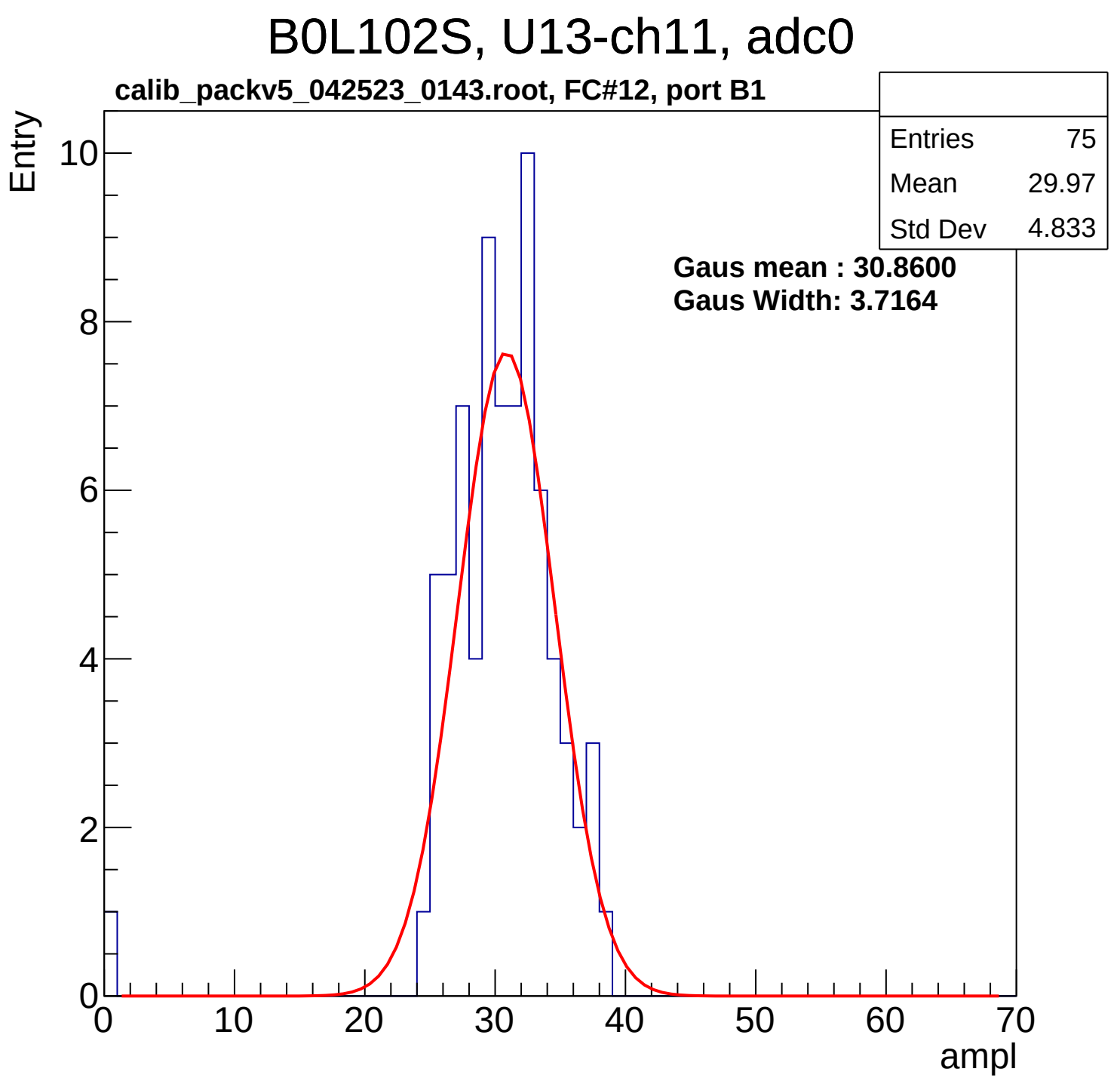
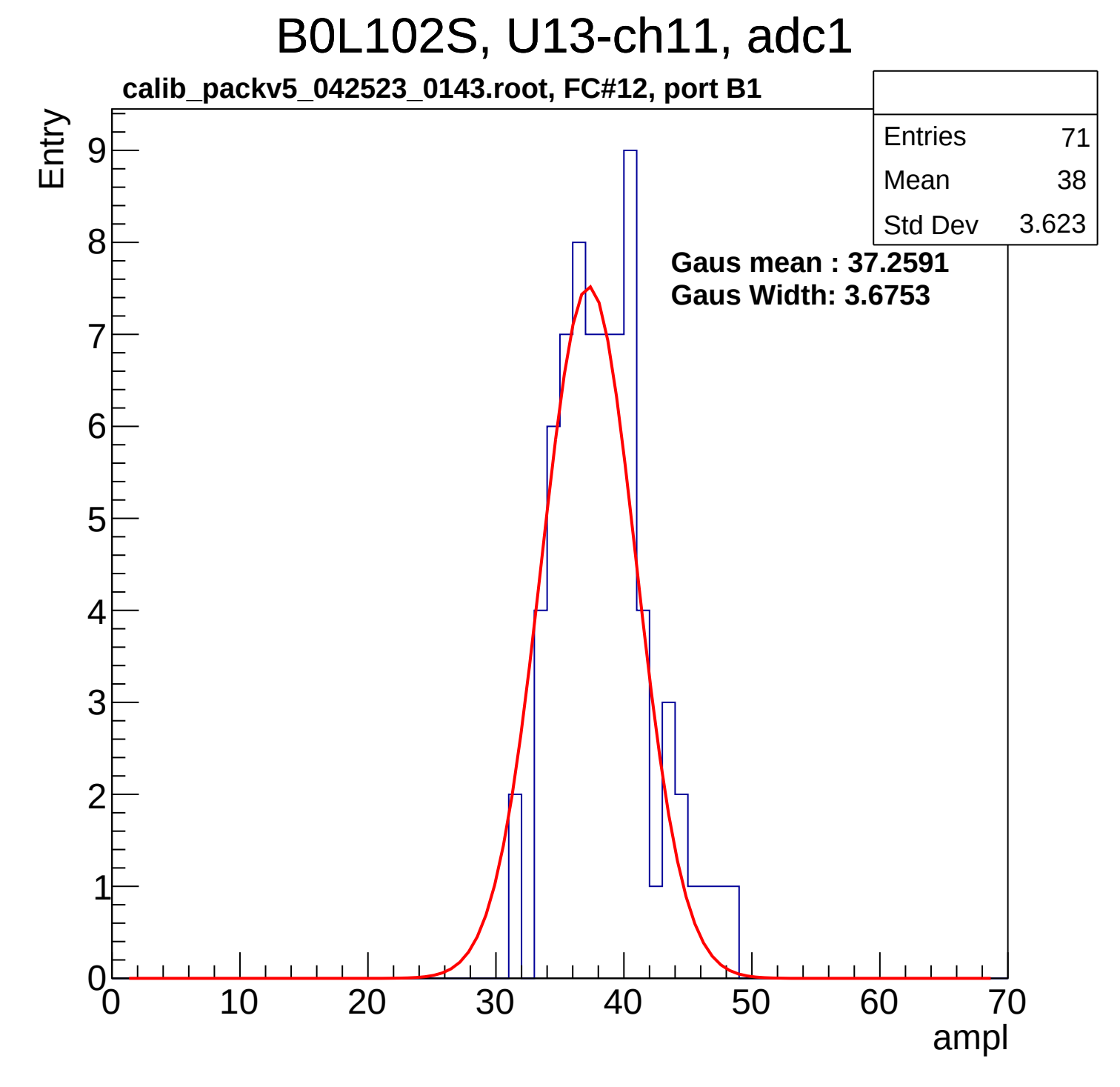

B0L102S, U13-ch11, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.97
Std Dev 4.833
Gaus mean : 30.8600
Gaus Width: 3.7164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch11, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 38
Std Dev 3.623
Gaus mean : 37.2591
Gaus Width: 3.6753
Entry
ampl
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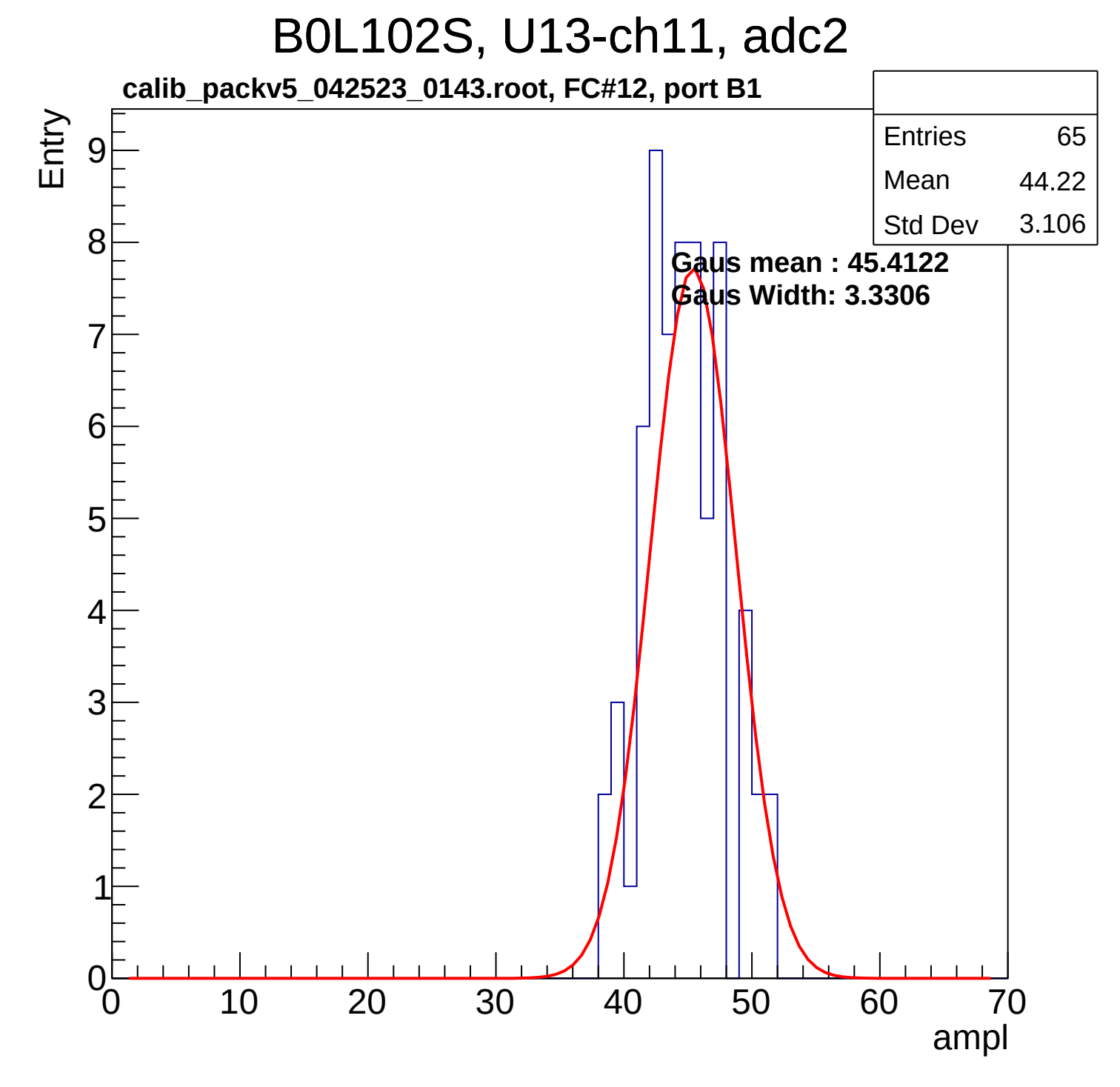
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch11, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 44.22
Std Dev 3.106
Gaus mean : 45.4122
Gaus Width: 3.3306
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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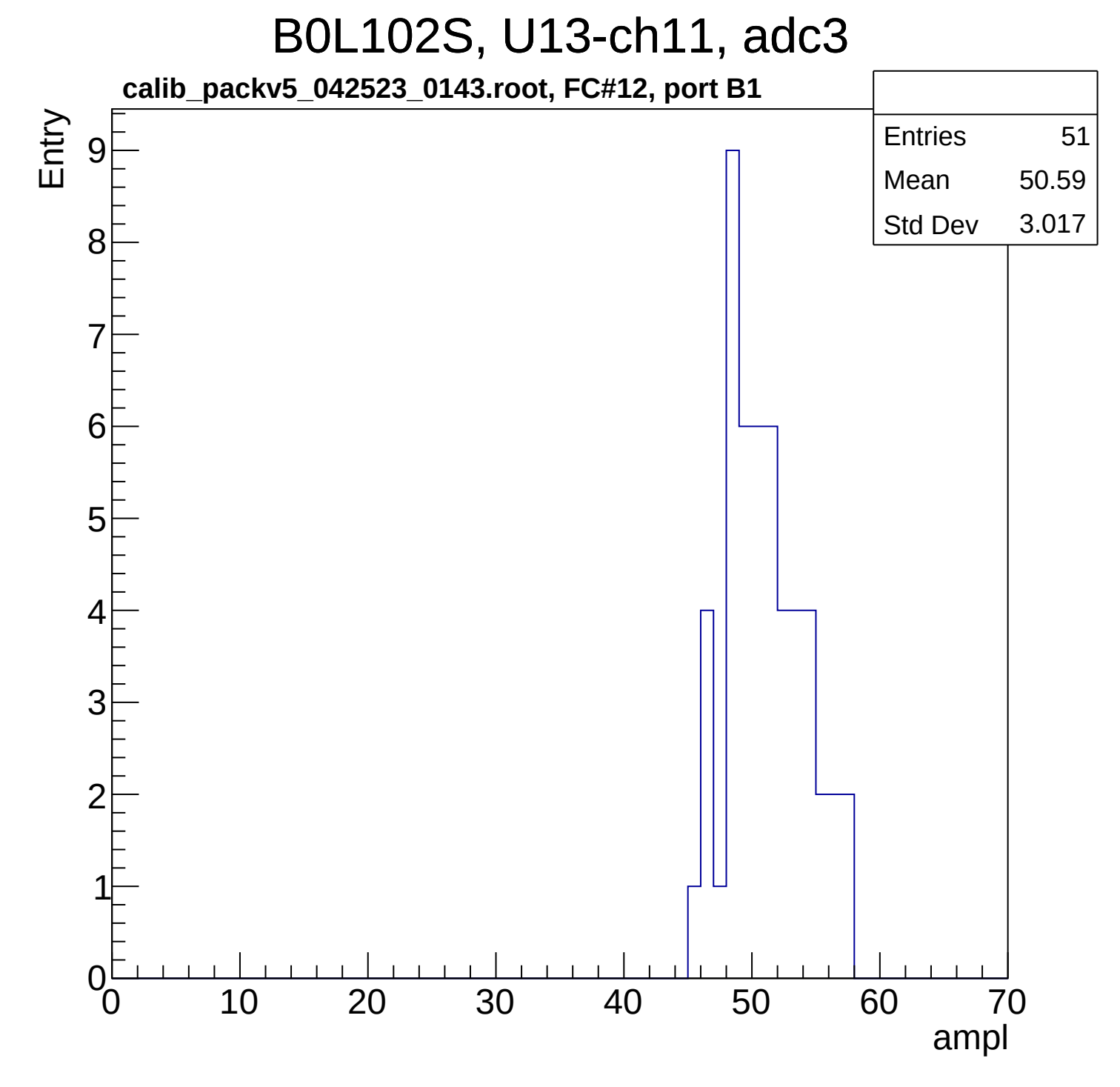
B0L102S, U13-ch11, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 50.59
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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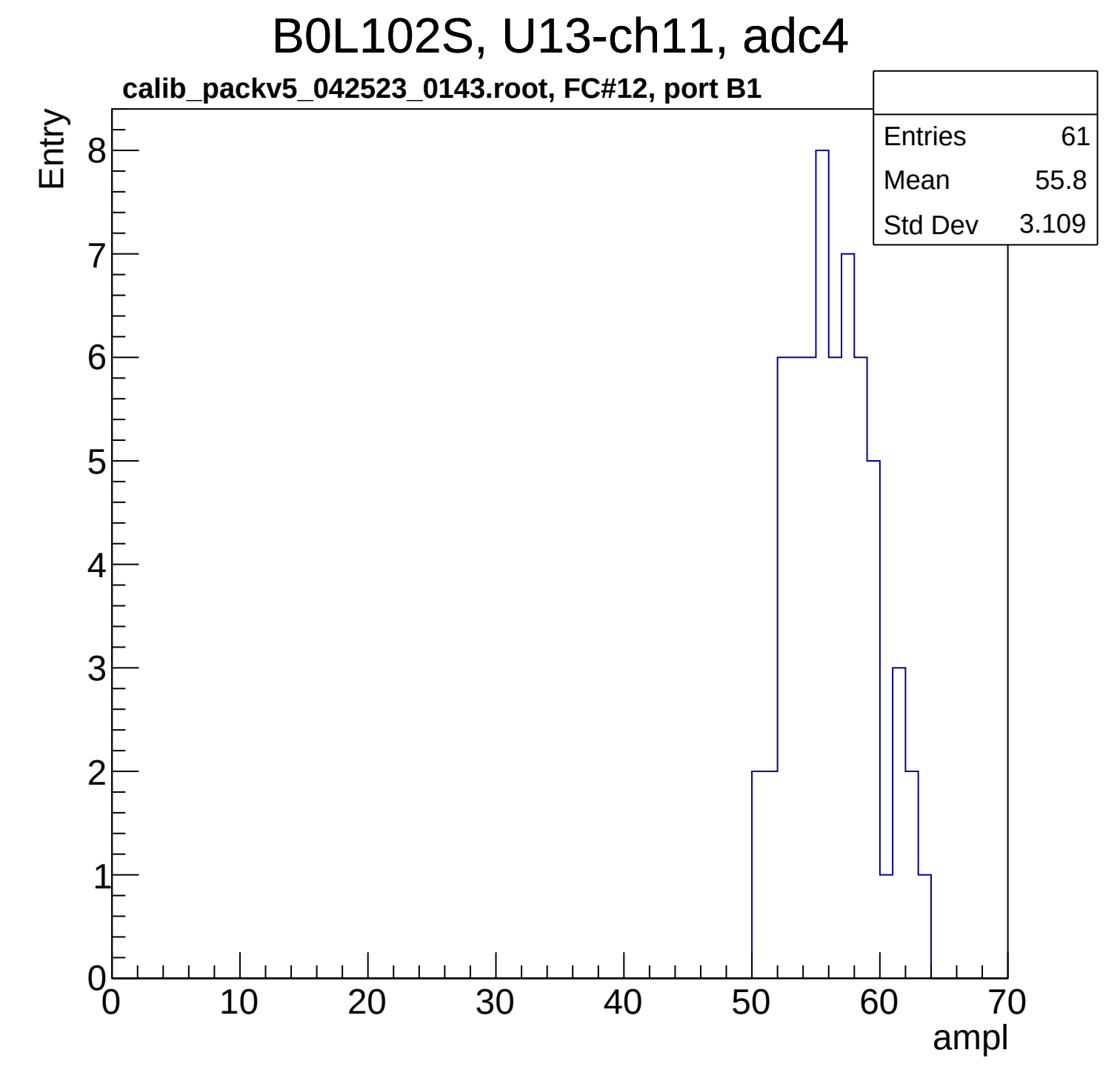
B0L102S, U13-ch11, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.8
Std Dev 3.109
Entry
0
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2
3
4
5
6
7
8
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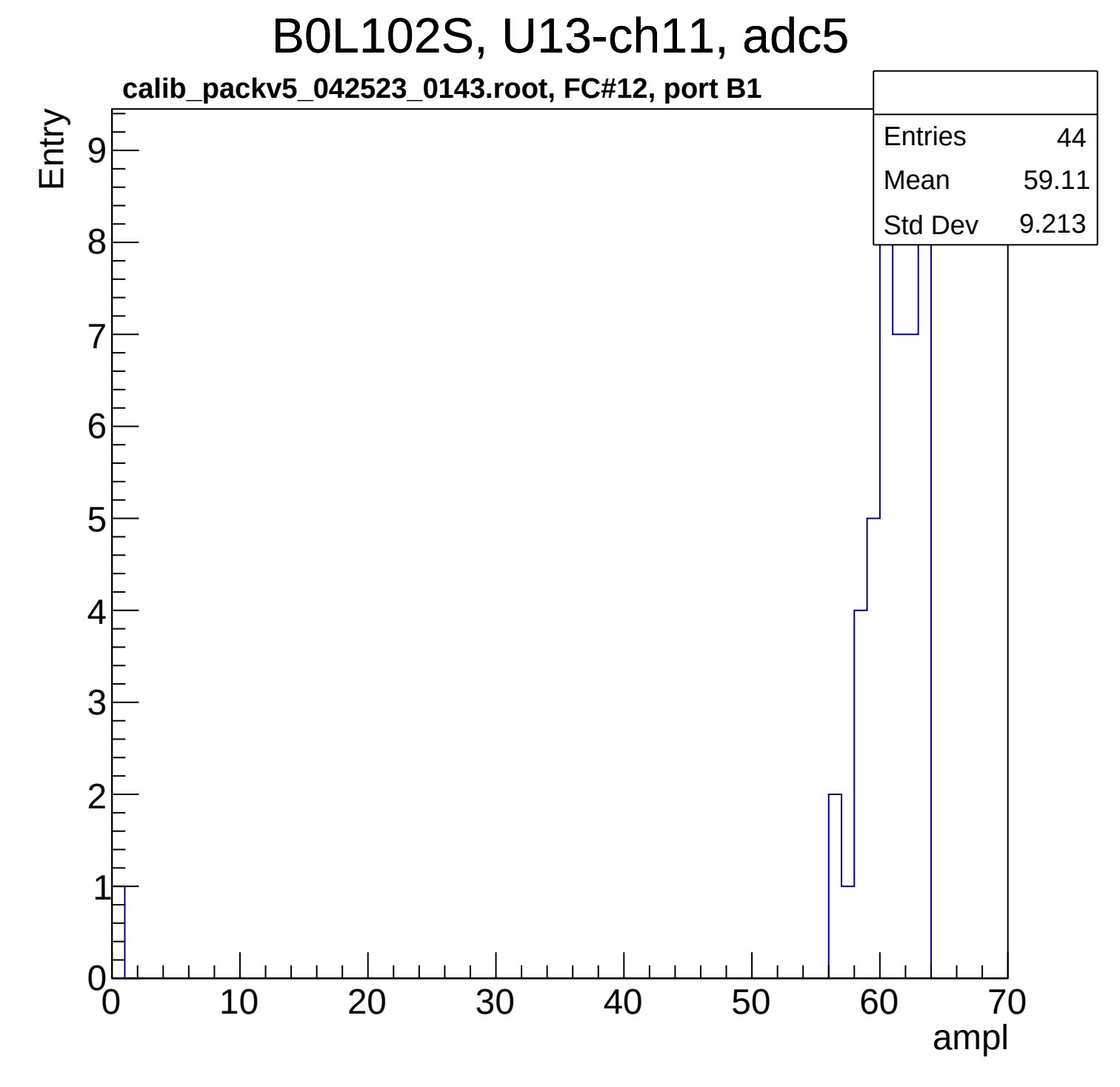
60
70
ampl


B0L102S, U13-ch11, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.11
Std Dev 9.213
Entry
ampl
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2
3
4
5
6
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0
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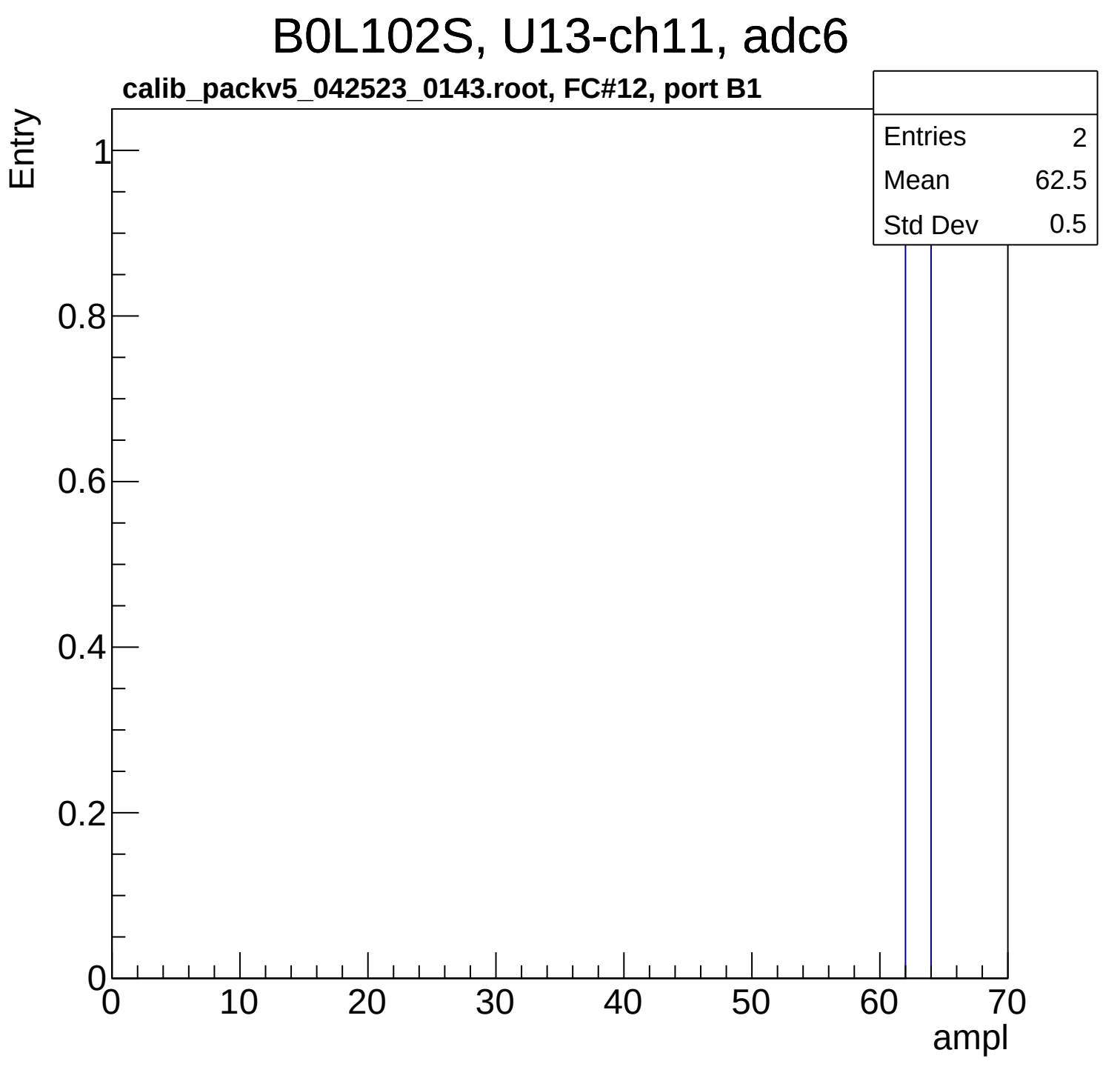
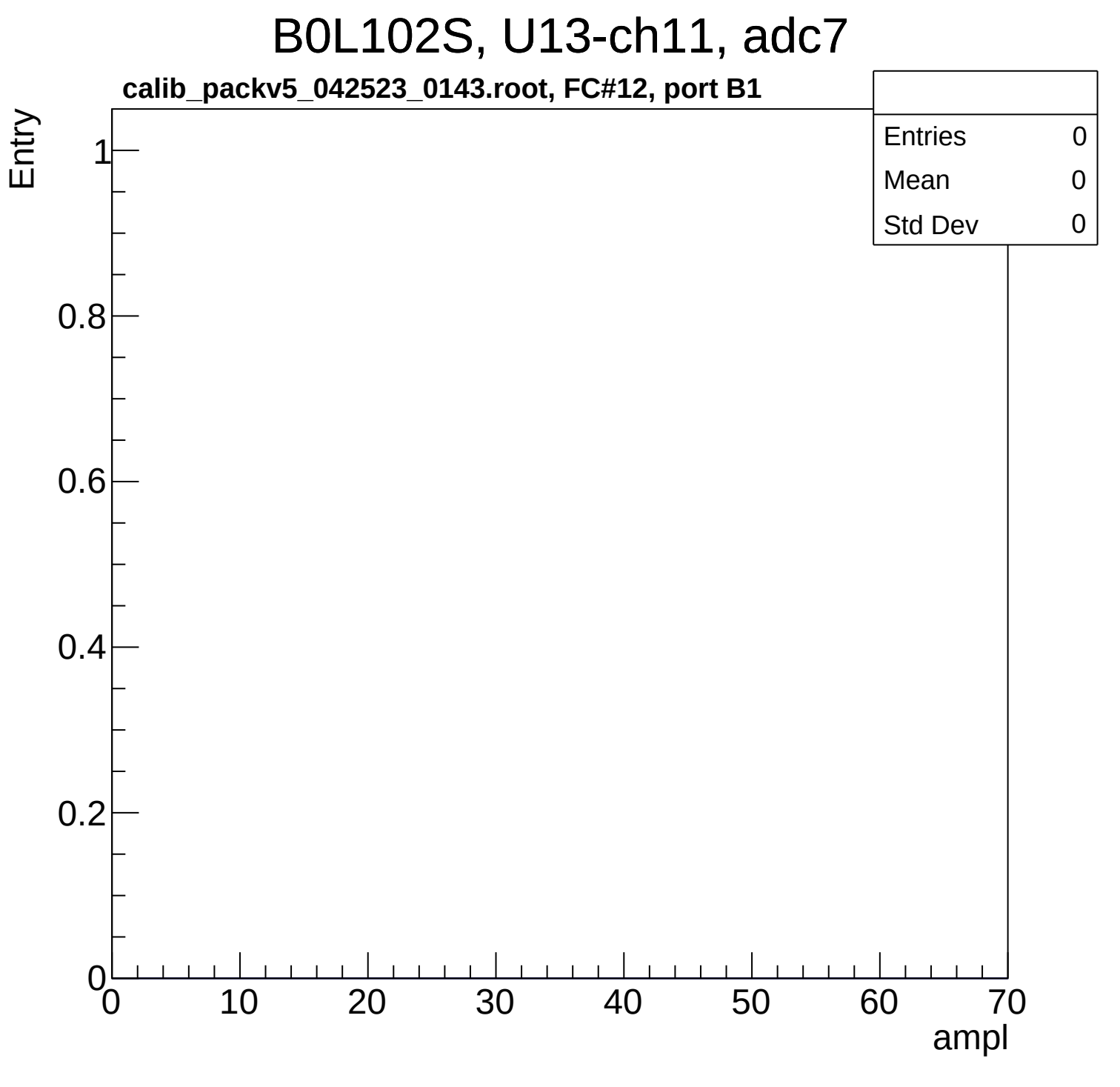
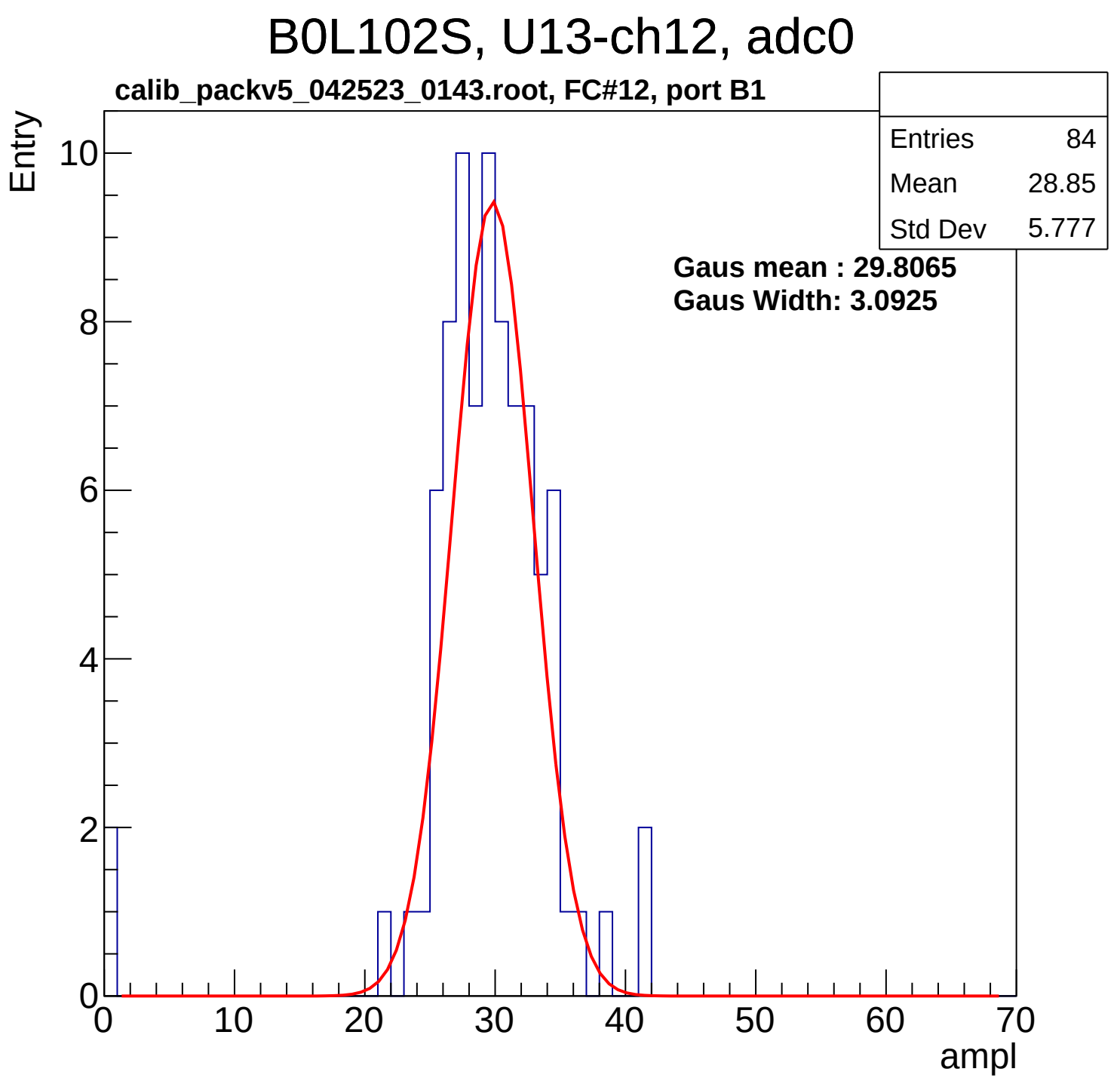

B0L102S, U13-ch11, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch11, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch12, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 28.85
Std Dev 5.777
Gaus mean : 29.8065
Gaus Width: 3.0925
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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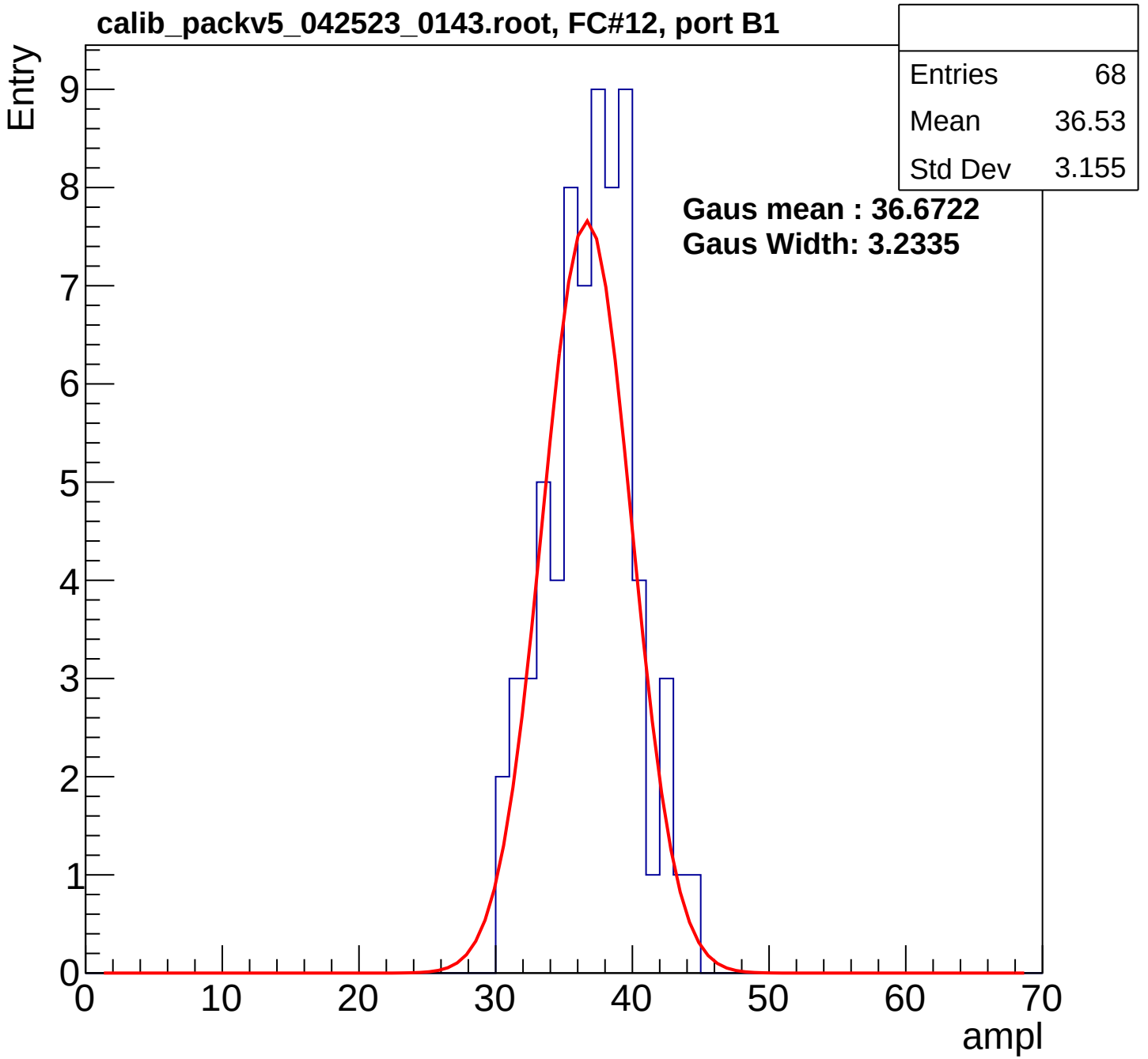
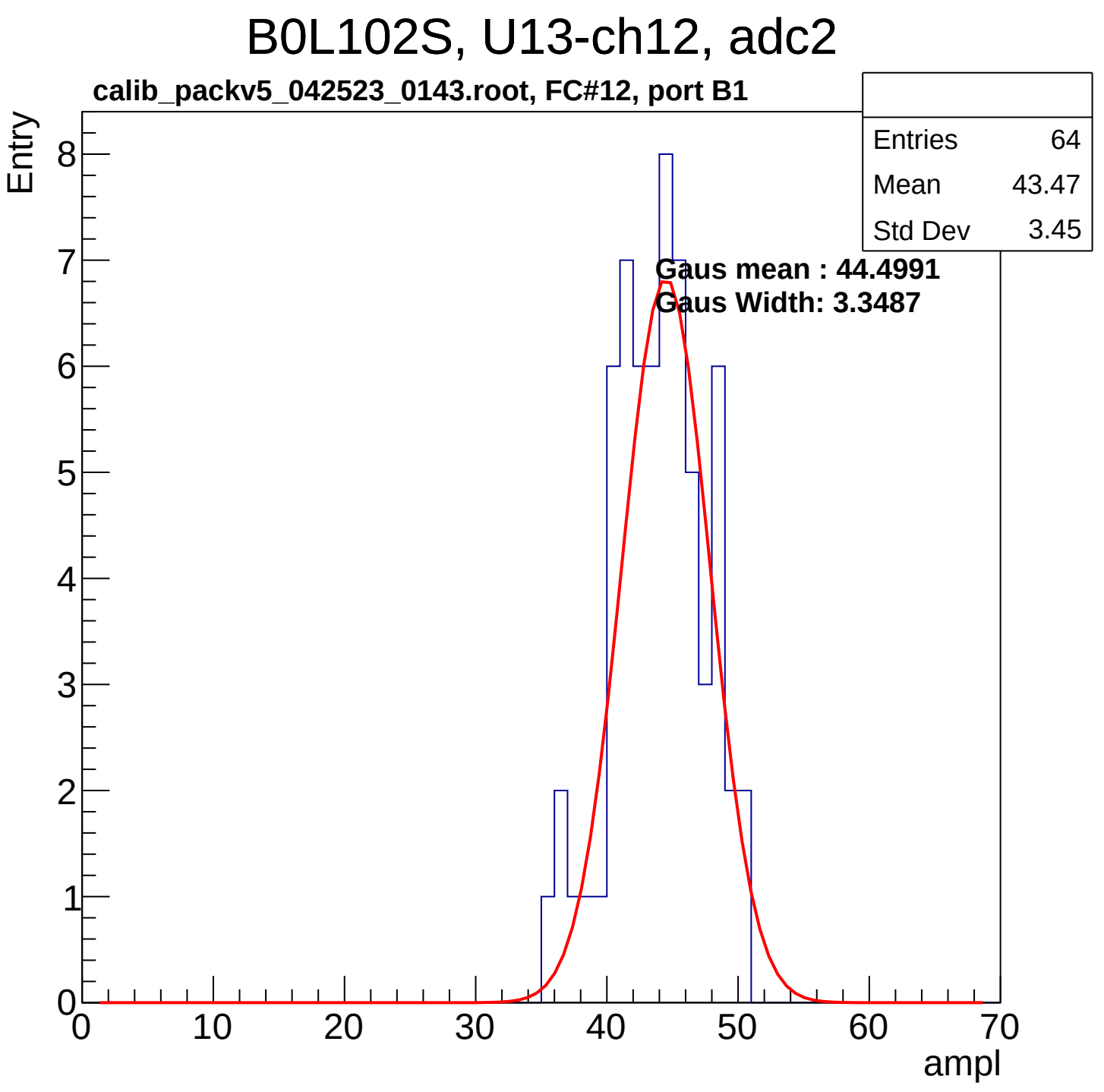

B0L102S, U13-ch12, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 36.53
Std Dev 3.155
Gaus mean : 36.6722
Gaus Width: 3.2335
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch12, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.47
Std Dev 3.45
Gaus mean : 44.4991
Gaus Width: 3.3487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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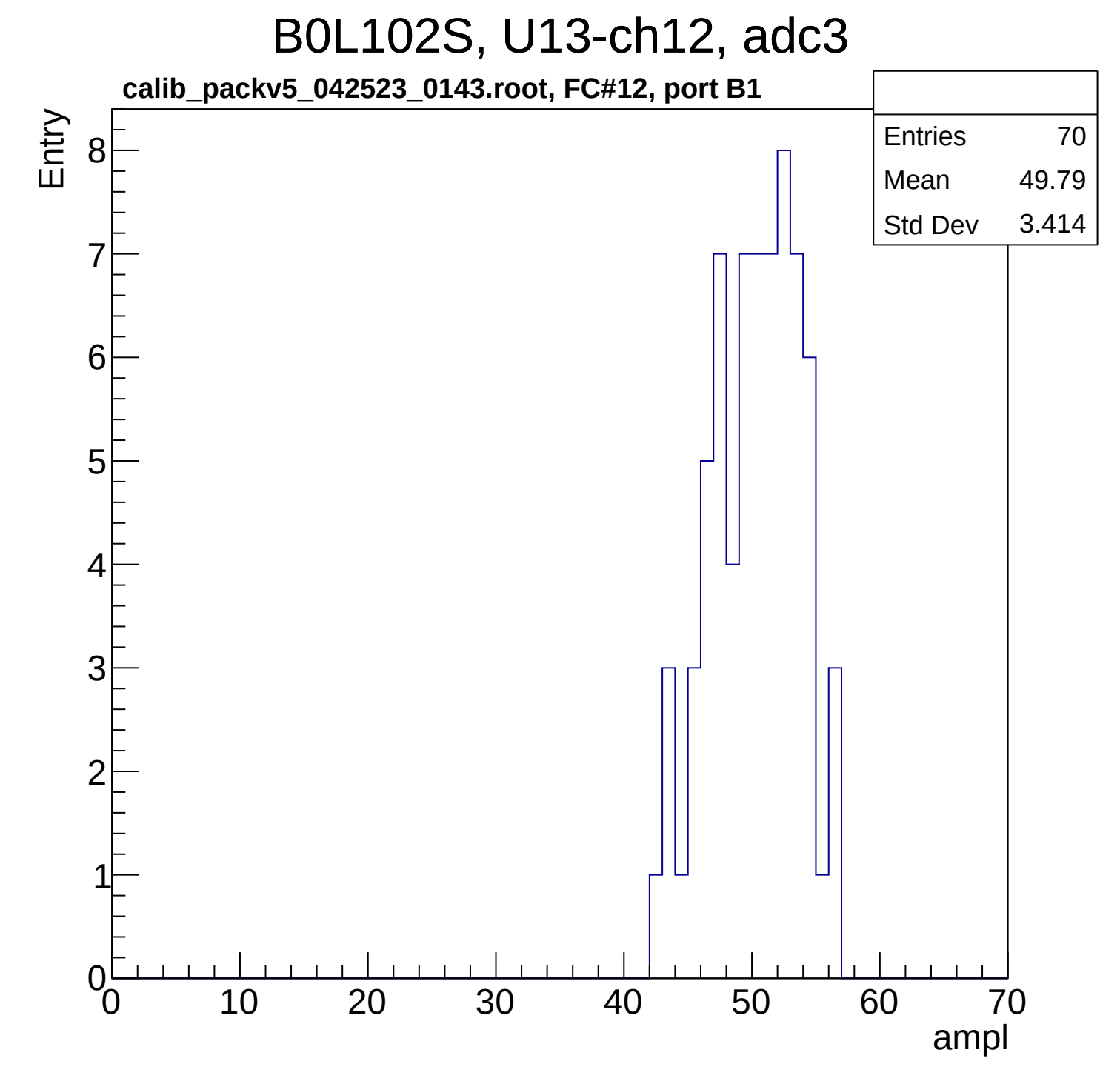
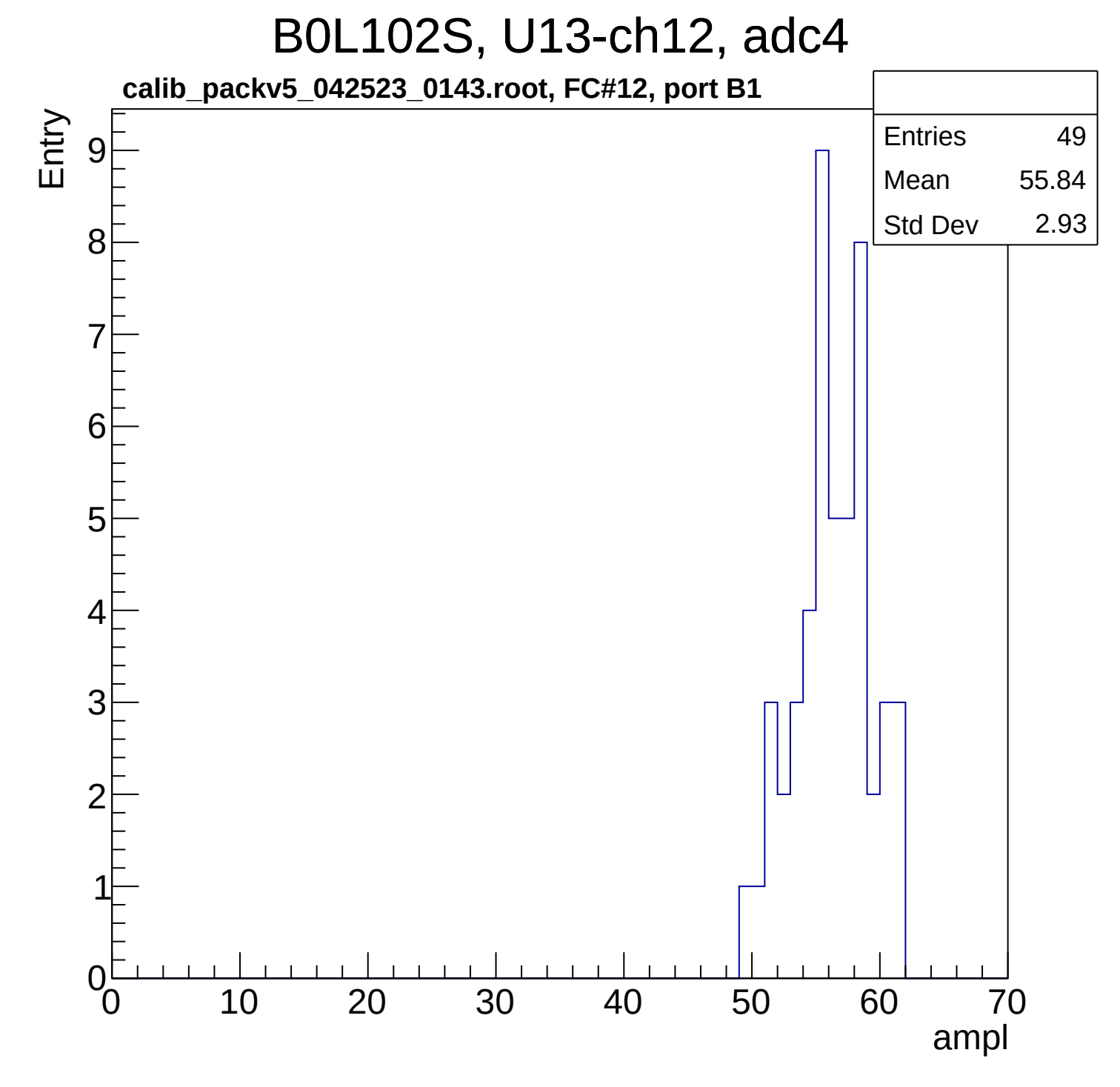
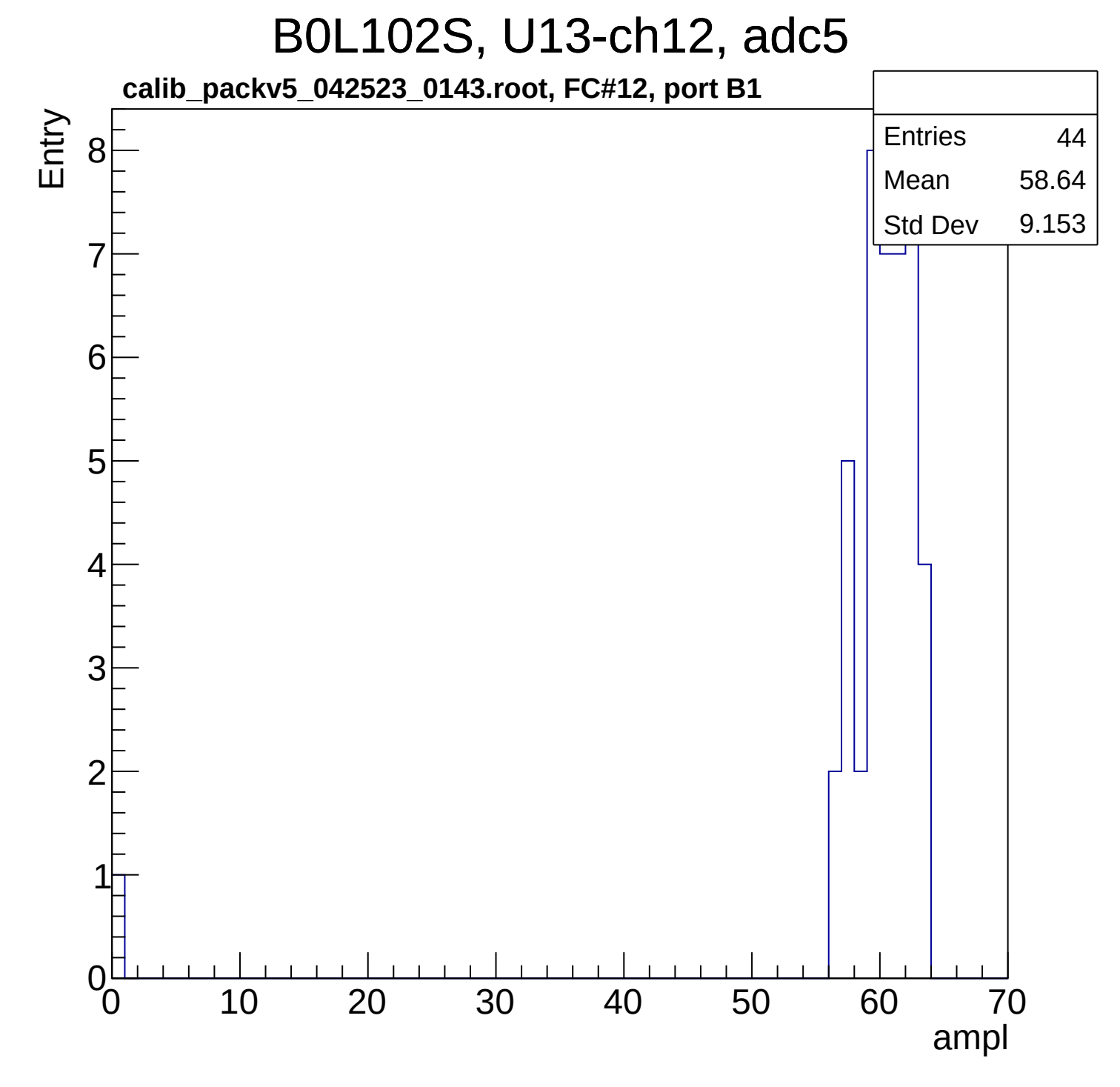

B0L102S, U13-ch12, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 49.79
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch12, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 55.84
Std Dev 2.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch12, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.64
Std Dev 9.153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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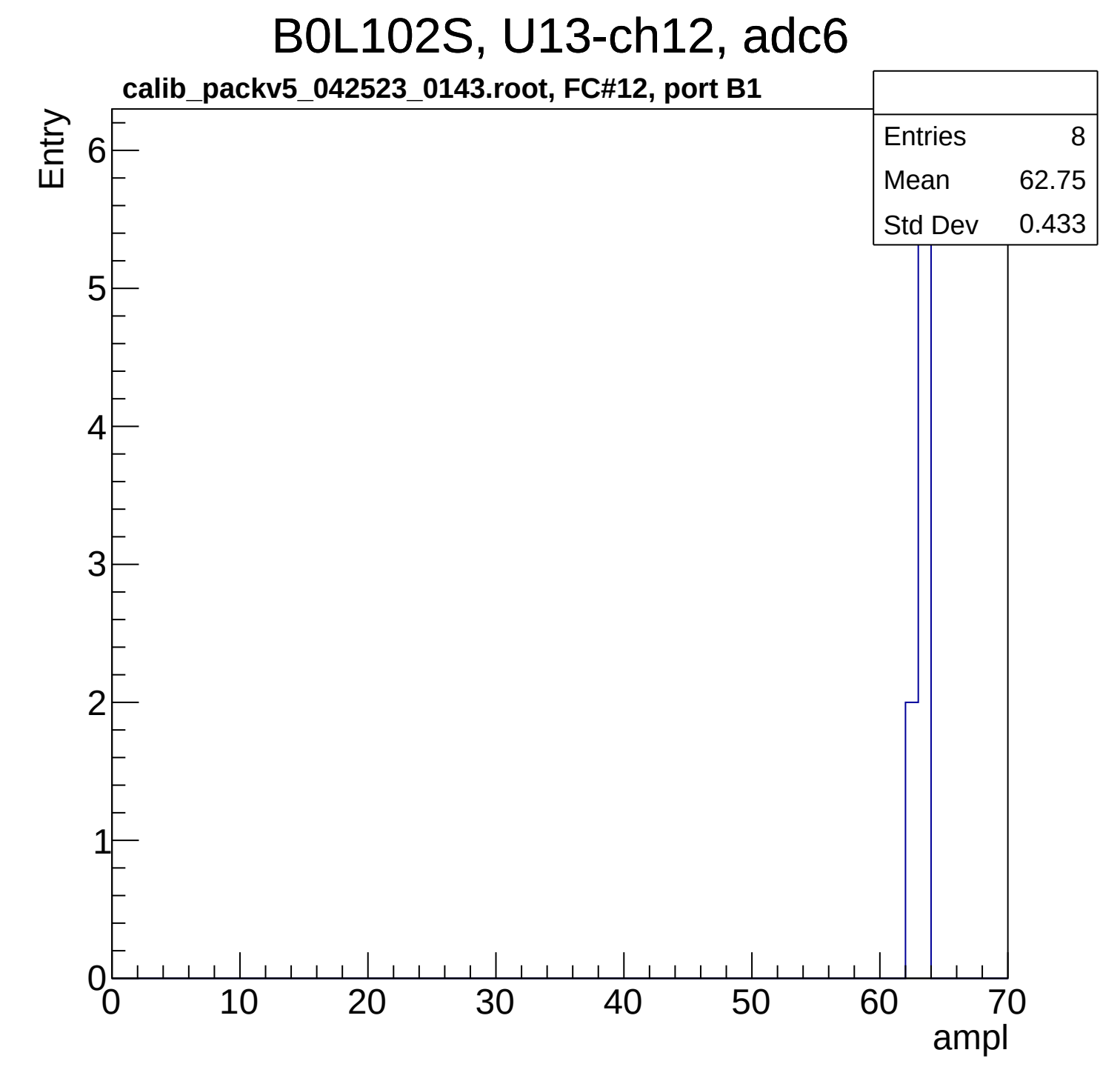
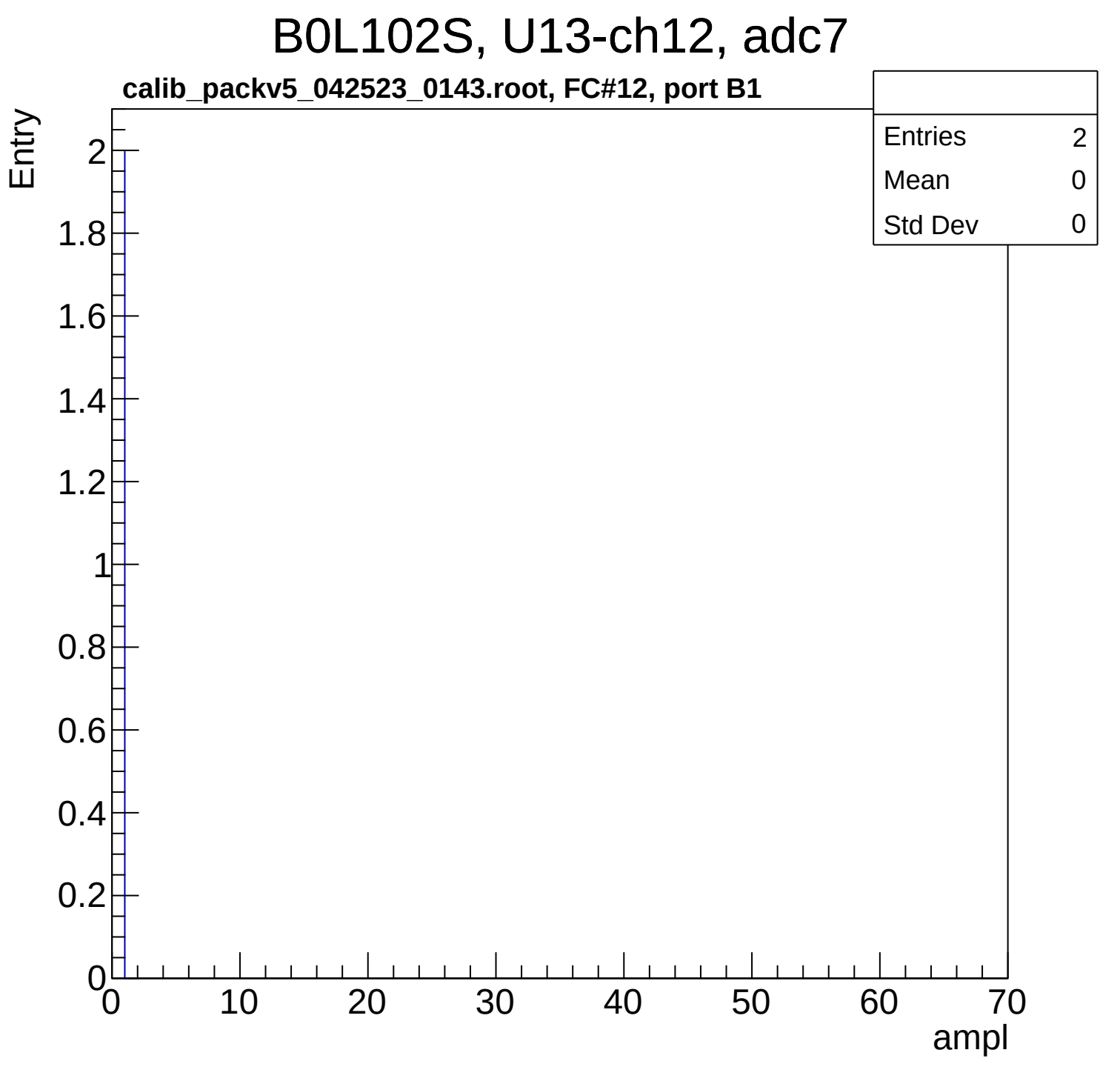
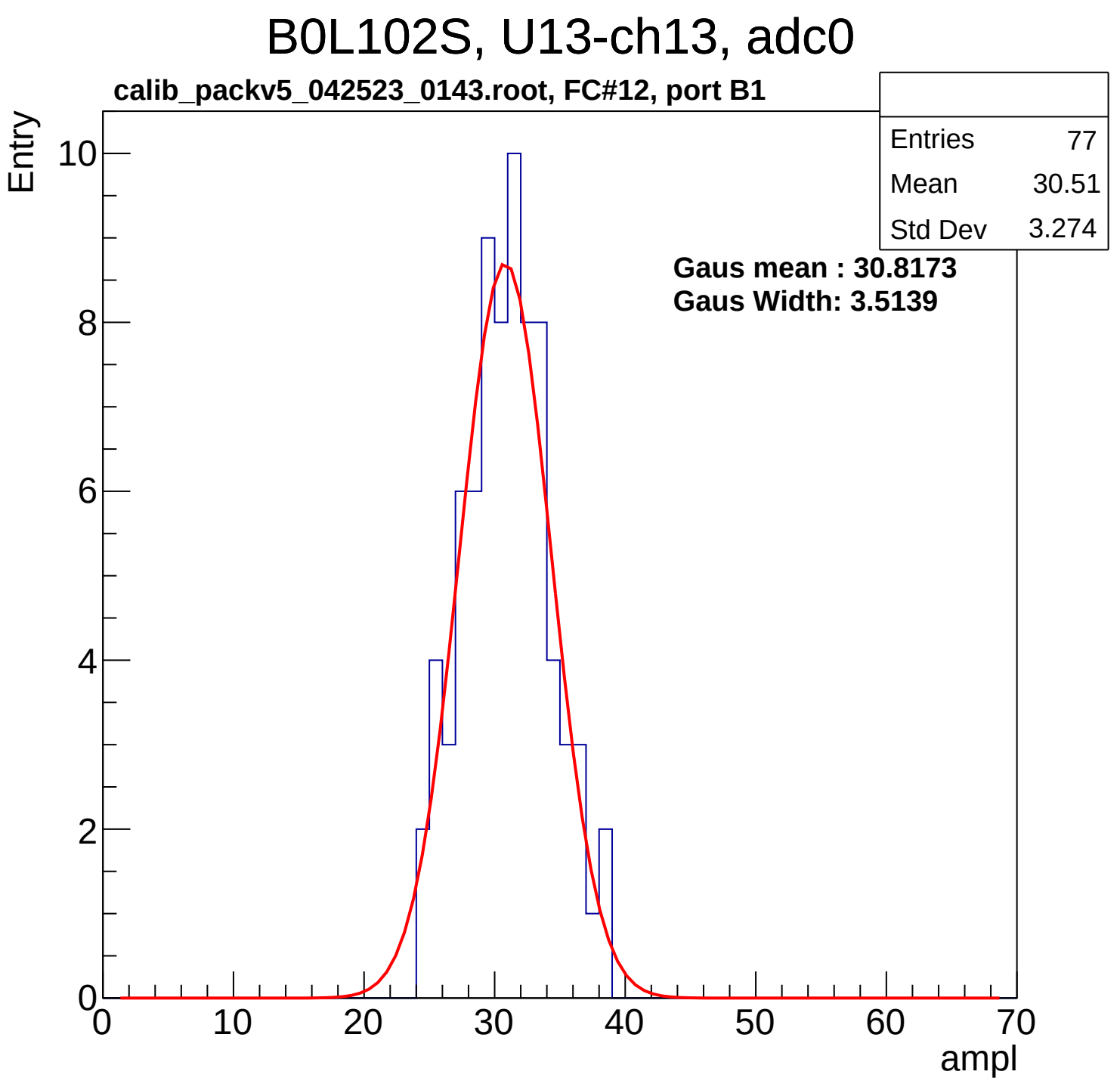
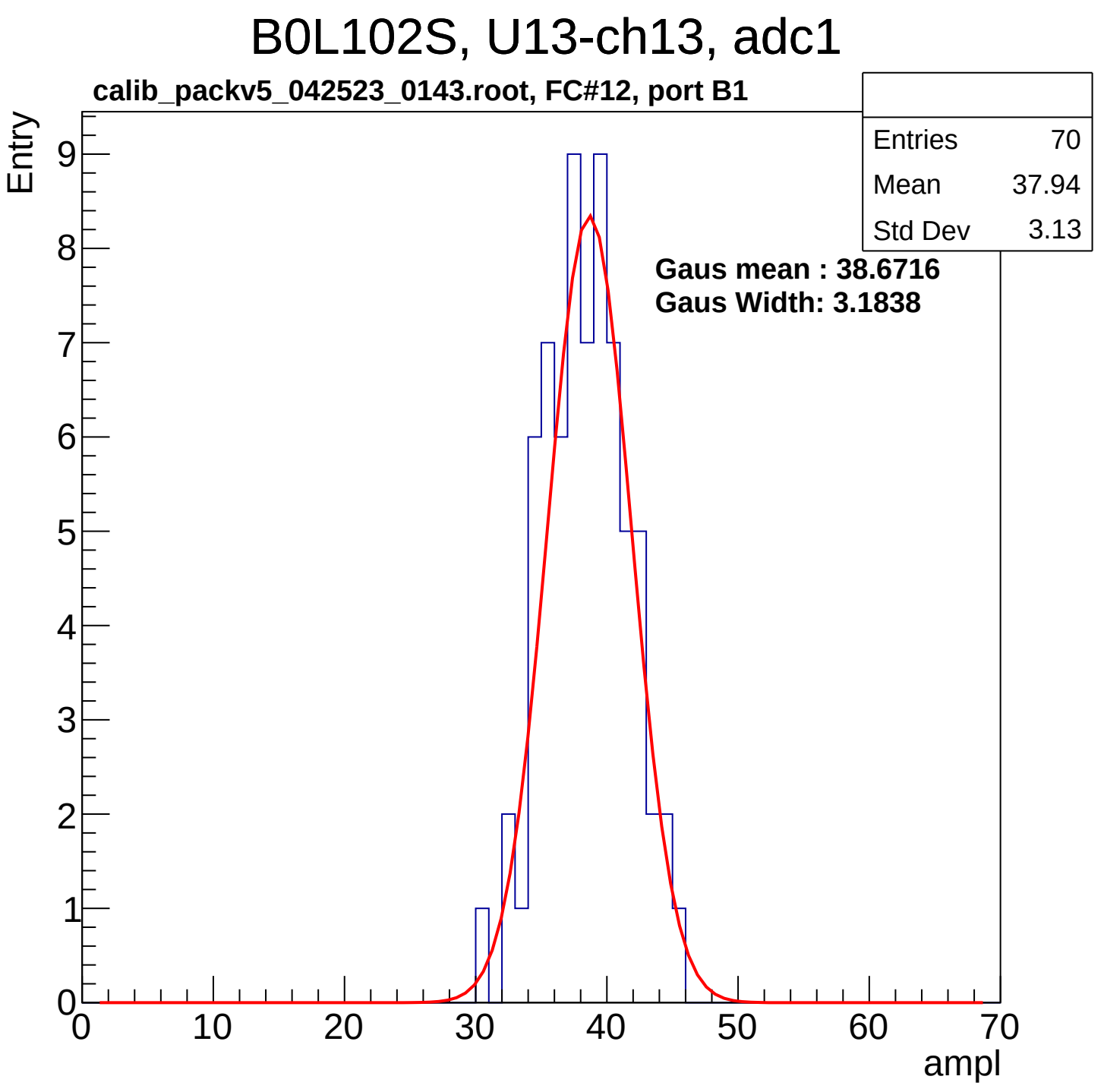

B0L102S, U13-ch12, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.75
Std Dev 0.433
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch12, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch13, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 30.51
Std Dev 3.274
Gaus mean : 30.8173
Gaus Width: 3.5139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch13, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 37.94
Std Dev 3.13
Gaus mean : 38.6716
Gaus Width: 3.1838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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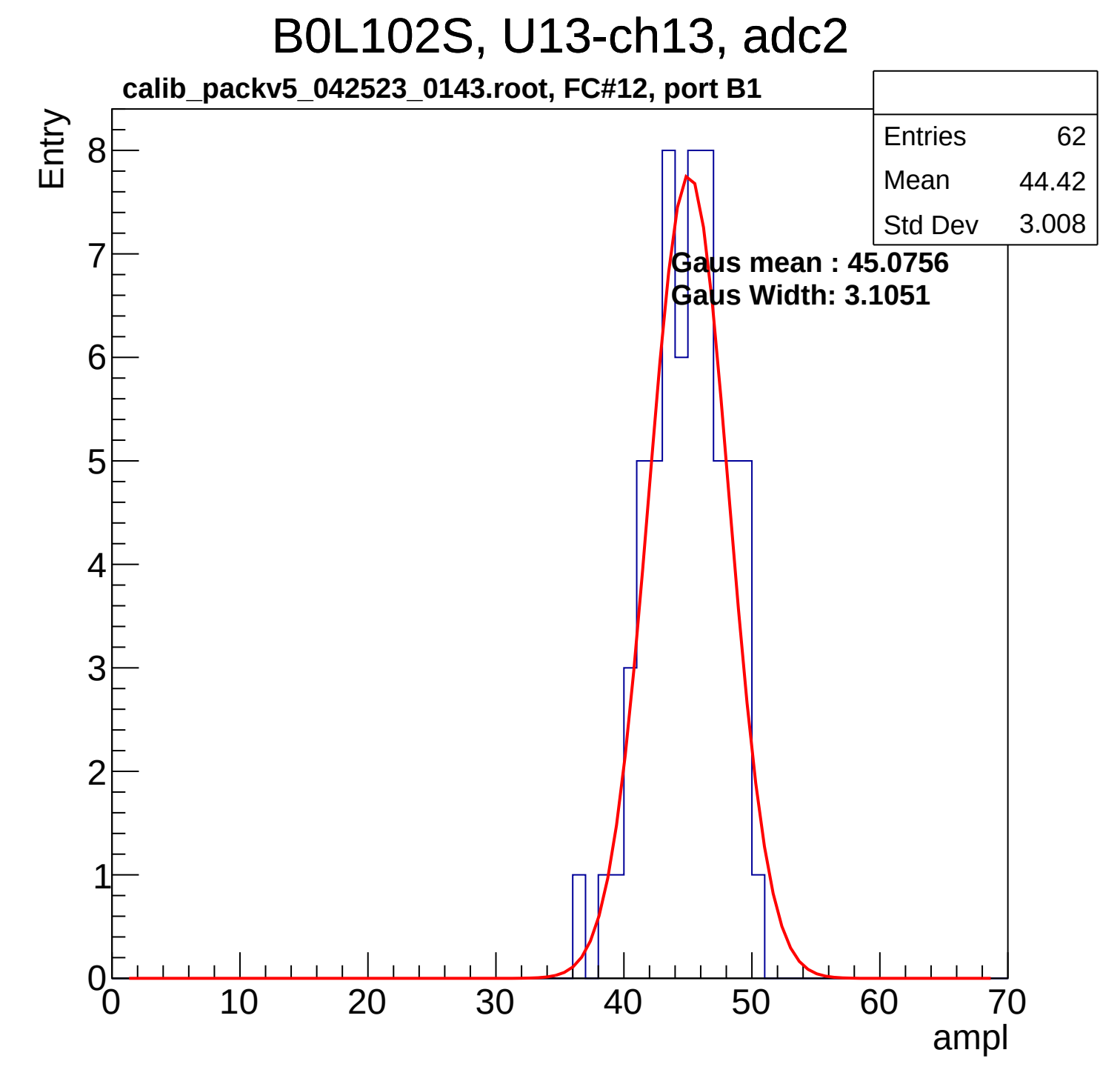
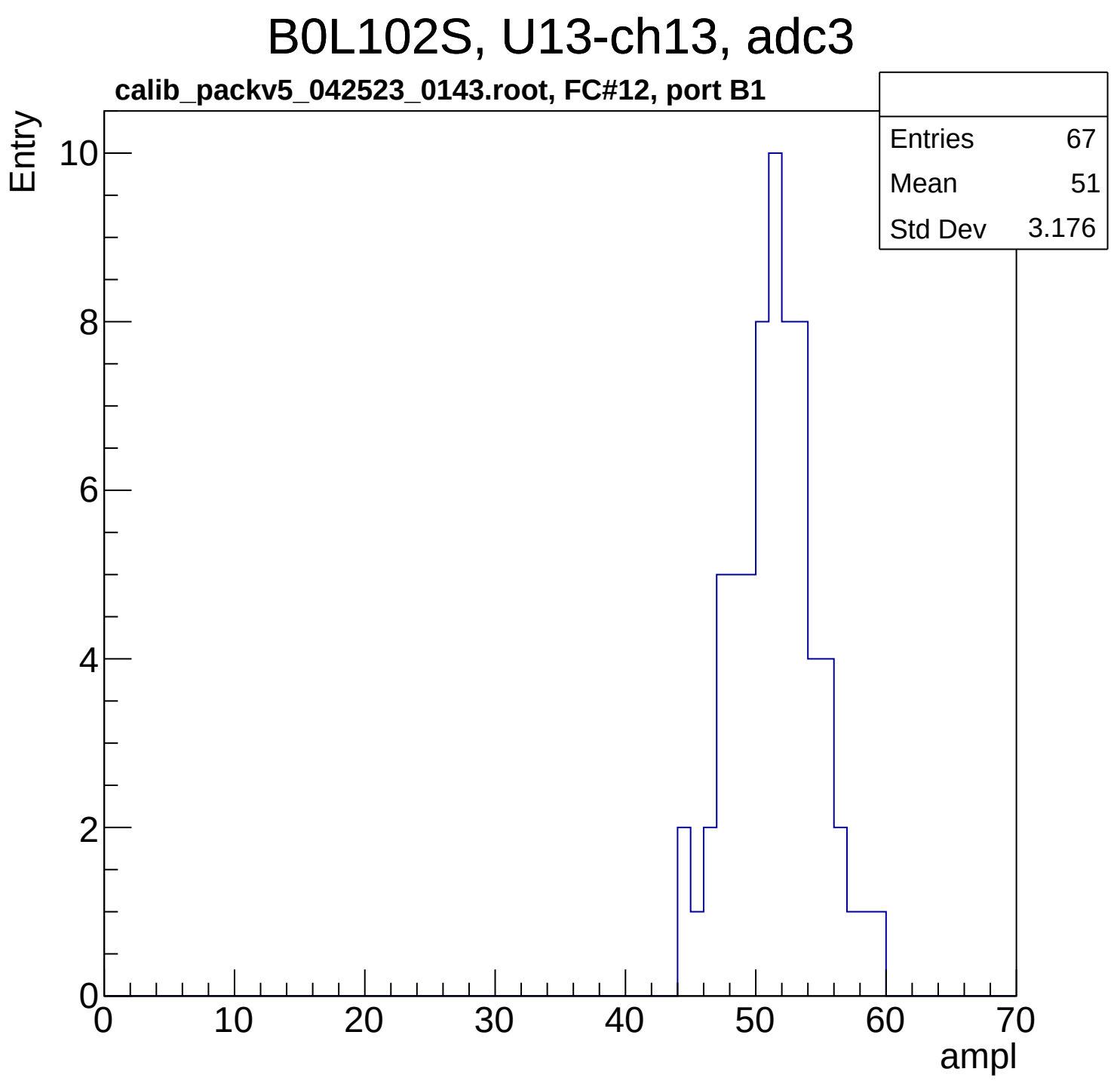
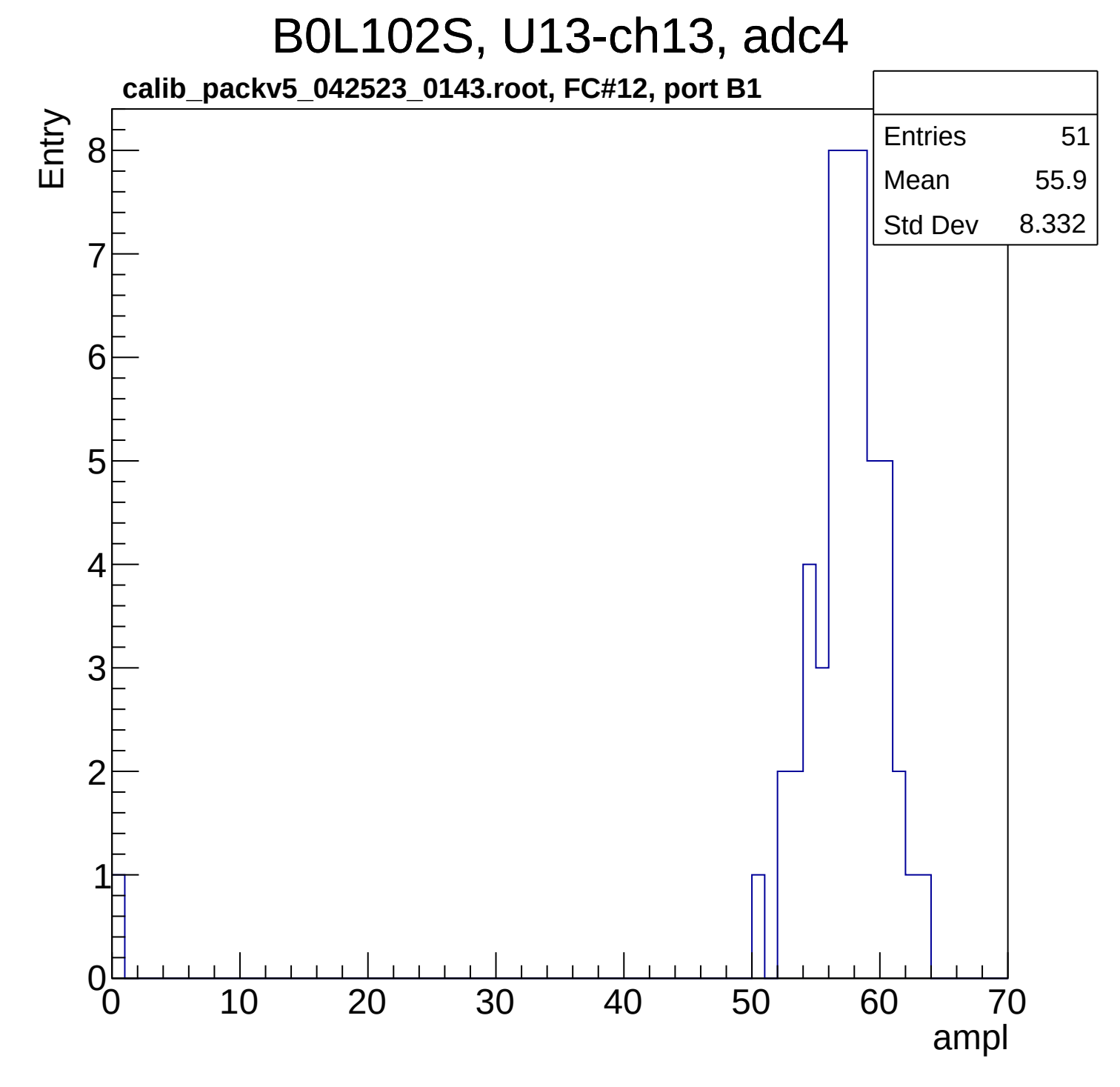
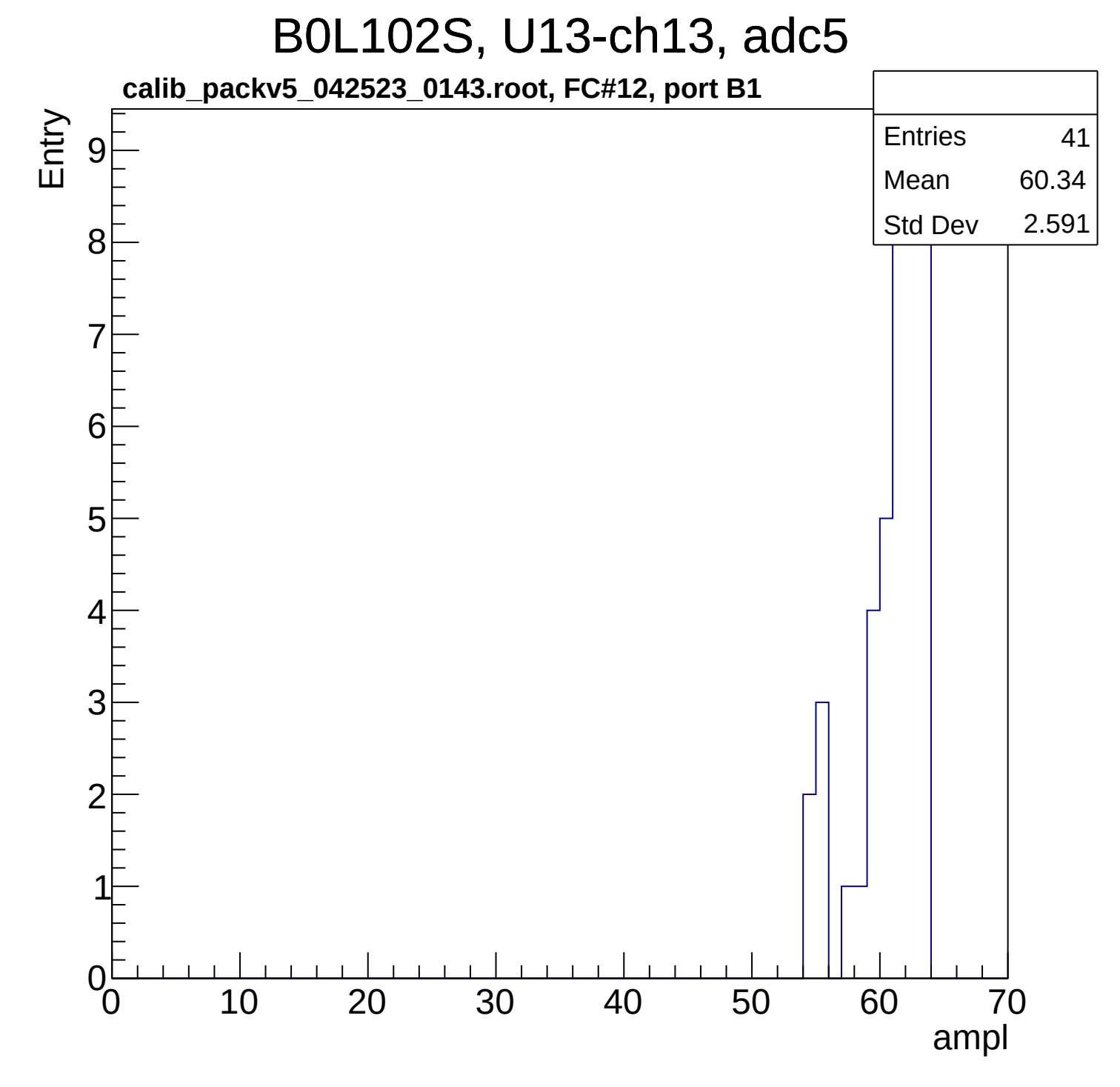

B0L102S, U13-ch13, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 44.42
Std Dev 3.008
Gaus mean : 45.0756
Gaus Width: 3.1051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch13, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 51
Std Dev 3.176
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch13, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.9
Std Dev 8.332
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch13, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 60.34
Std Dev 2.591
Entry
ampl
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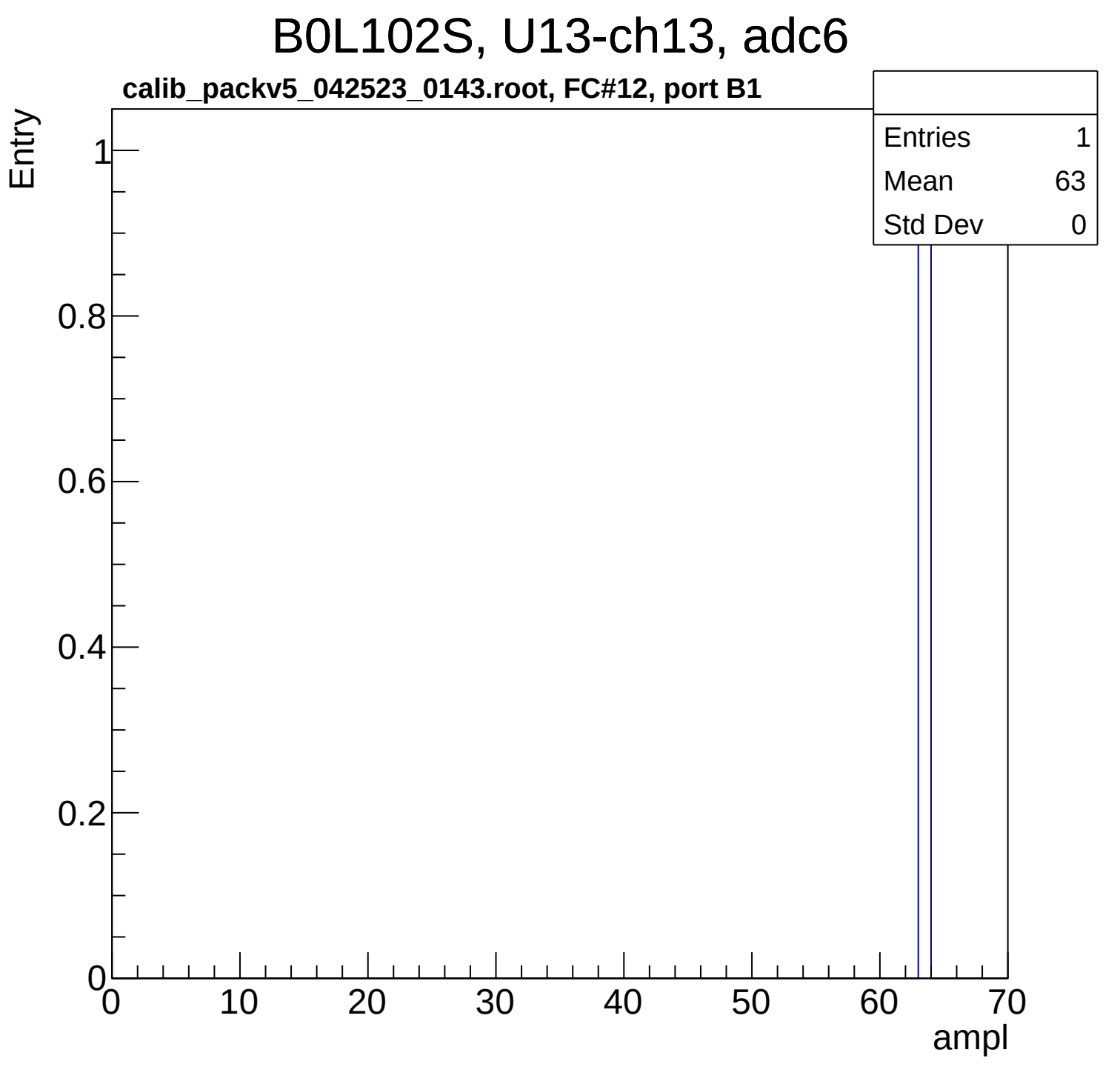
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch13, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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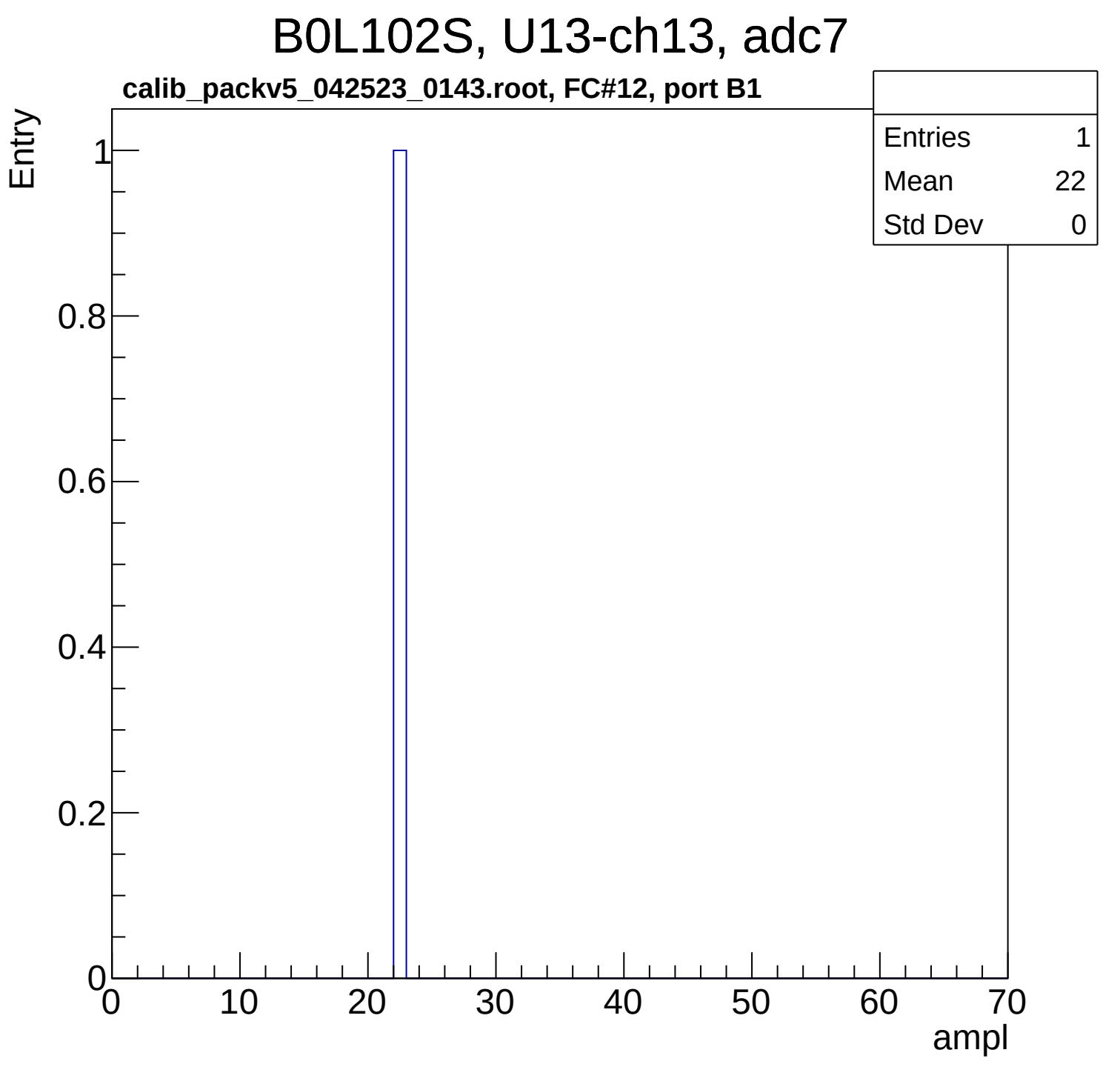
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch13, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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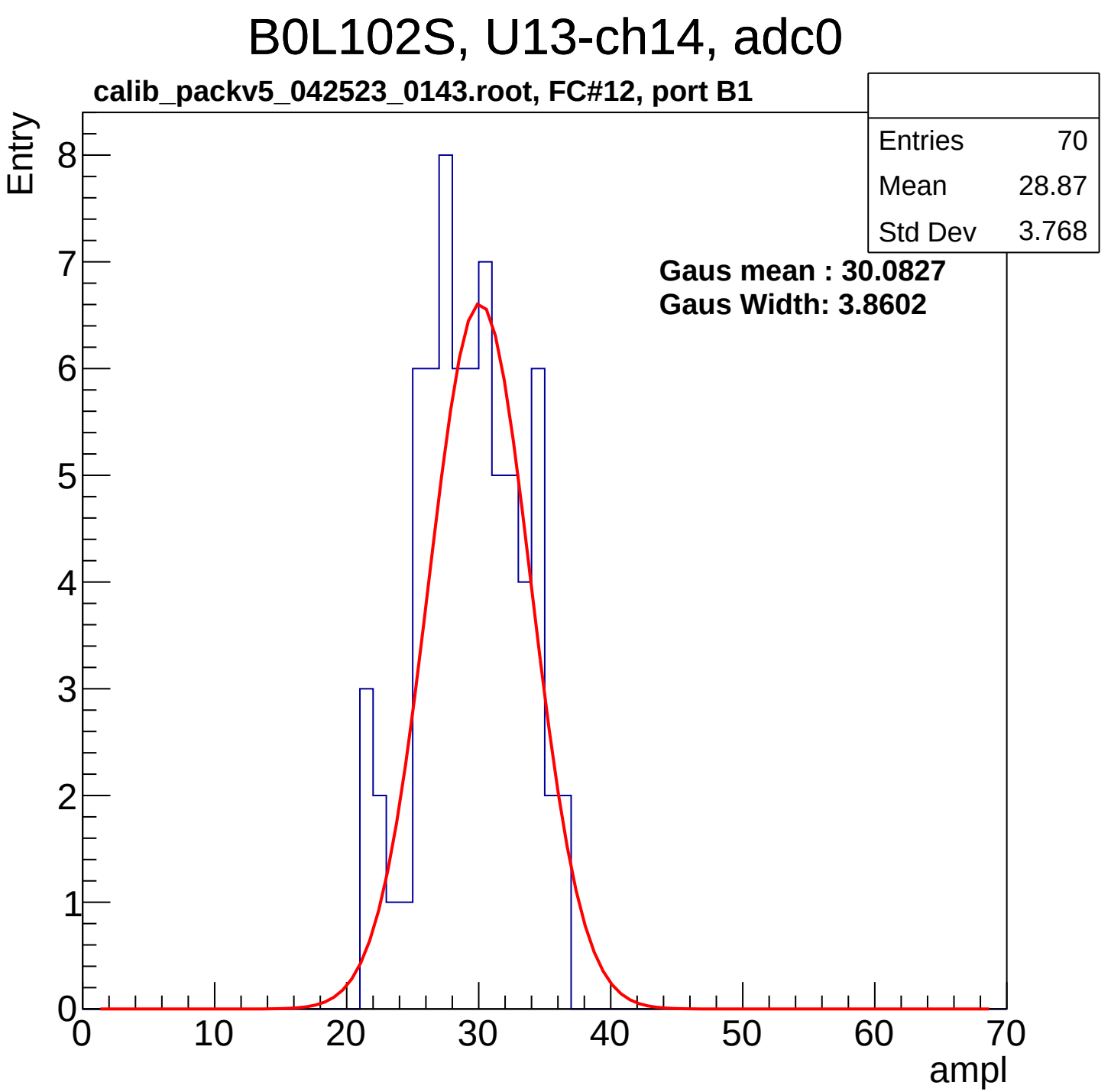
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch14, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.87
Std Dev 3.768
Gaus mean : 30.0827
Gaus Width: 3.8602
Entry
ampl
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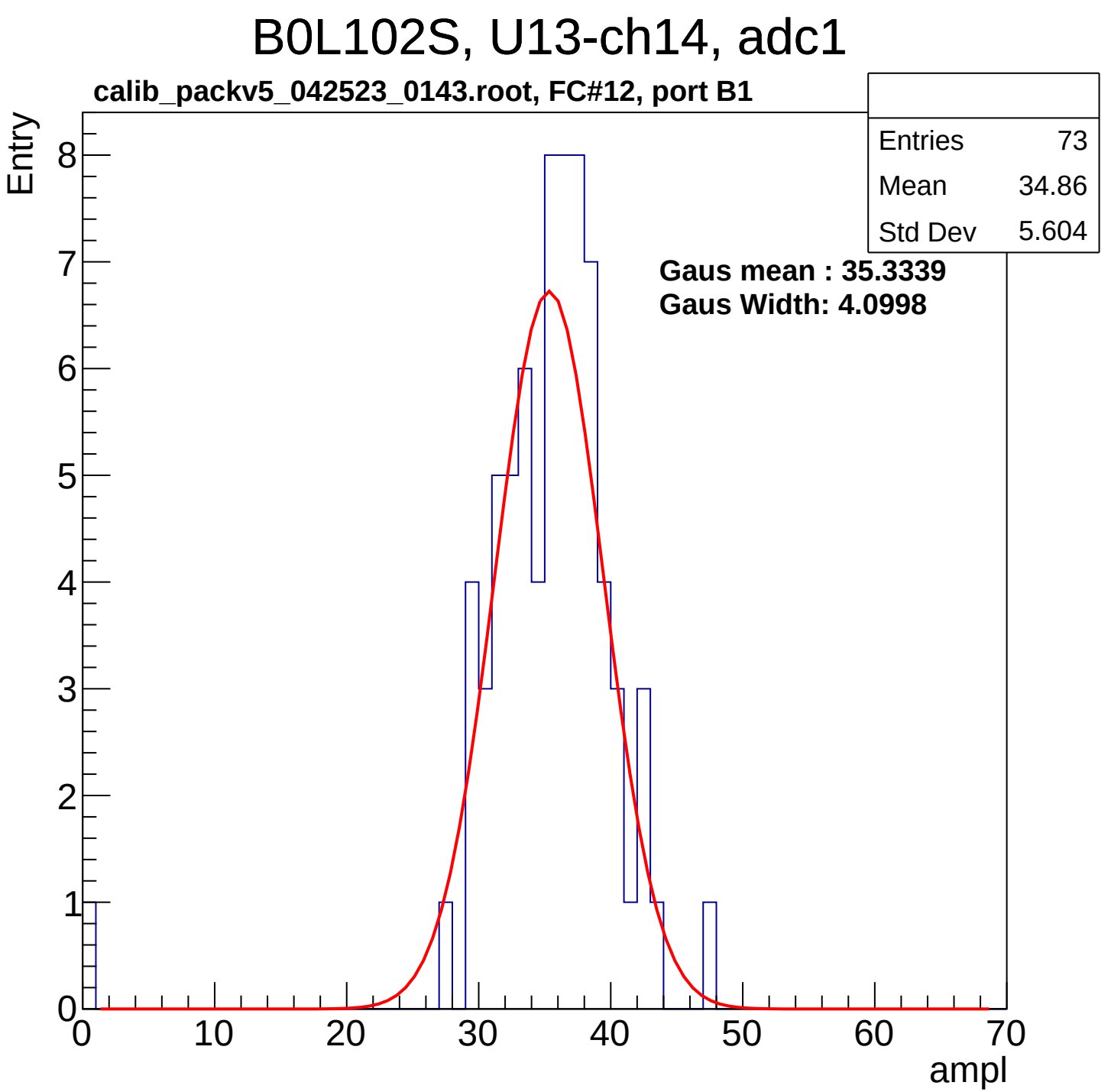
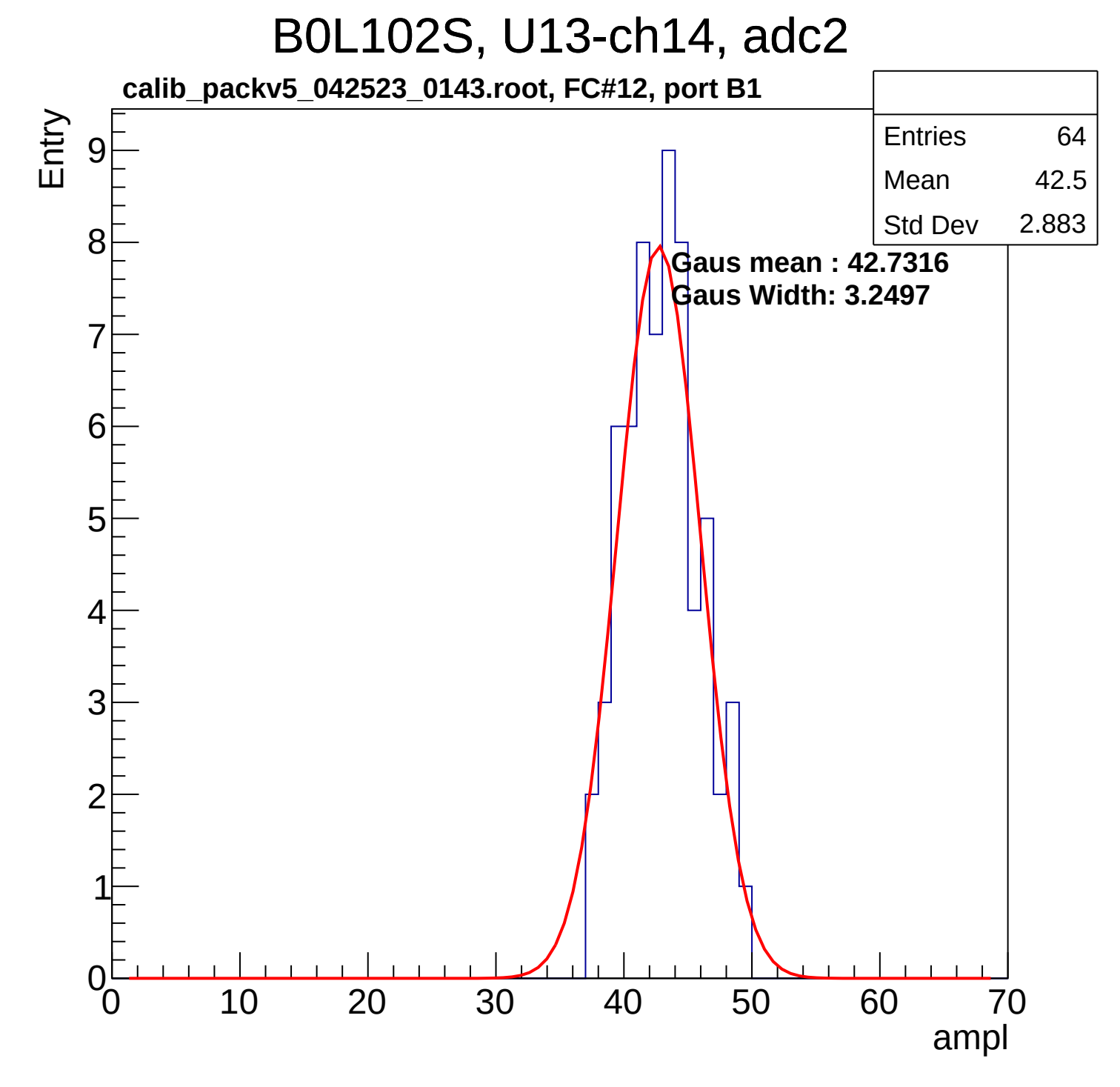
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch14, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 34.86
Std Dev 5.604
Gaus mean : 35.3339
Gaus Width: 4.0998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch14, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.5
Std Dev 2.883
Gaus mean : 42.7316
Gaus Width: 3.2497
Entry
ampl
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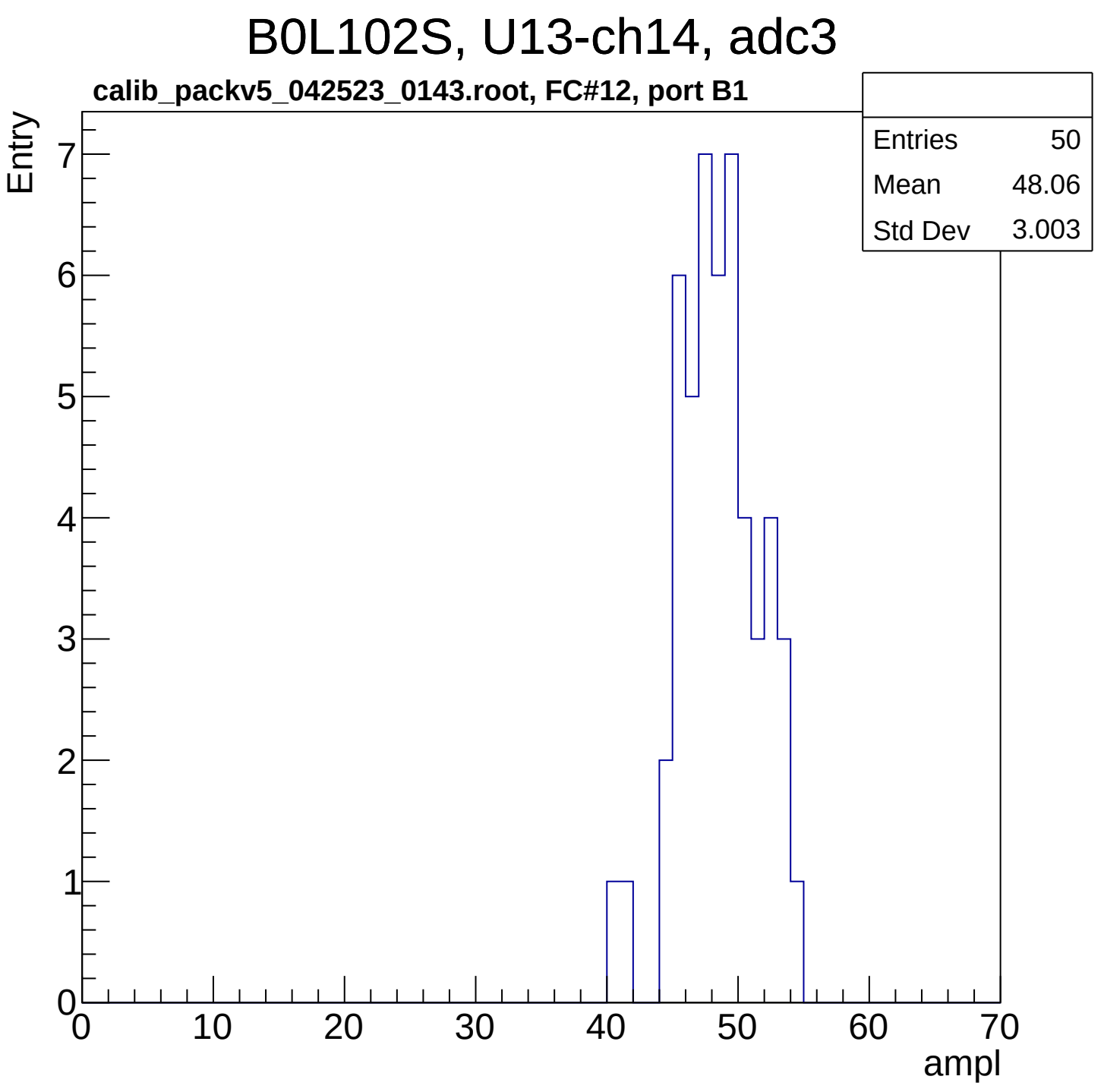
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch14, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 48.06
Std Dev 3.003
Entry
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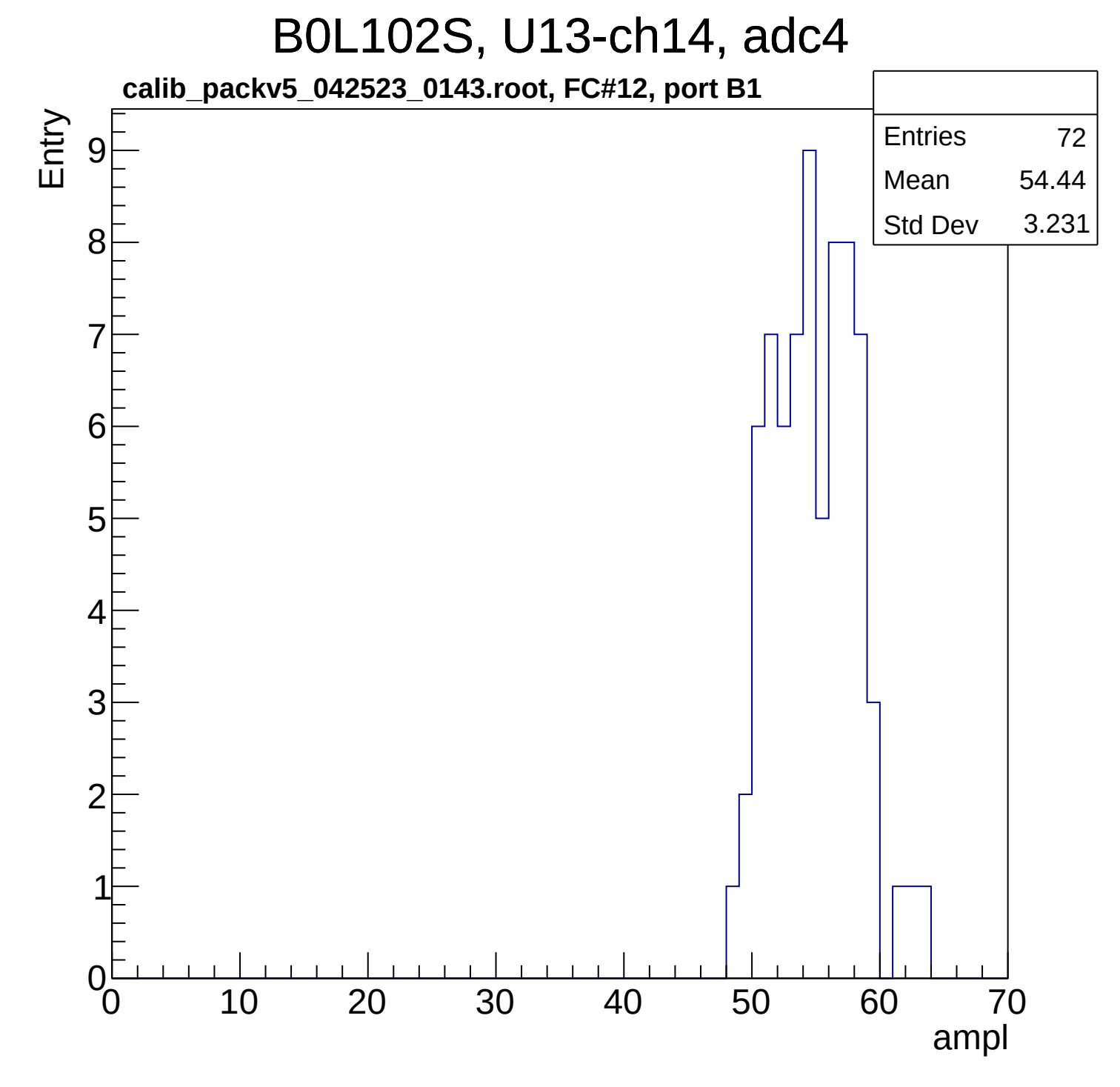
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch14, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 54.44
Std Dev 3.231
Entry
ampl
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3
4
5
6
7
8
9
0
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40
50
60
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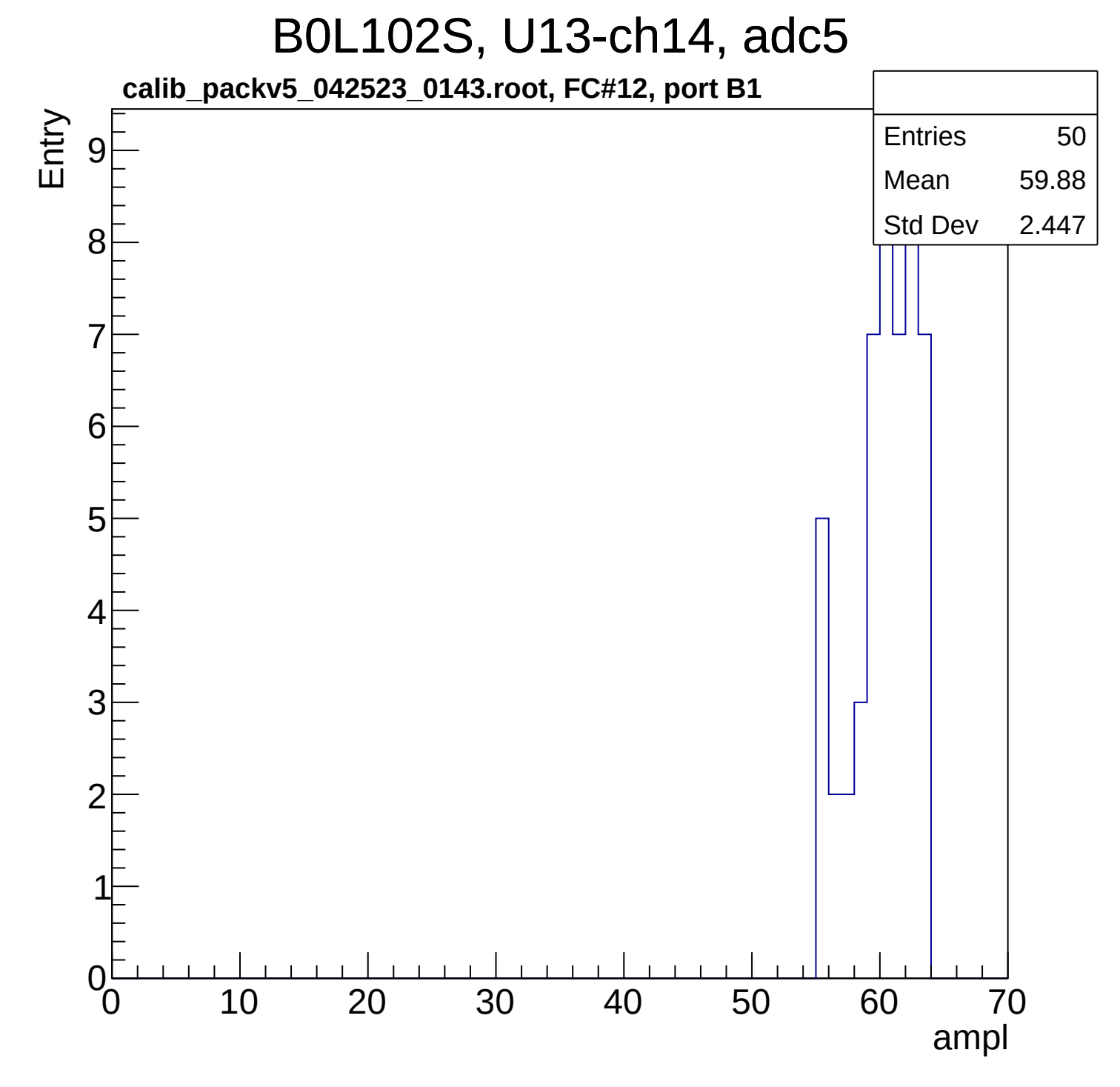
B0L102S, U13-ch14, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 59.88
Std Dev 2.447
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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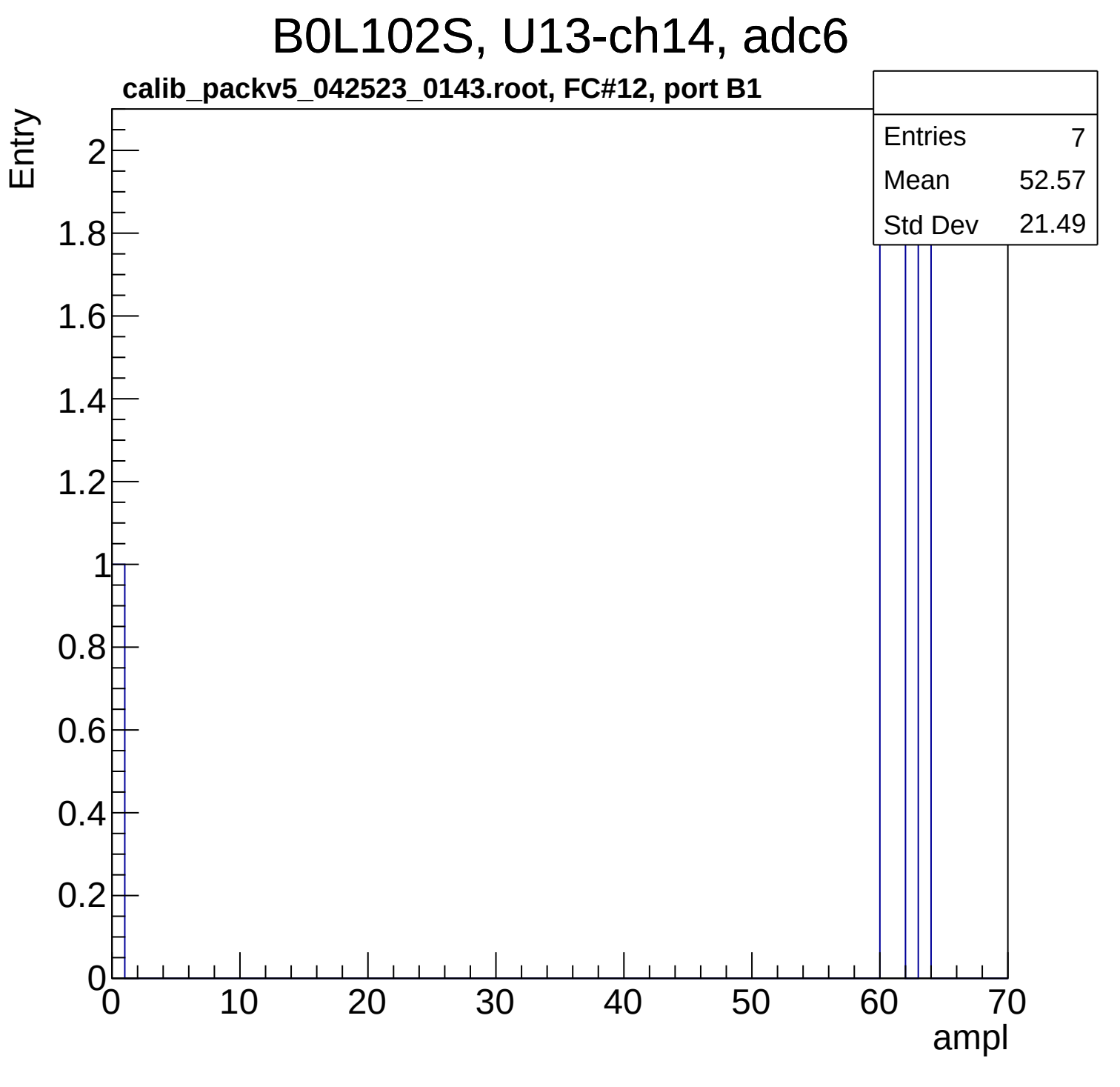
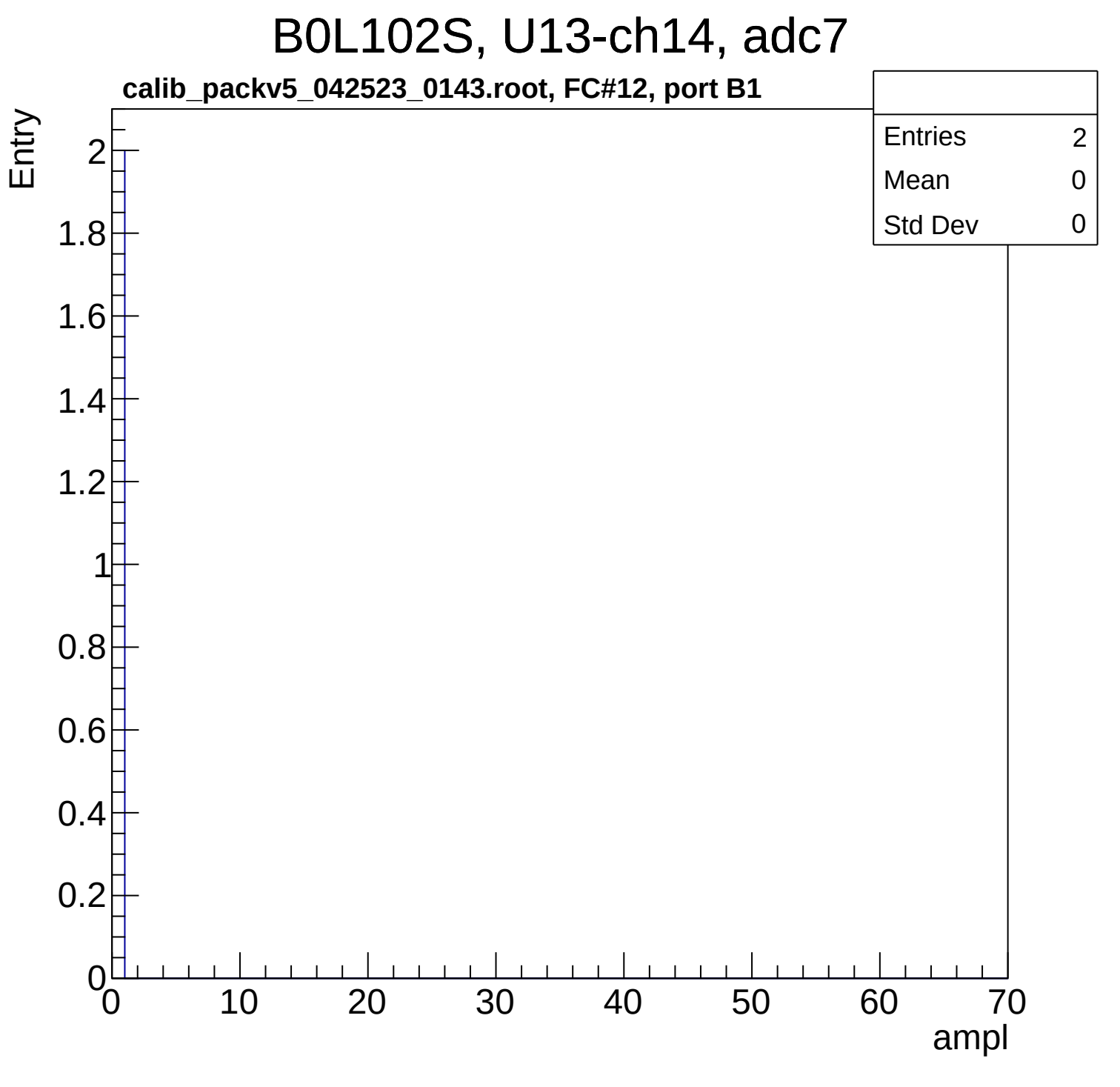
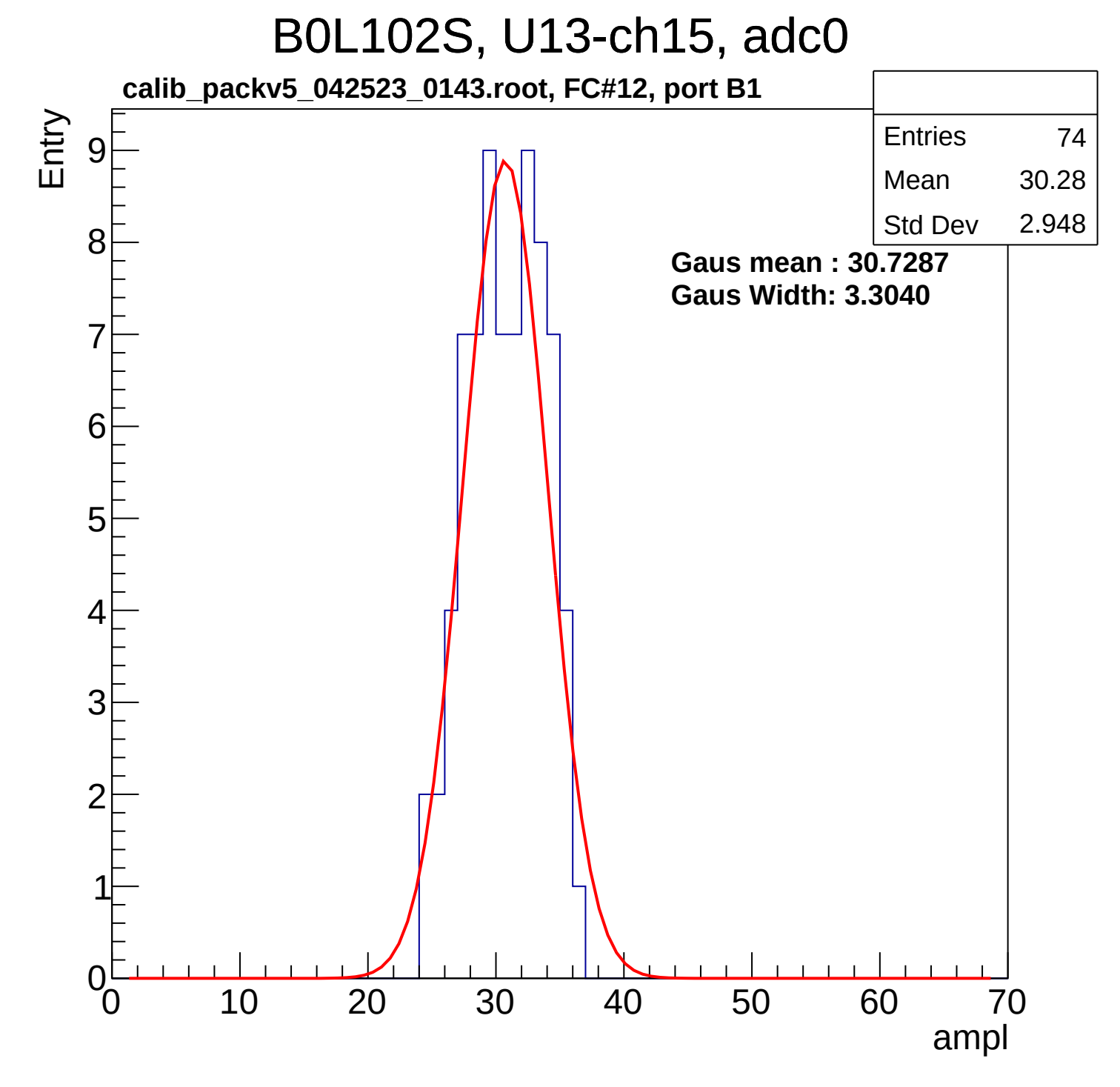

B0L102S, U13-ch14, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 52.57
Std Dev 21.49
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch14, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch15, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 30.28
Std Dev 2.948
Gaus mean : 30.7287
Gaus Width: 3.3040
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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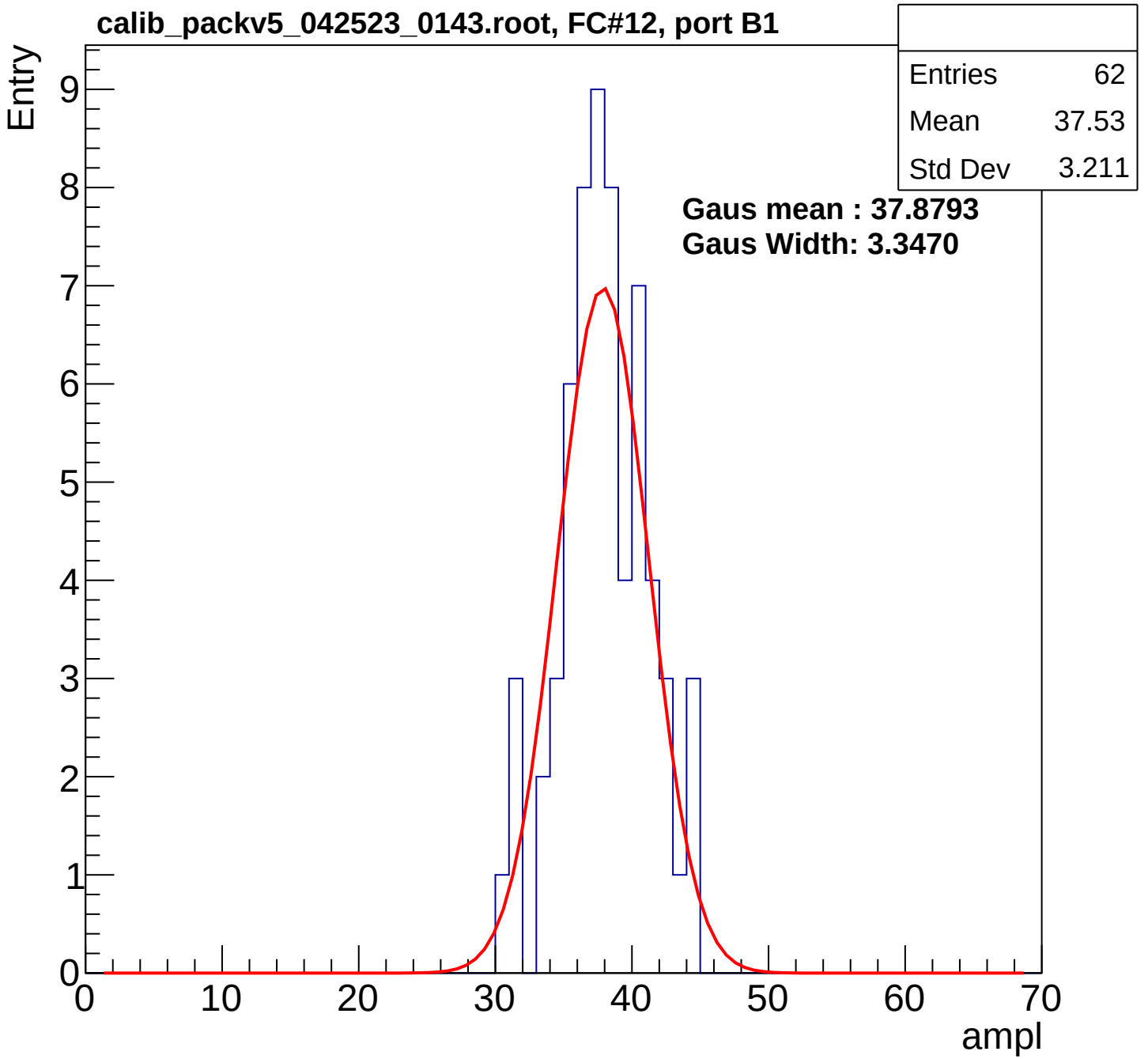
B0L102S, U13-ch15, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 37.53
Std Dev 3.211
Gaus mean : 37.8793
Gaus Width: 3.3470
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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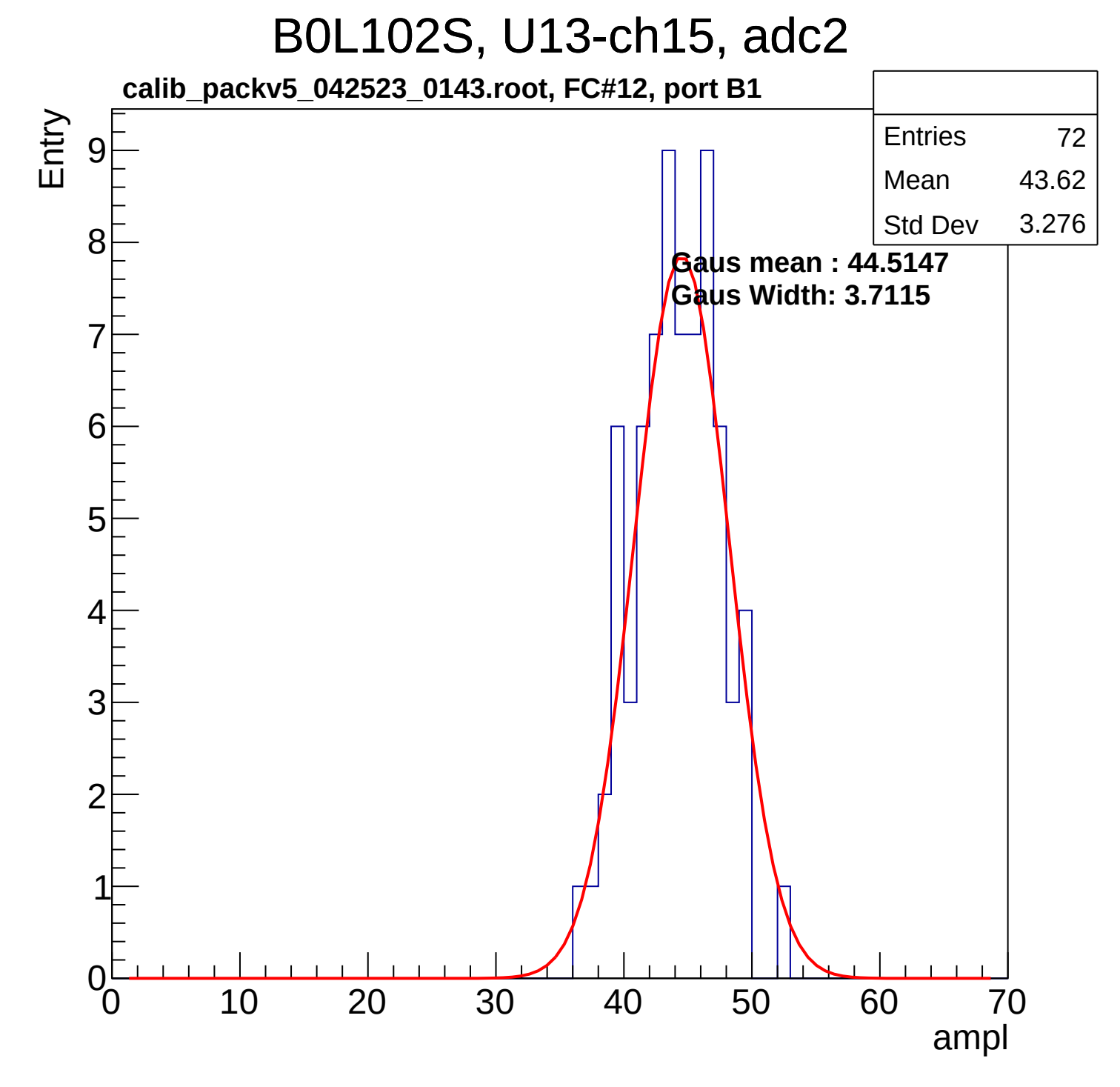
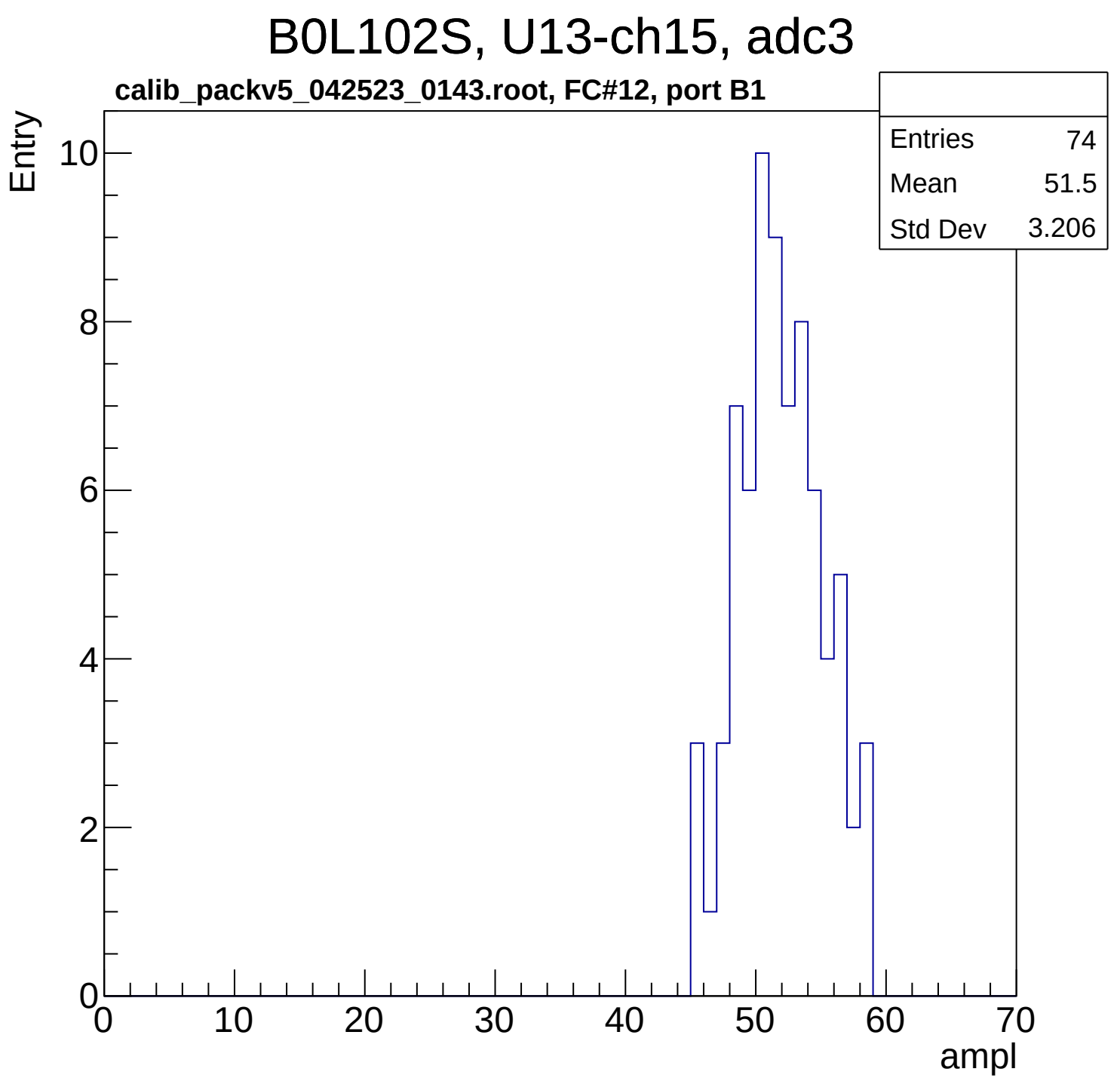
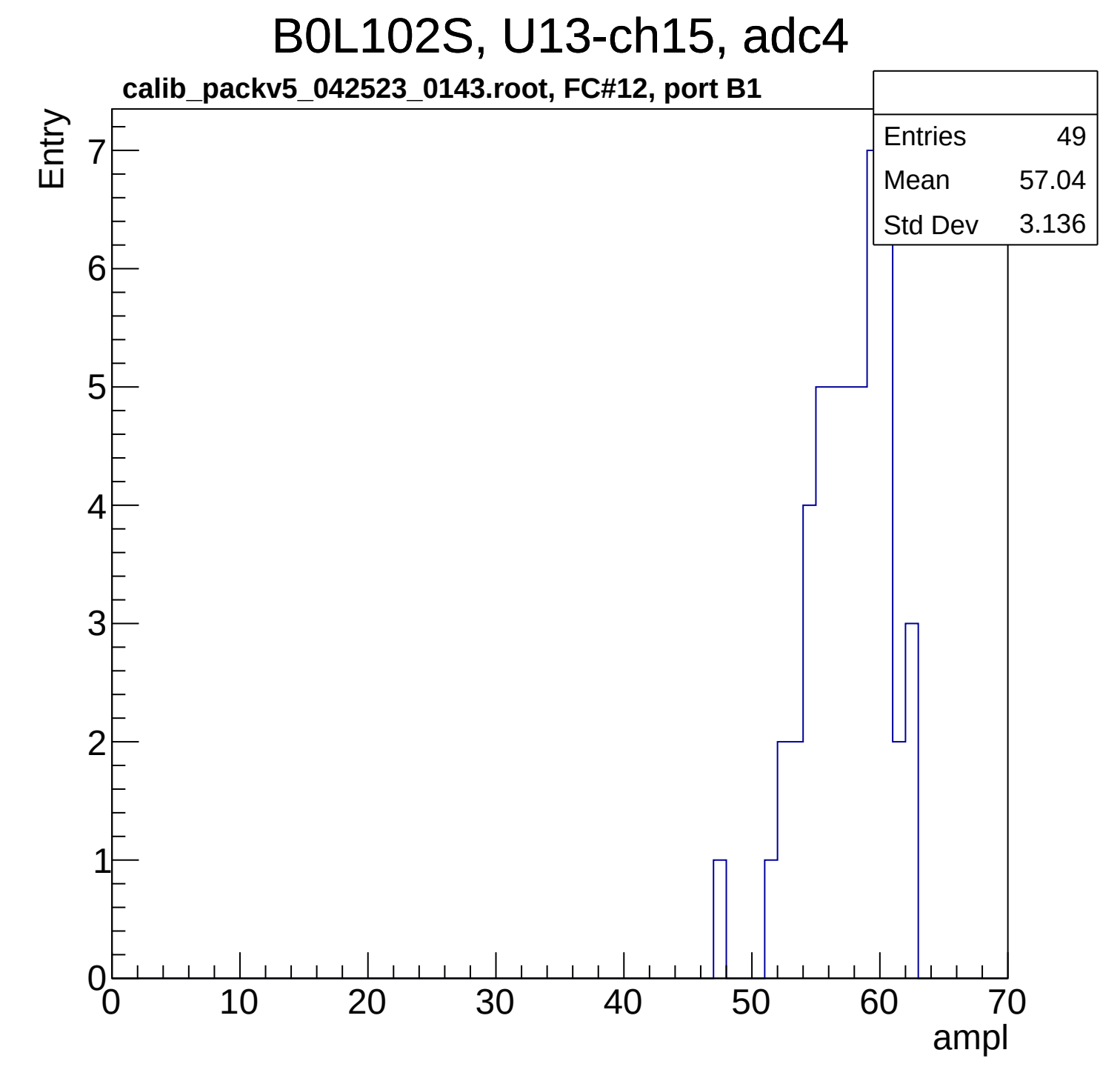
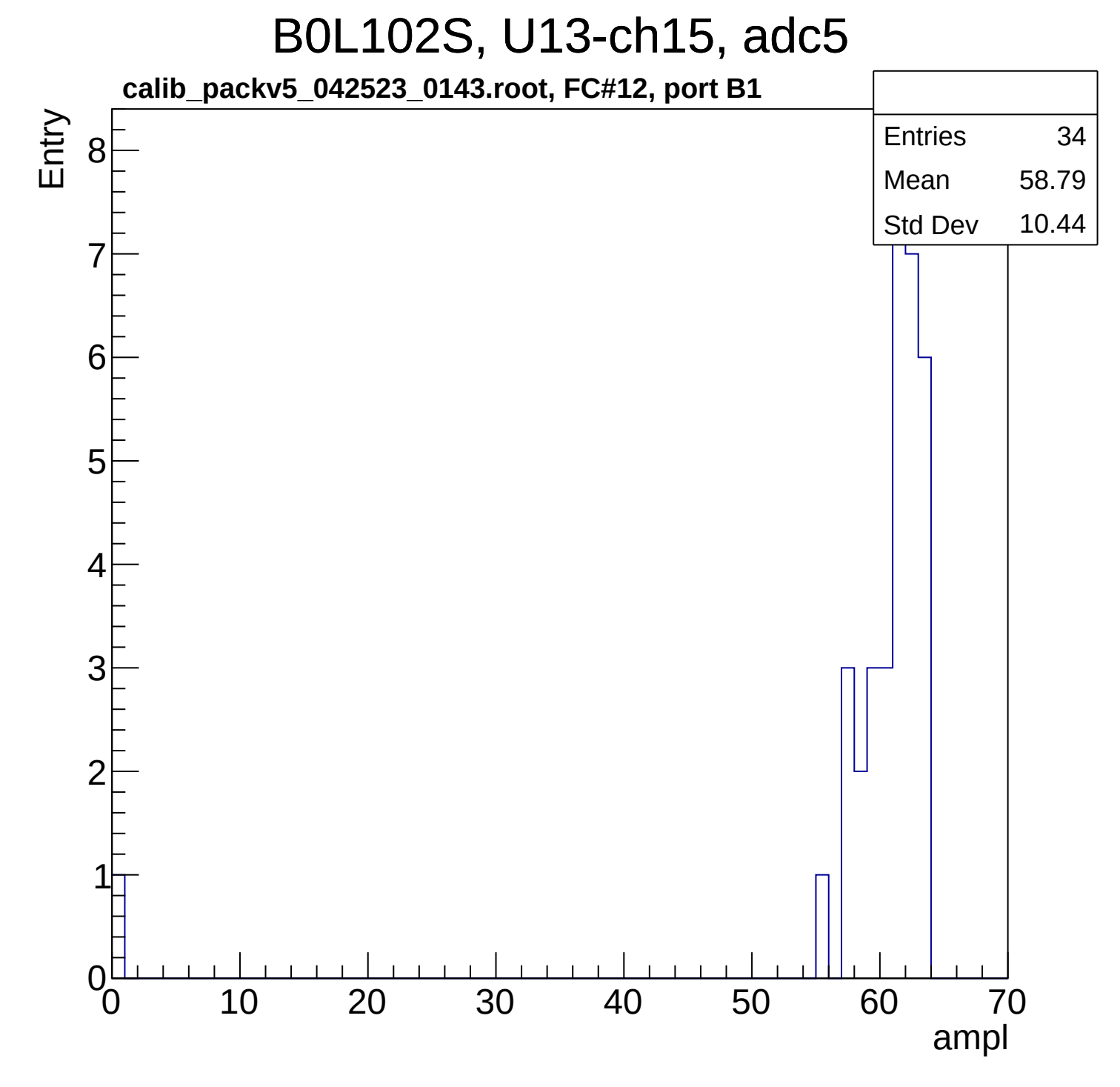
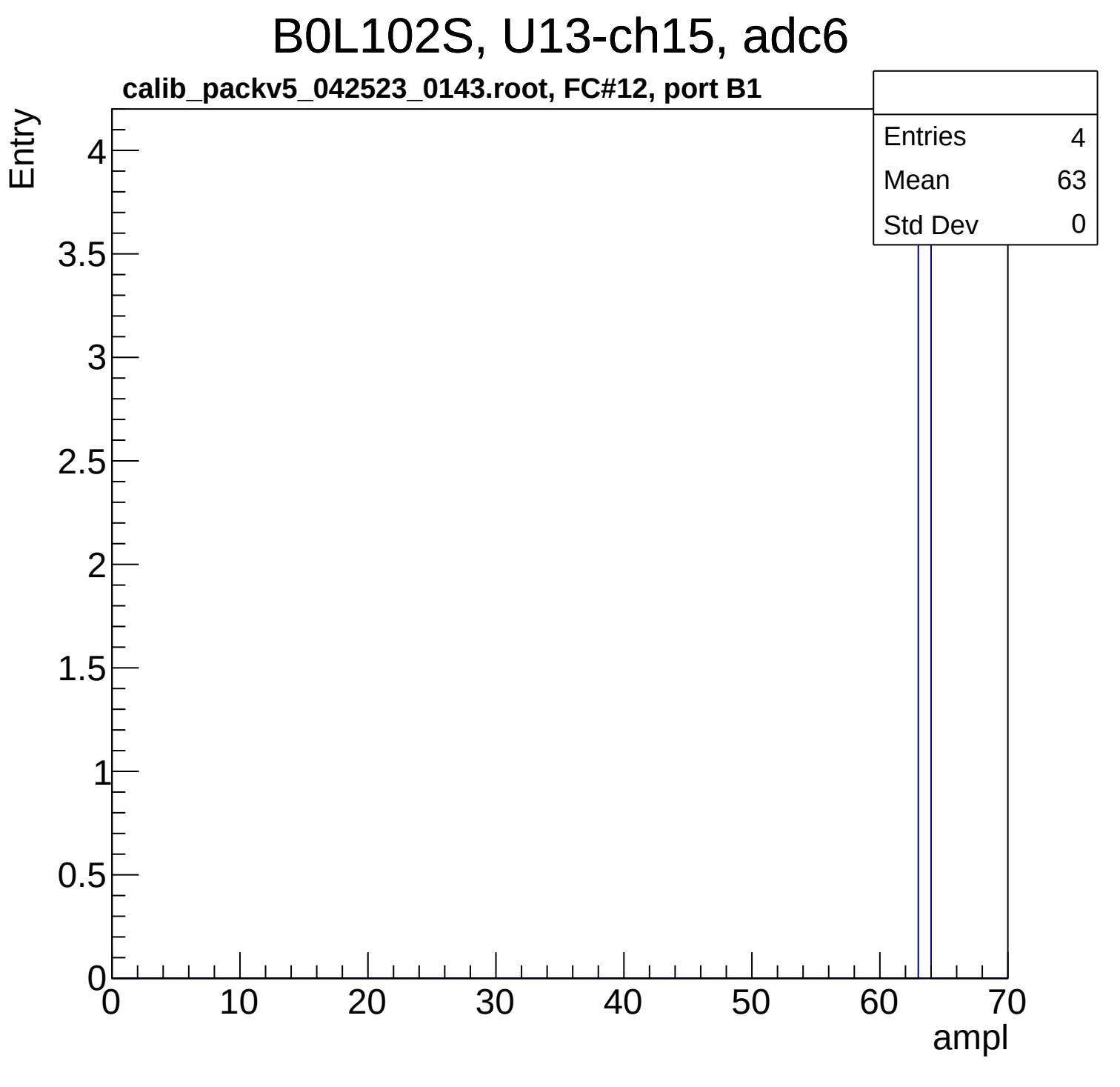
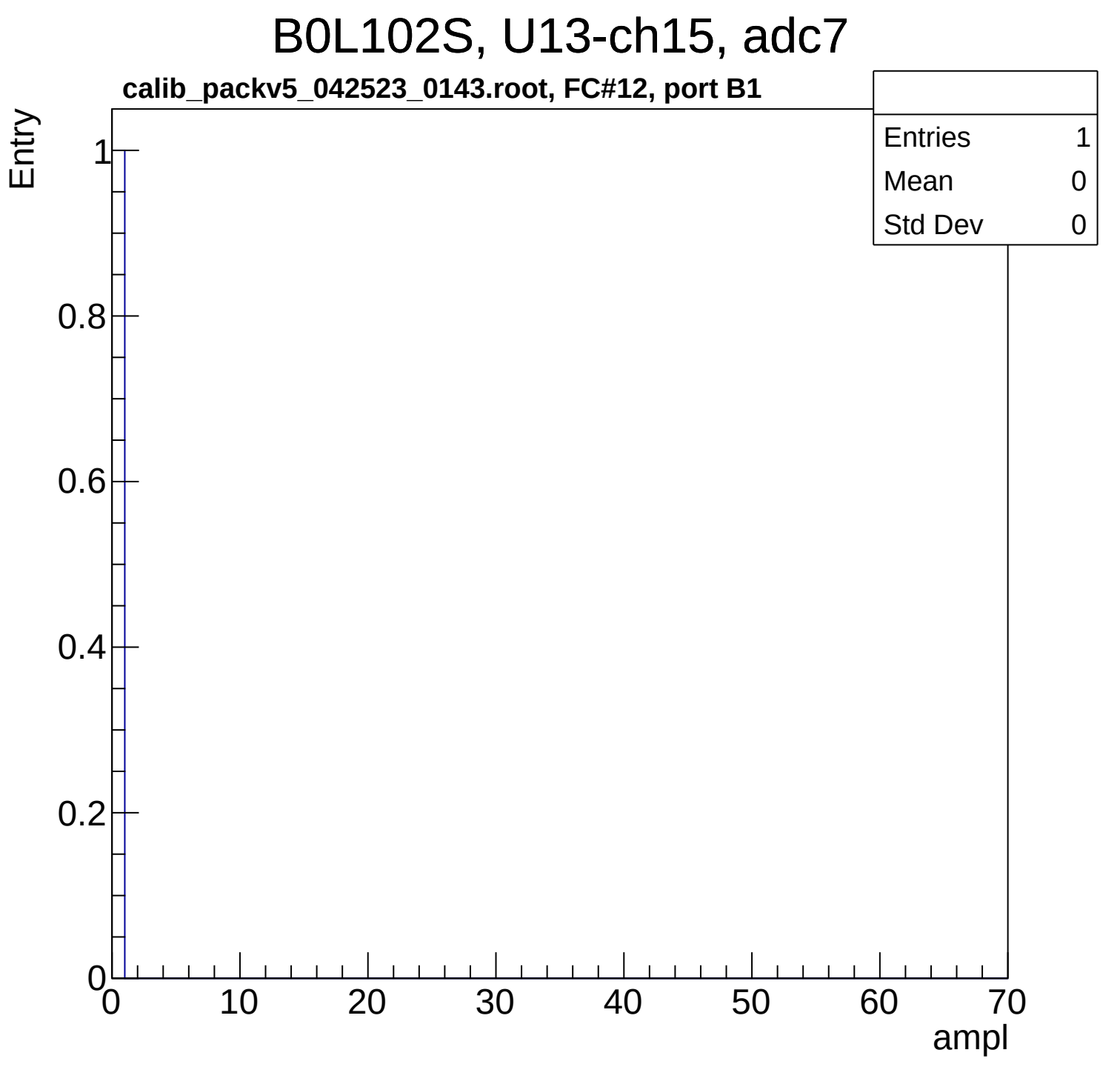

B0L102S, U13-ch15, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 43.62
Std Dev 3.276
Gaus mean : 44.5147
Gaus Width: 3.7115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

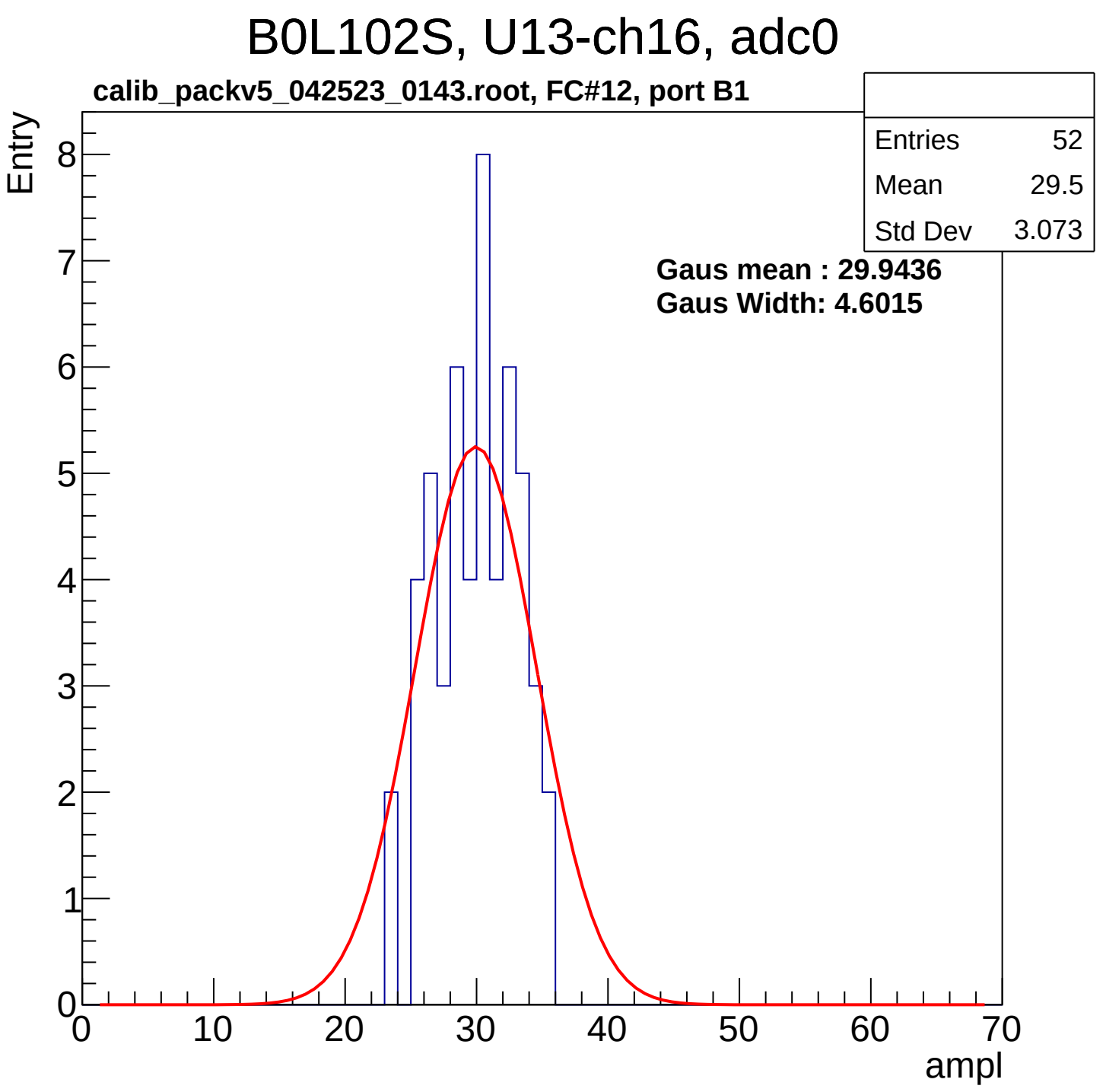

B0L102S, U13-ch15, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 51.5
Std Dev 3.206
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

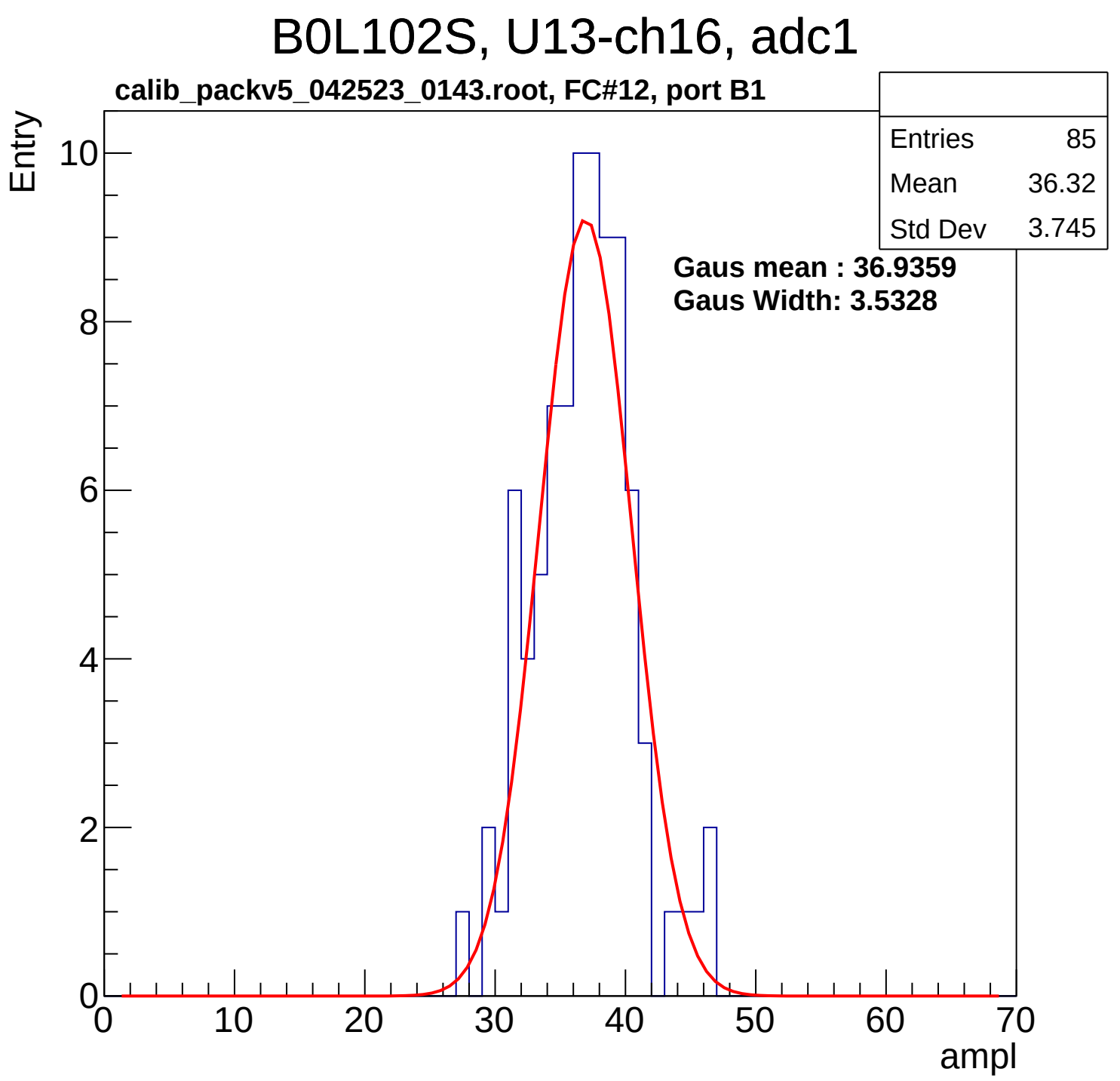

B0L102S, U13-ch15, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 57.04
Std Dev 3.136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

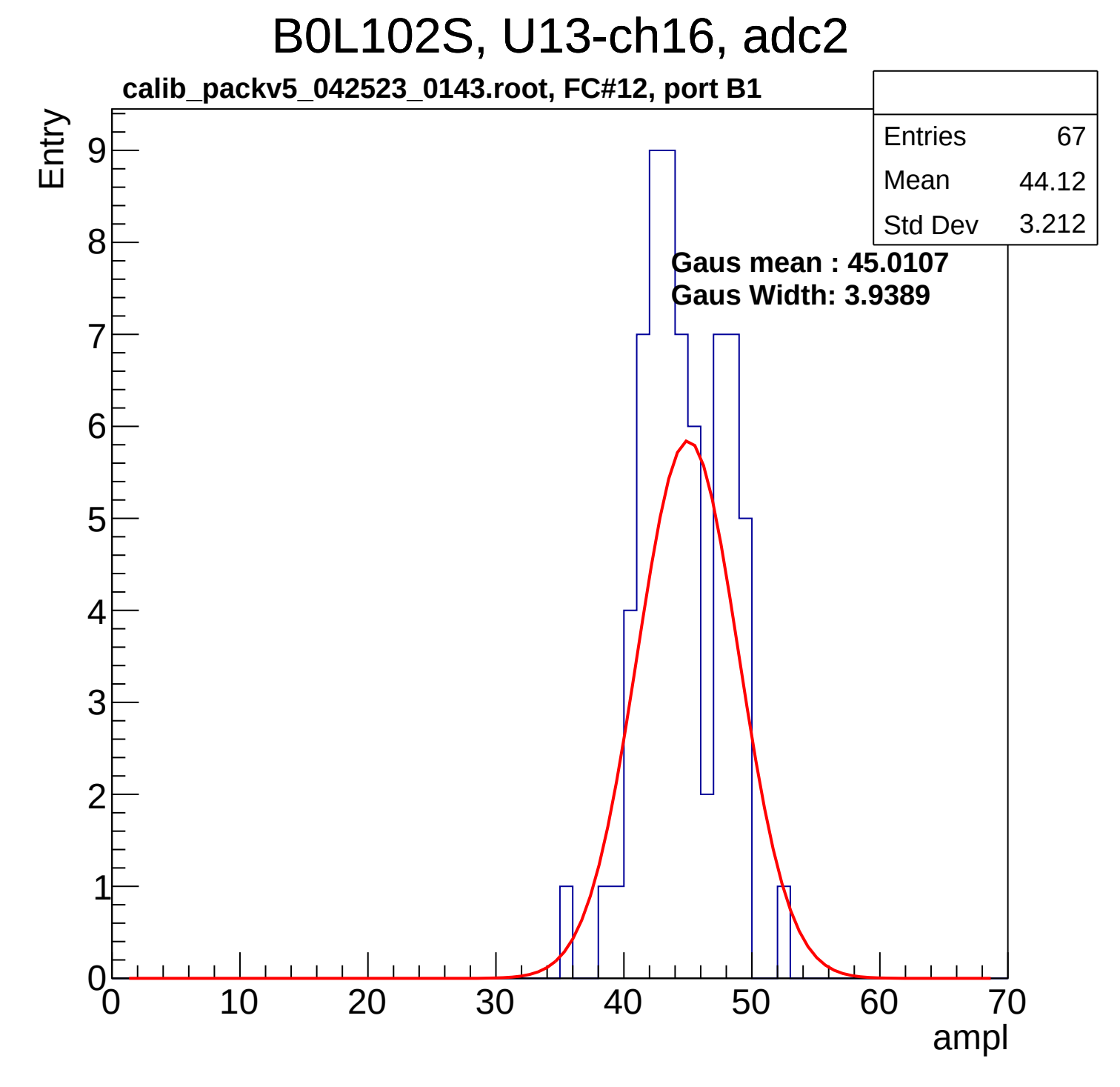

B0L102S, U13-ch15, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 58.79
Std Dev 10.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

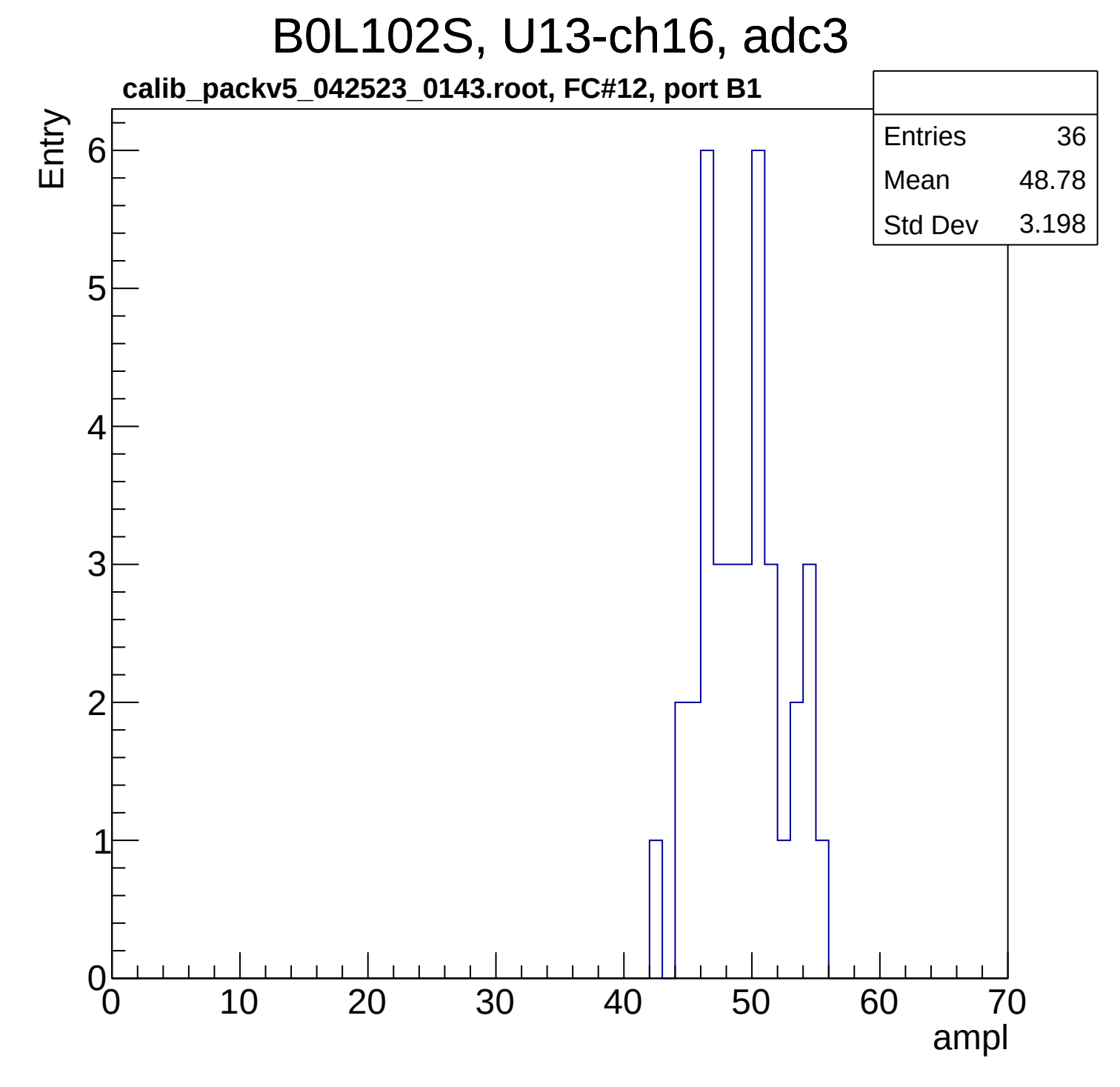

B0L102S, U13-ch15, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

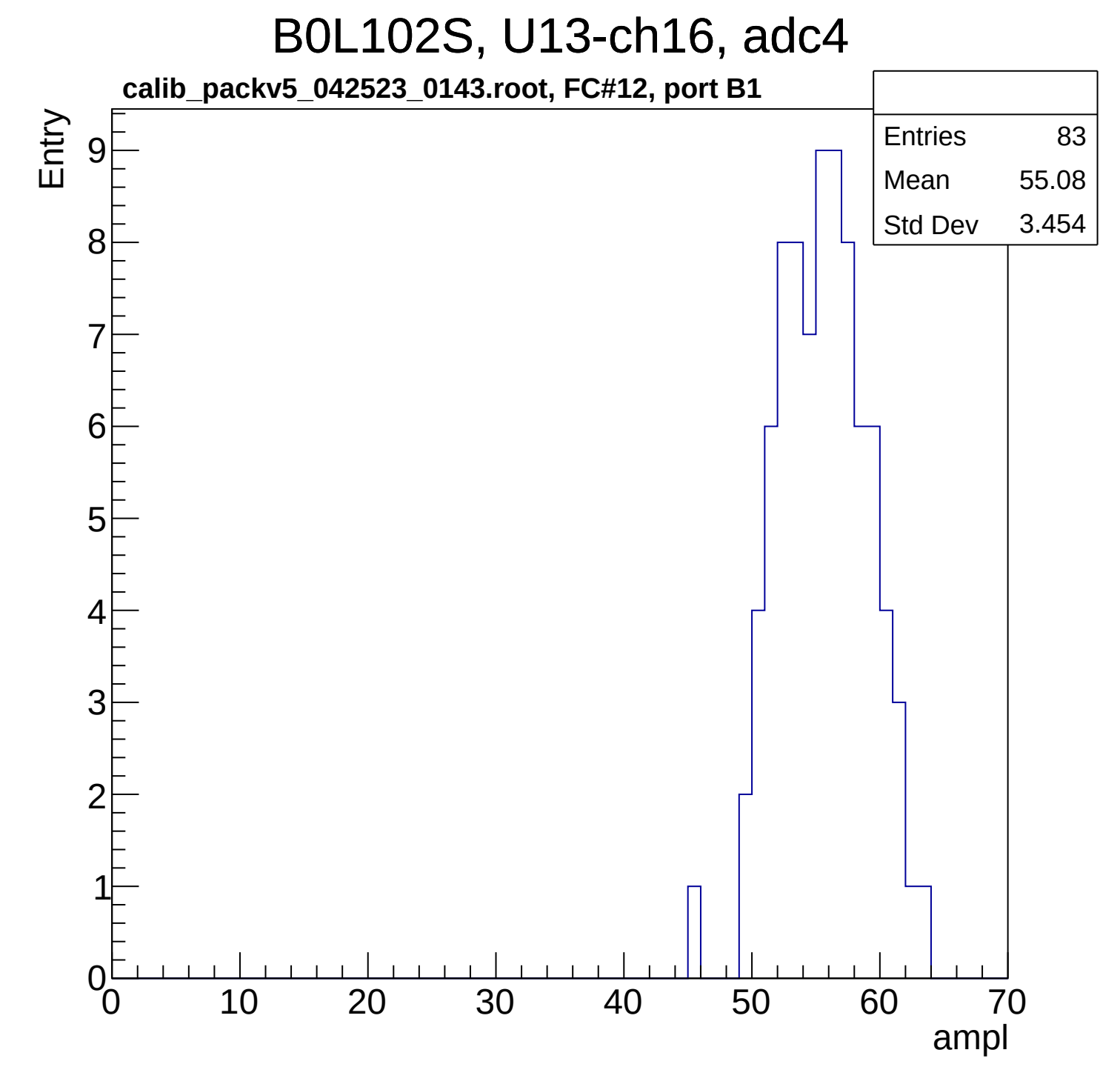

B0L102S, U13-ch15, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

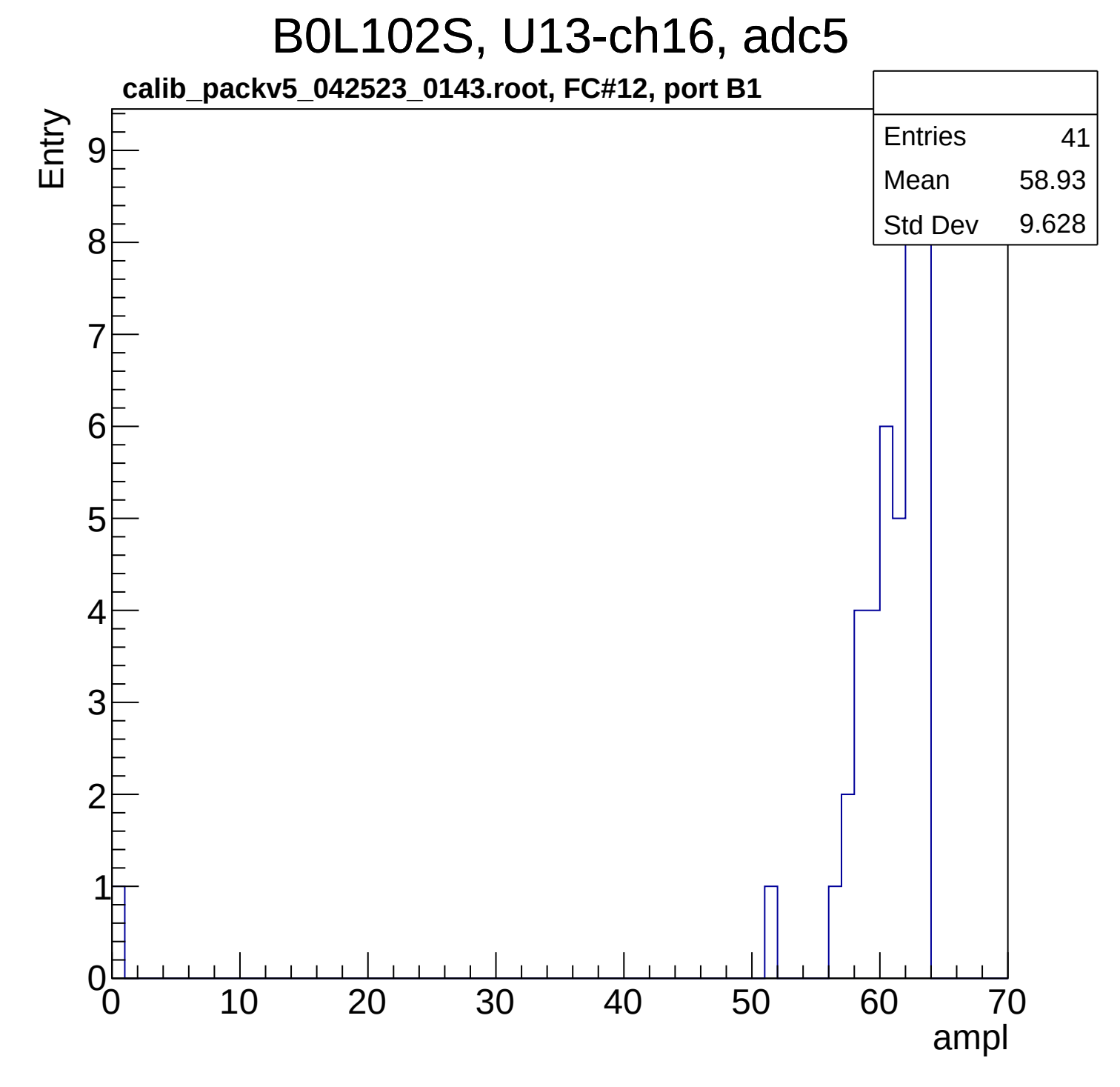

B0L102S, U13-ch16, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 29.5
Std Dev 3.073
Gaus mean : 29.9436
Gaus Width: 4.6015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

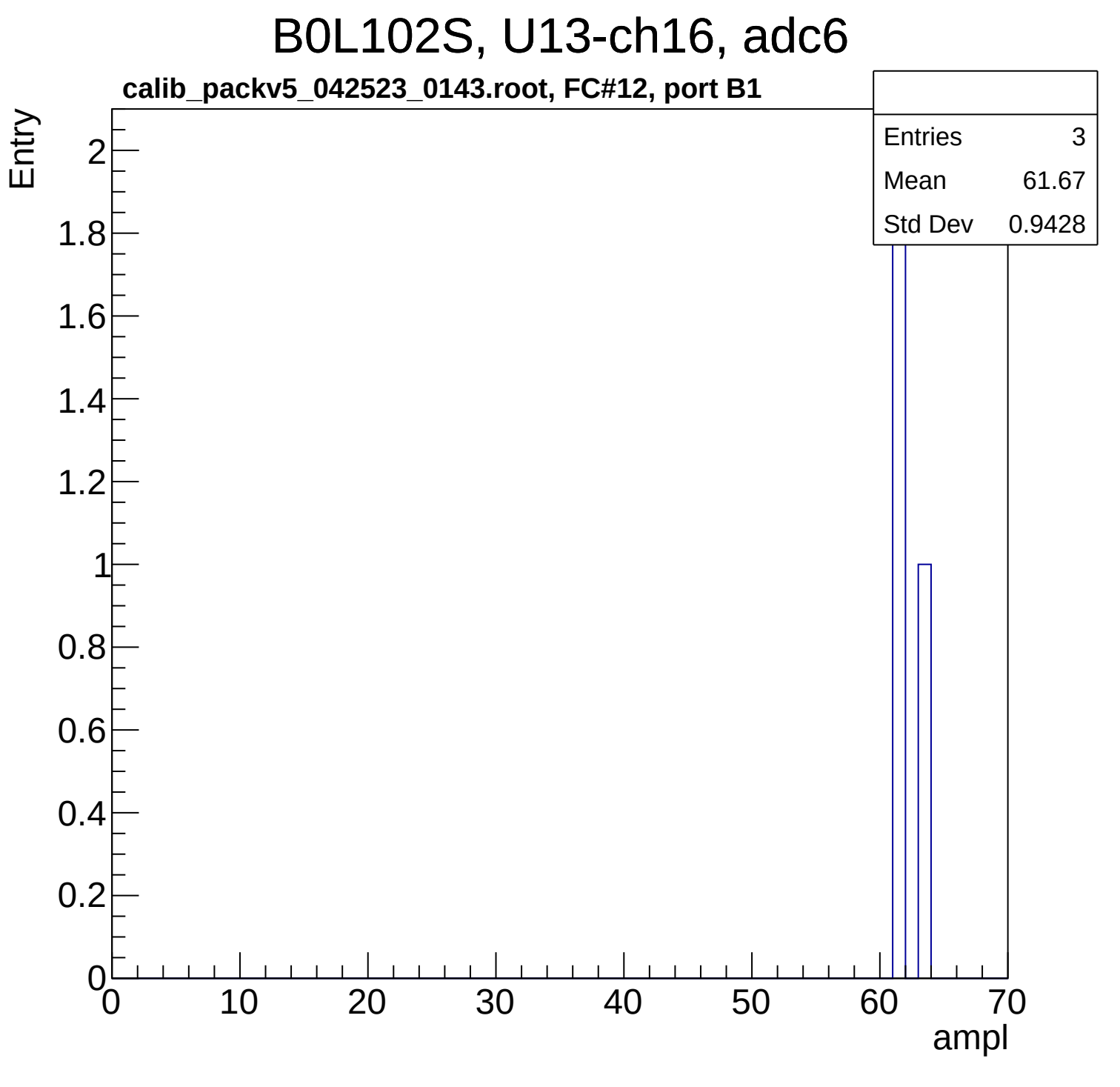

B0L102S, U13-ch16, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 36.32
Std Dev 3.745
Gaus mean : 36.9359
Gaus Width: 3.5328
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

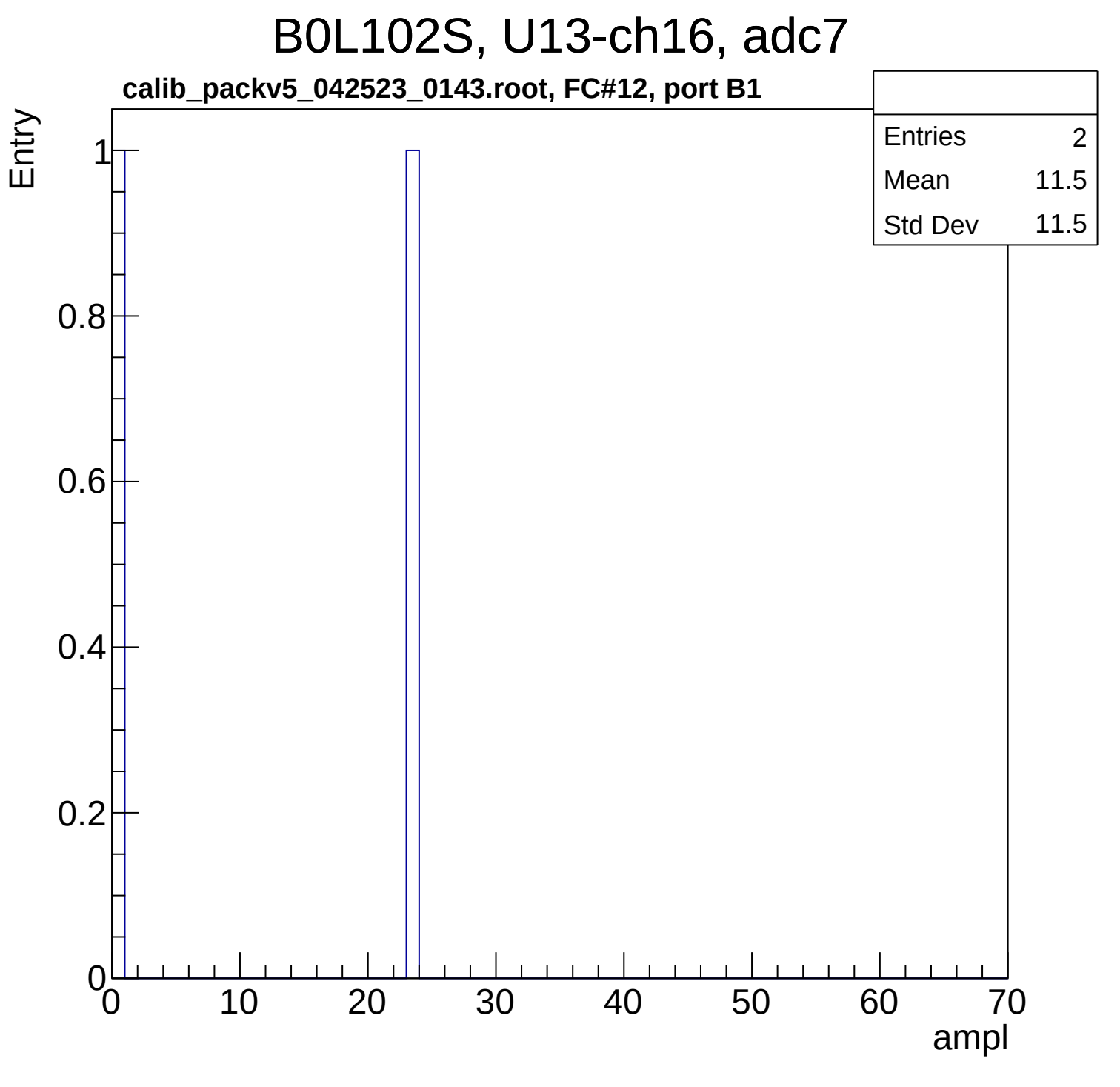

B0L102S, U13-ch16, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 44.12
Std Dev 3.212
Gaus mean : 45.0107
Gaus Width: 3.9389
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

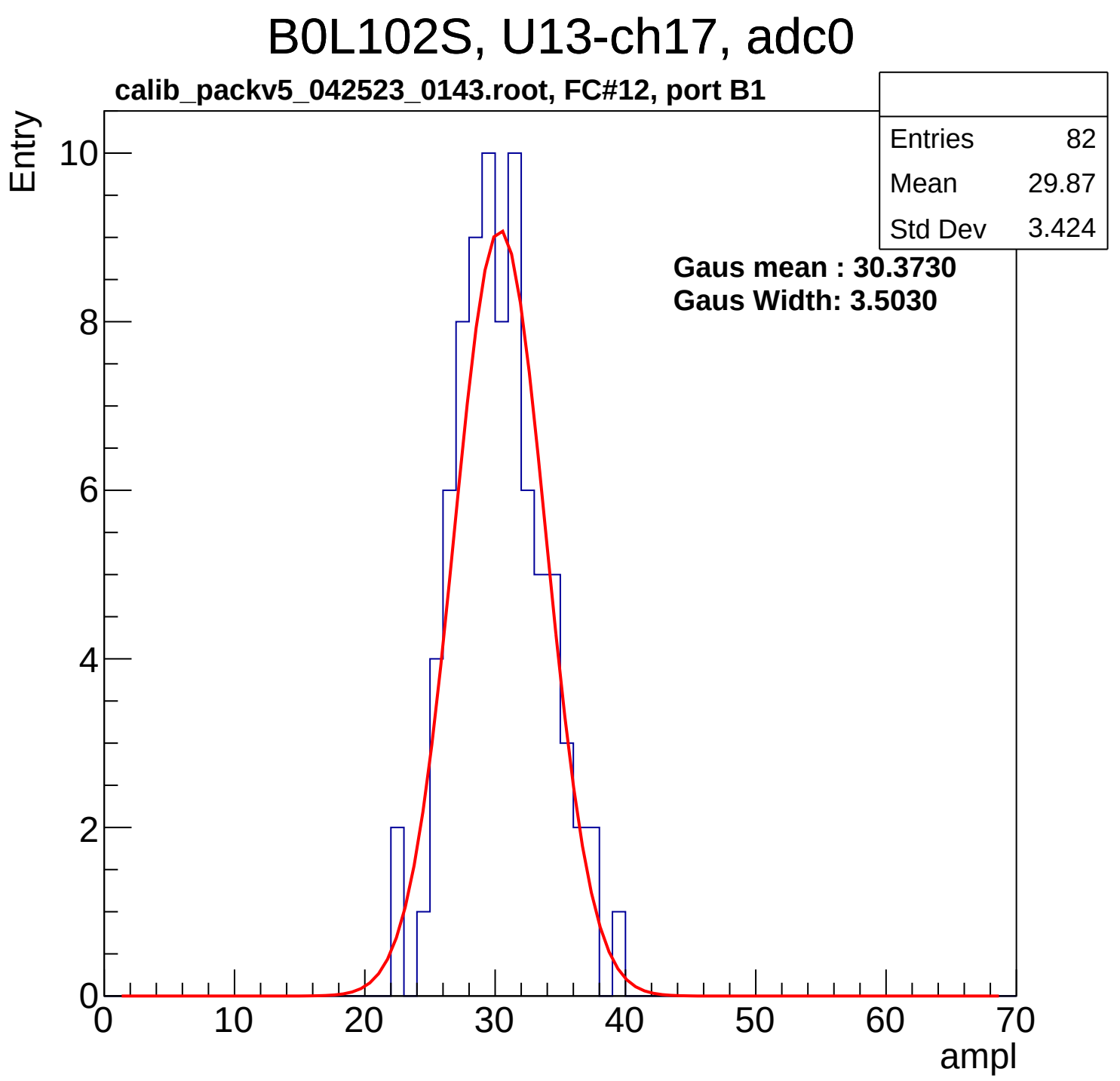

B0L102S, U13-ch16, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 48.78
Std Dev 3.198
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

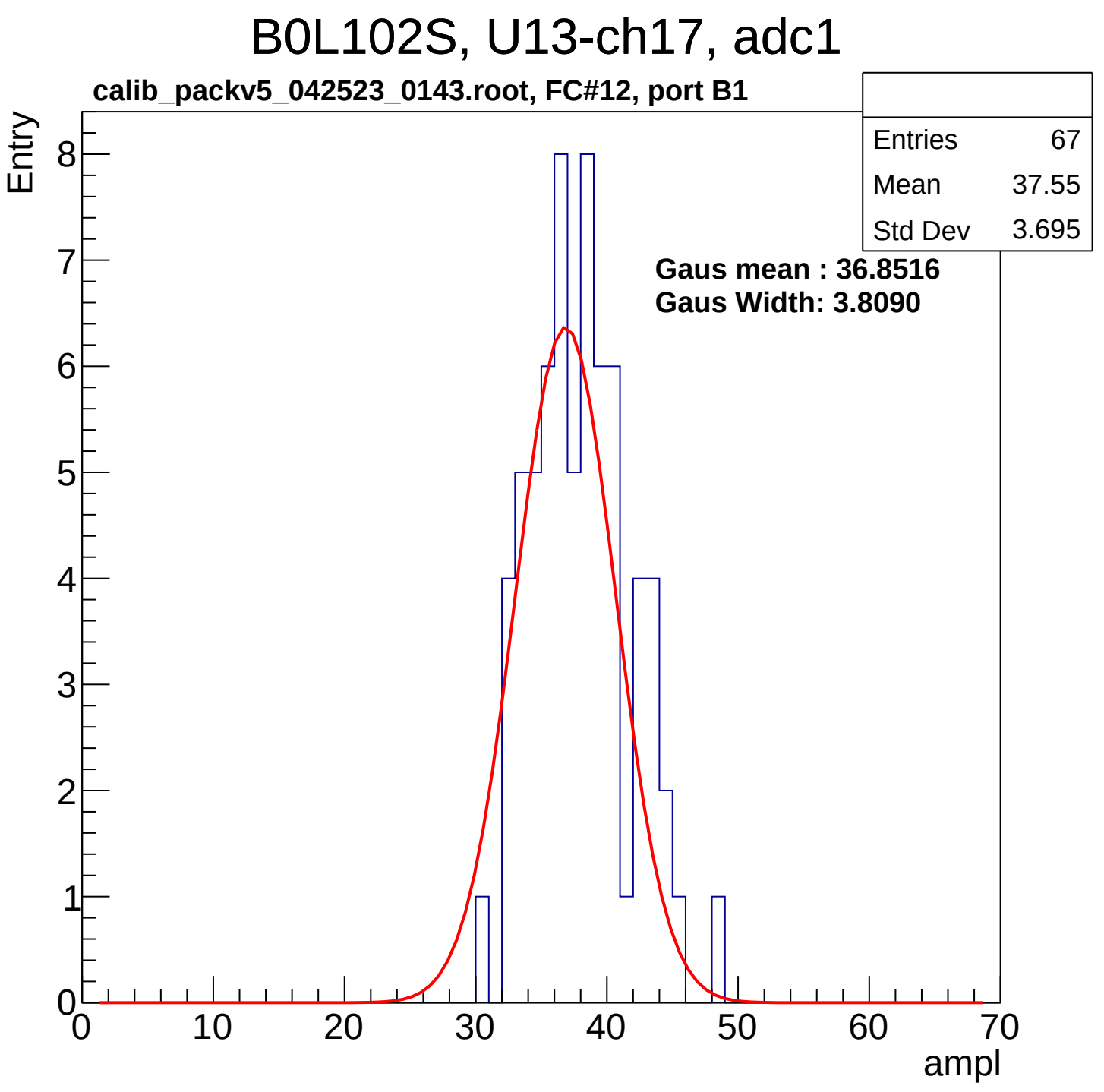

B0L102S, U13-ch16, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 55.08
Std Dev 3.454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

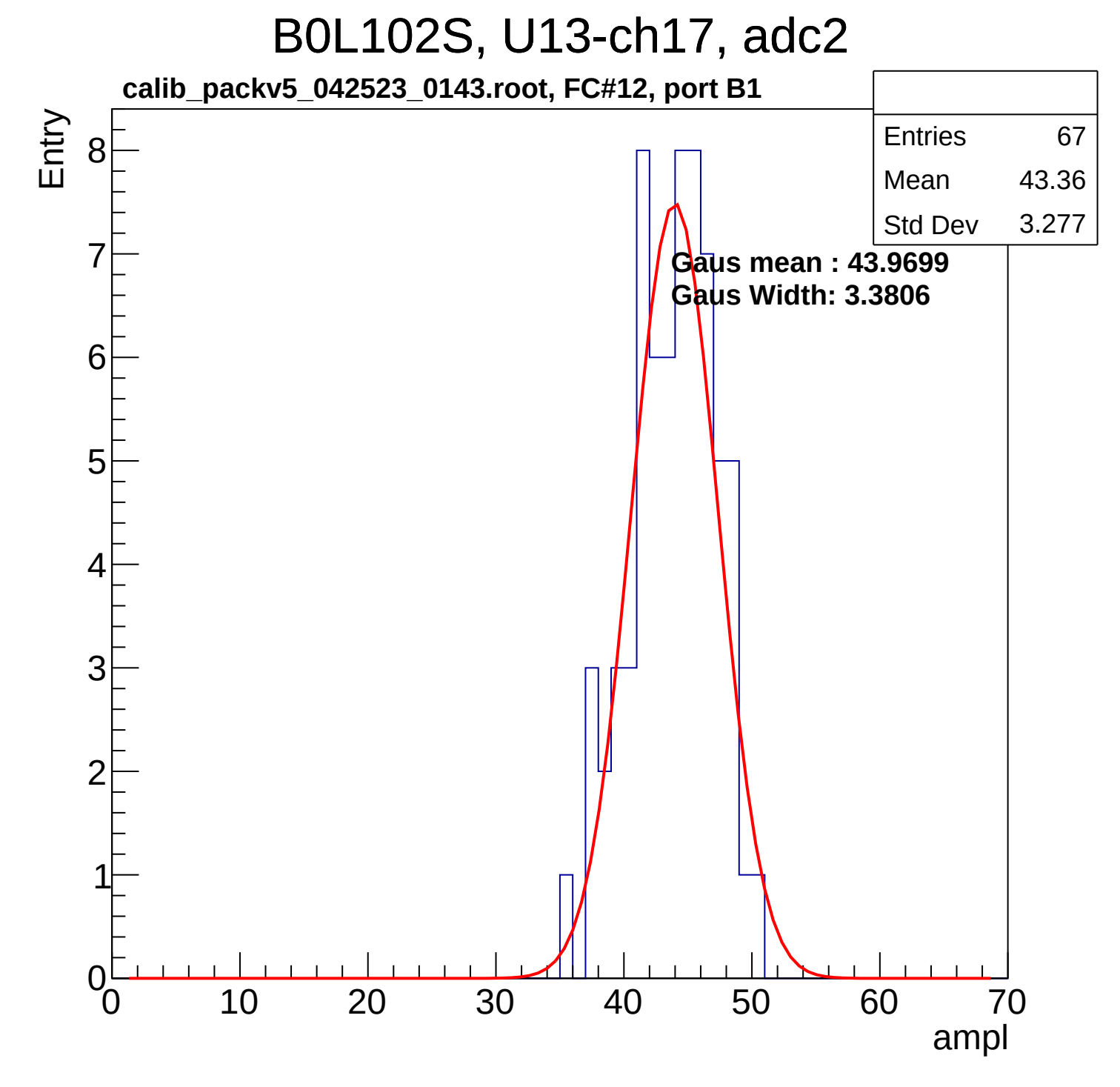

B0L102S, U13-ch16, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.93
Std Dev 9.628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

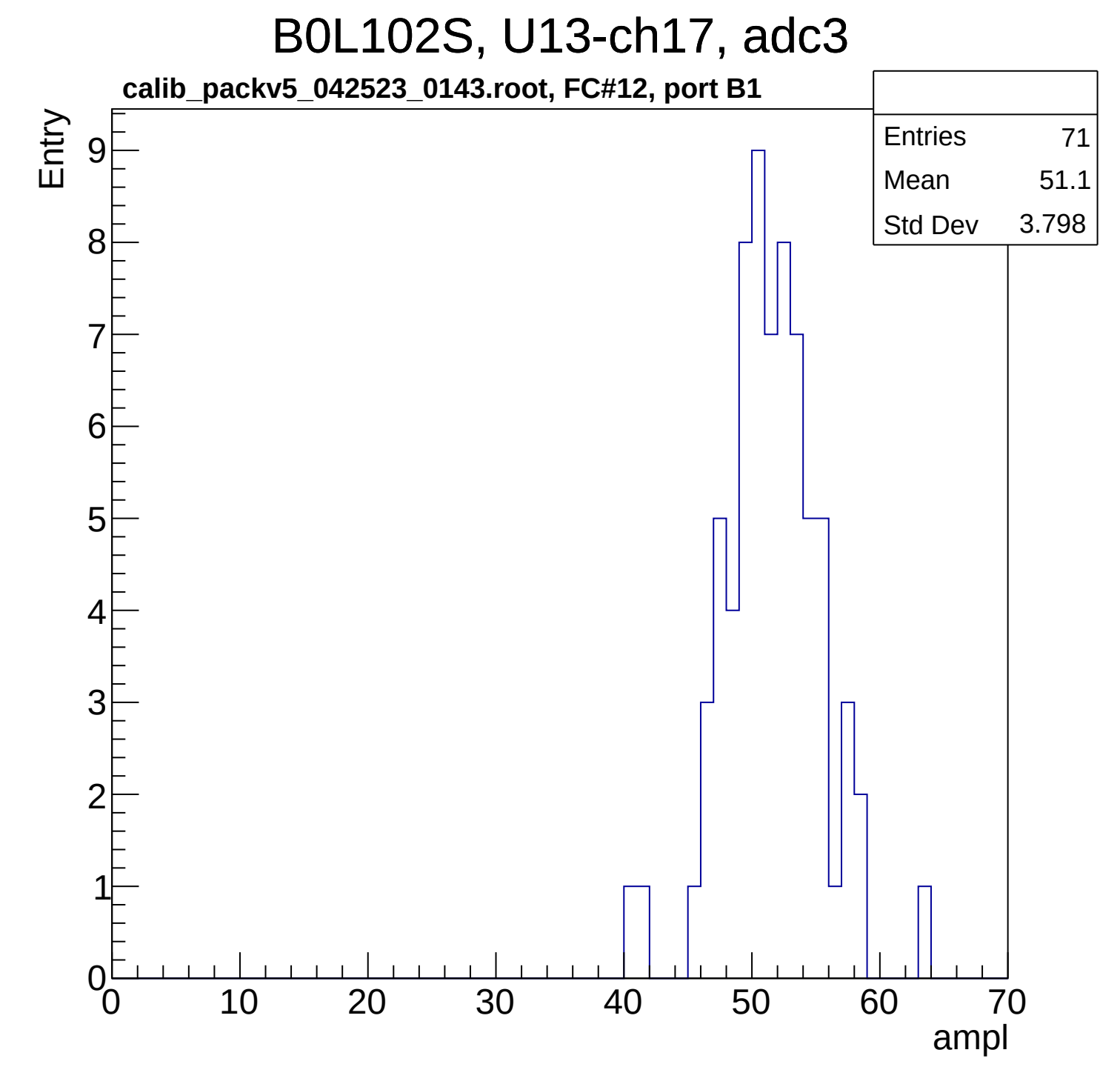

B0L102S, U13-ch16, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

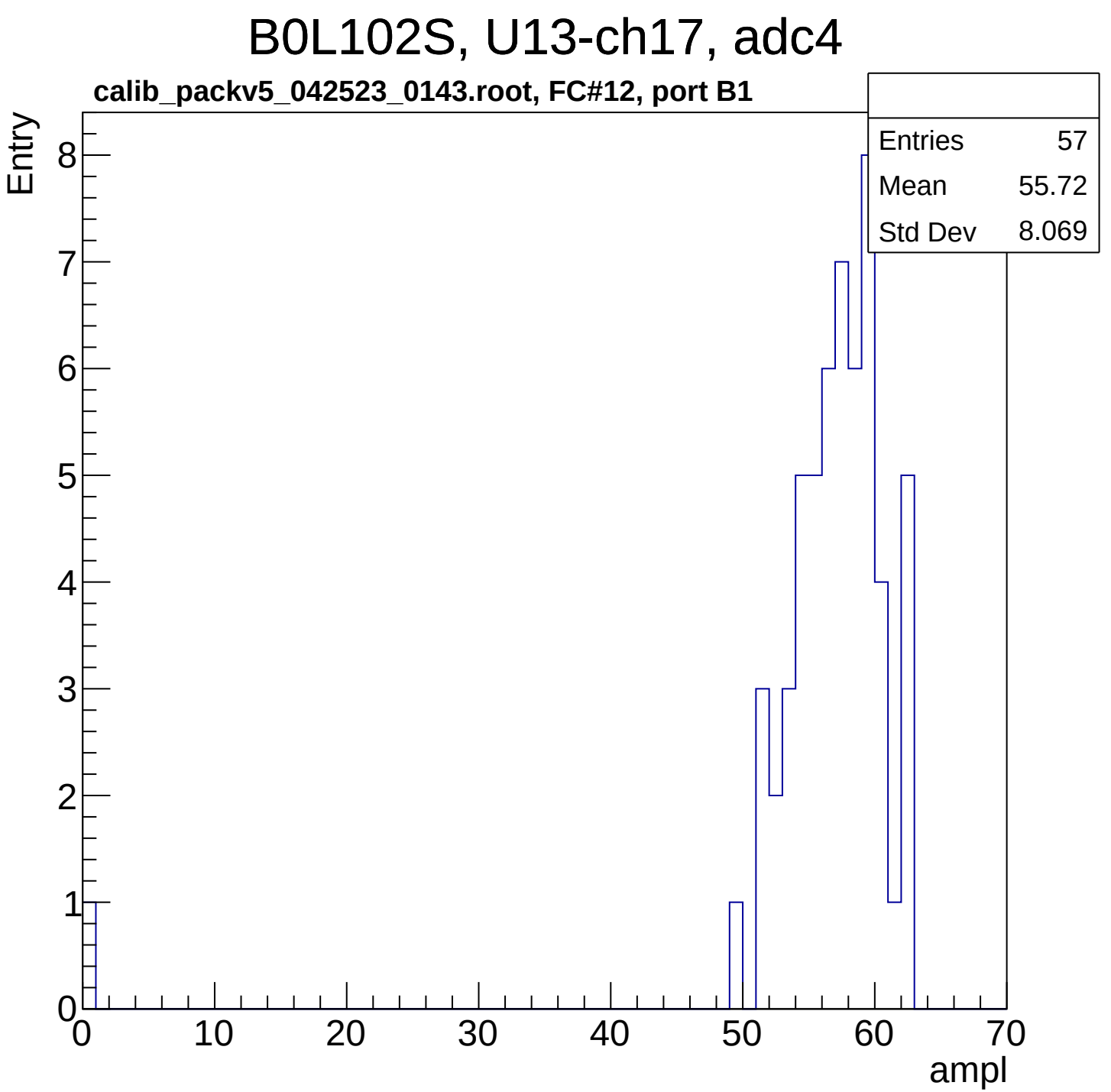

B0L102S, U13-ch16, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

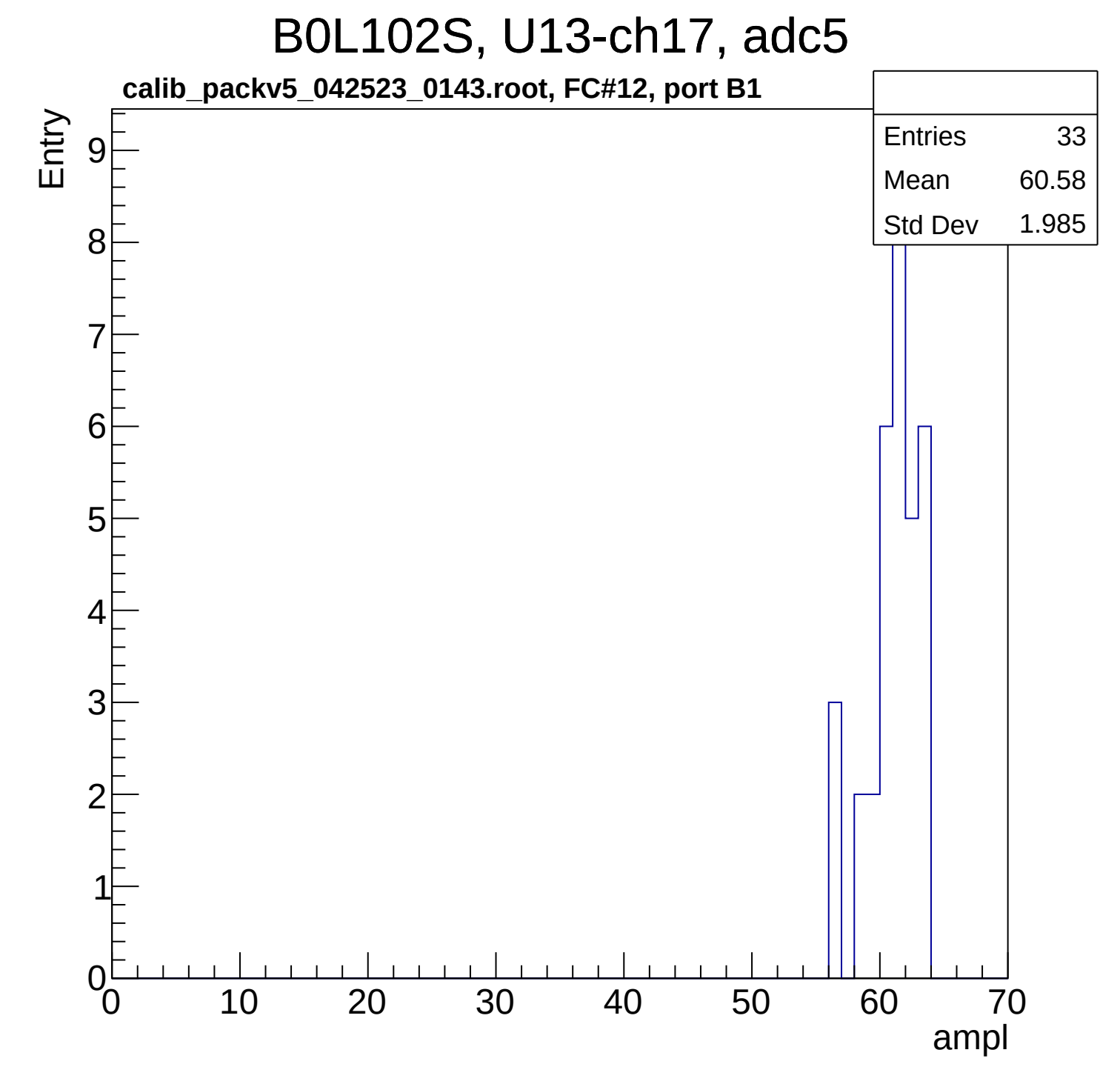

B0L102S, U13-ch17, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 29.87
Std Dev 3.424
Gaus mean : 30.3730
Gaus Width: 3.5030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch17, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 37.55
Std Dev 3.695
Gaus mean : 36.8516
Gaus Width: 3.8090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch17, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.36
Std Dev 3.277
Gaus mean : 43.9699
Gaus Width: 3.3806
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch17, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 51.1
Std Dev 3.798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch17, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.72
Std Dev 8.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch17, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.58
Std Dev 1.985
Entry
ampl
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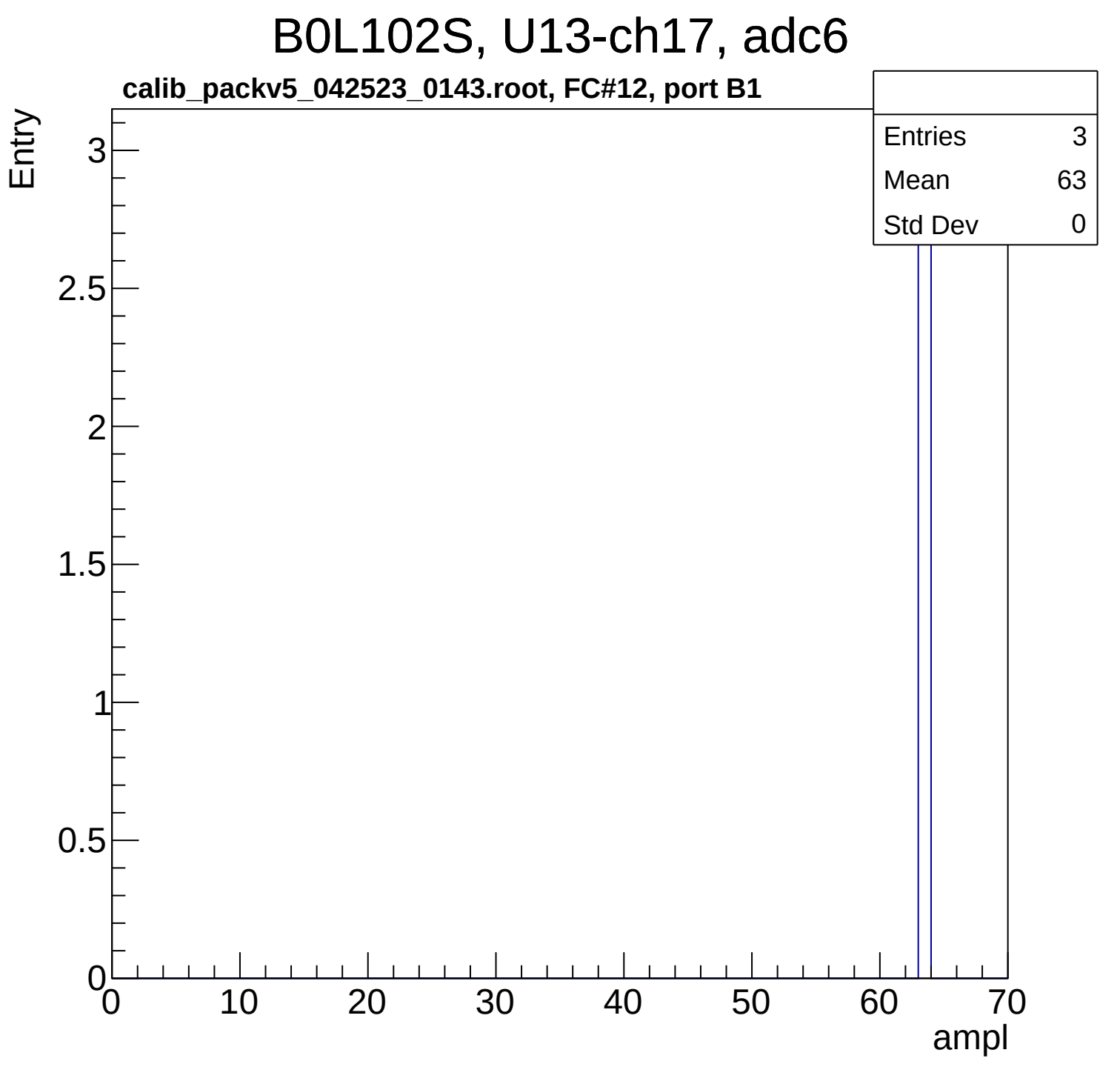
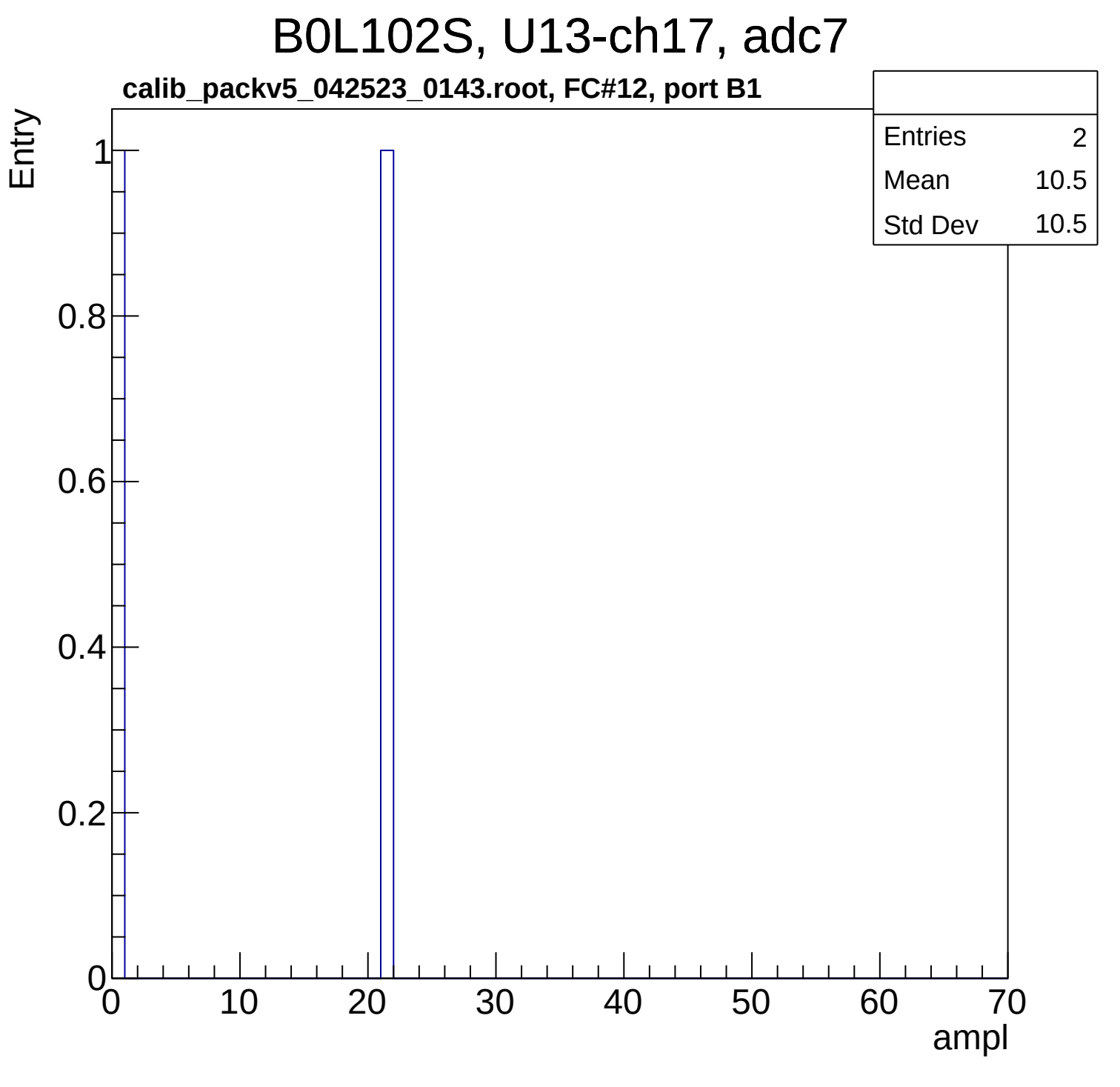
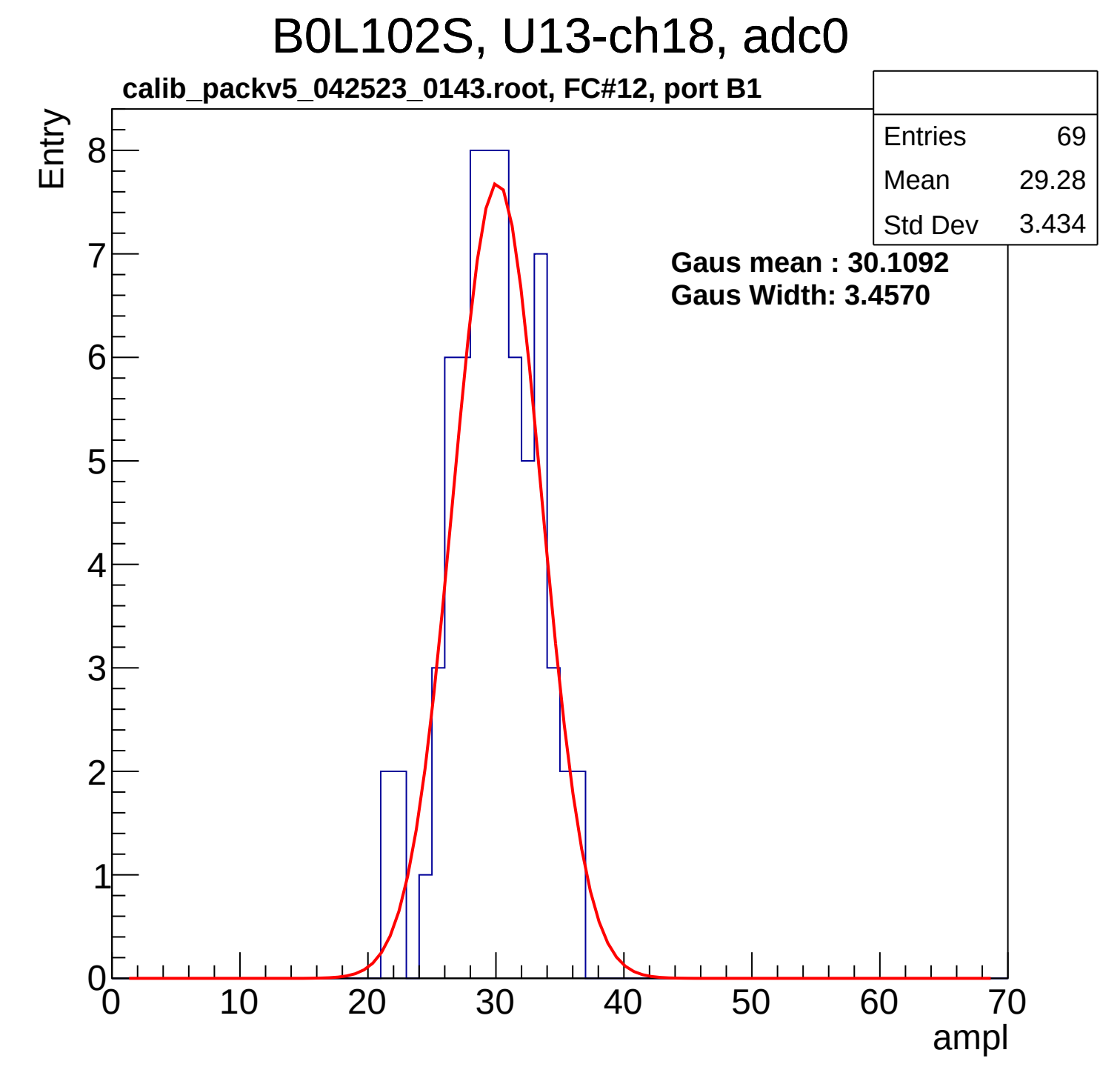
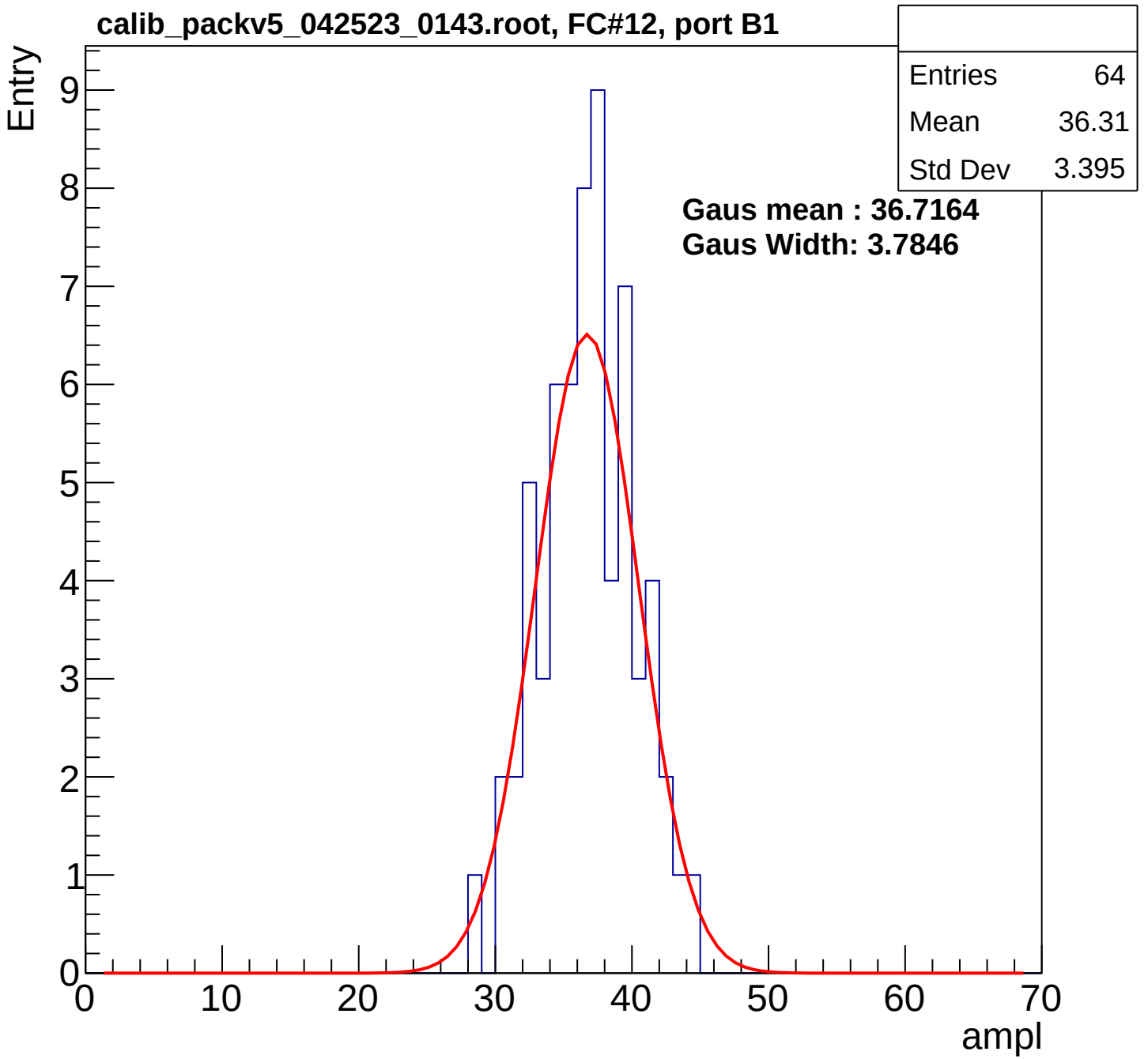
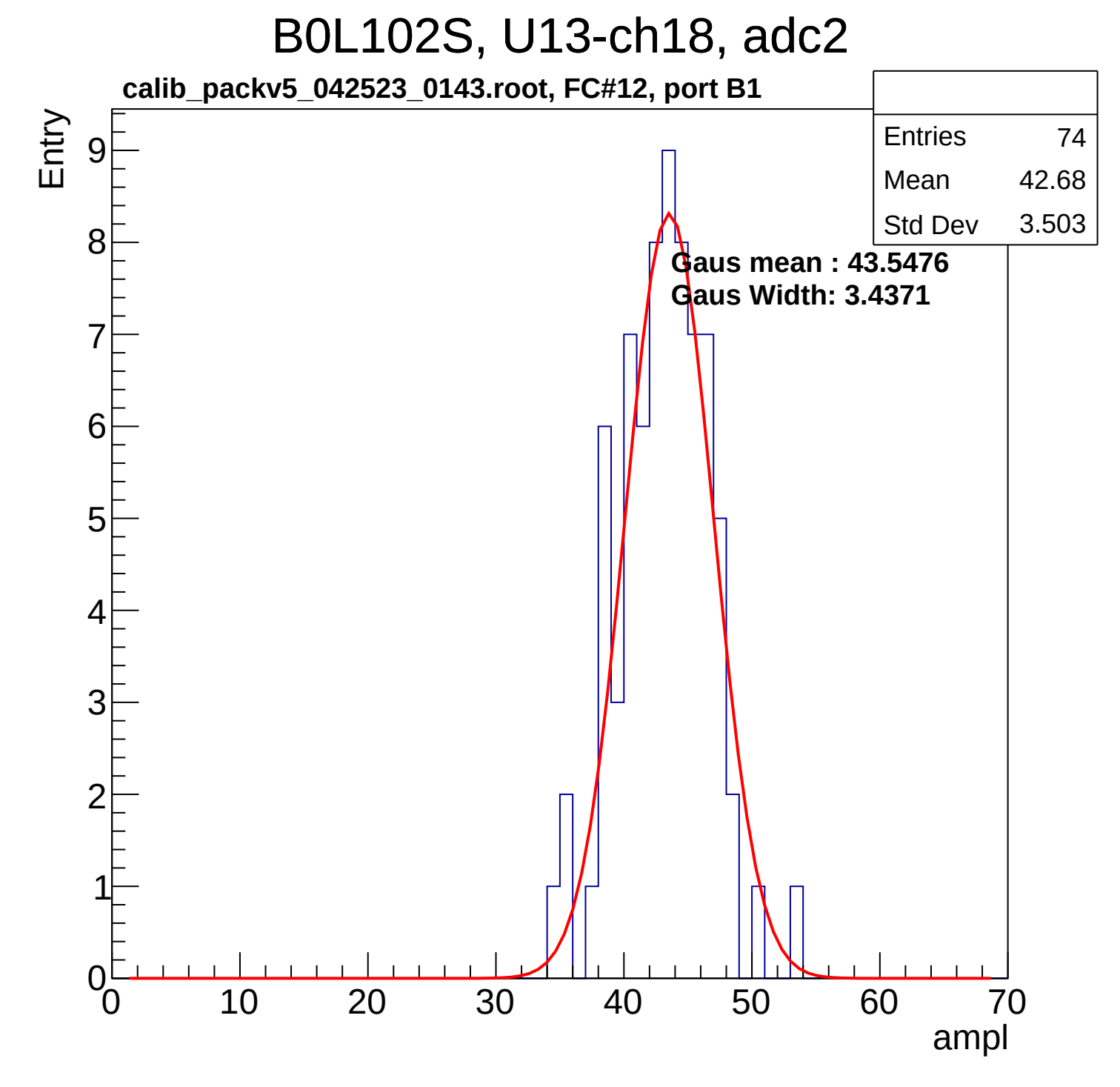
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

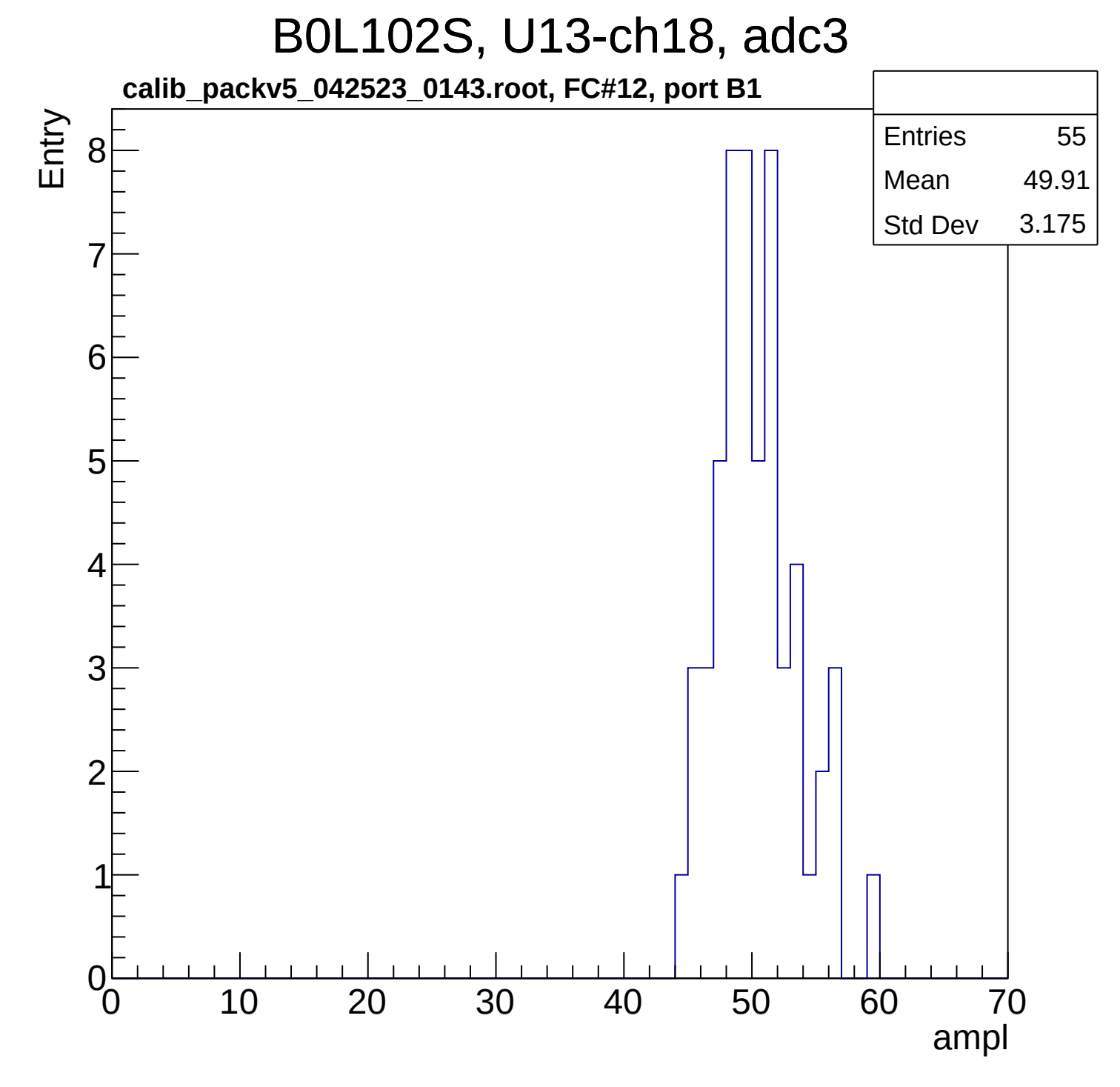

B0L102S, U13-ch17, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

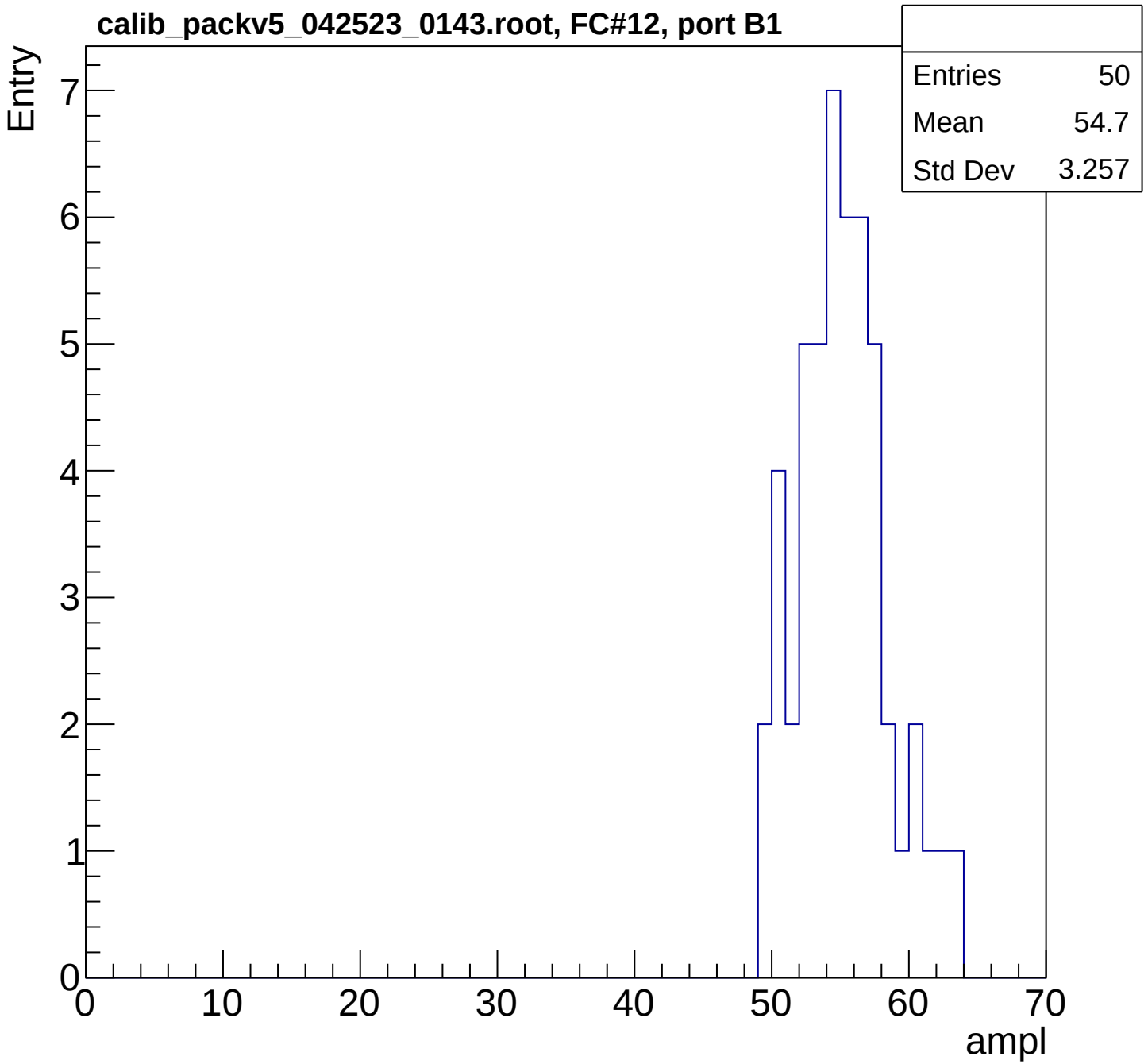

B0L102S, U13-ch17, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

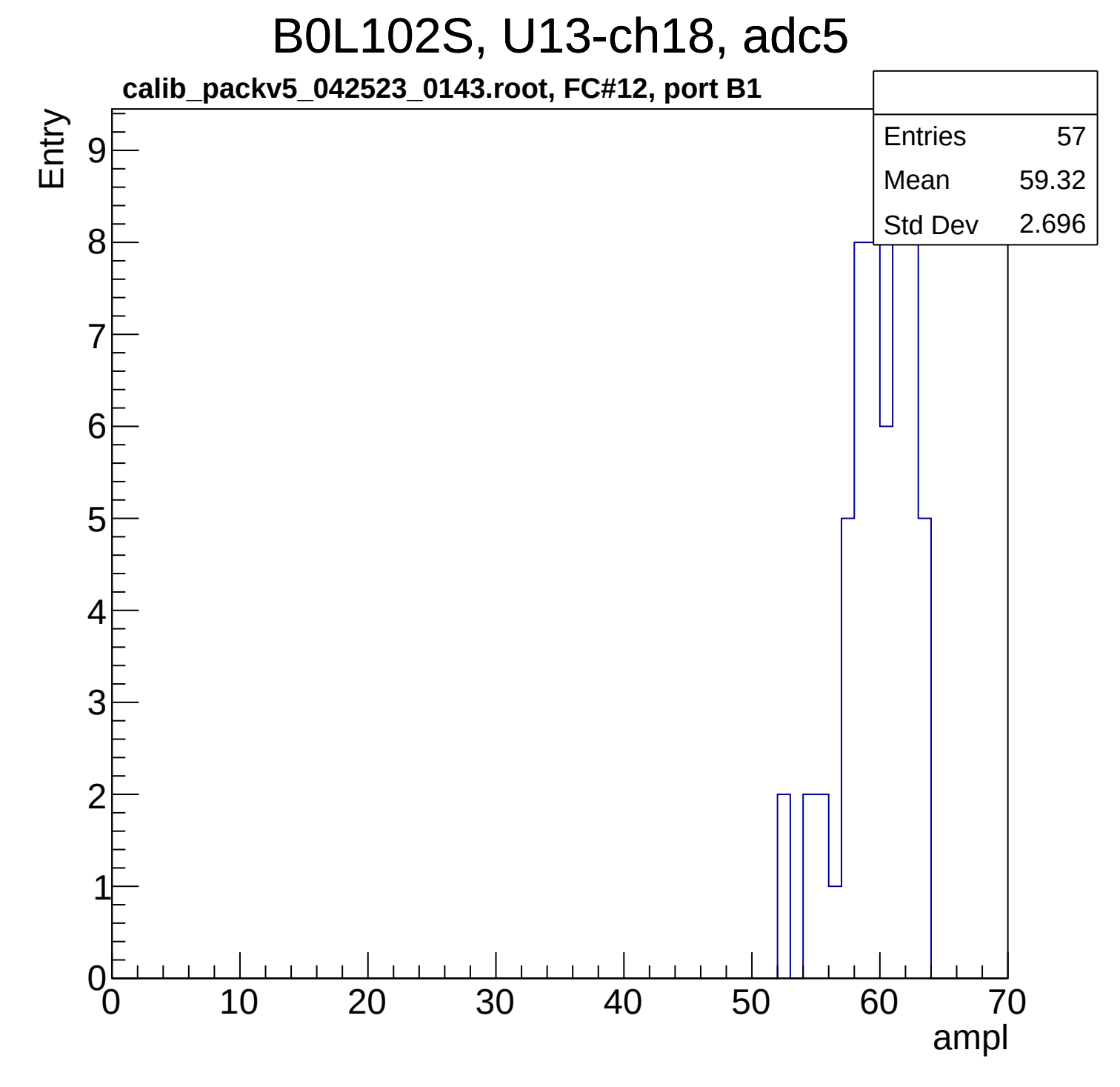

B0L102S, U13-ch18, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 29.28
Std Dev 3.434
Gaus mean : 30.1092
Gaus Width: 3.4570
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

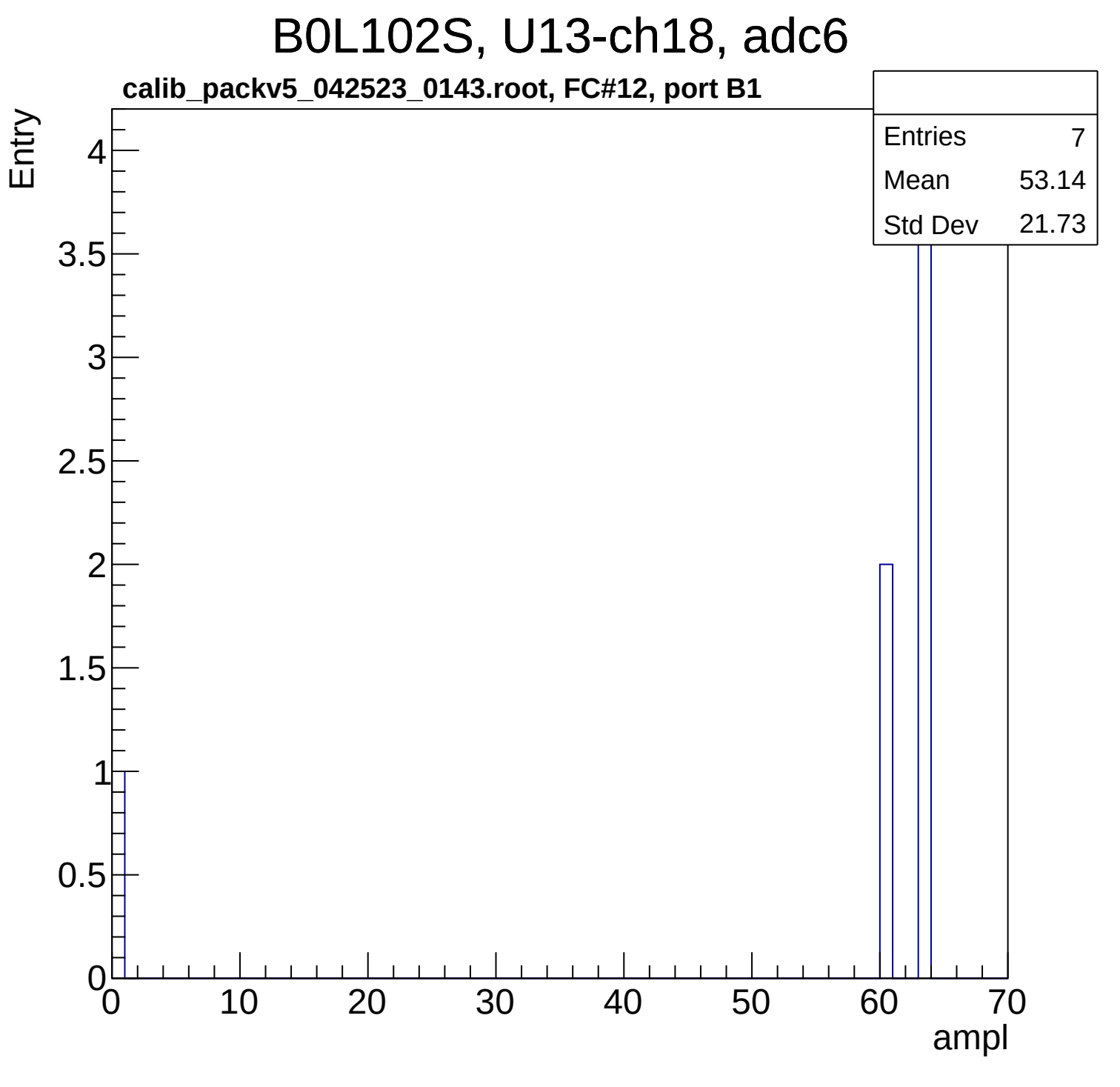

B0L102S, U13-ch18, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.31
Std Dev 3.395
Gaus mean : 36.7164
Gaus Width: 3.7846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

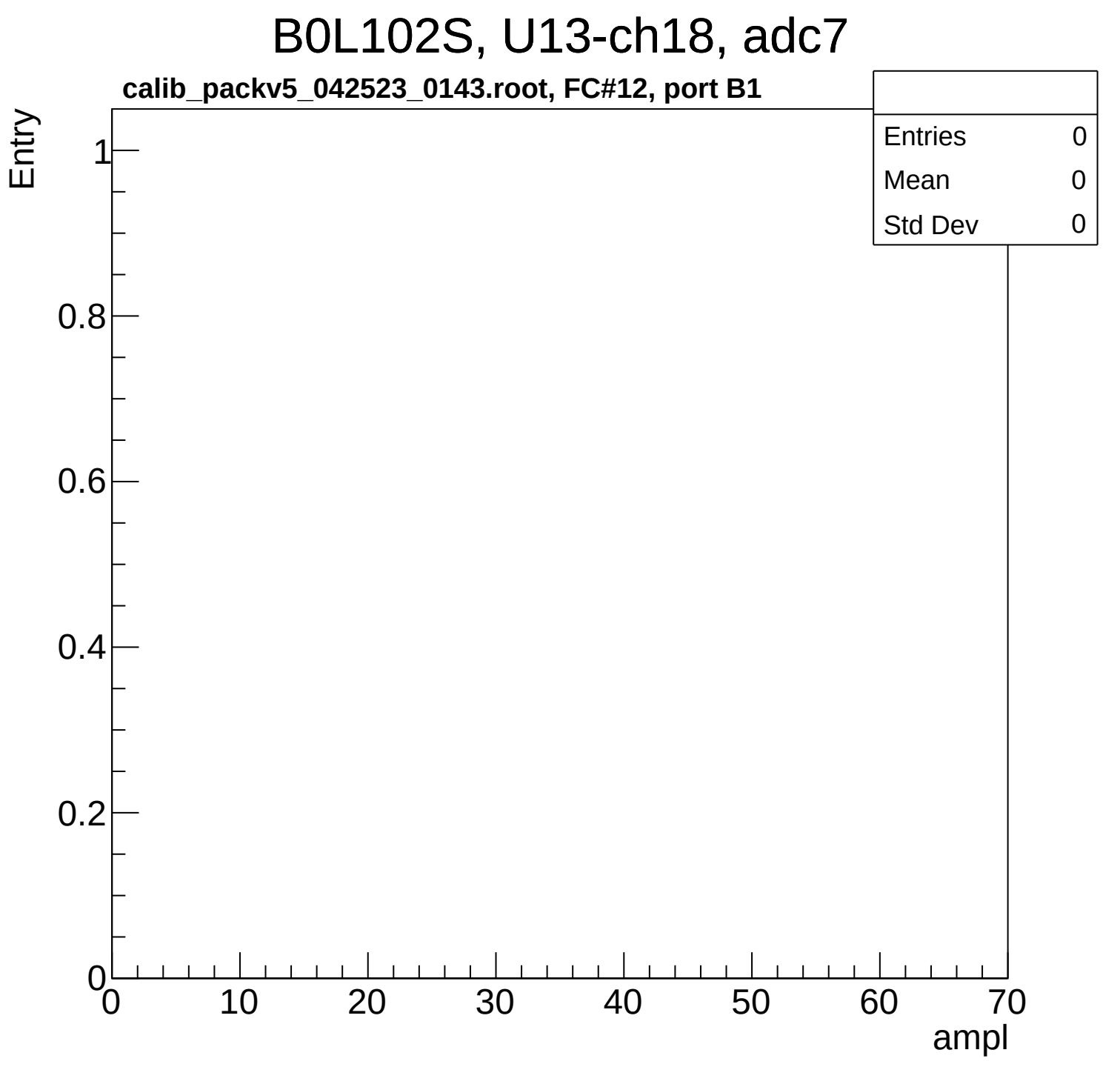

B0L102S, U13-ch18, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 42.68
Std Dev 3.503
Gaus mean : 43.5476
Gaus Width: 3.4371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

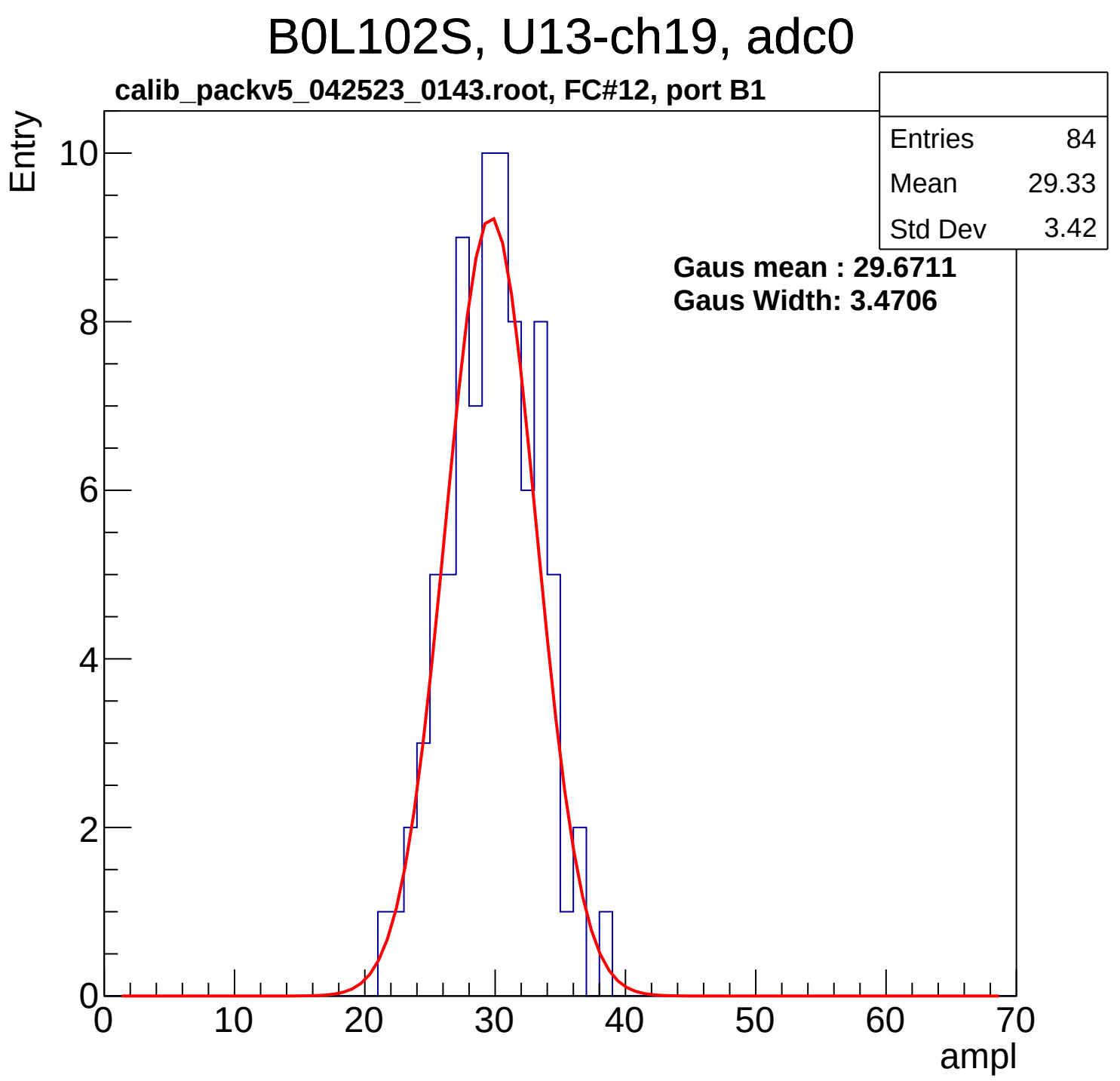

B0L102S, U13-ch18, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 49.91
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

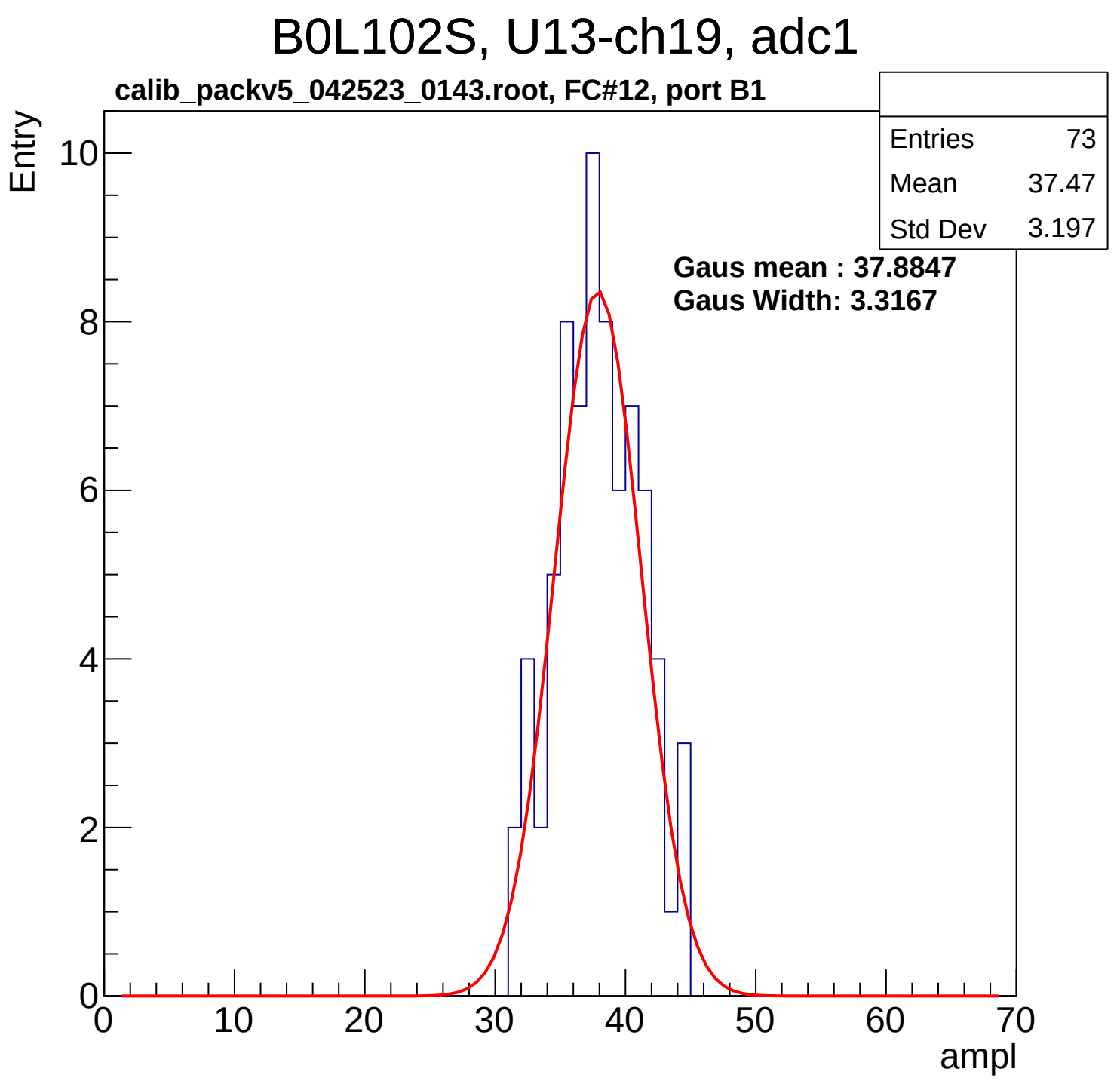

B0L102S, U13-ch18, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 54.7
Std Dev 3.257
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

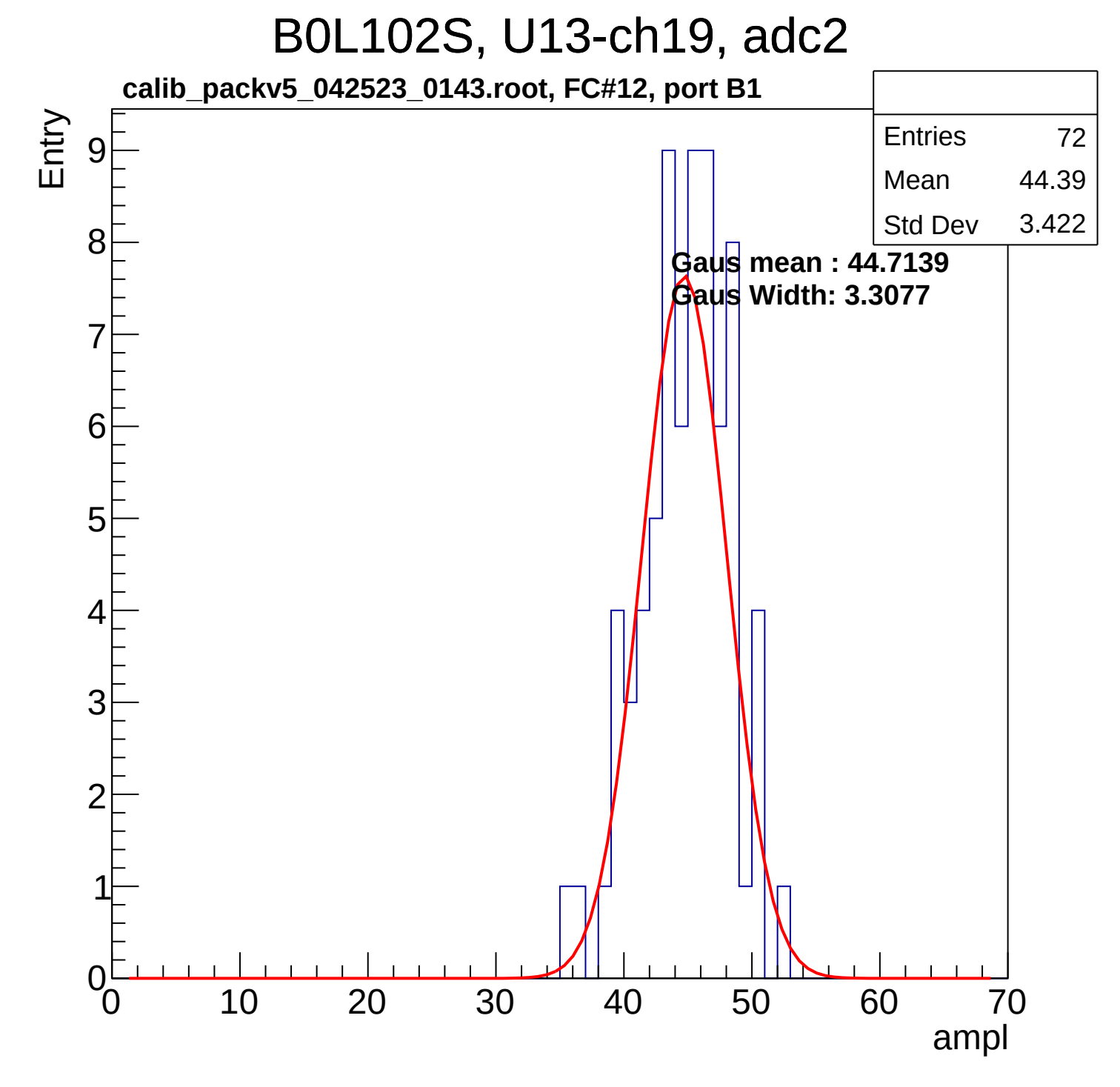

B0L102S, U13-ch18, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.32
Std Dev 2.696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch18, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.14
Std Dev 21.73
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch18, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch19, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 29.33
Std Dev 3.42
Gaus mean : 29.6711
Gaus Width: 3.4706
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch19, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 37.47
Std Dev 3.197
Gaus mean : 37.8847
Gaus Width: 3.3167
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch19, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 44.39
Std Dev 3.422
Gaus mean : 44.7139
Gaus Width: 3.3077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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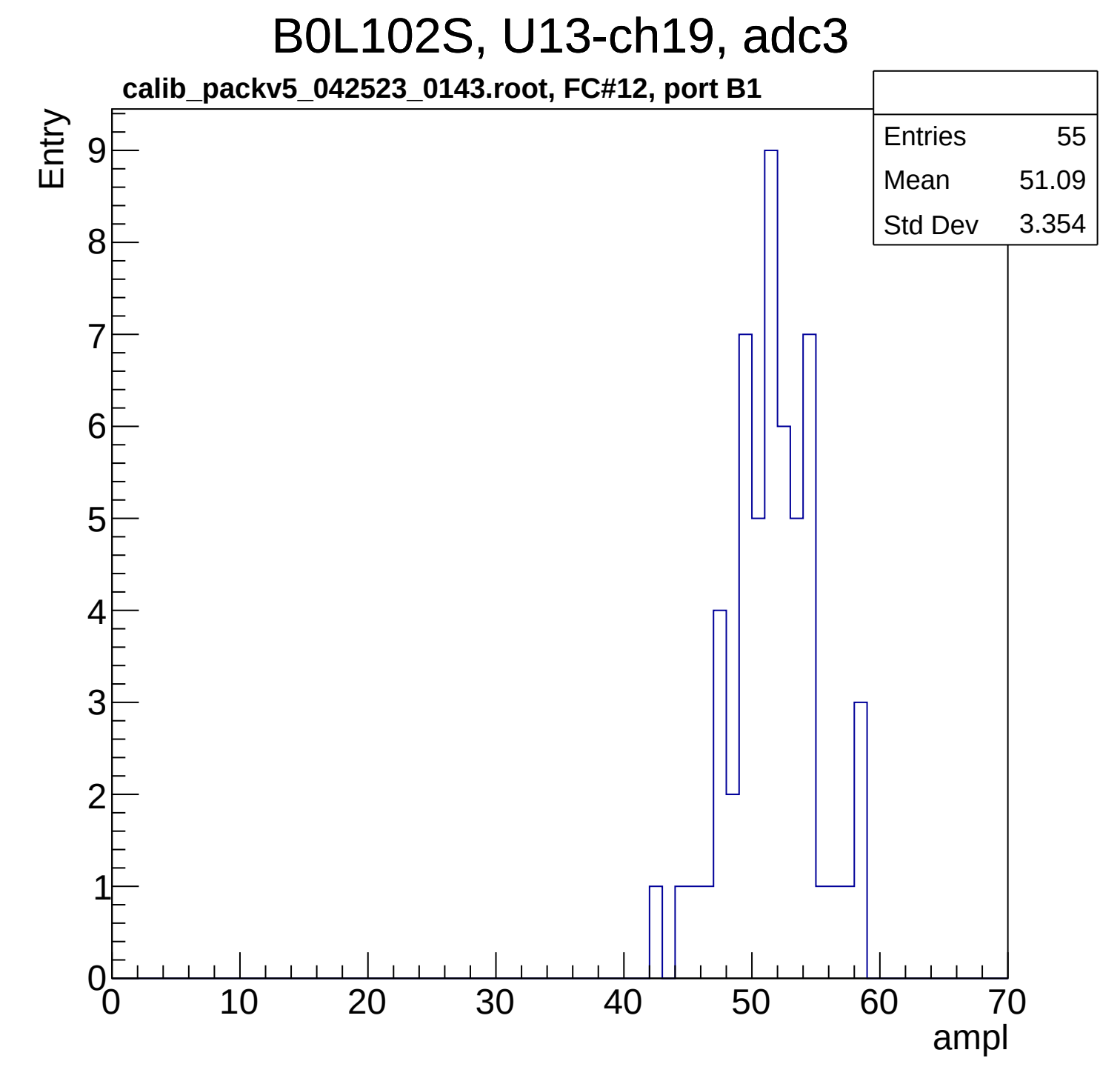
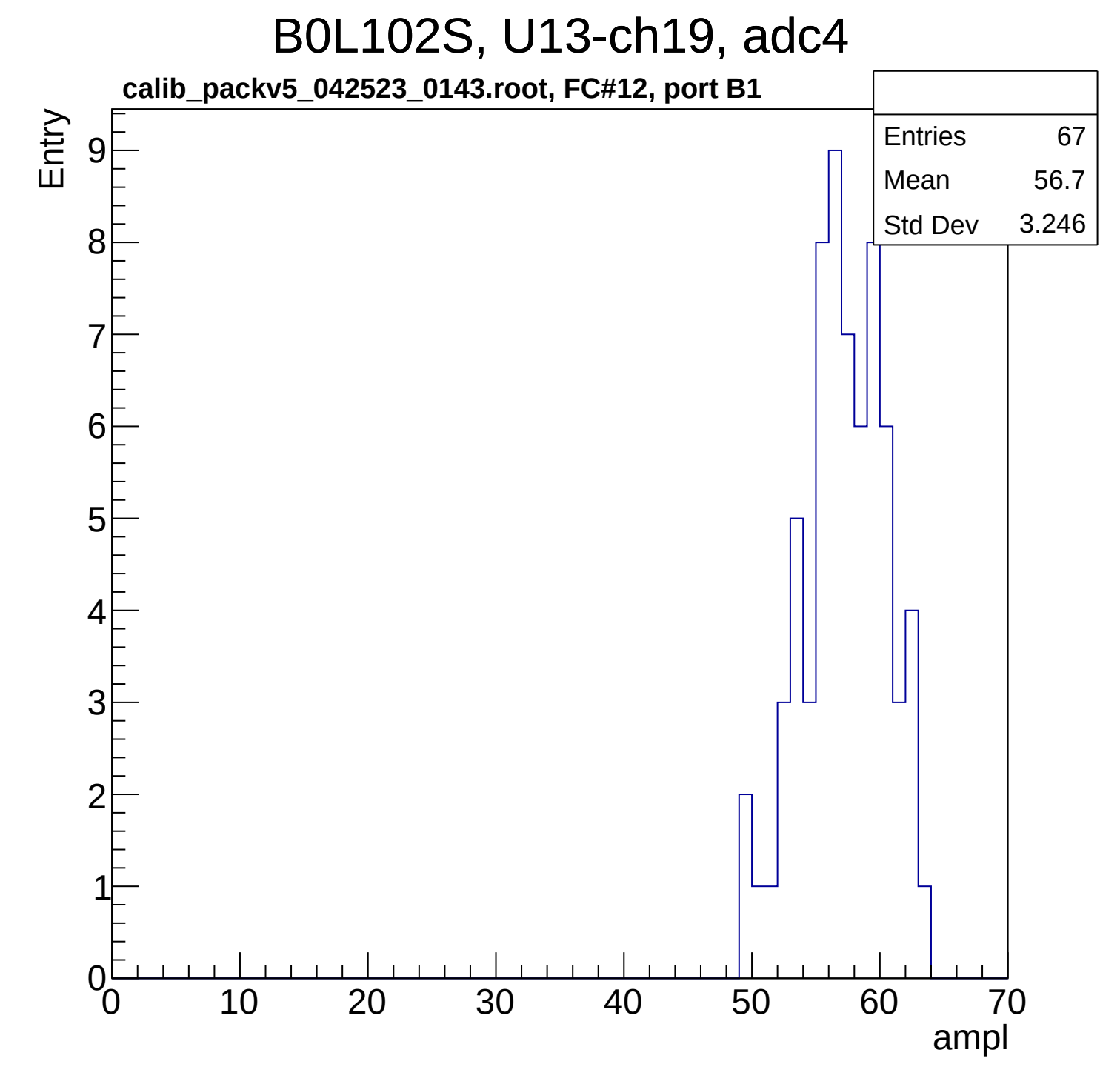
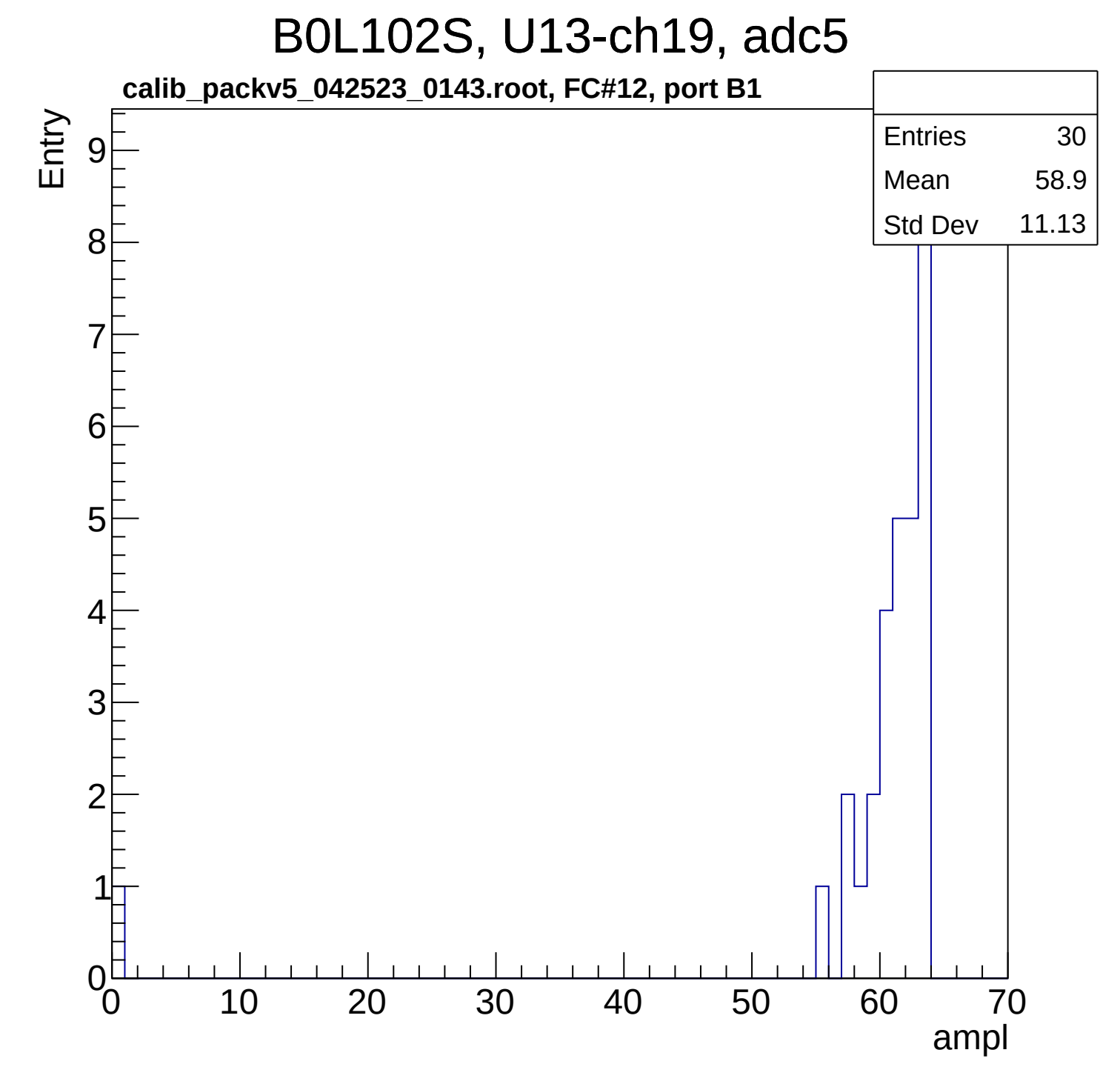
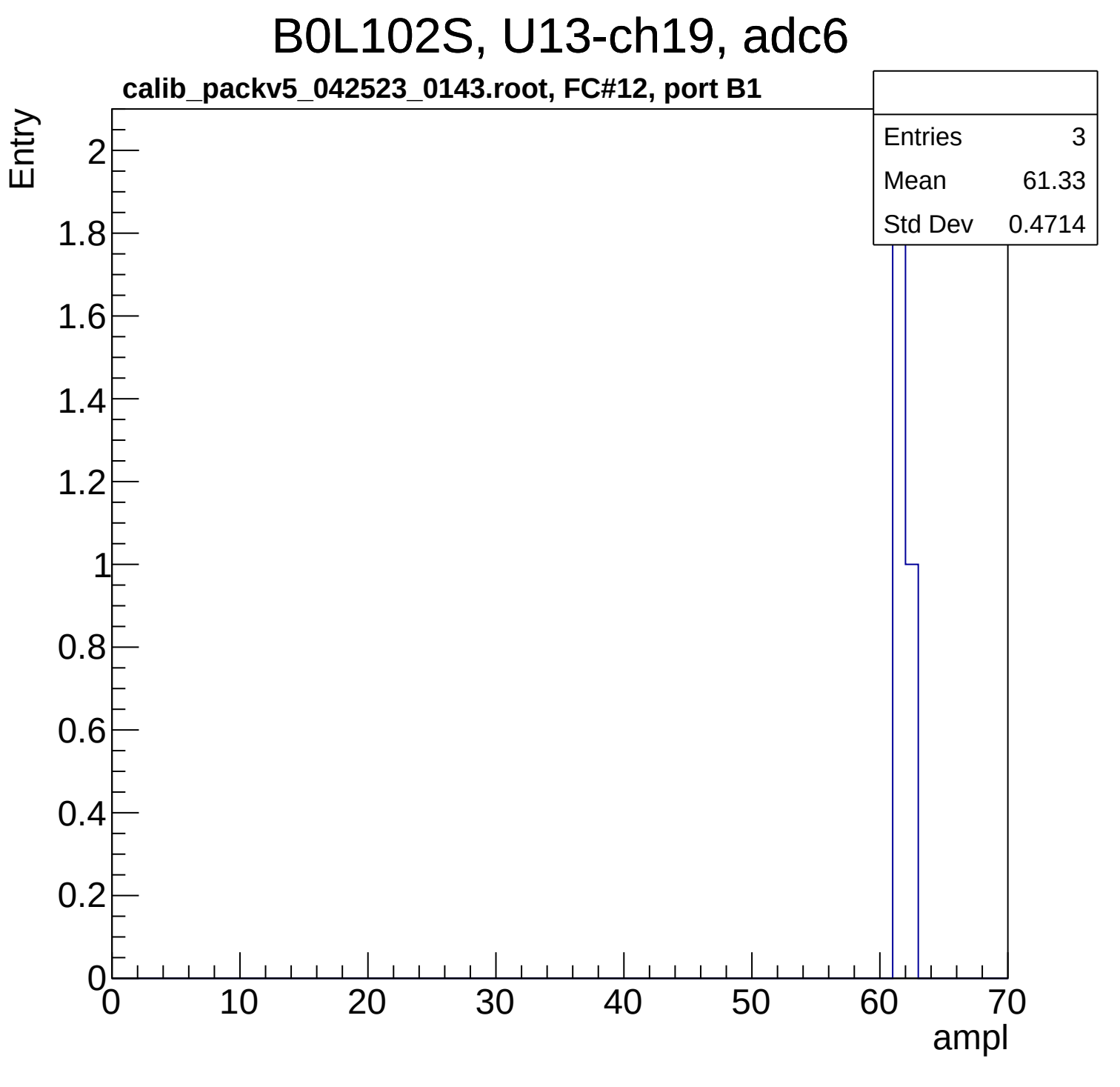
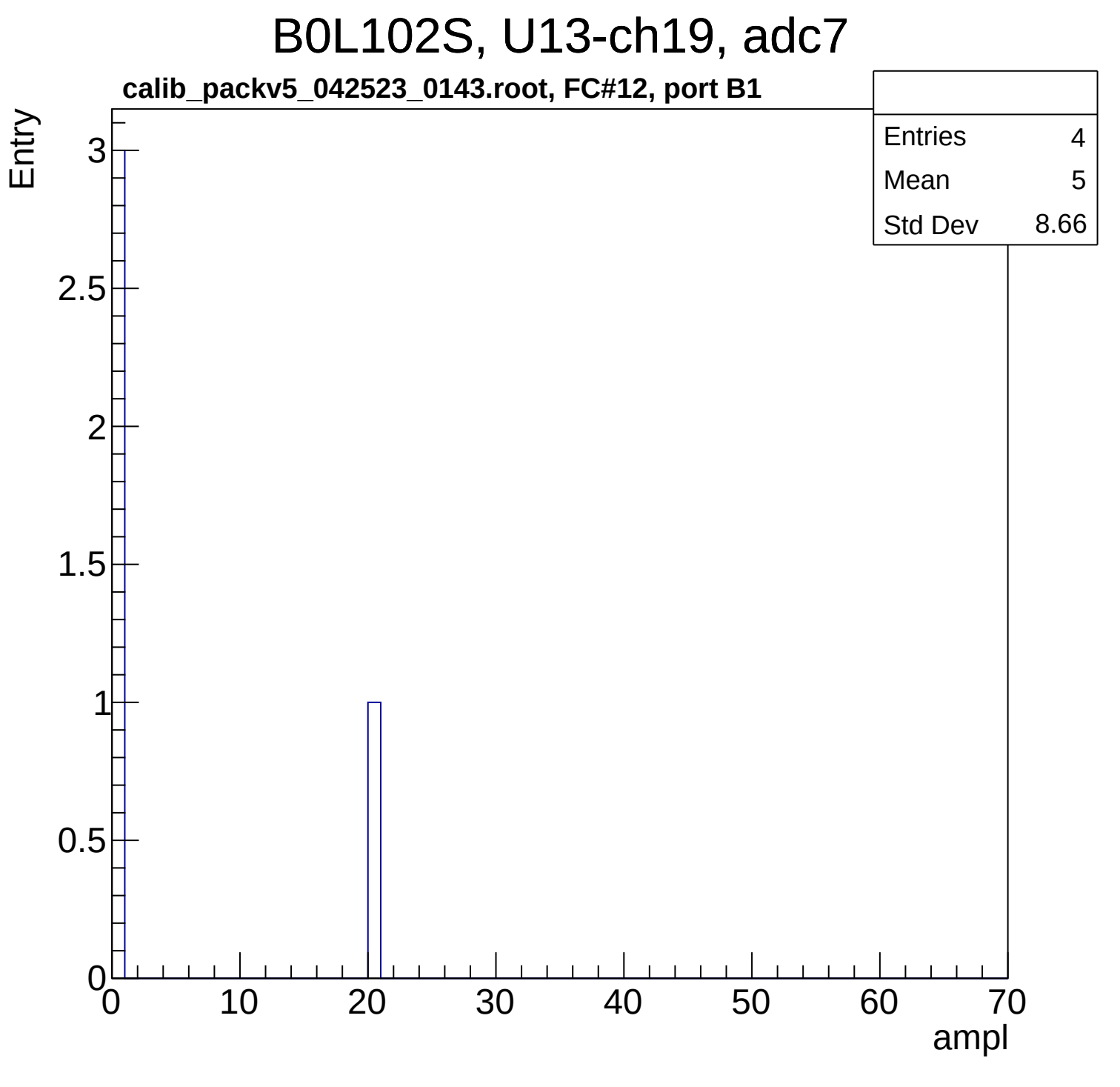
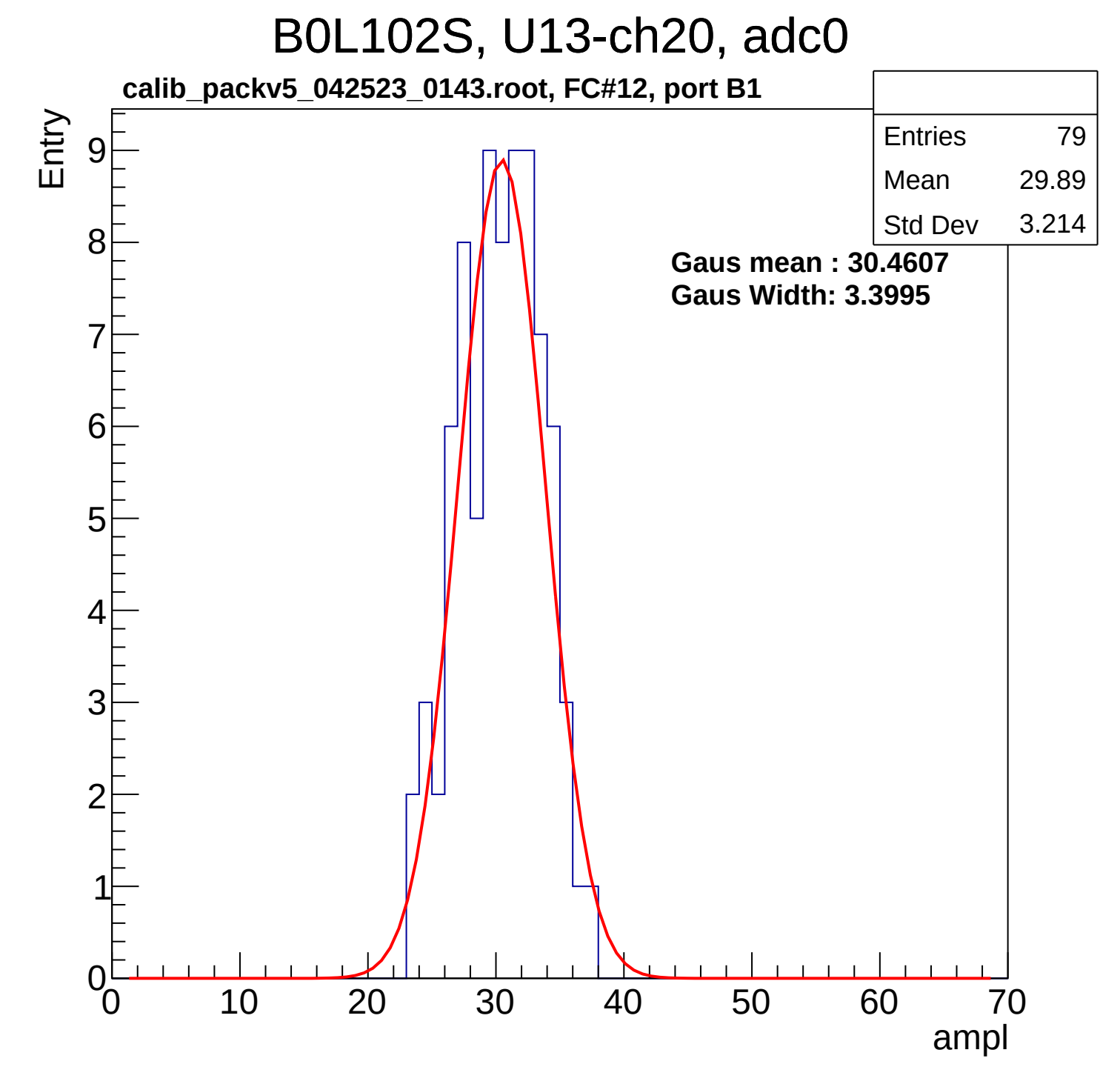

B0L102S, U13-ch19, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 51.09
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

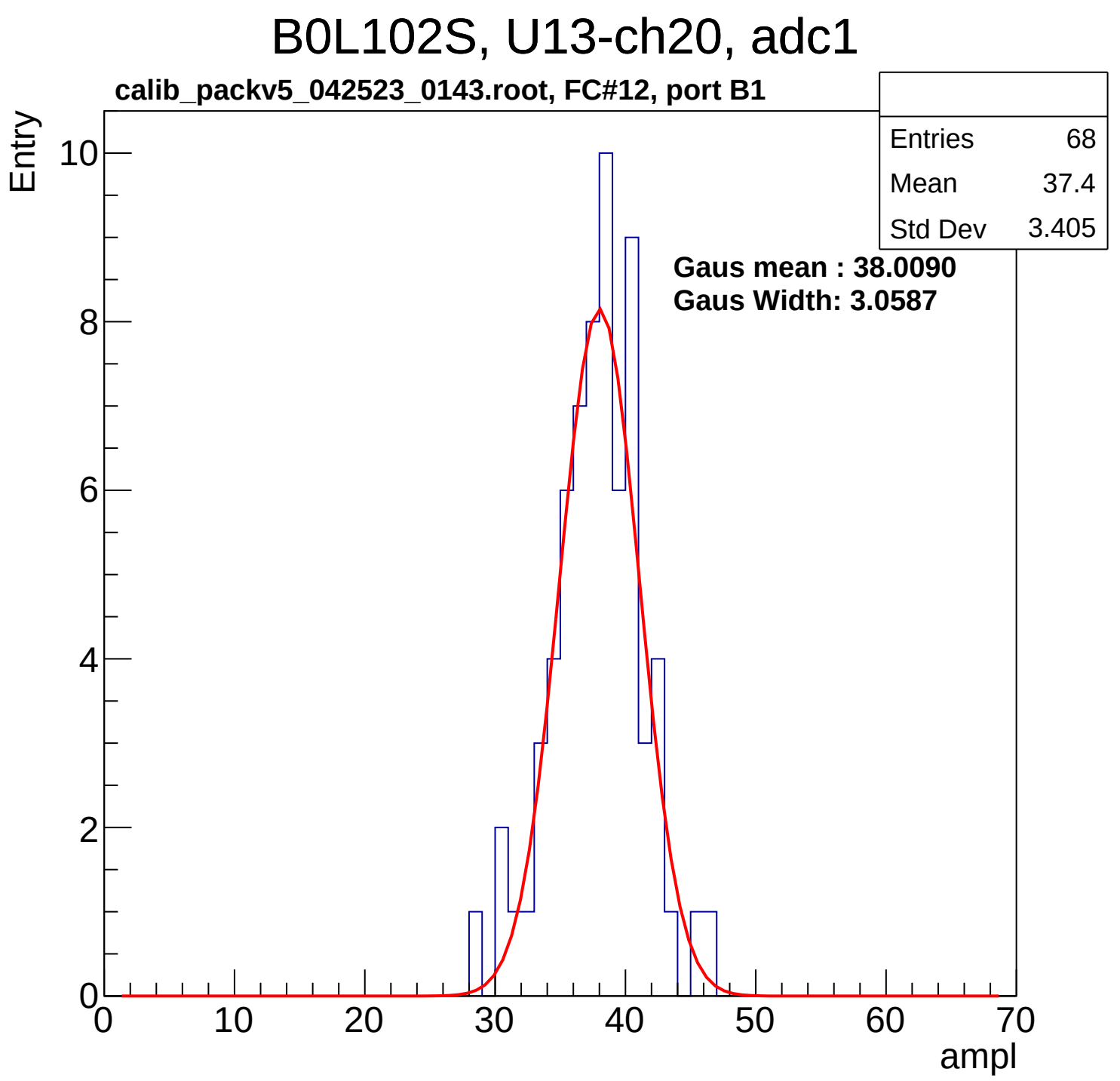

B0L102S, U13-ch19, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 56.7
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

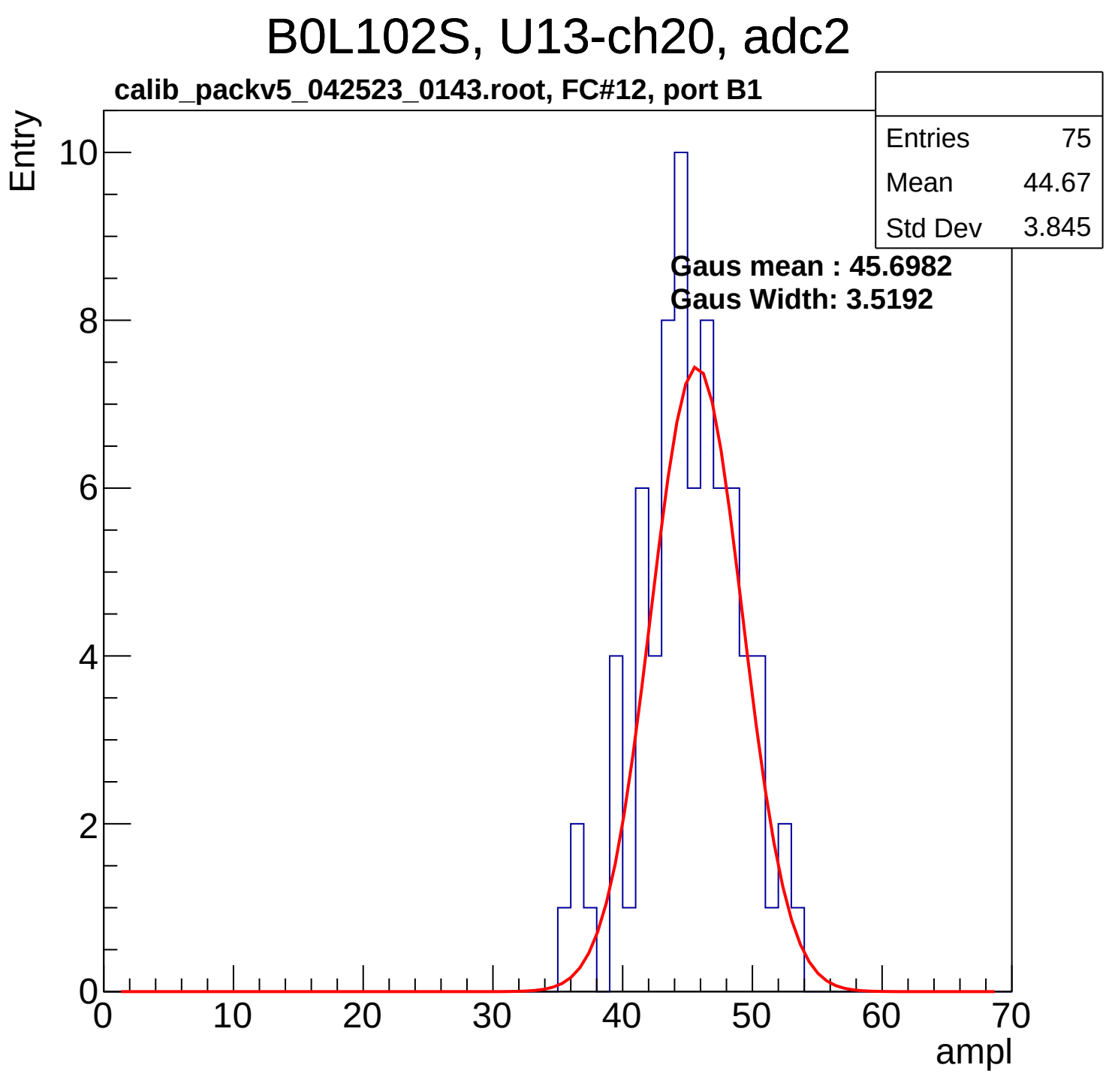

B0L102S, U13-ch19, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 58.9
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

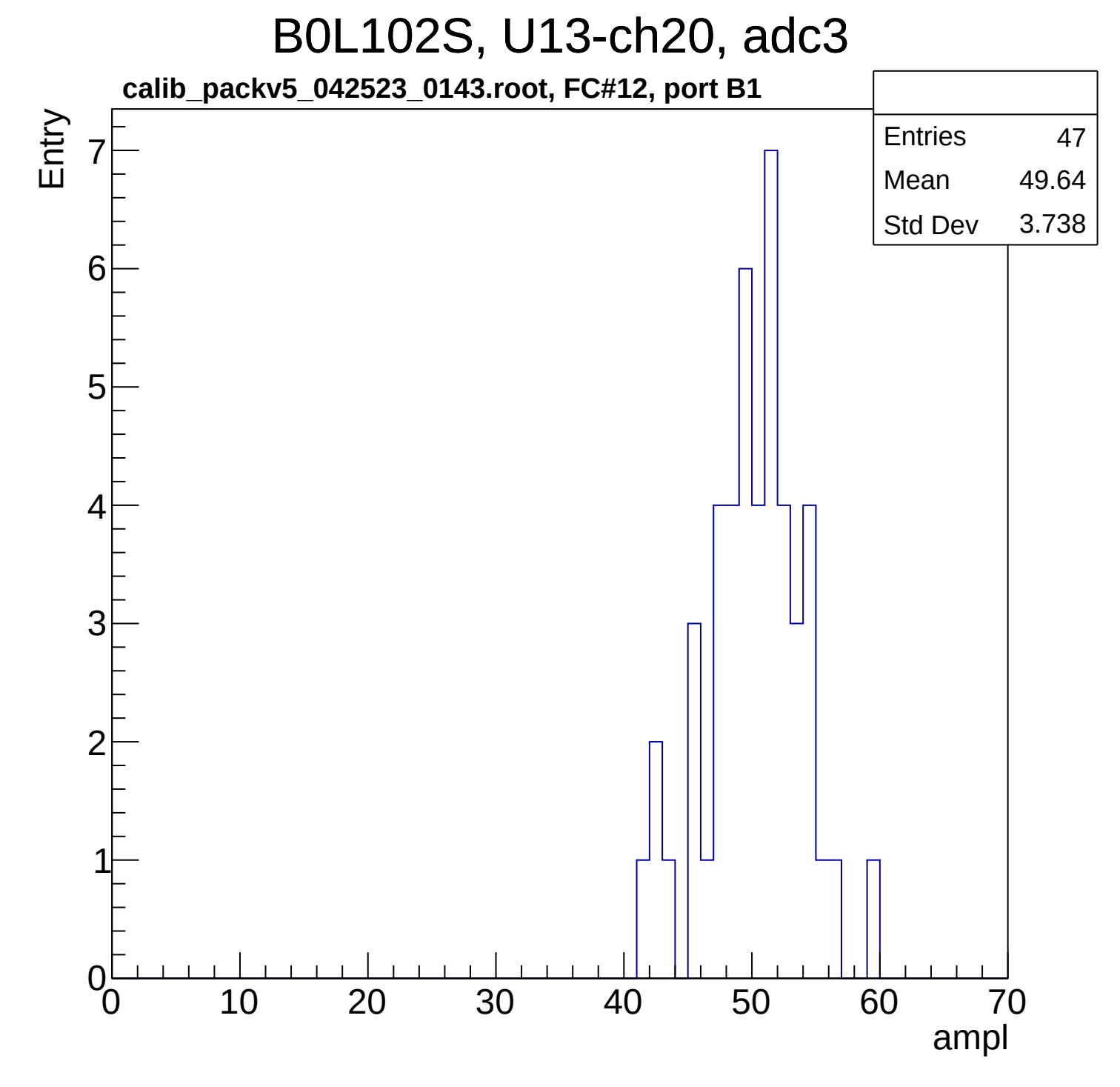

B0L102S, U13-ch19, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

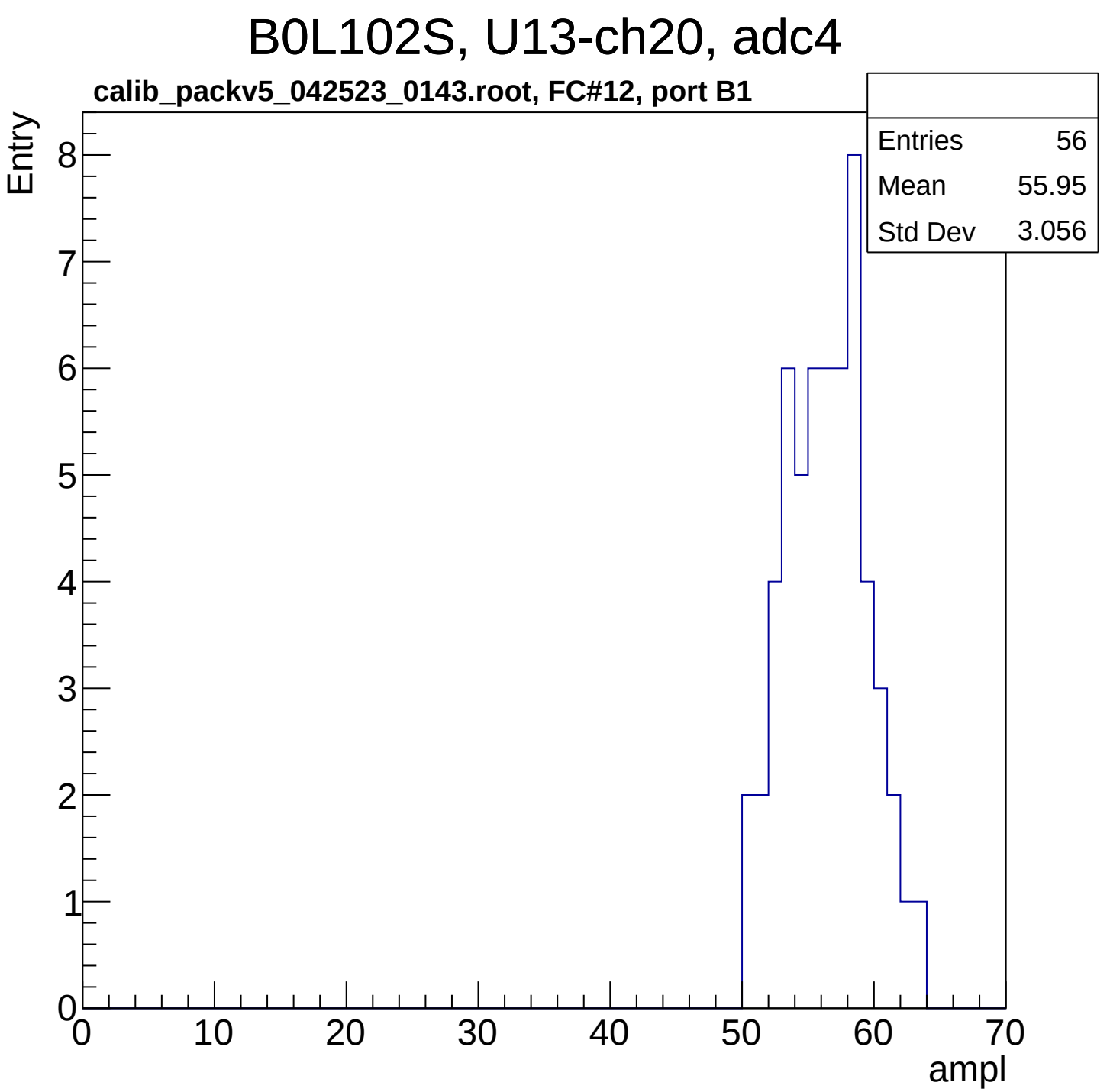

B0L102S, U13-ch19, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 5
Std Dev 8.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

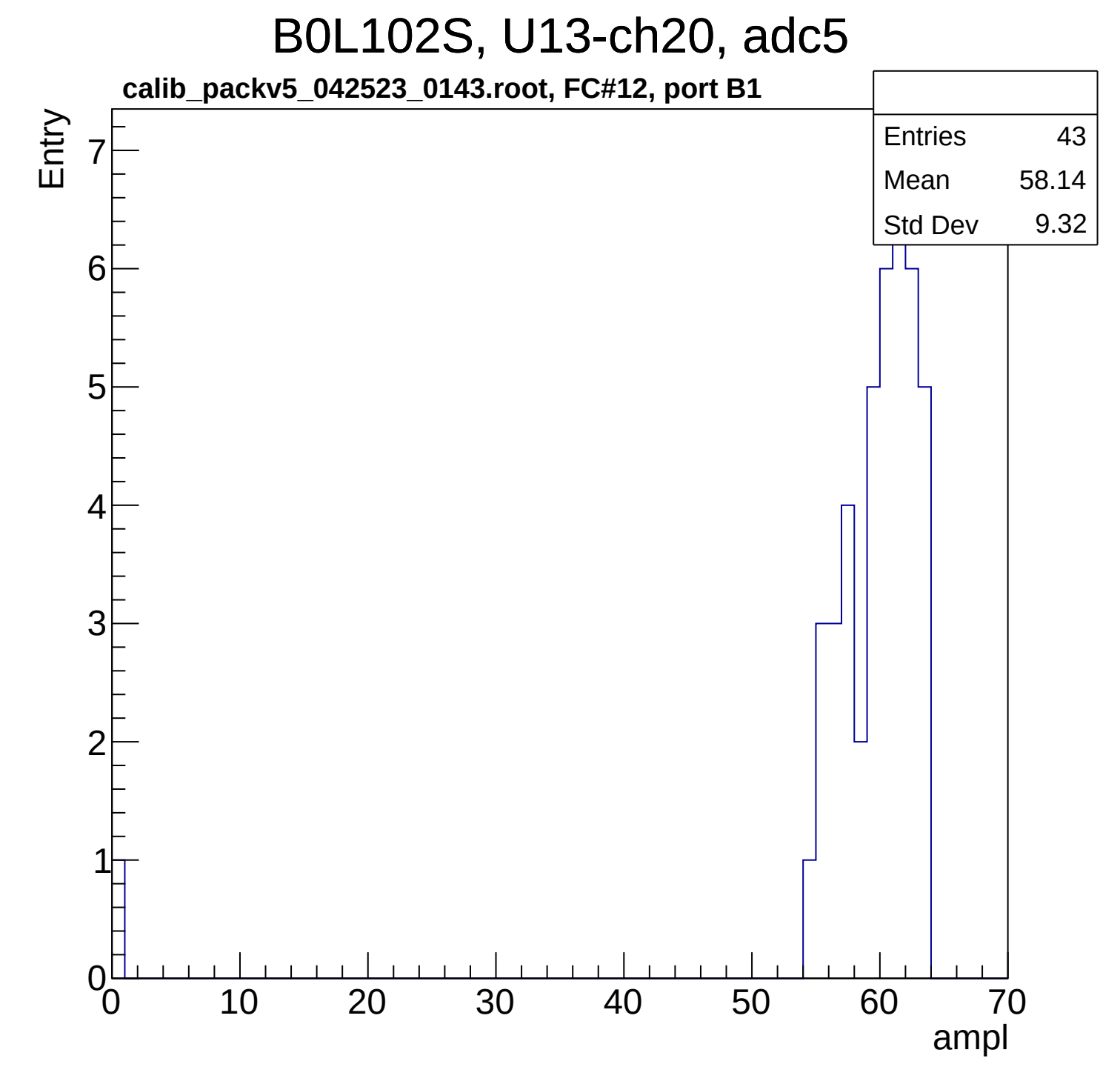

B0L102S, U13-ch20, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 29.89
Std Dev 3.214
Gaus mean : 30.4607
Gaus Width: 3.3995
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

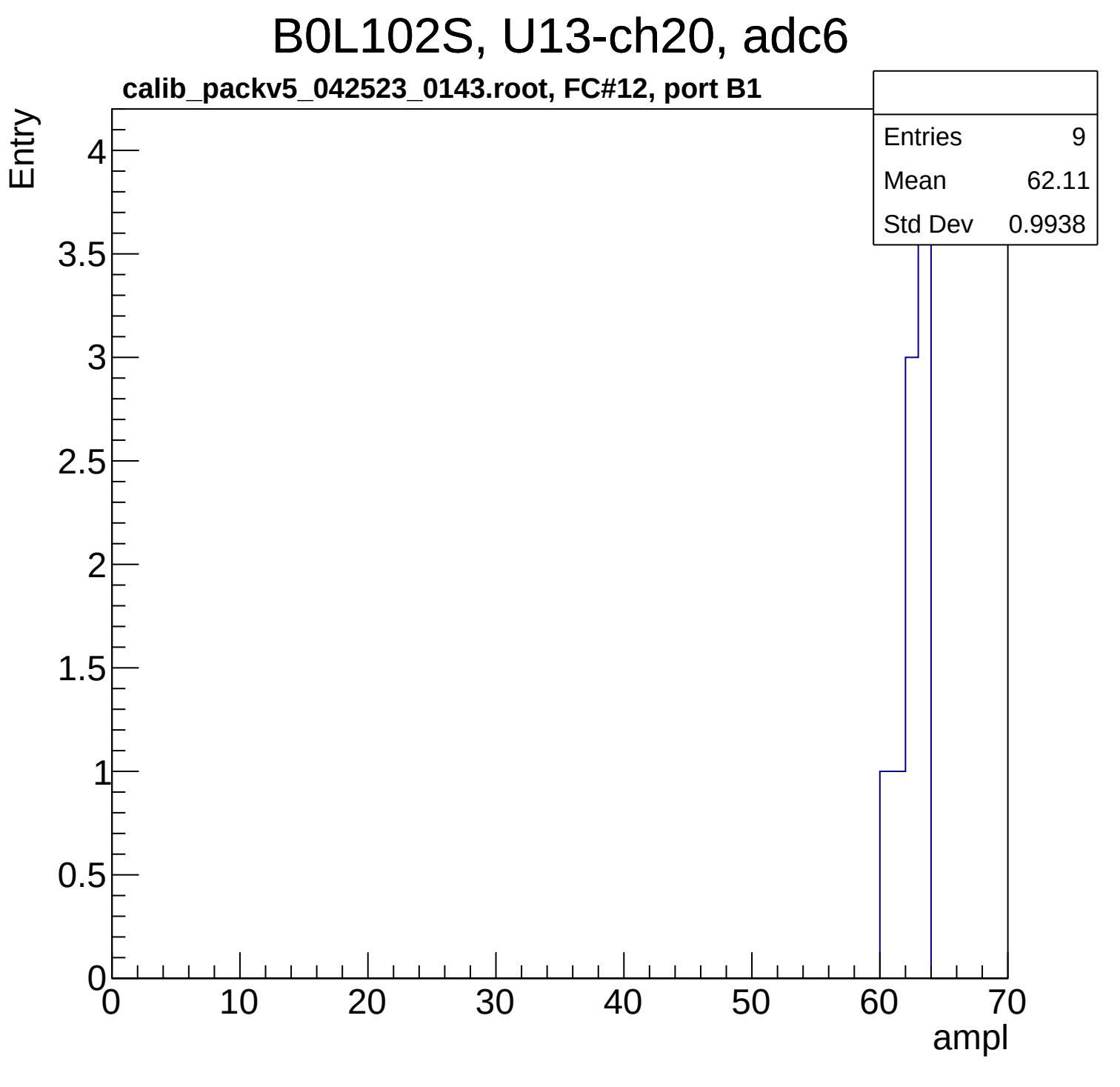

B0L102S, U13-ch20, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 37.4
Std Dev 3.405
Gaus mean : 38.0090
Gaus Width: 3.0587
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

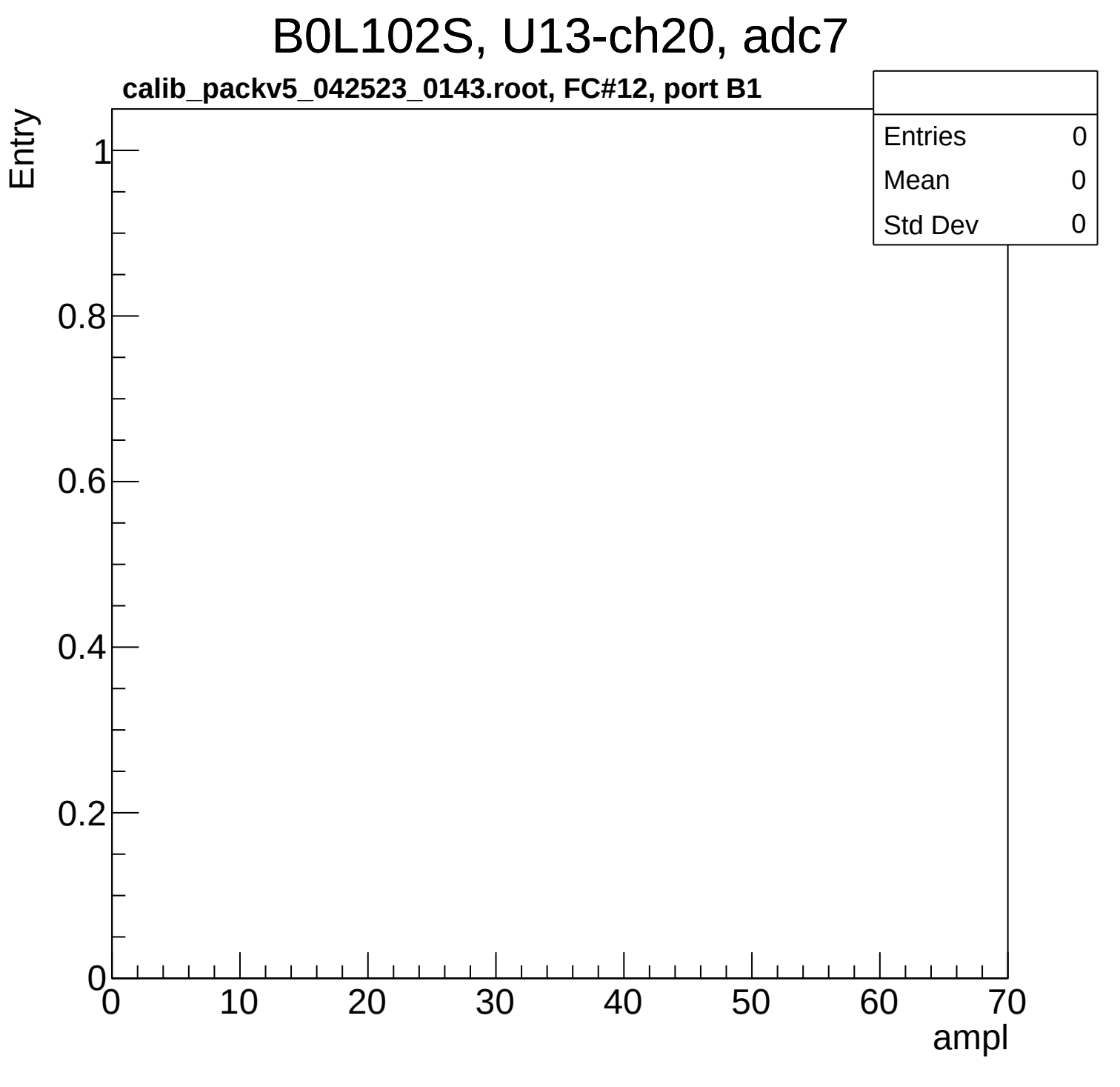

B0L102S, U13-ch20, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 44.67
Std Dev 3.845
Gaus mean : 45.6982
Gaus Width: 3.5192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

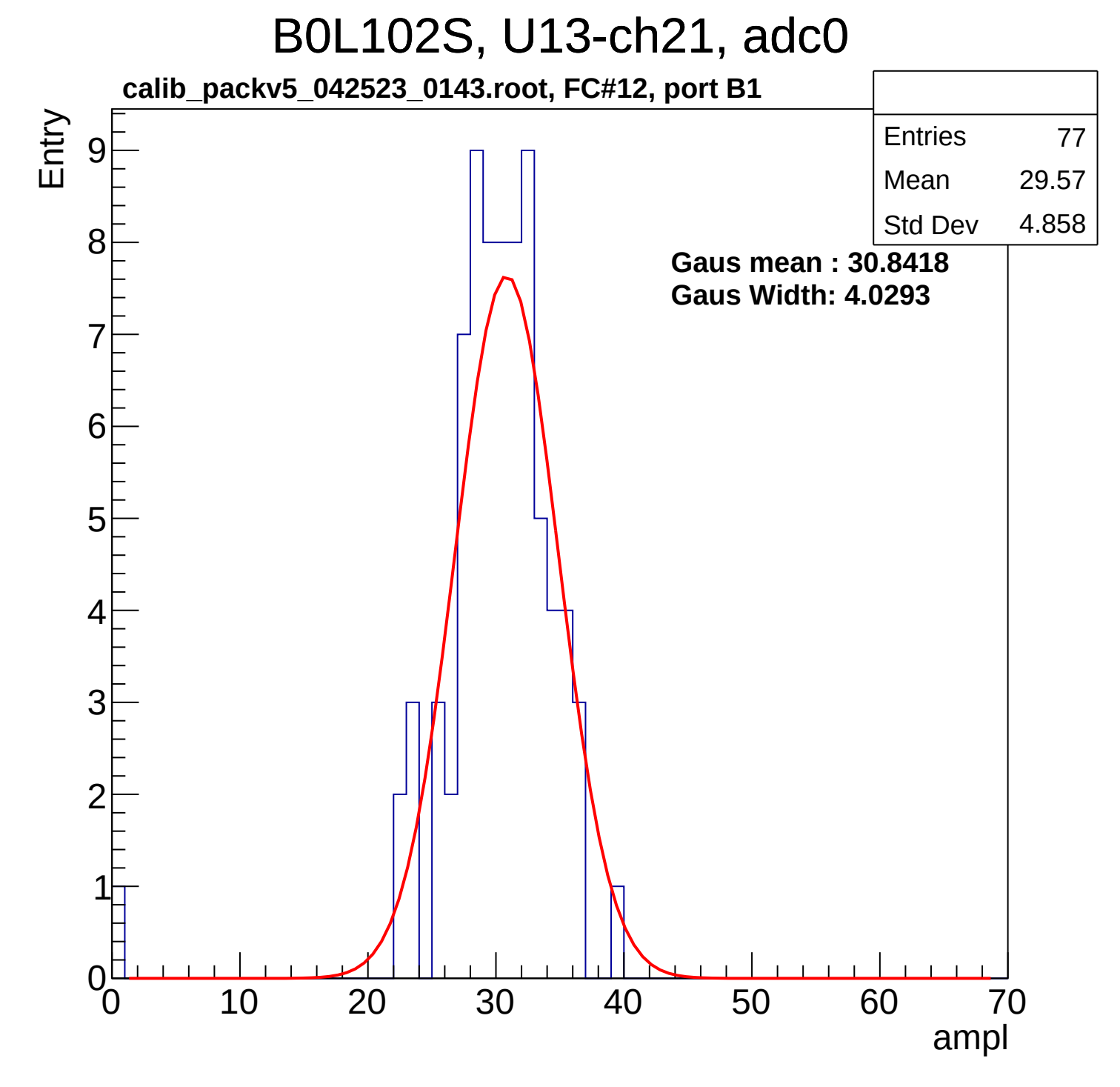

B0L102S, U13-ch20, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 49.64
Std Dev 3.738
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

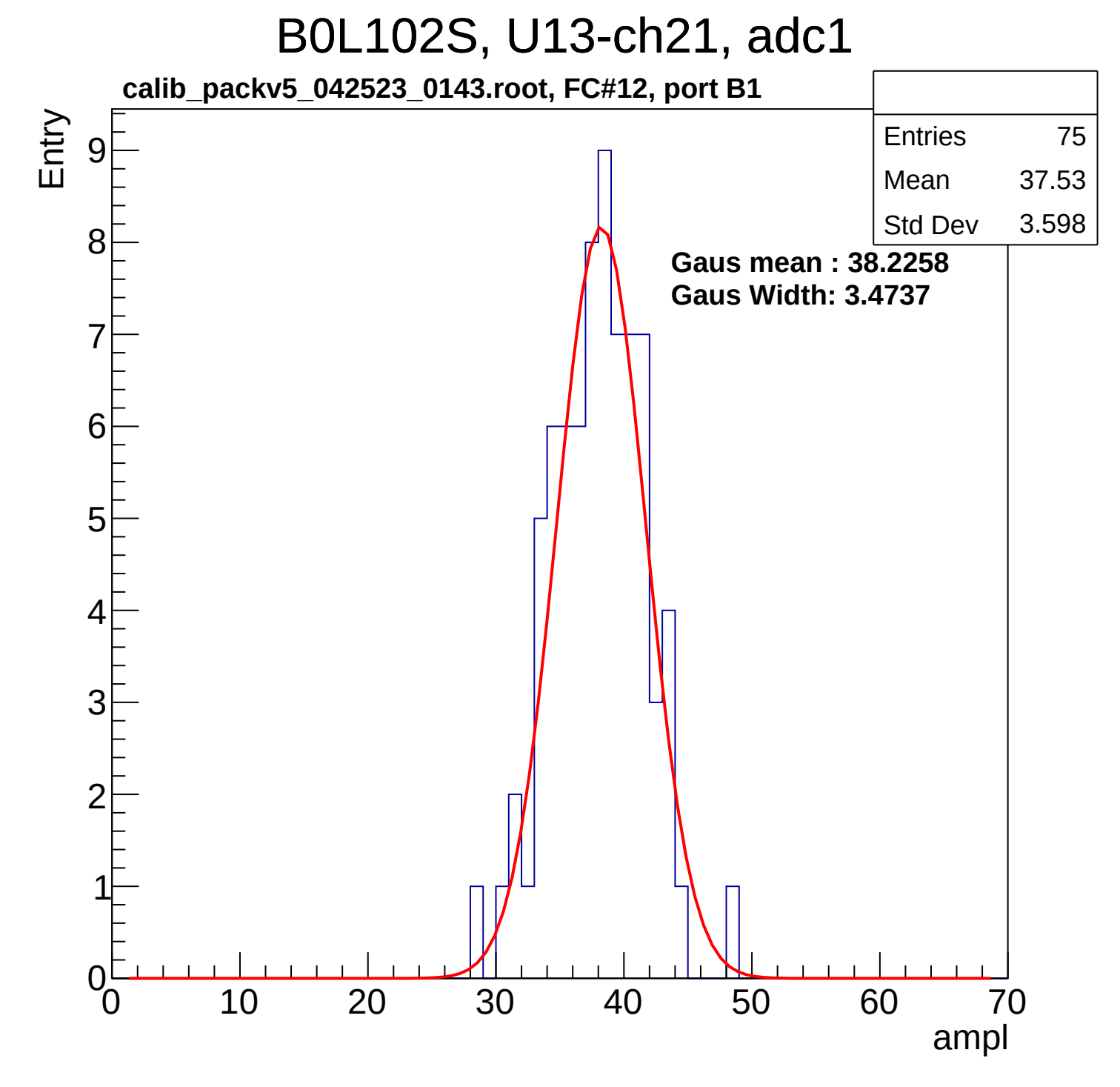

B0L102S, U13-ch20, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.95
Std Dev 3.056
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

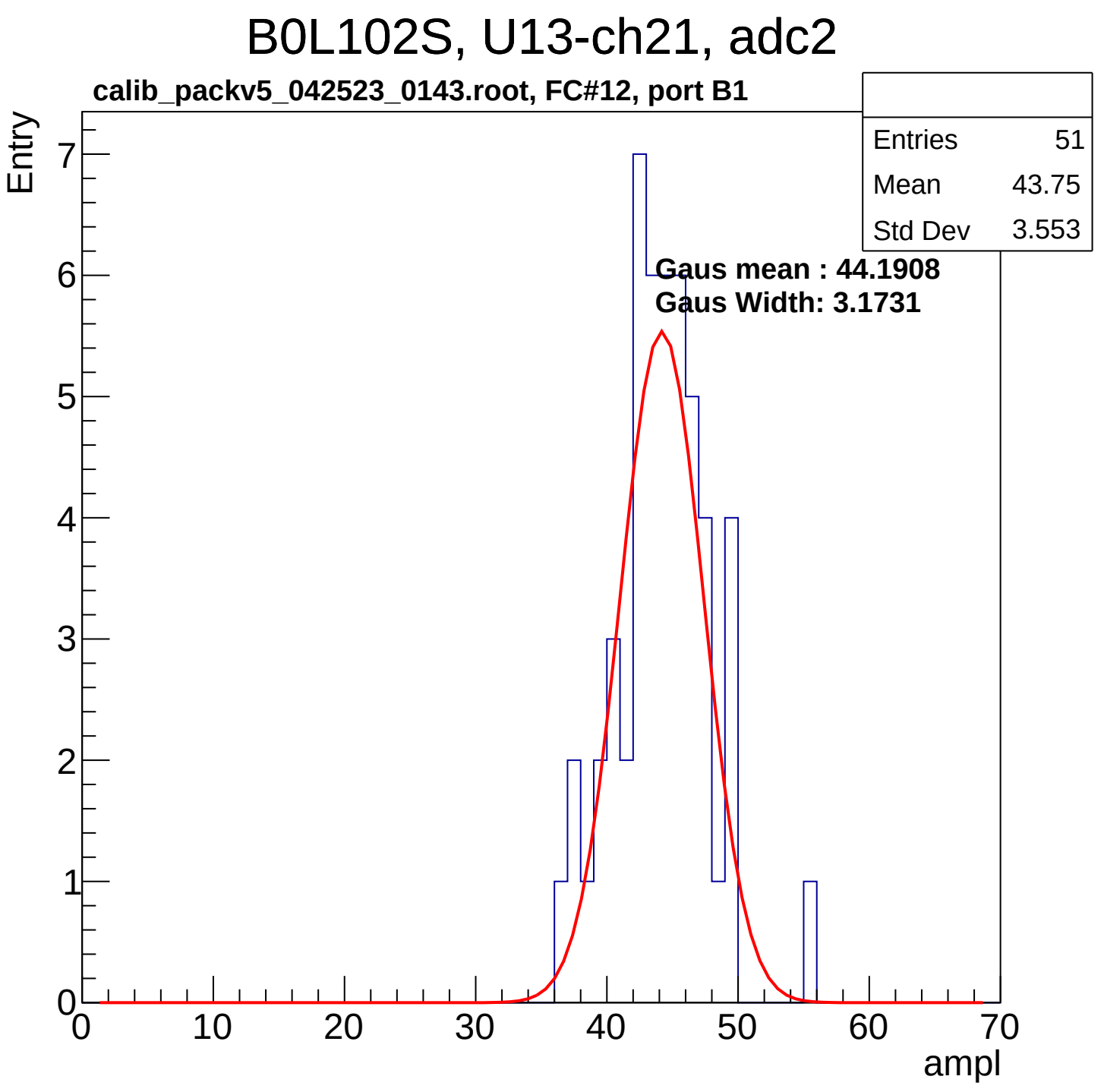

B0L102S, U13-ch20, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.14
Std Dev 9.32
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

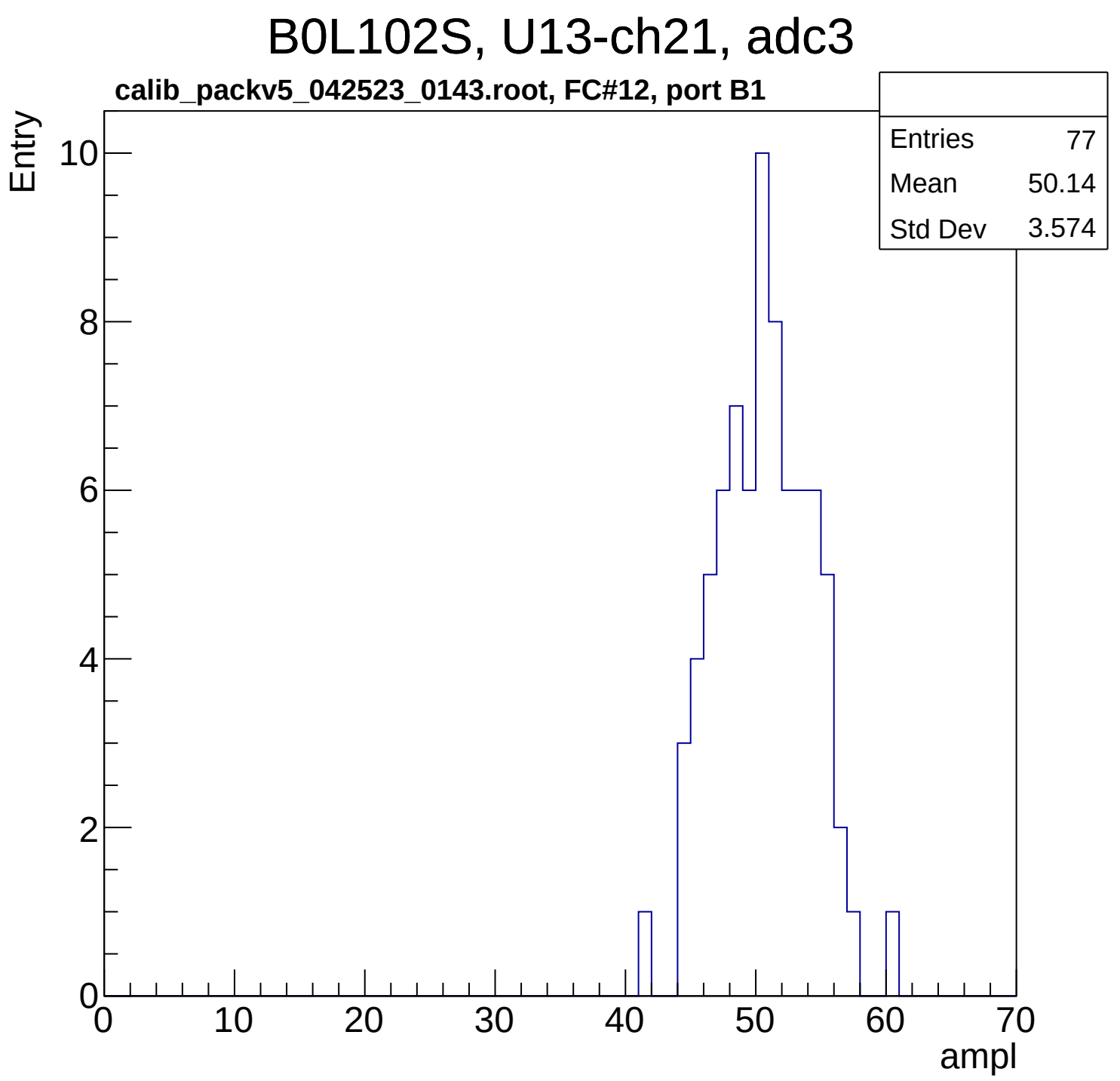

B0L102S, U13-ch20, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

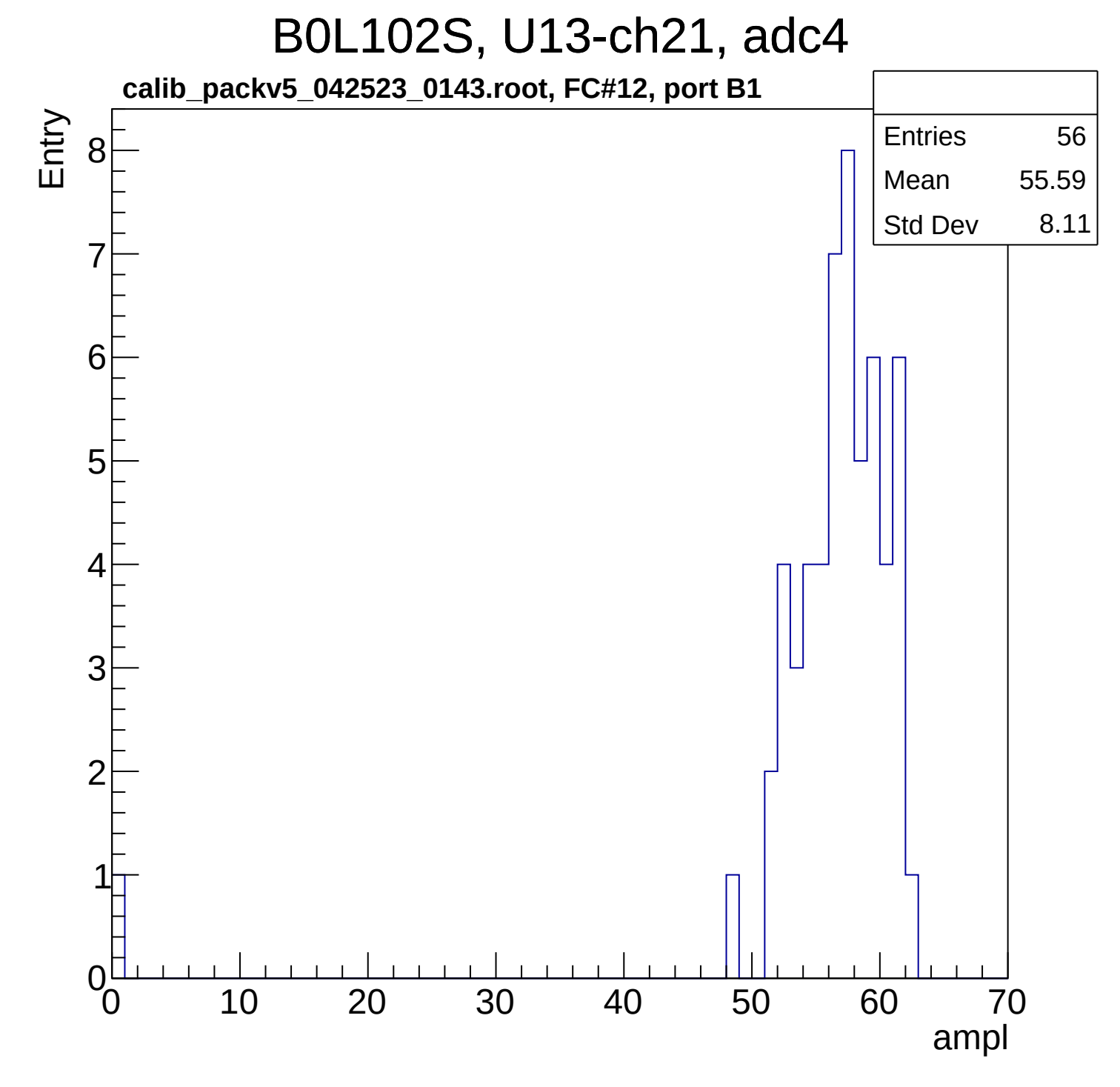

B0L102S, U13-ch20, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

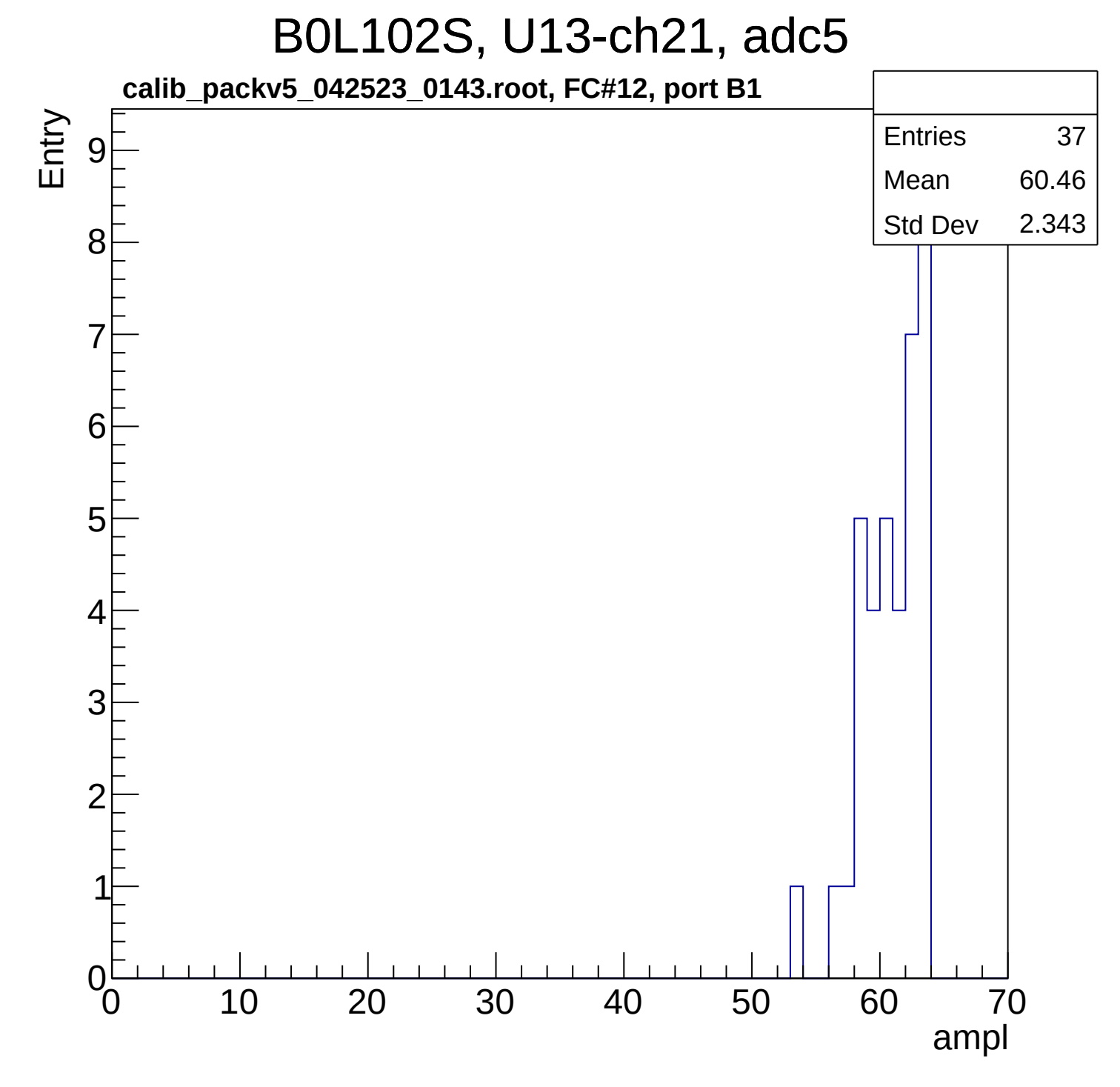

B0L102S, U13-ch21, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 29.57
Std Dev 4.858
Gaus mean : 30.8418
Gaus Width: 4.0293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch21, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.53
Std Dev 3.598
Gaus mean : 38.2258
Gaus Width: 3.4737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch21, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 43.75
Std Dev 3.553
Gaus mean : 44.1908
Gaus Width: 3.1731
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch21, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 50.14
Std Dev 3.574
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch21, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.59
Std Dev 8.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch21, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 60.46
Std Dev 2.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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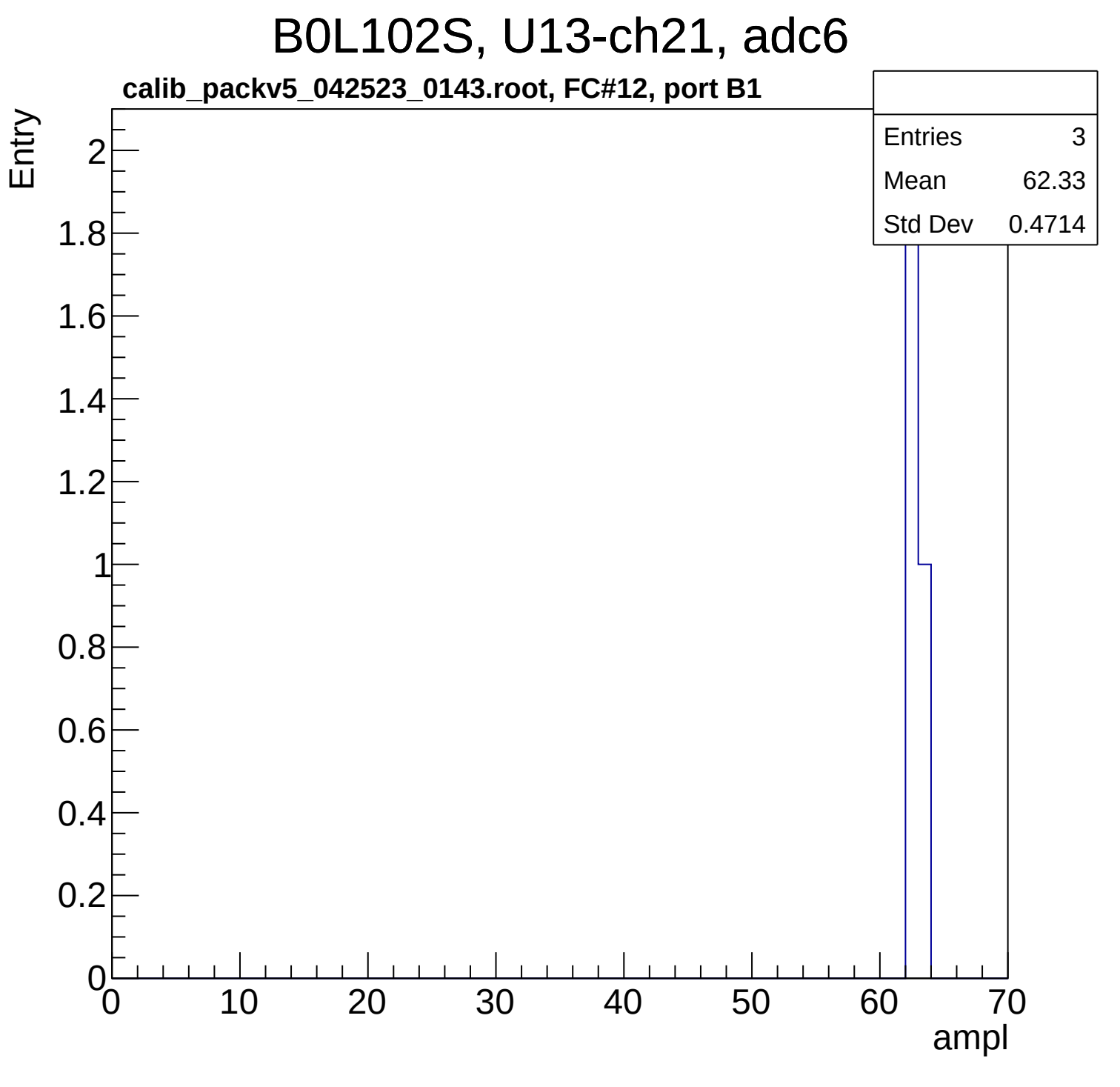
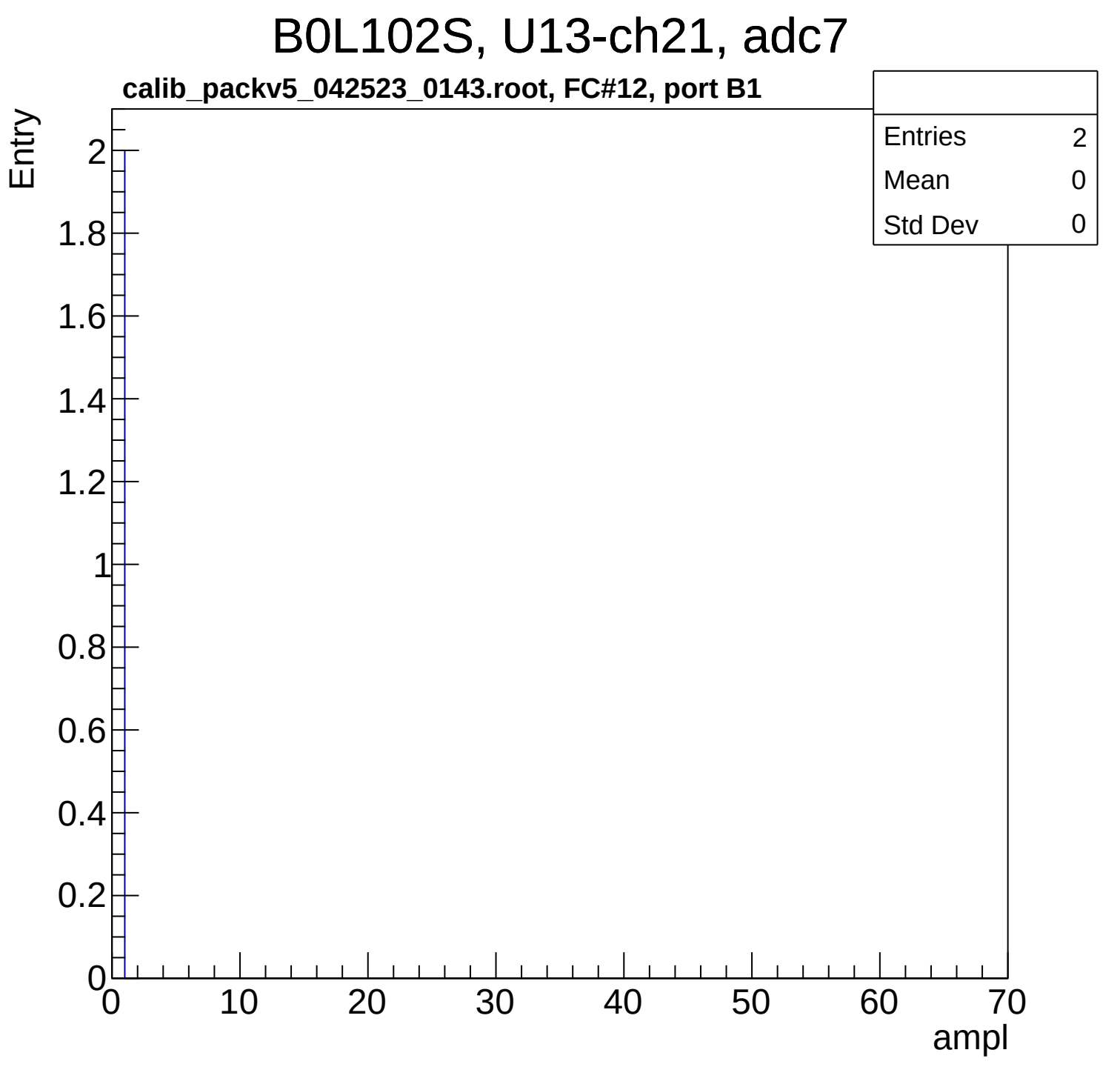
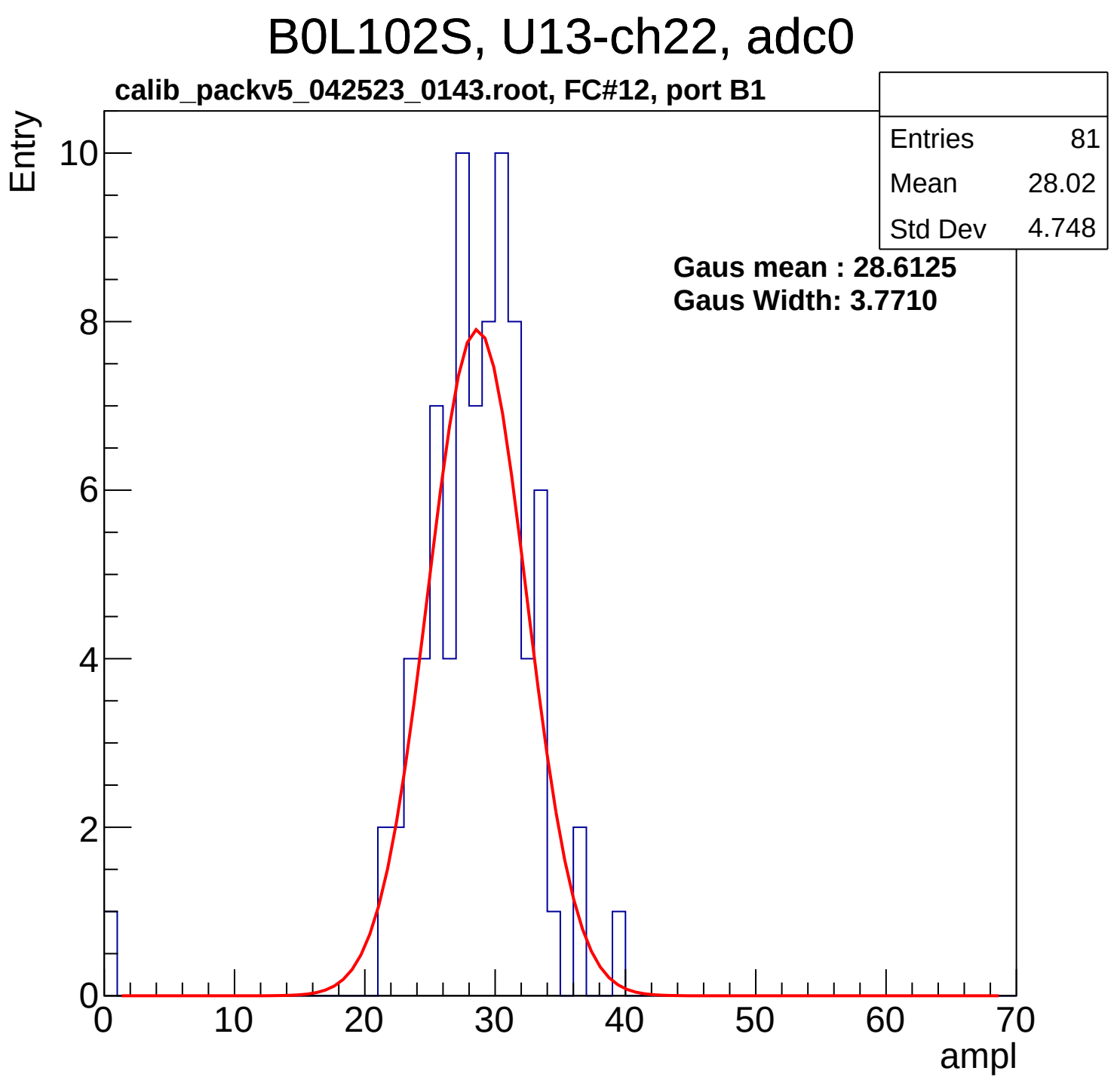
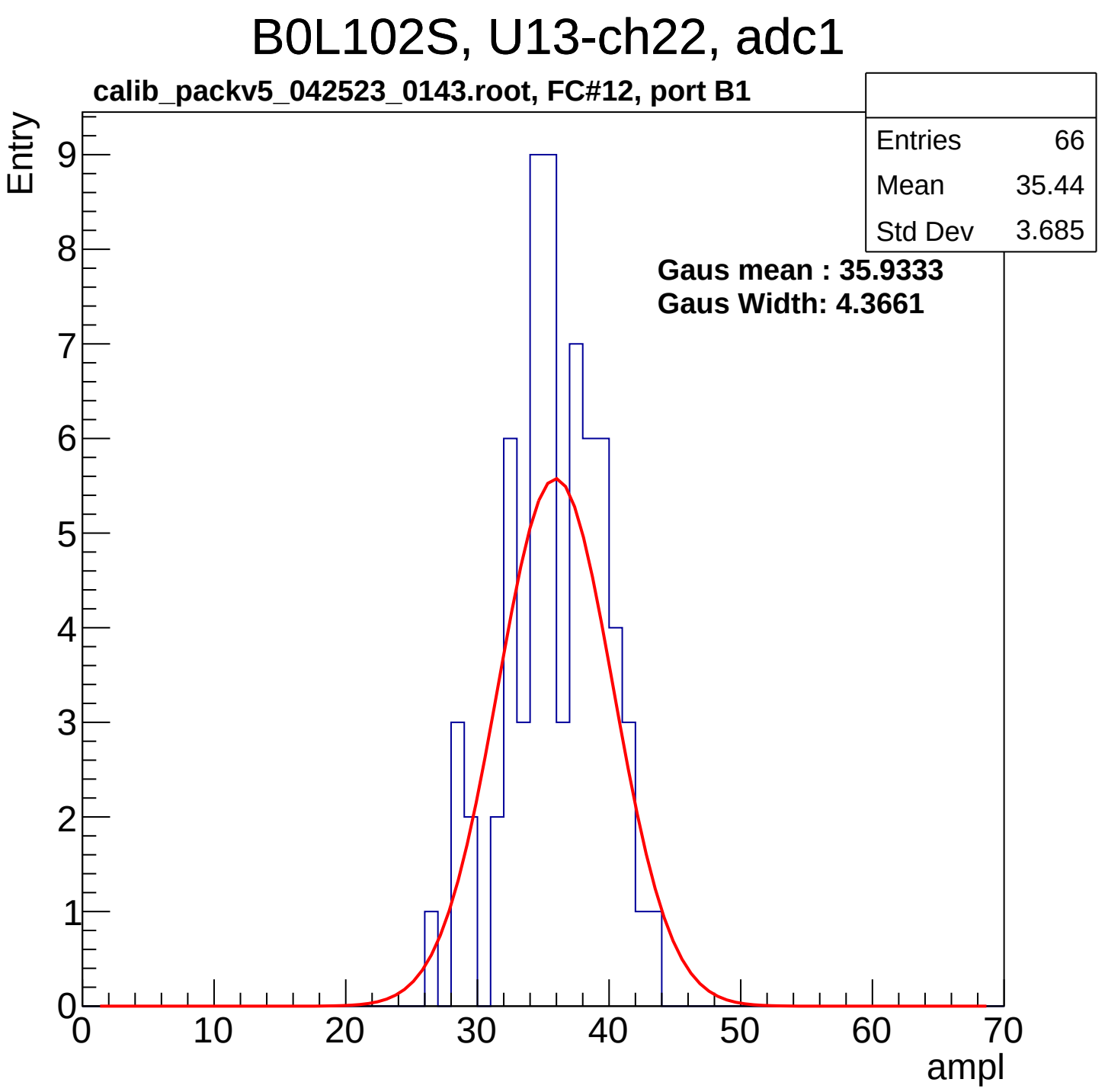
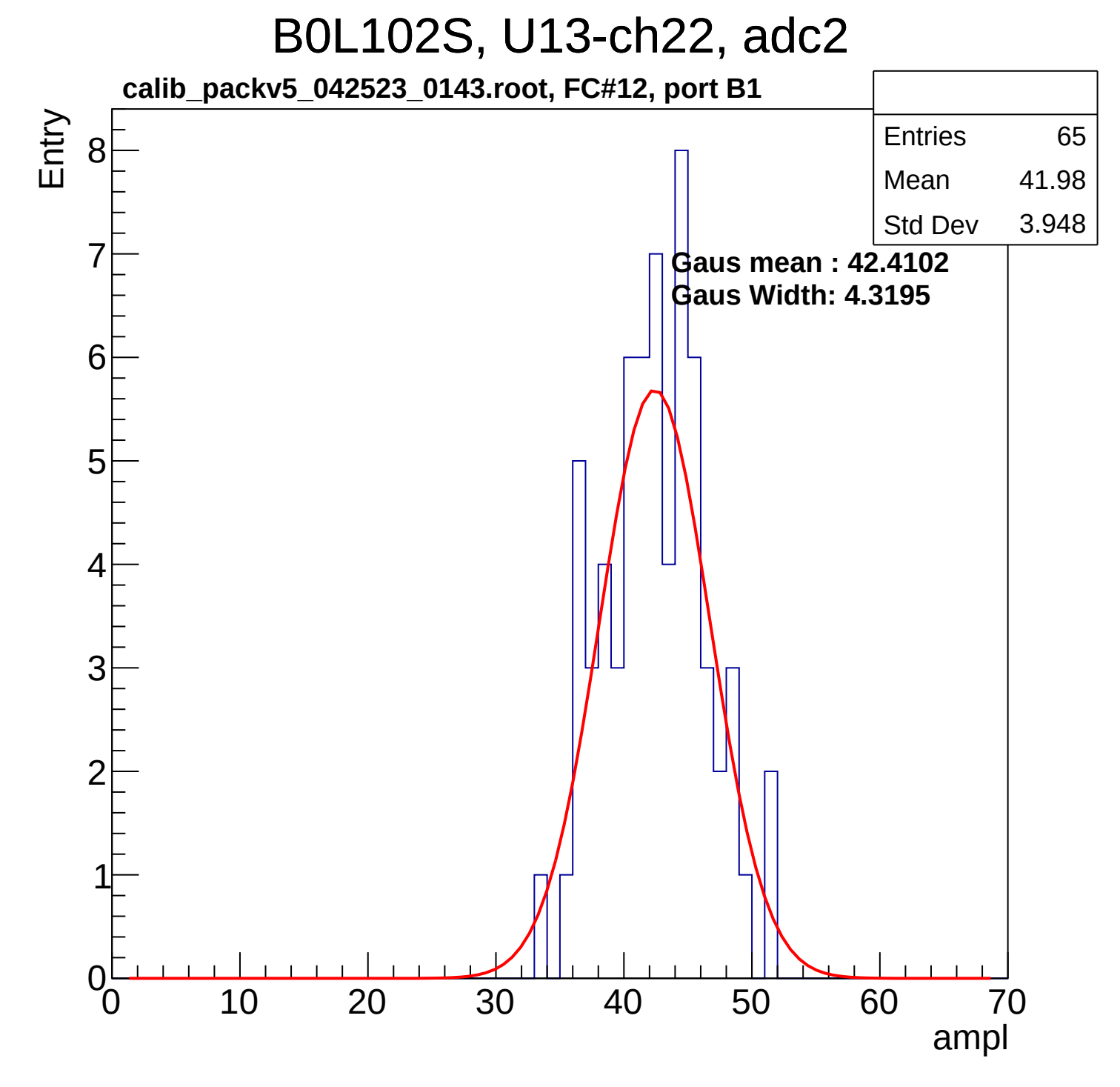
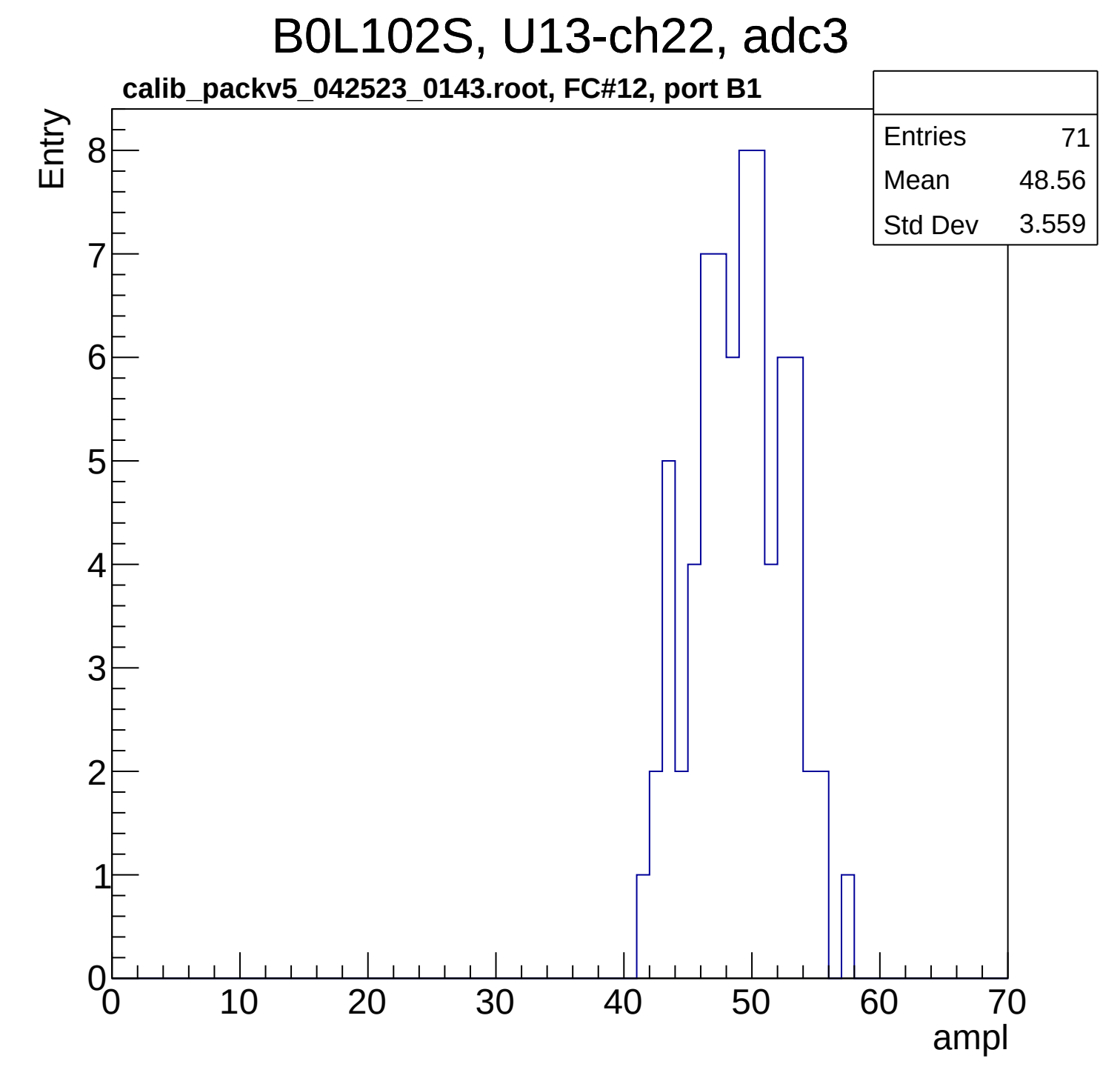

B0L102S, U13-ch21, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch21, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch22, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 28.02
Std Dev 4.748
Gaus mean : 28.6125
Gaus Width: 3.7710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch22, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 35.44
Std Dev 3.685
Gaus mean : 35.9333
Gaus Width: 4.3661
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch22, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 41.98
Std Dev 3.948
Gaus mean : 42.4102
Gaus Width: 4.3195
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch22, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.56
Std Dev 3.559
Entry
ampl
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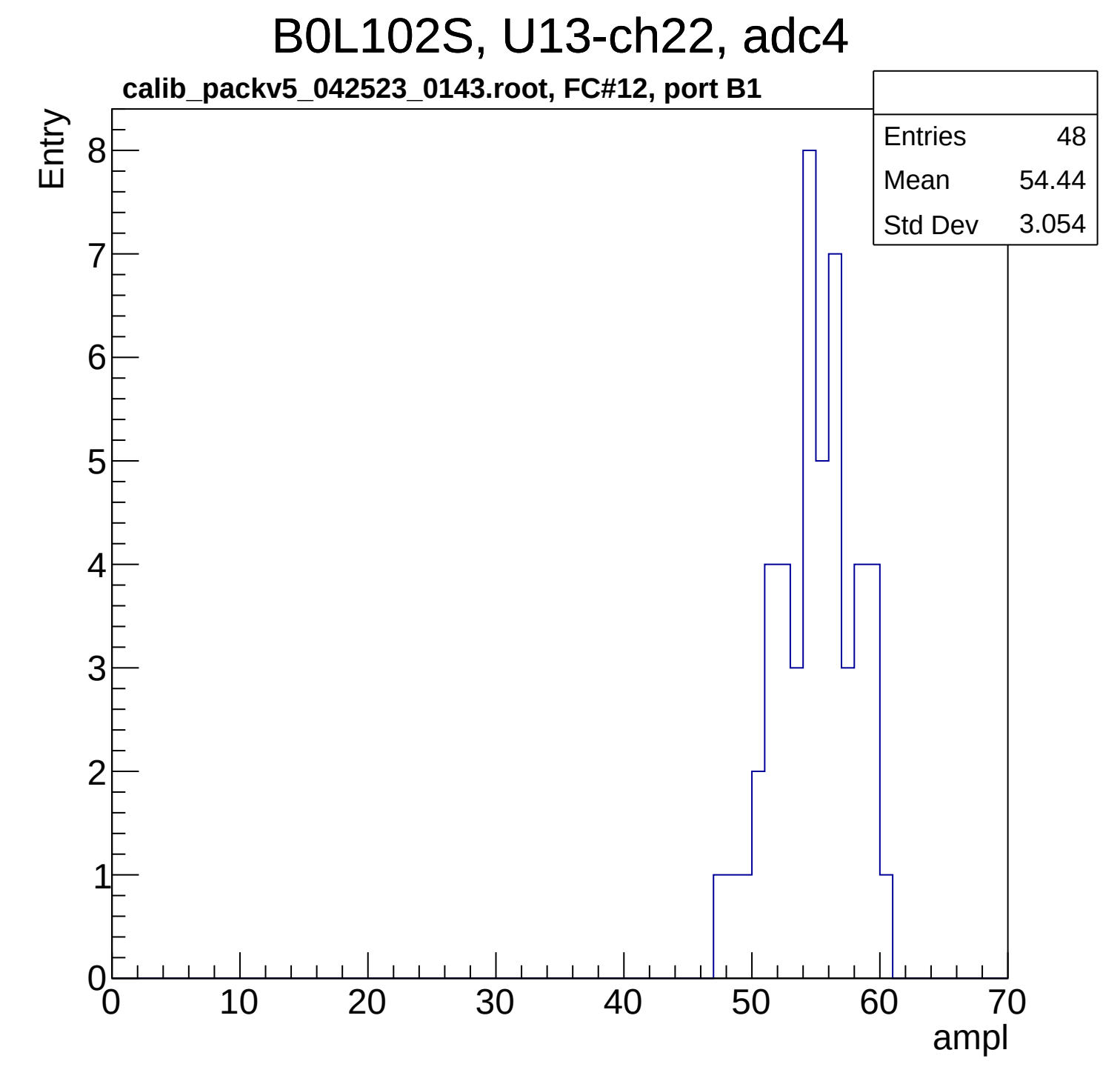
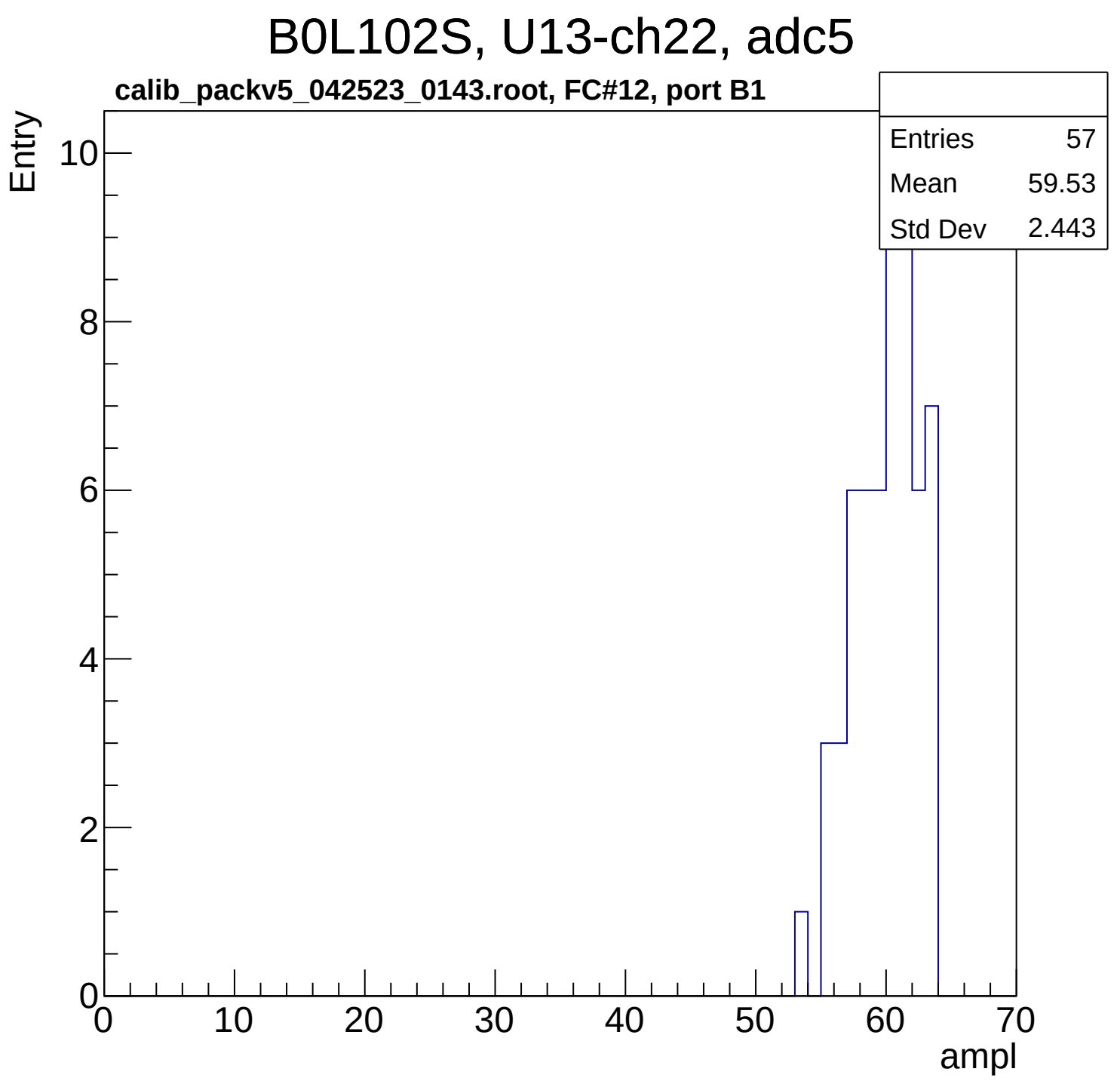
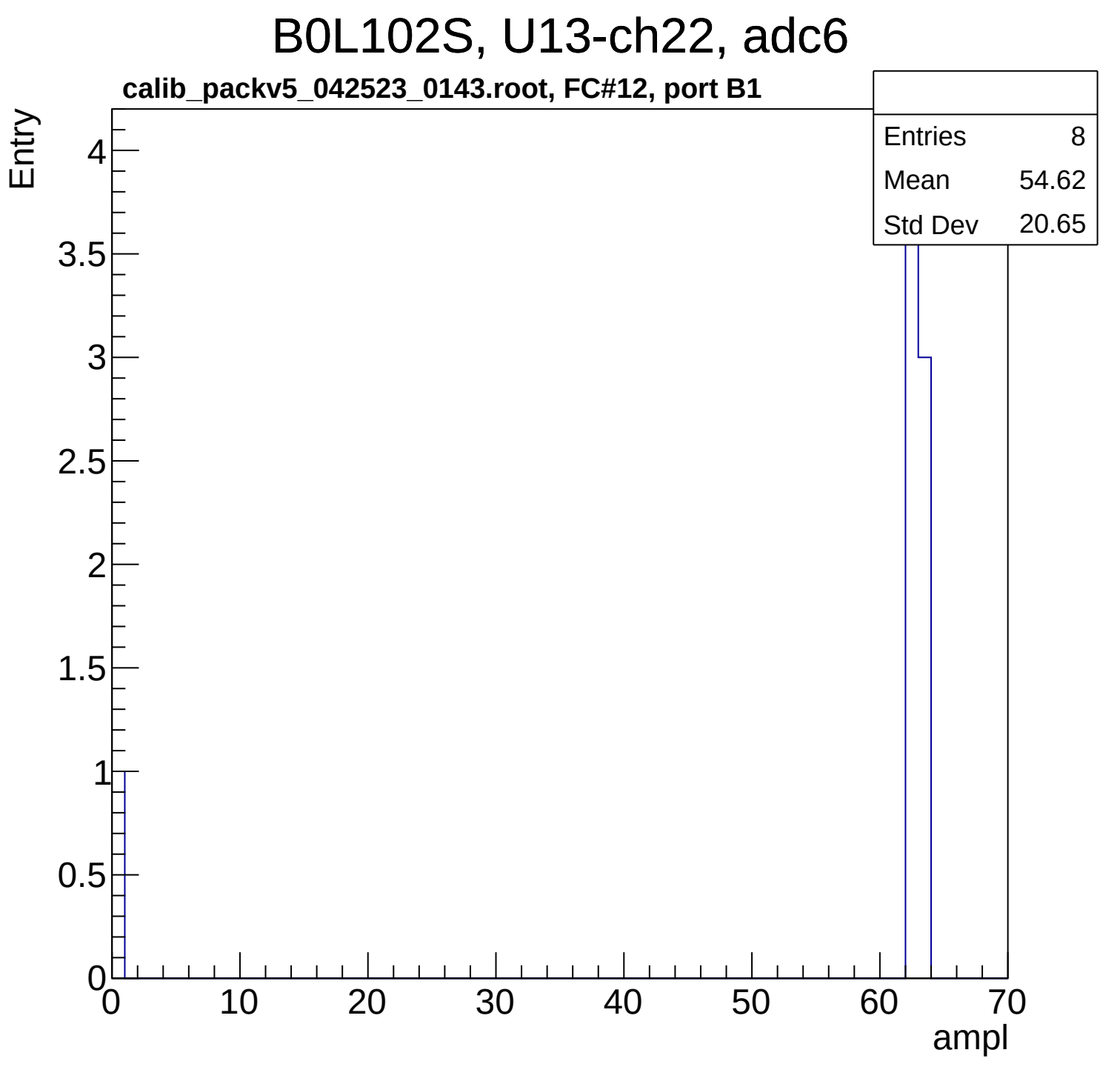
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch22, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 54.44
Std Dev 3.054
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch22, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.53
Std Dev 2.443
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch22, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.62
Std Dev 20.65
Entry
ampl
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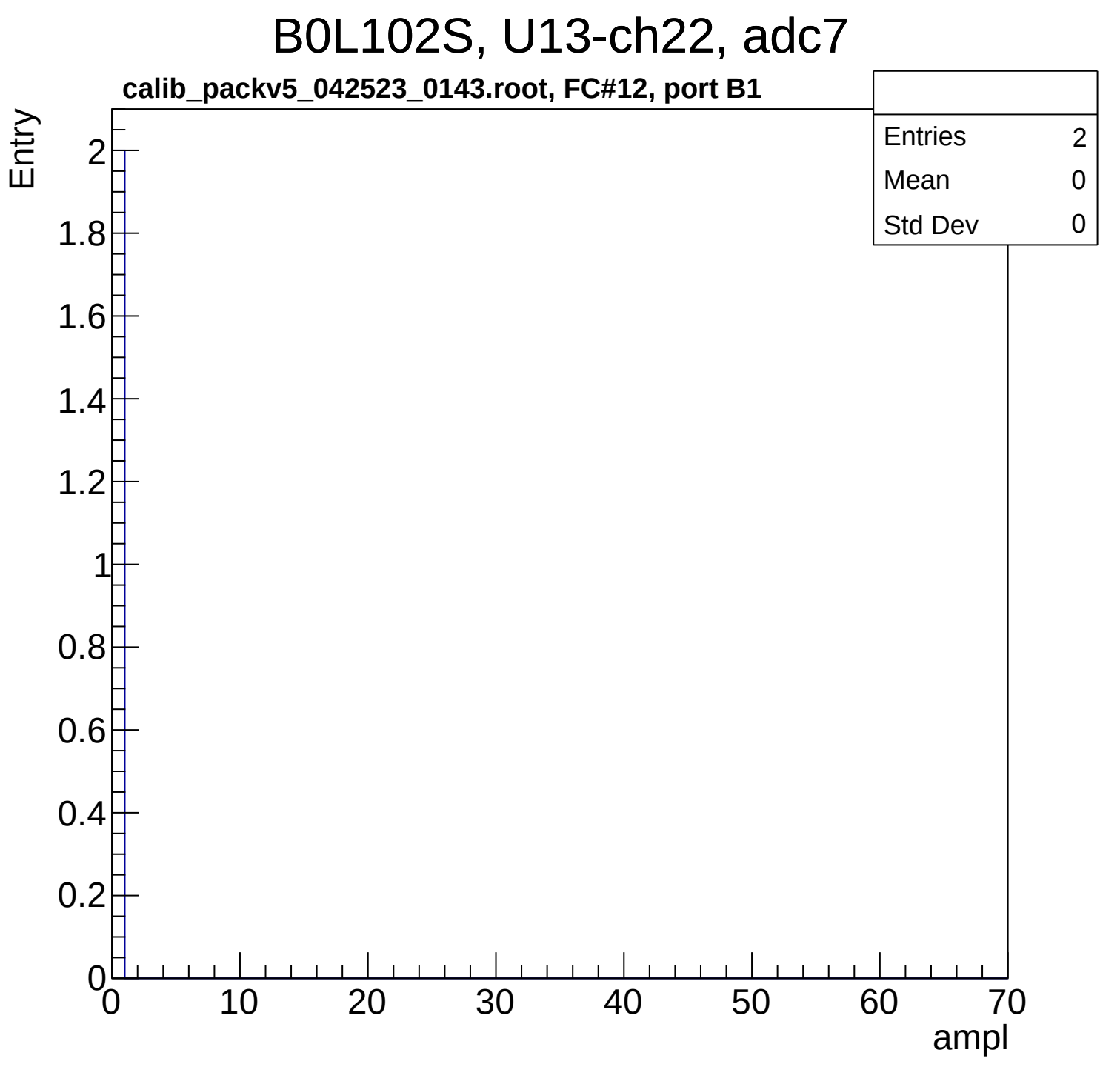
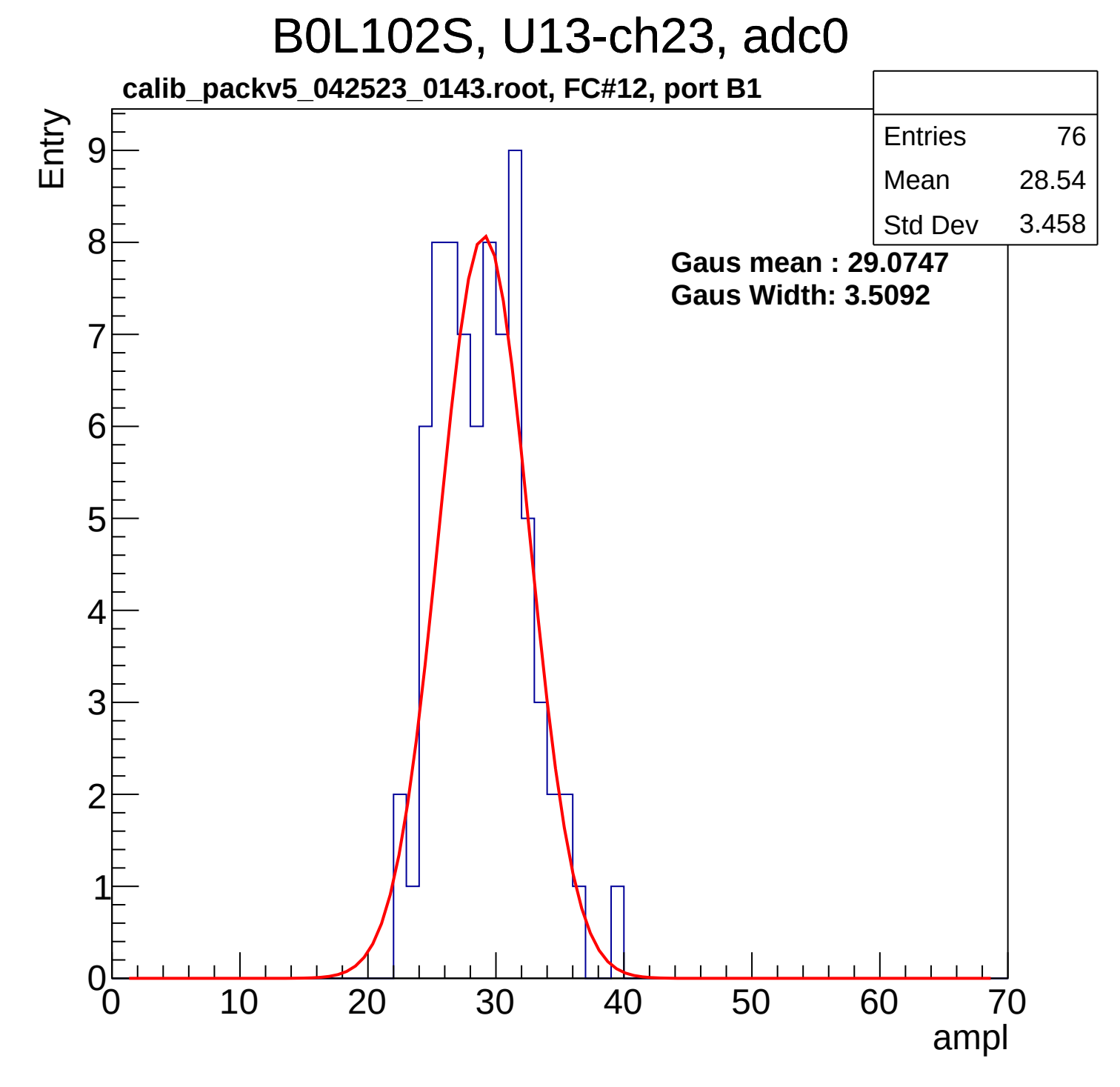
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch22, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch23, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.54
Std Dev 3.458
Gaus mean : 29.0747
Gaus Width: 3.5092
Entry
ampl
0
10
20
30
40
50
60
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1
2
3
4
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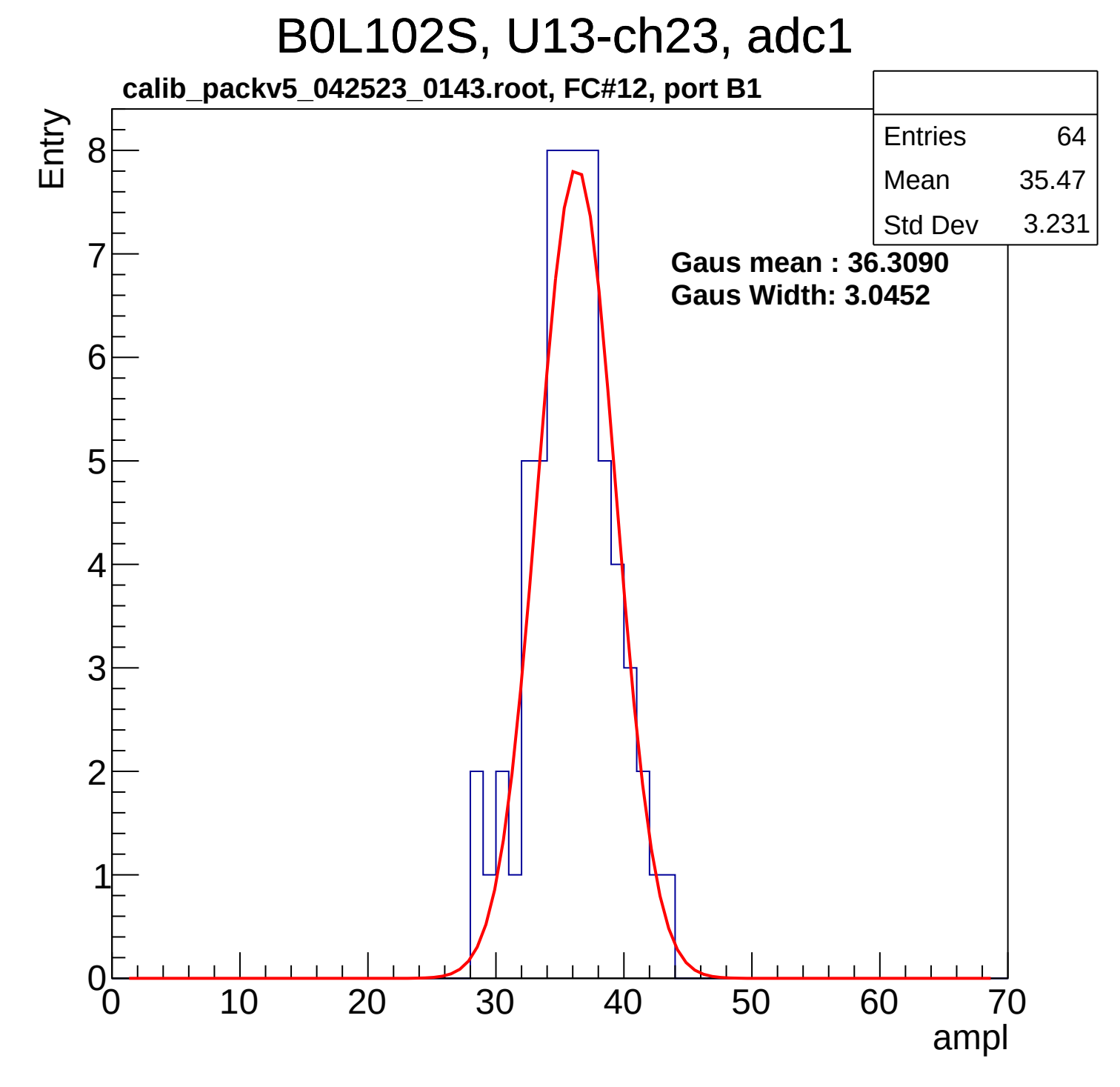
6
7
8
9


B0L102S, U13-ch23, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 35.47
Std Dev 3.231
Gaus mean : 36.3090
Gaus Width: 3.0452
Entry
ampl
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1
2
3
4
5
6
7
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0
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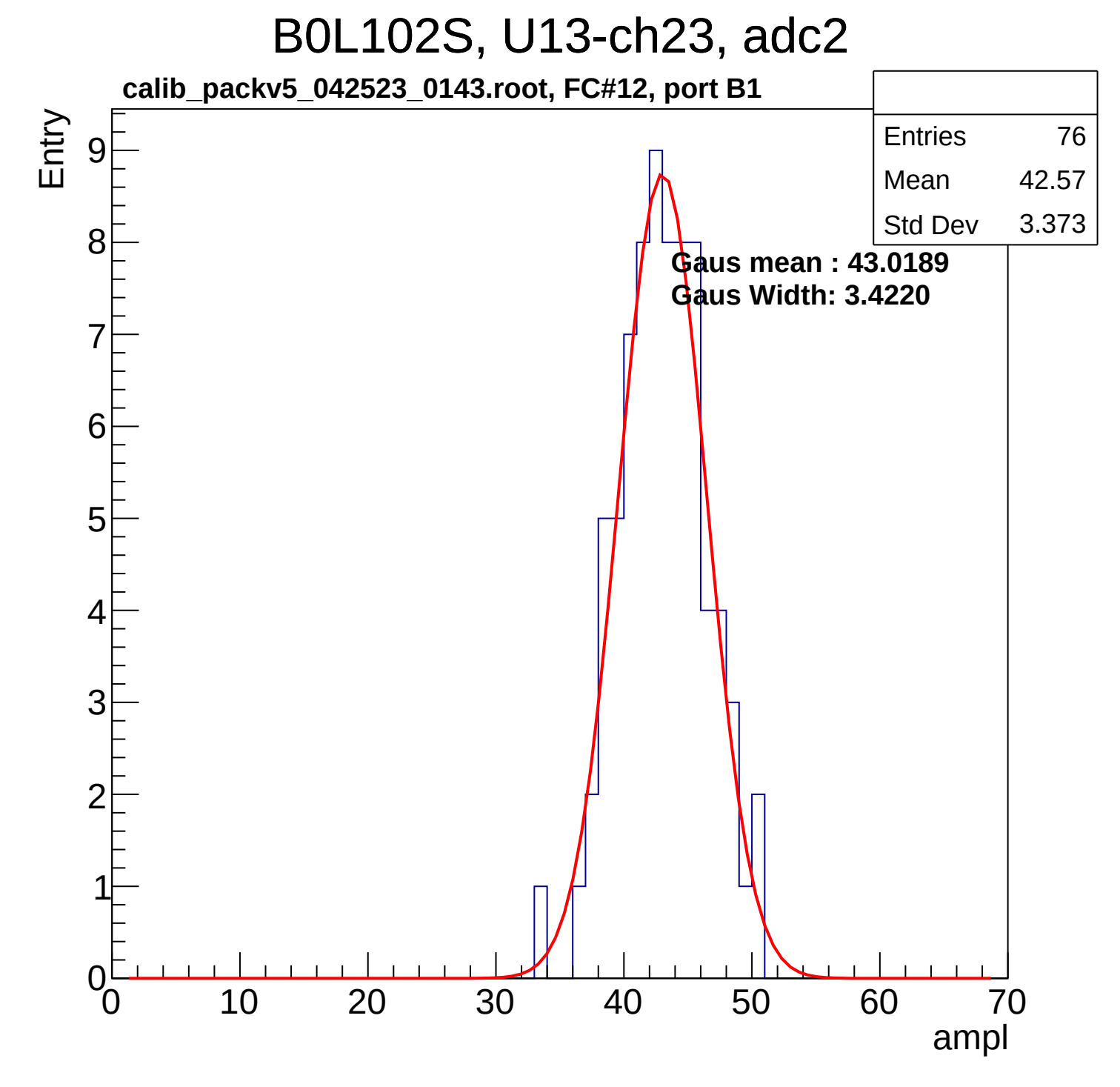
B0L102S, U13-ch23, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 42.57
Std Dev 3.373
Gaus mean : 43.0189
Gaus Width: 3.4220
Entry
ampl
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3
4
5
6
7
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0
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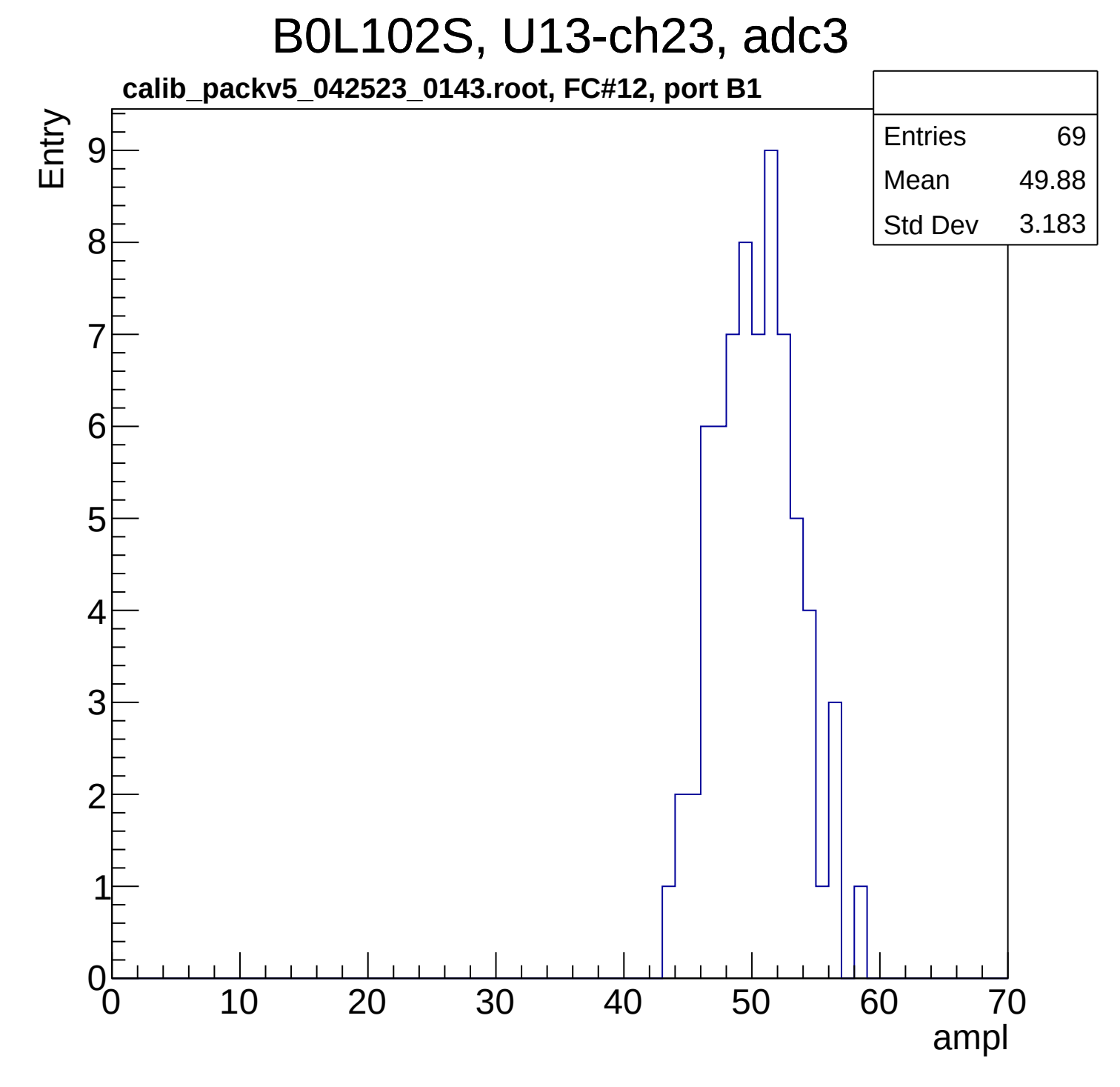
B0L102S, U13-ch23, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.88
Std Dev 3.183
Entry
ampl
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2
3
4
5
6
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8
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0
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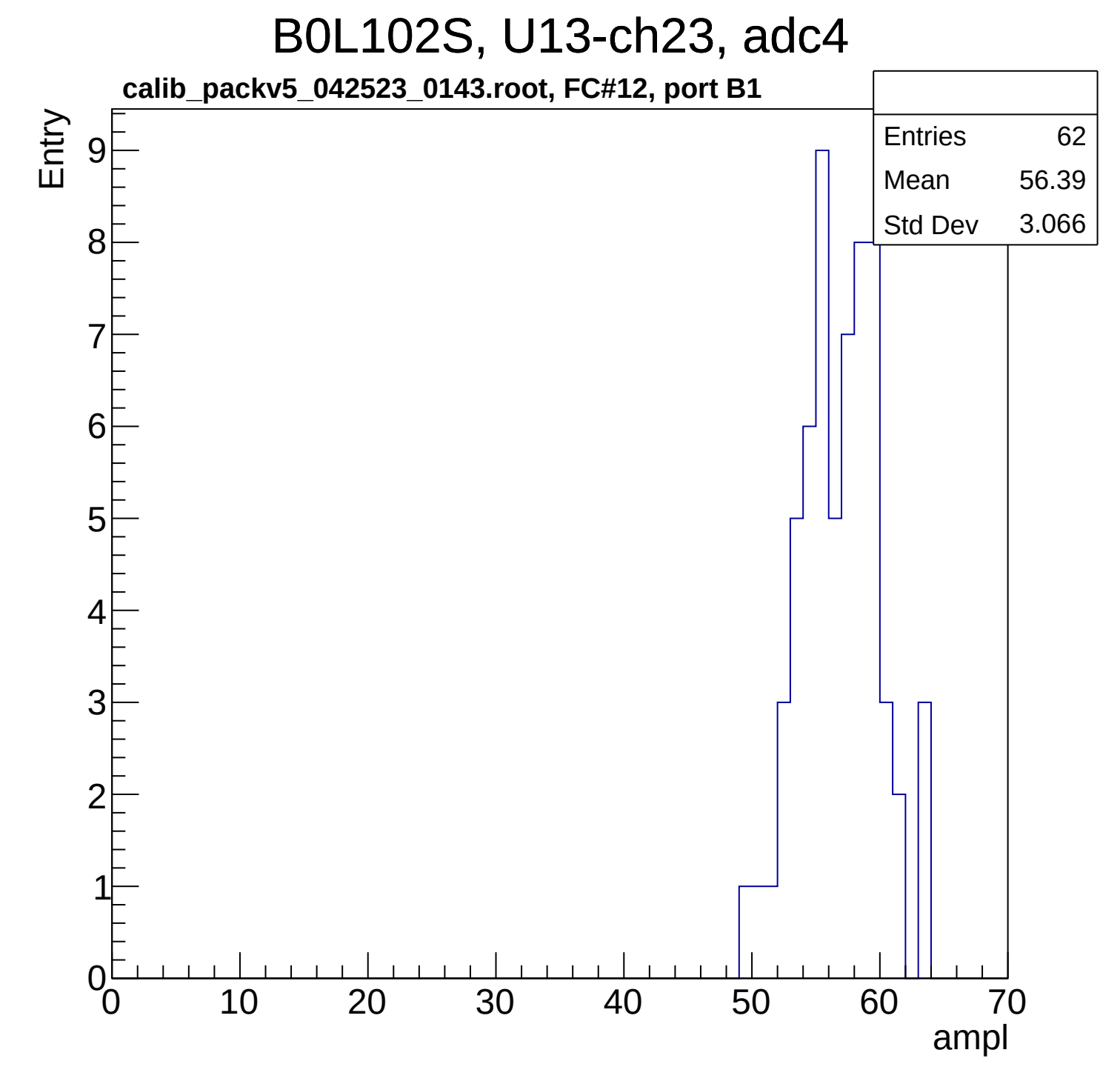
B0L102S, U13-ch23, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 56.39
Std Dev 3.066
Entry
ampl
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2
3
4
5
6
7
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0
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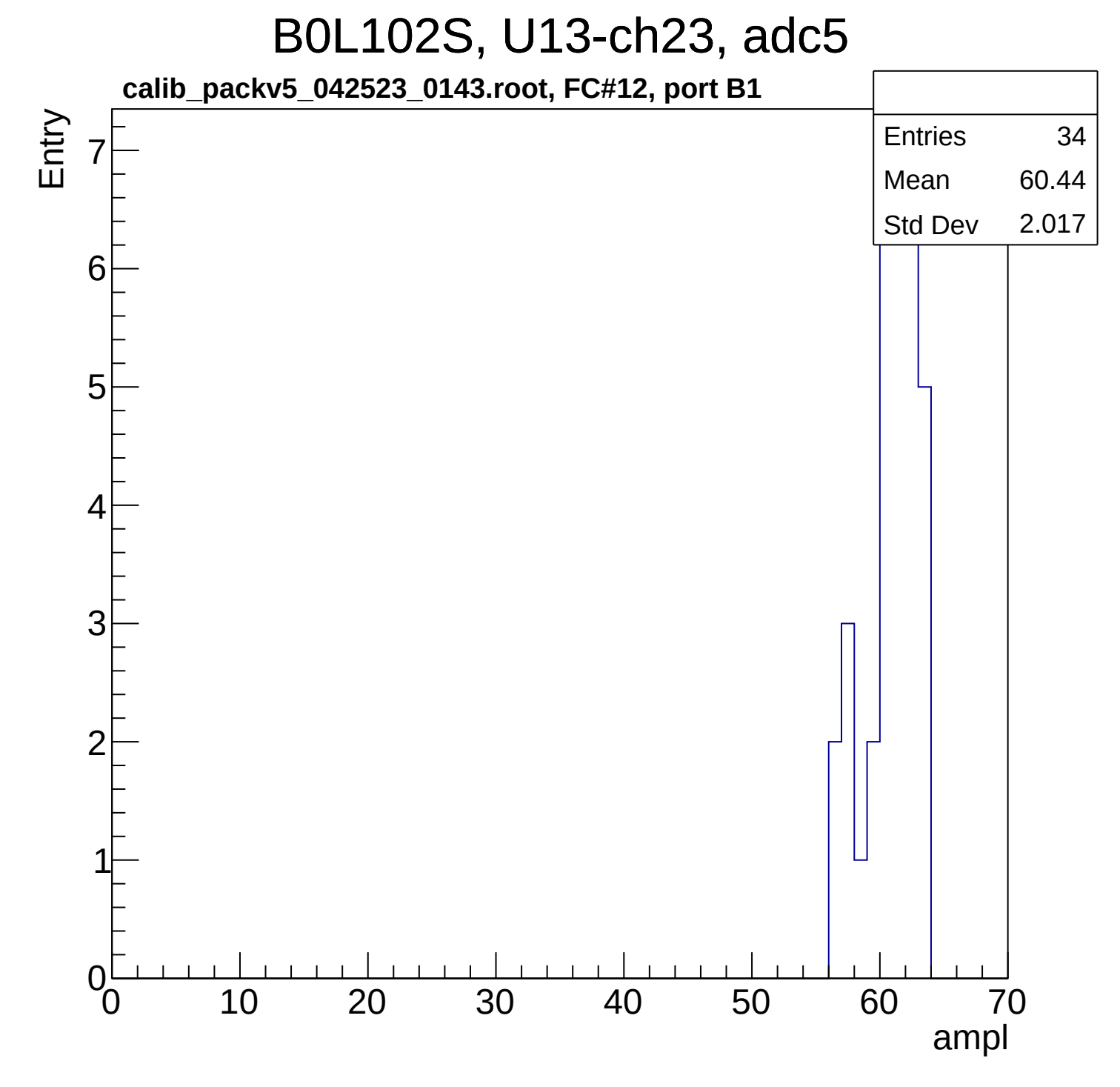
B0L102S, U13-ch23, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 60.44
Std Dev 2.017
Entry
0
1
2
3
4
5
6
7
0
10
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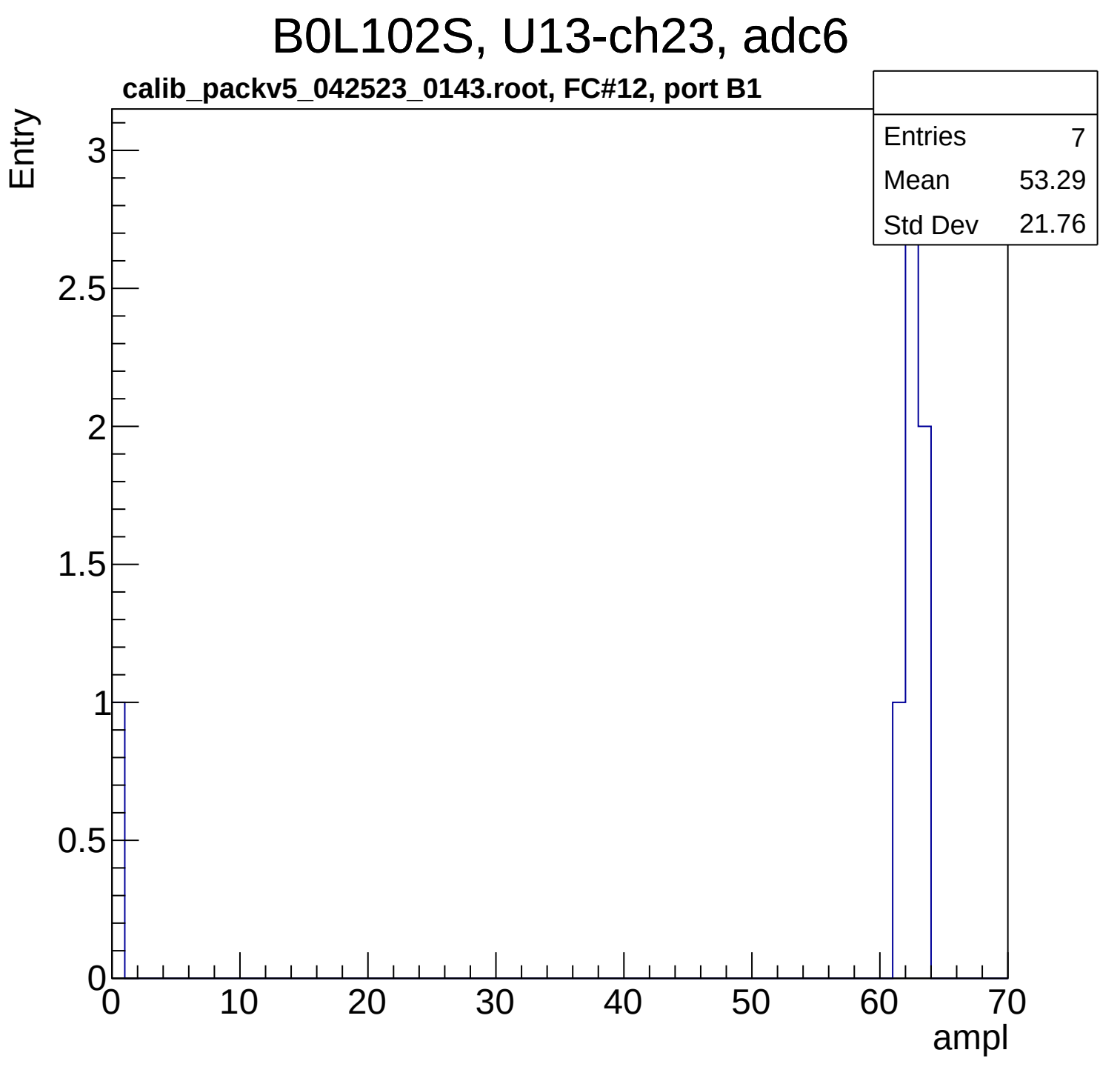
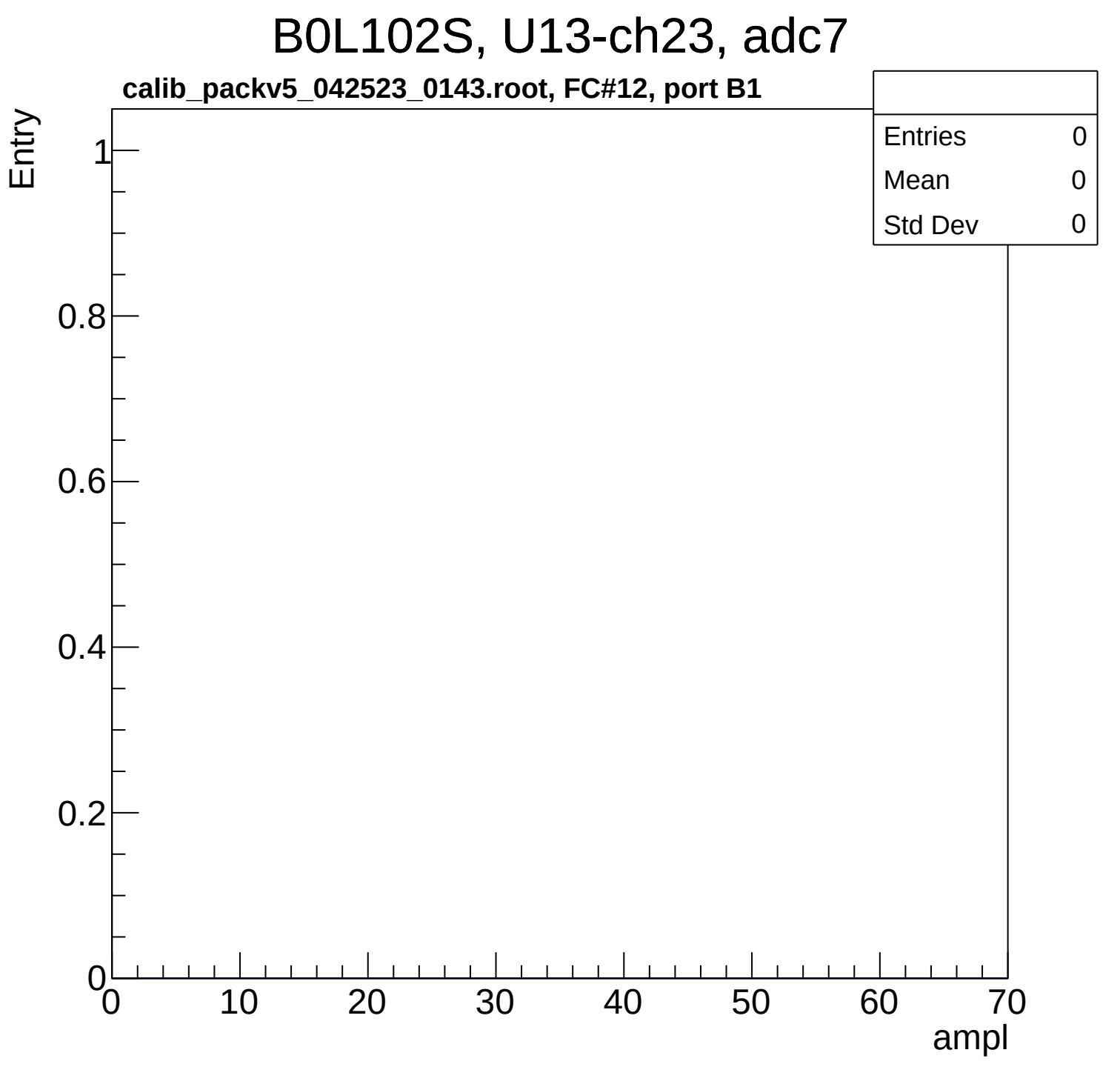
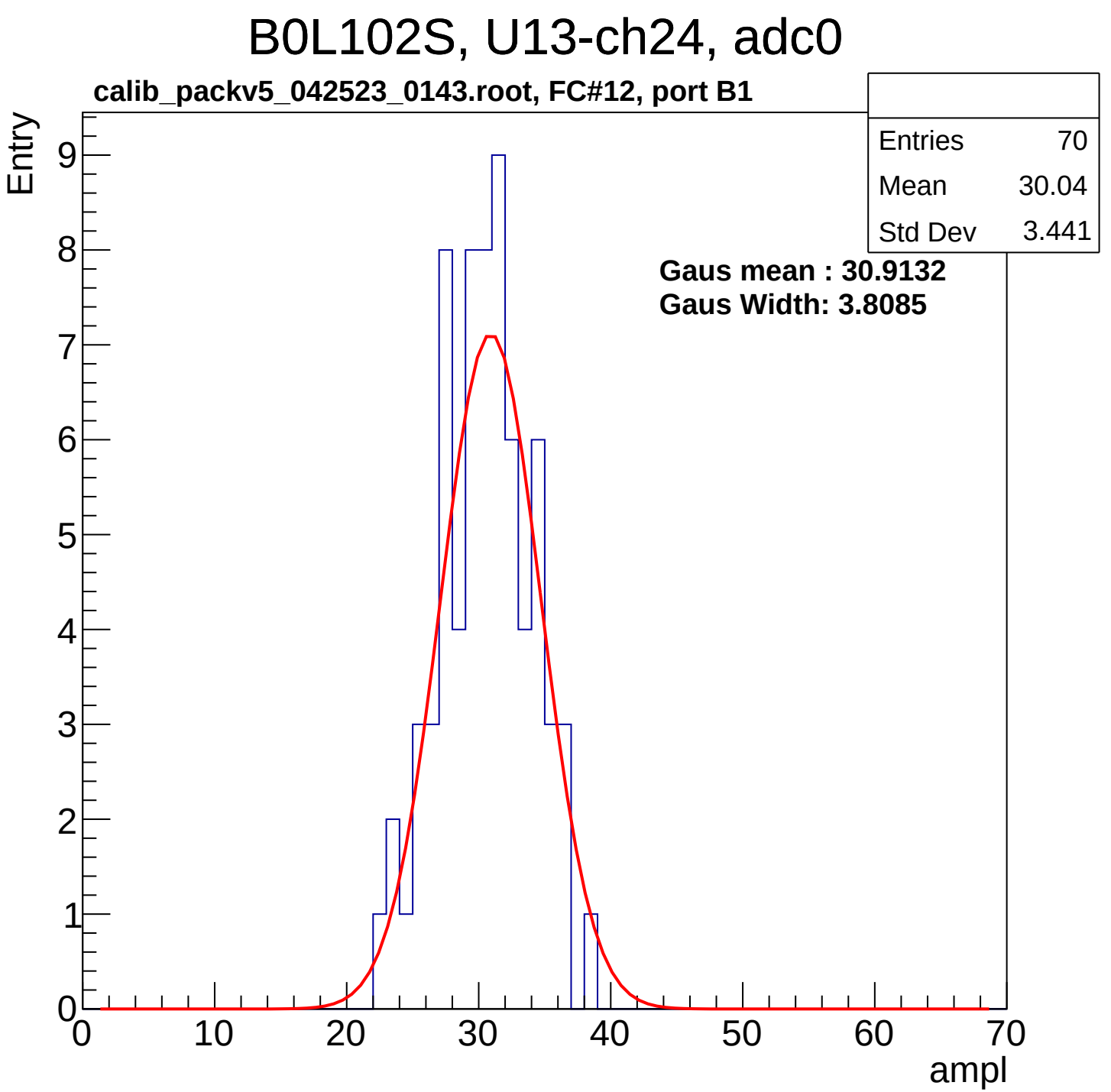
70
ampl


B0L102S, U13-ch23, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch23, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch24, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 30.04
Std Dev 3.441
Gaus mean : 30.9132
Gaus Width: 3.8085
Entry
ampl
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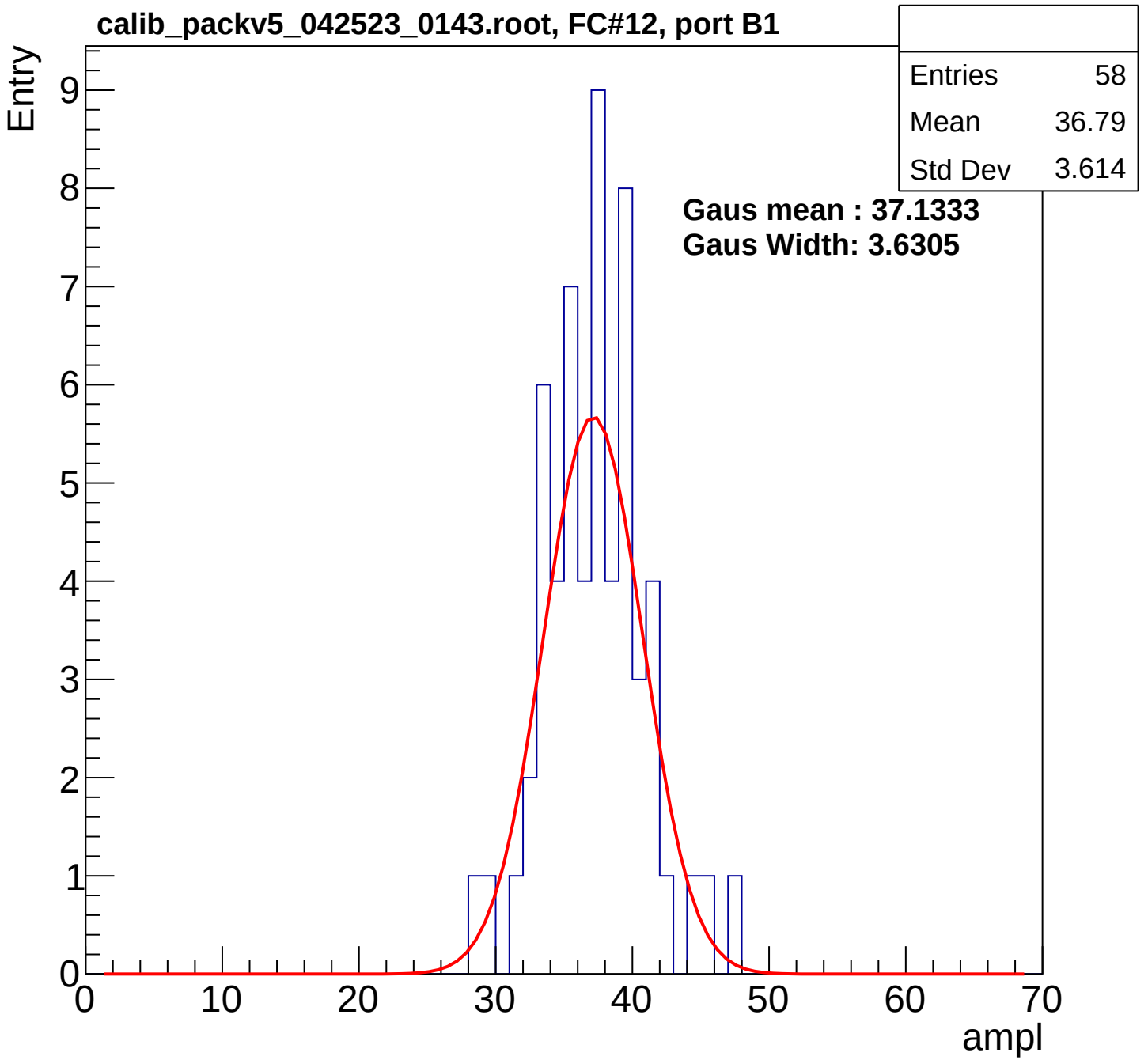
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch24, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 36.79
Std Dev 3.614
Gaus mean : 37.1333
Gaus Width: 3.6305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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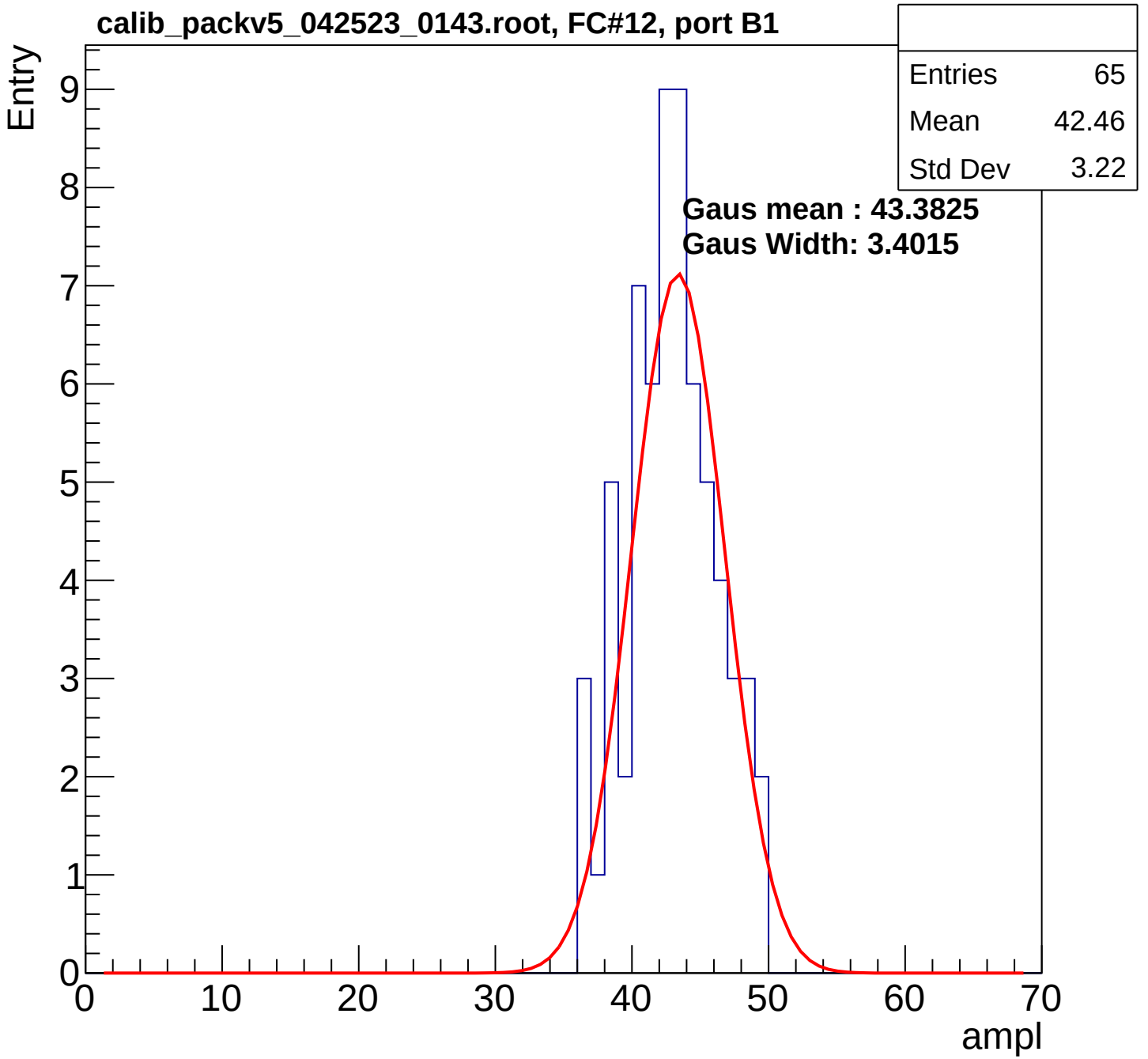
B0L102S, U13-ch24, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 42.46
Std Dev 3.22
Gaus mean : 43.3825
Gaus Width: 3.4015
Entry
ampl
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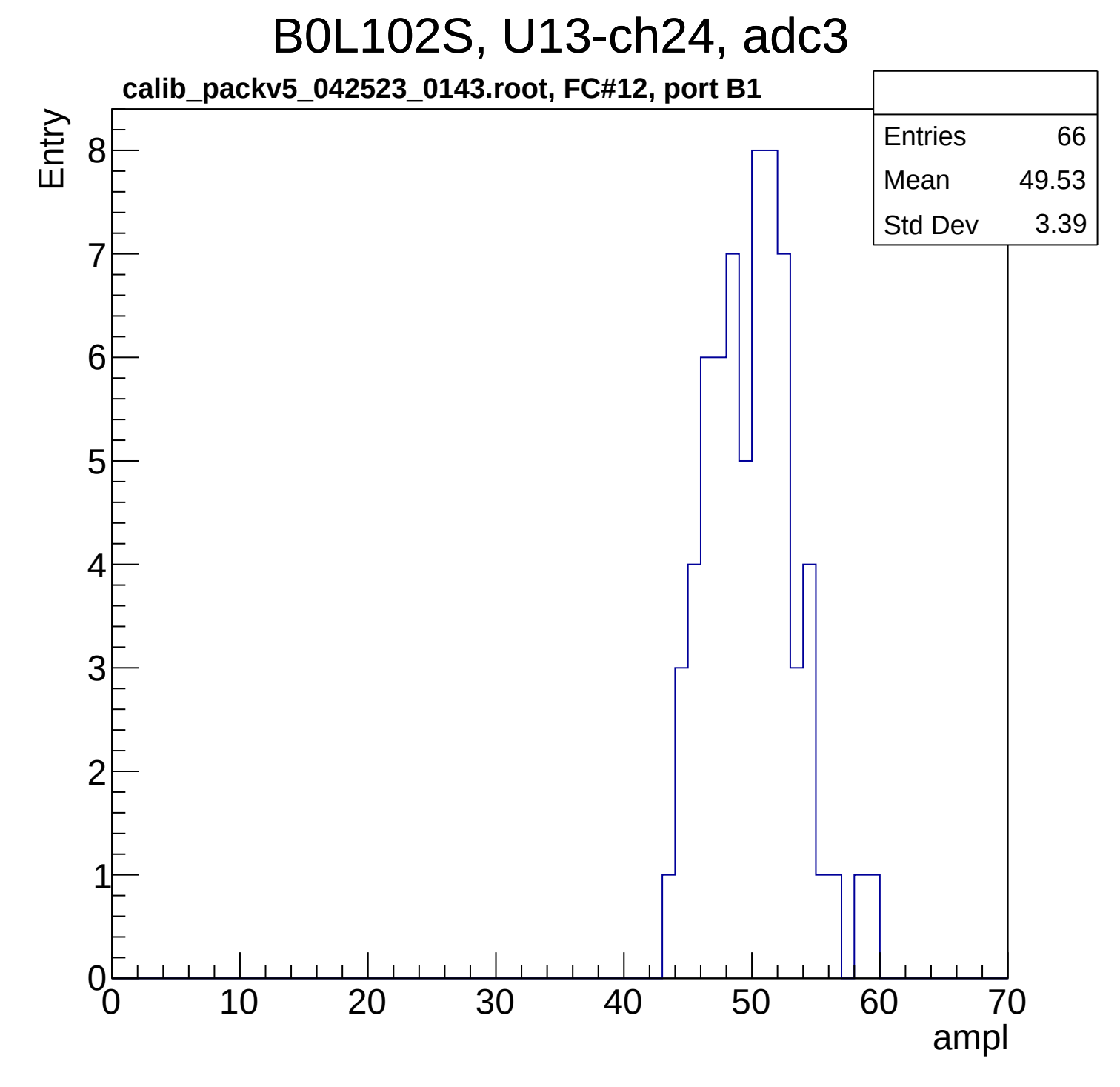
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch24, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.53
Std Dev 3.39
Entry
ampl
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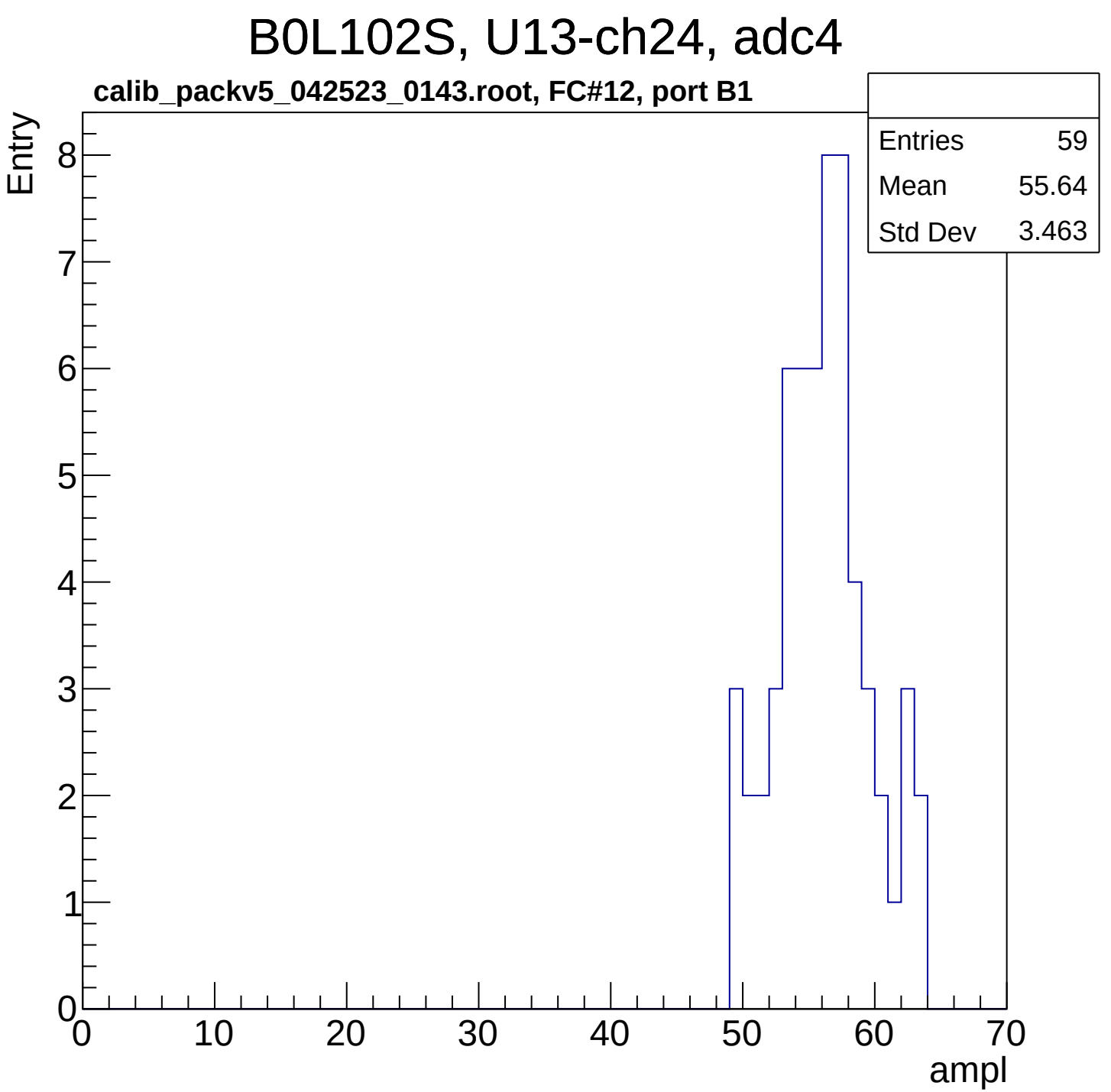
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch24, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.64
Std Dev 3.463
Entry
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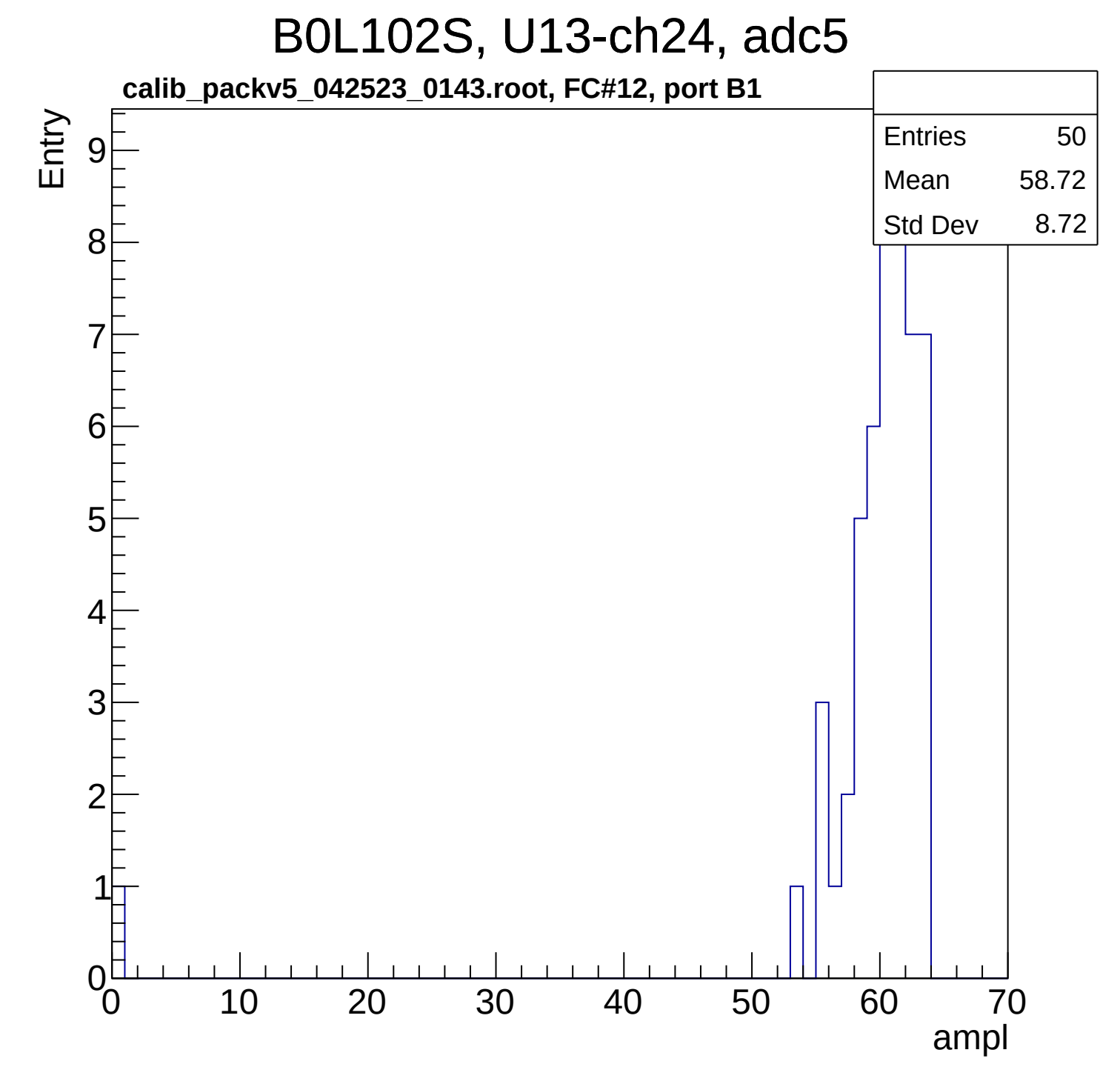
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch24, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.72
Std Dev 8.72
Entry
0
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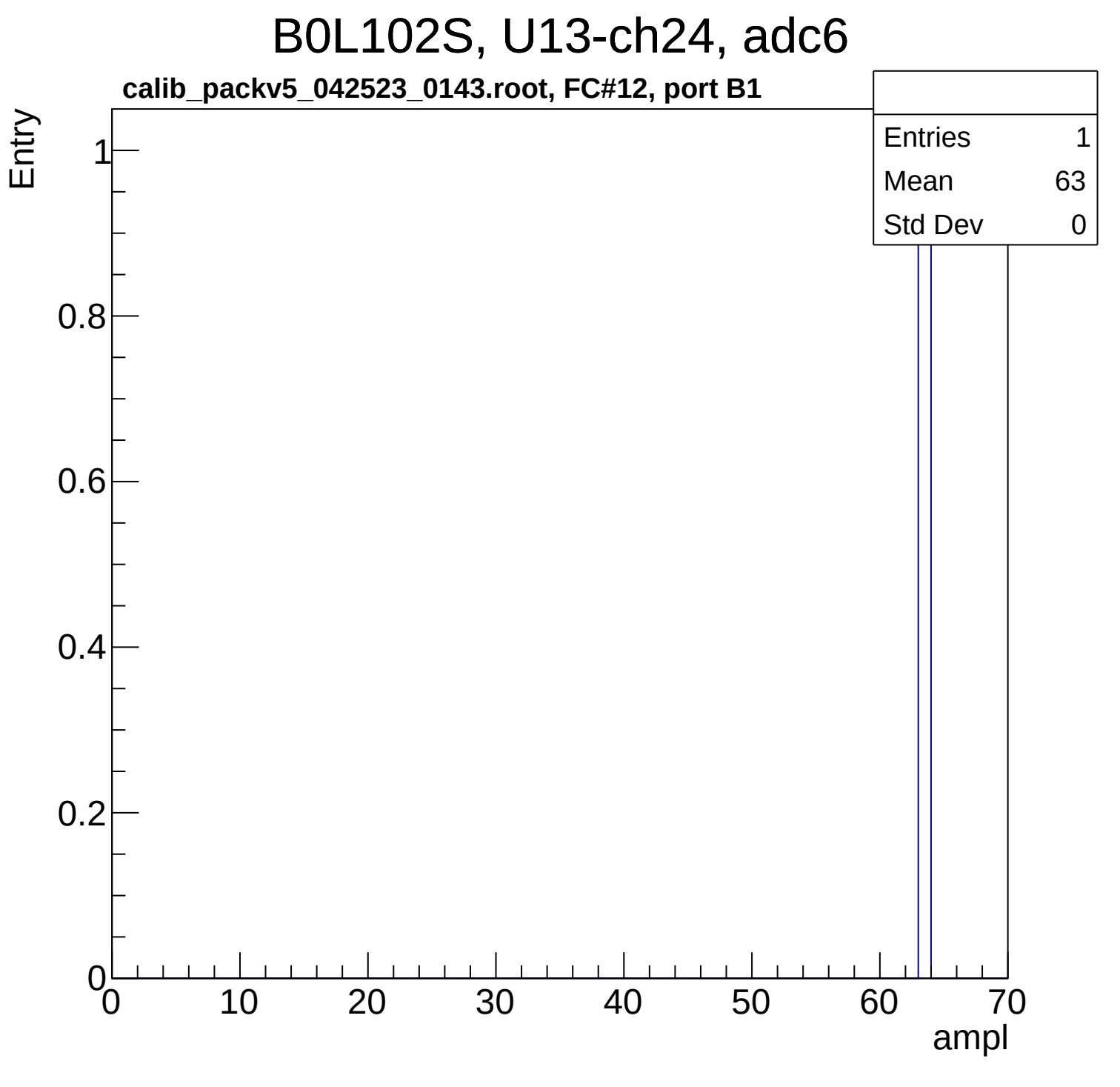
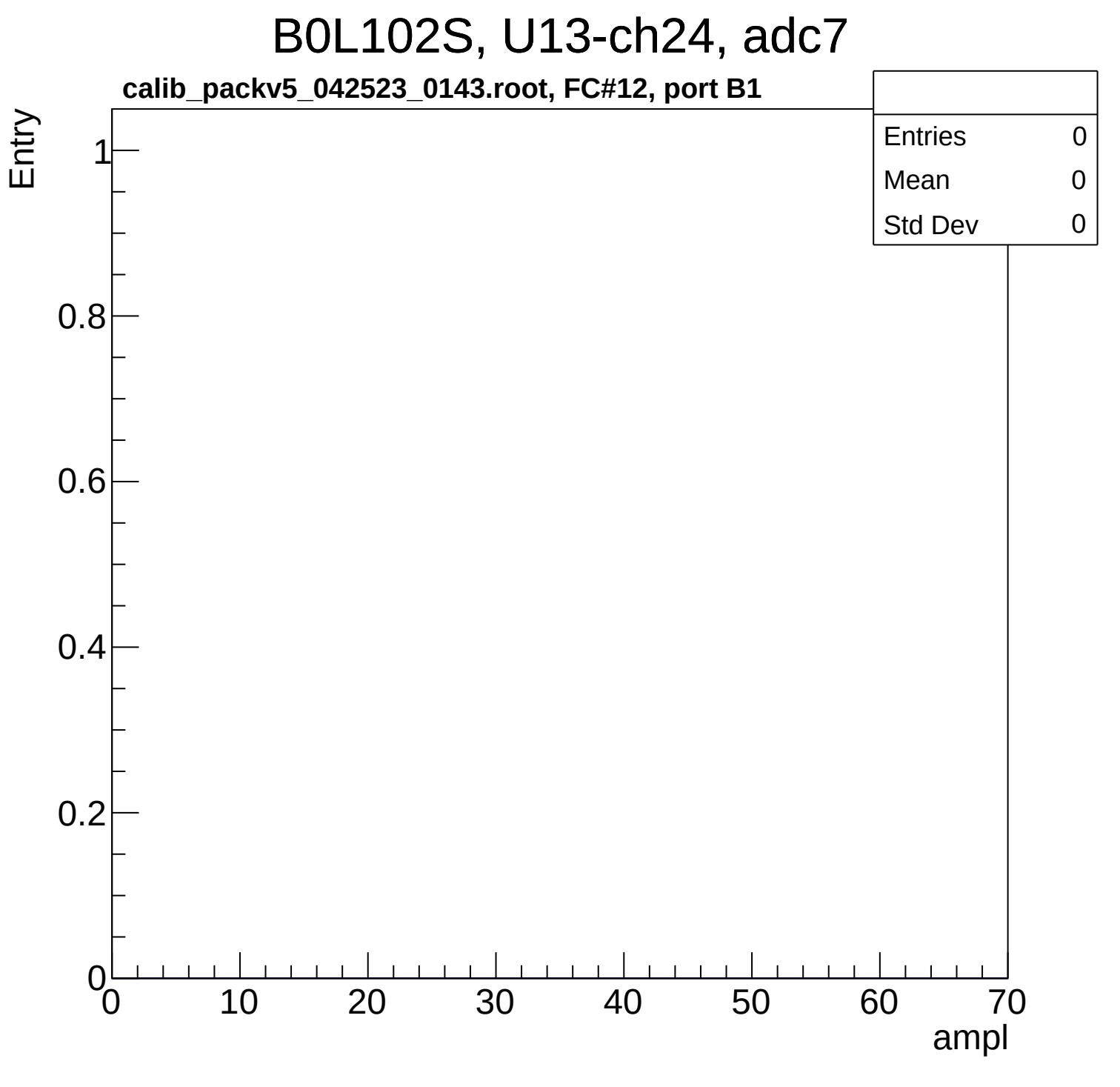
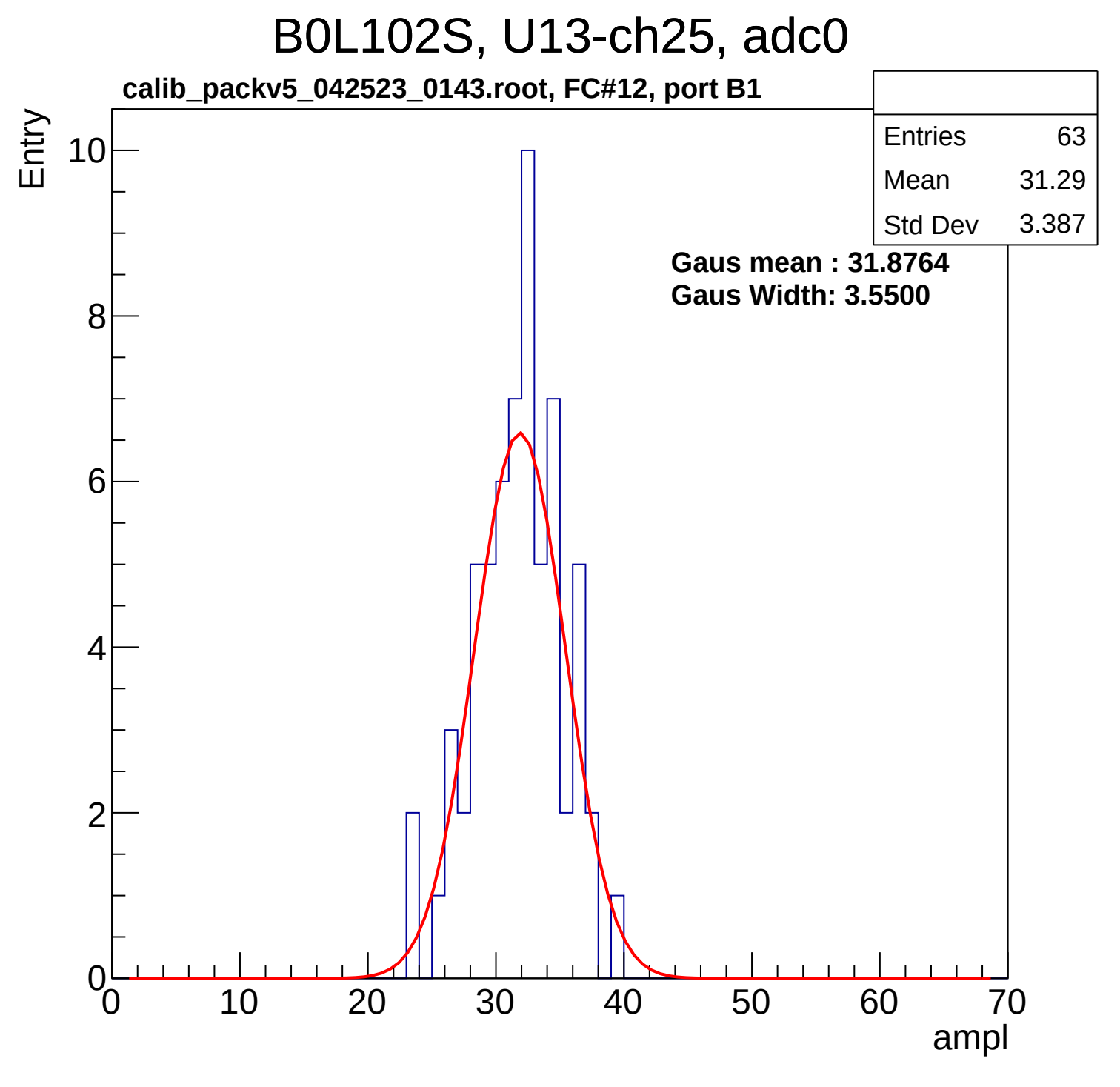
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch24, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch24, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch25, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 31.29
Std Dev 3.387
Gaus mean : 31.8764
Gaus Width: 3.5500
Entry
ampl
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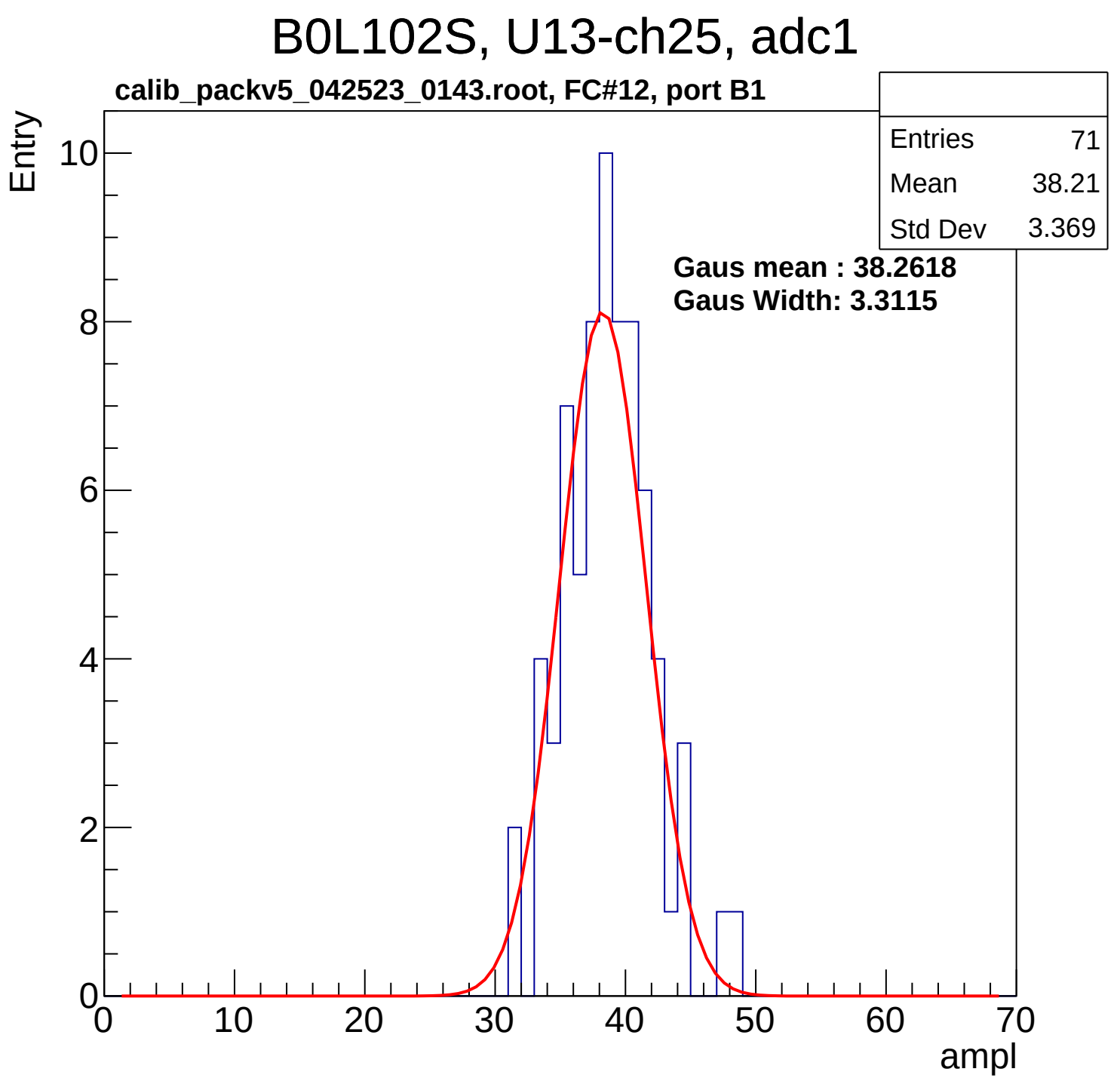
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch25, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 38.21
Std Dev 3.369
Gaus mean : 38.2618
Gaus Width: 3.3115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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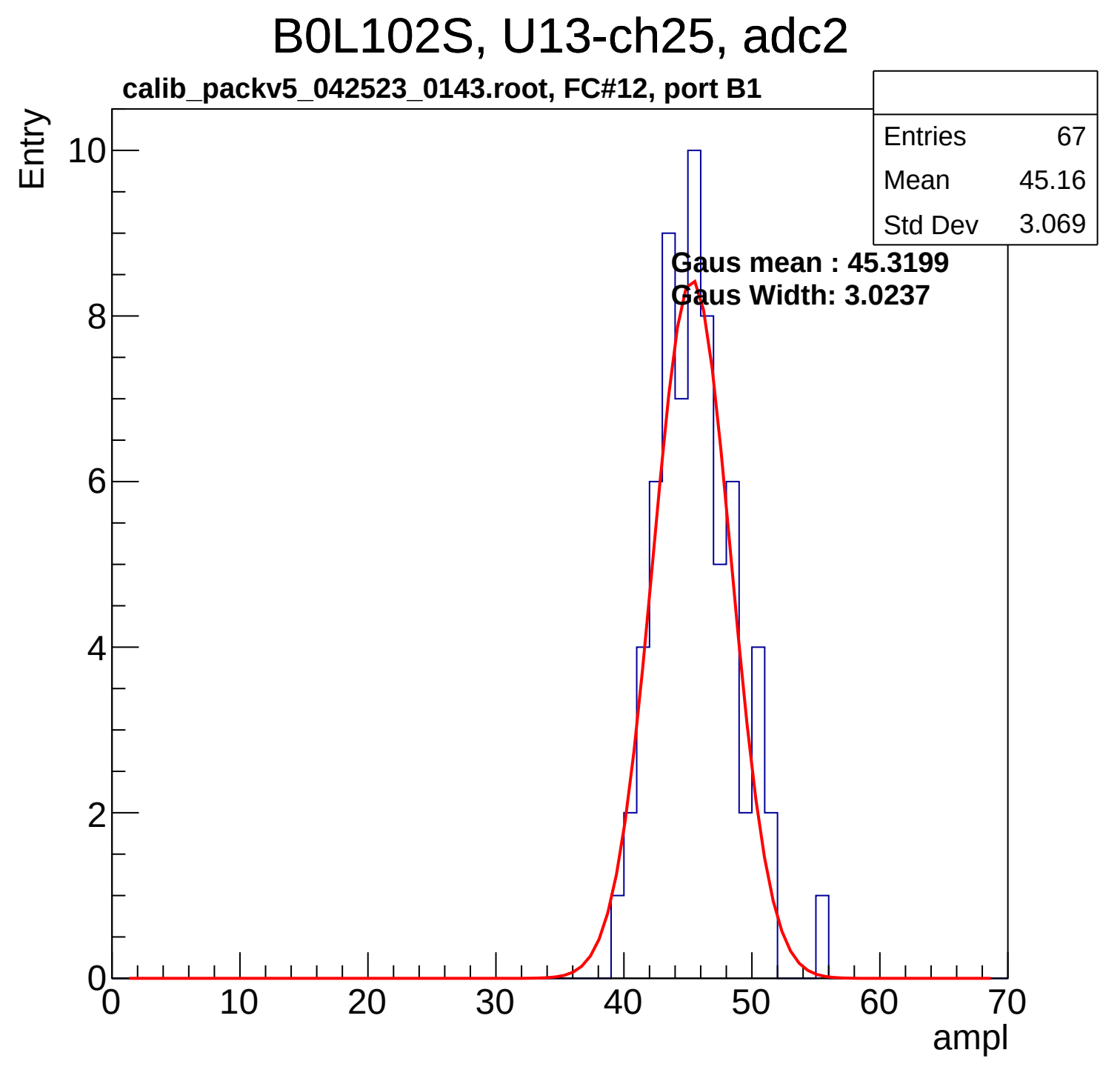
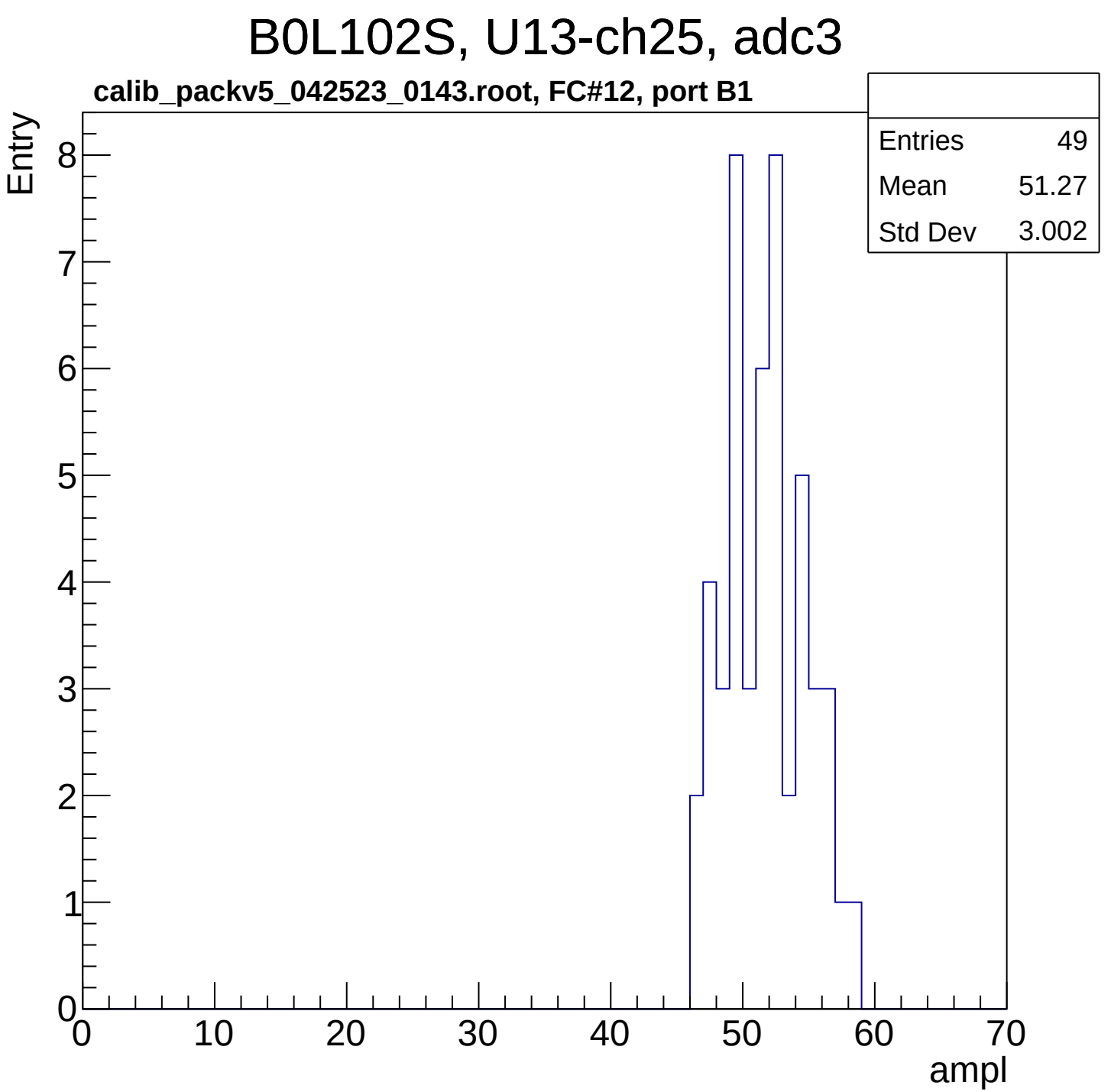

B0L102S, U13-ch25, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 45.16
Std Dev 3.069
Gaus mean : 45.3199
Gaus Width: 3.0237
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch25, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 51.27
Std Dev 3.002
Entry
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3
4
5
6
7
8
0
10
20
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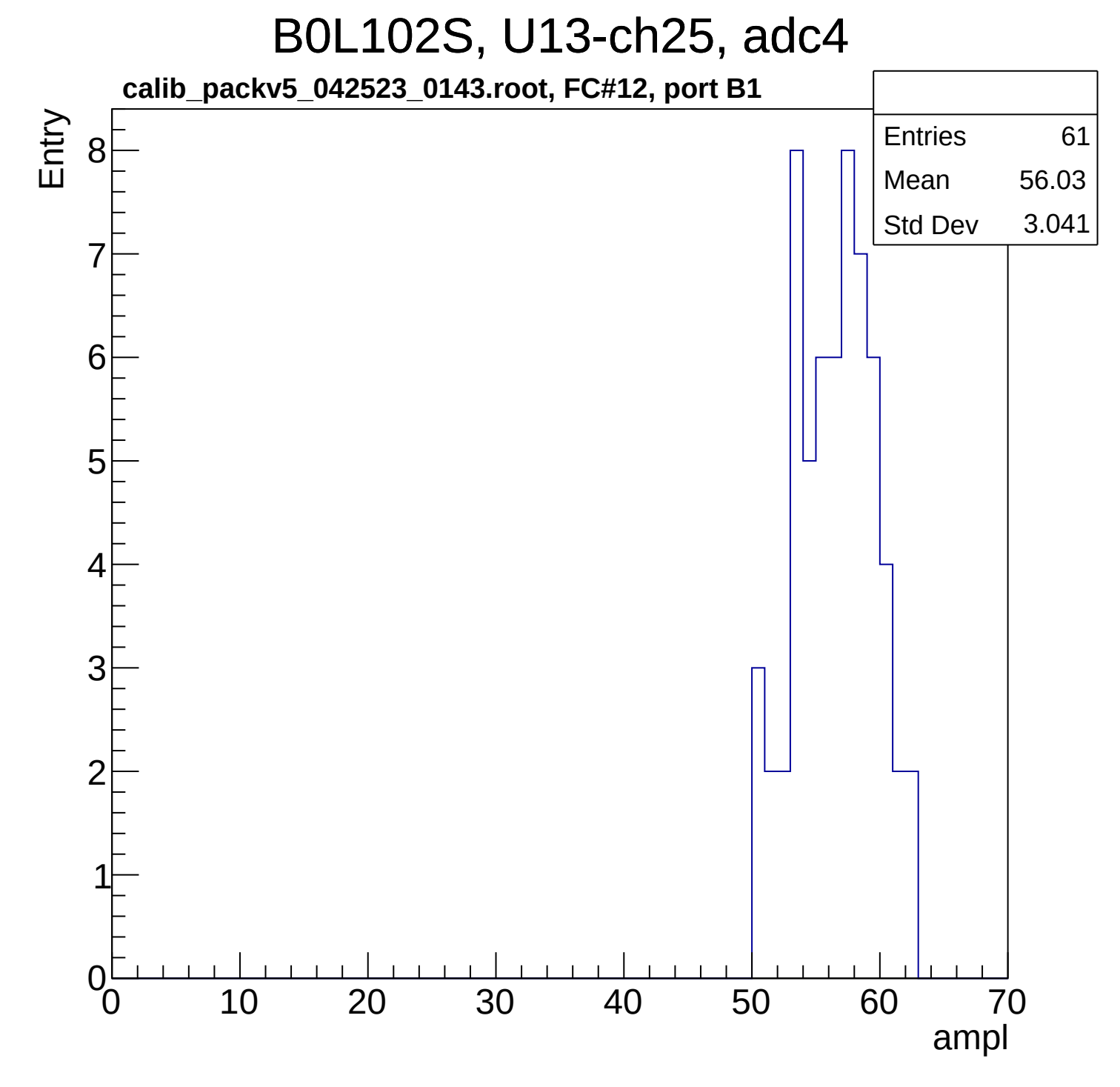
50
60
70
ampl


B0L102S, U13-ch25, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.03
Std Dev 3.041
Entry
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2
3
4
5
6
7
8
0
10
20
30
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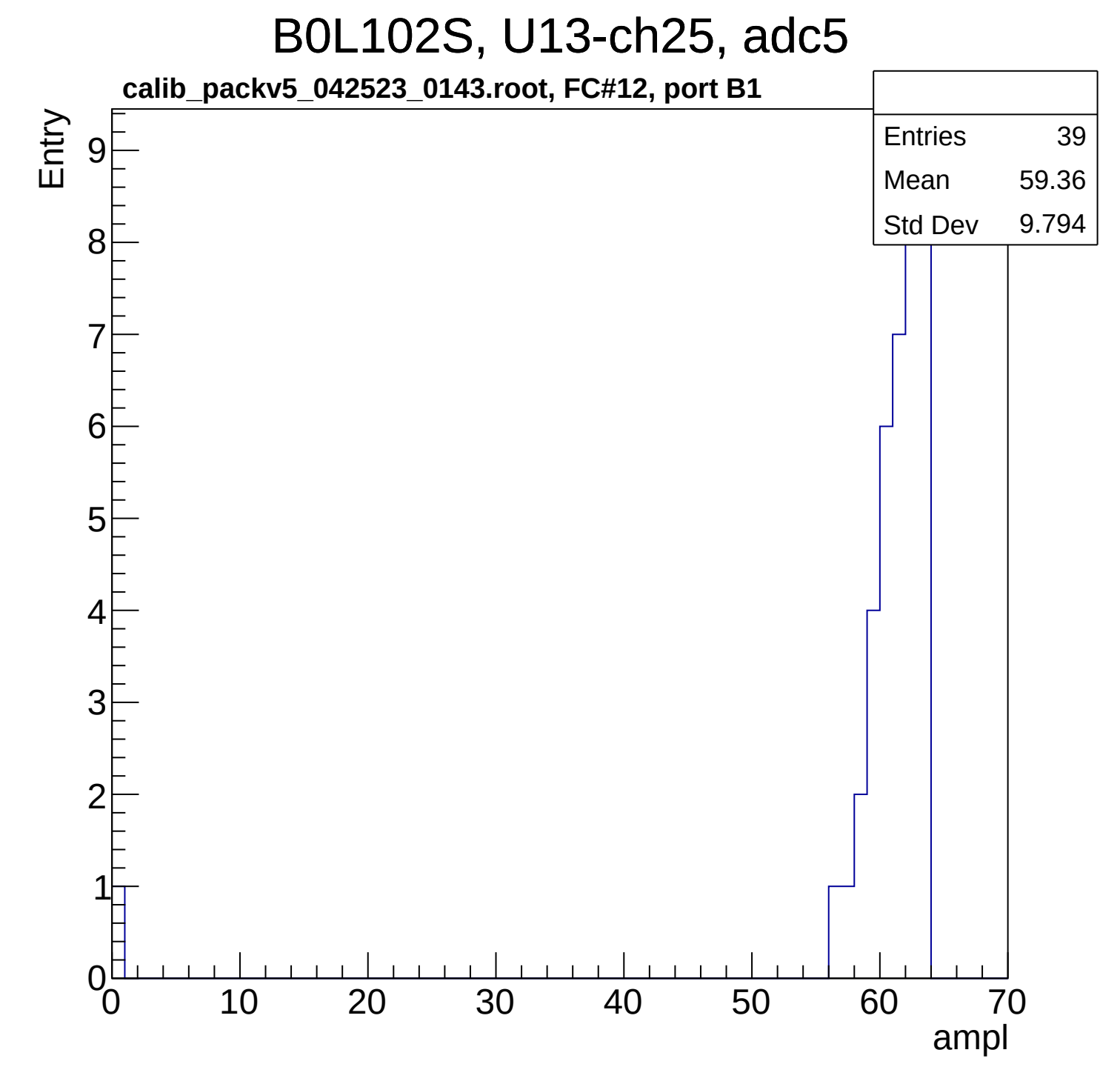
50
60
70
ampl


B0L102S, U13-ch25, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 59.36
Std Dev 9.794
Entry
ampl
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3
4
5
6
7
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9
0
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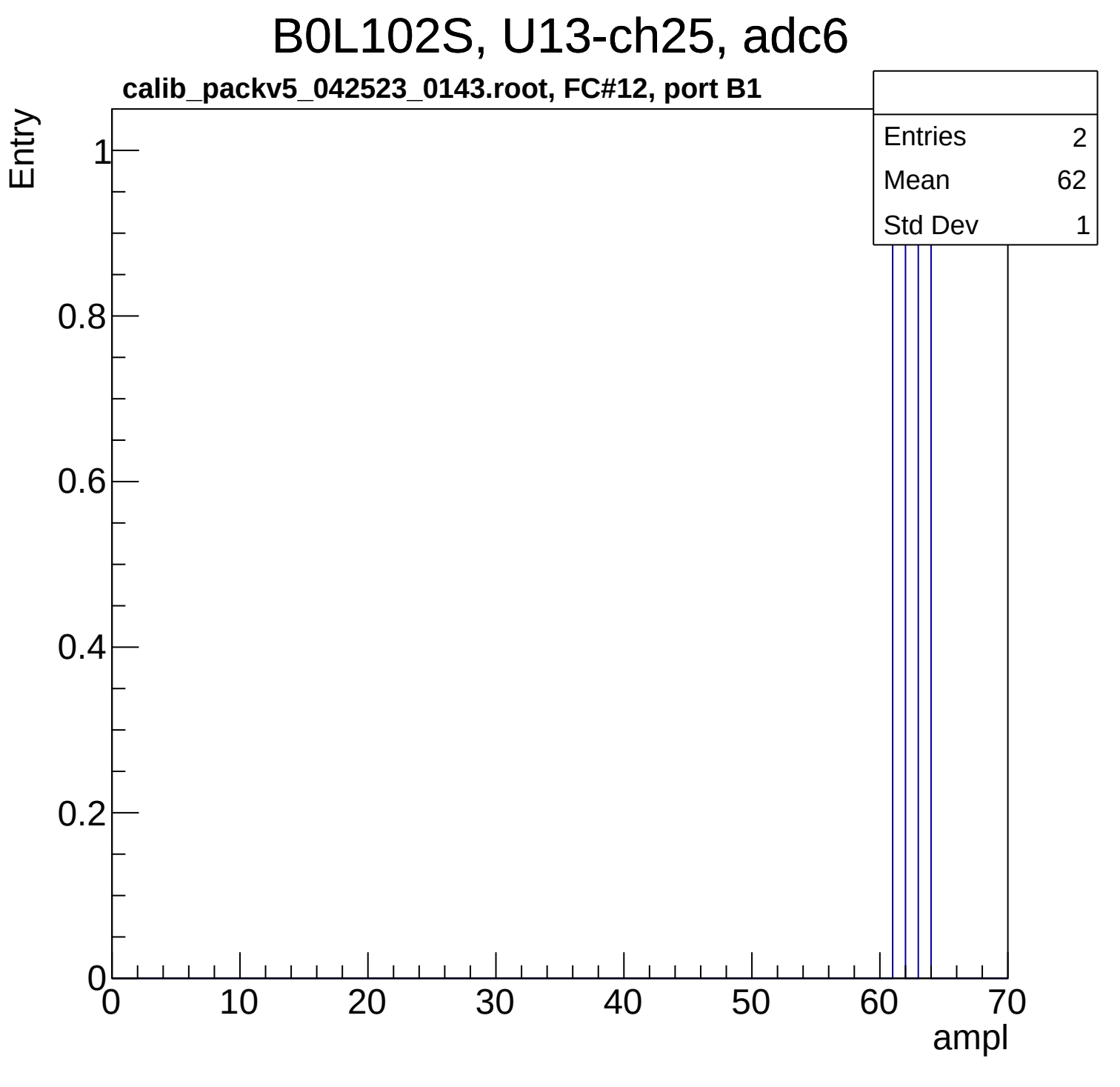
B0L102S, U13-ch25, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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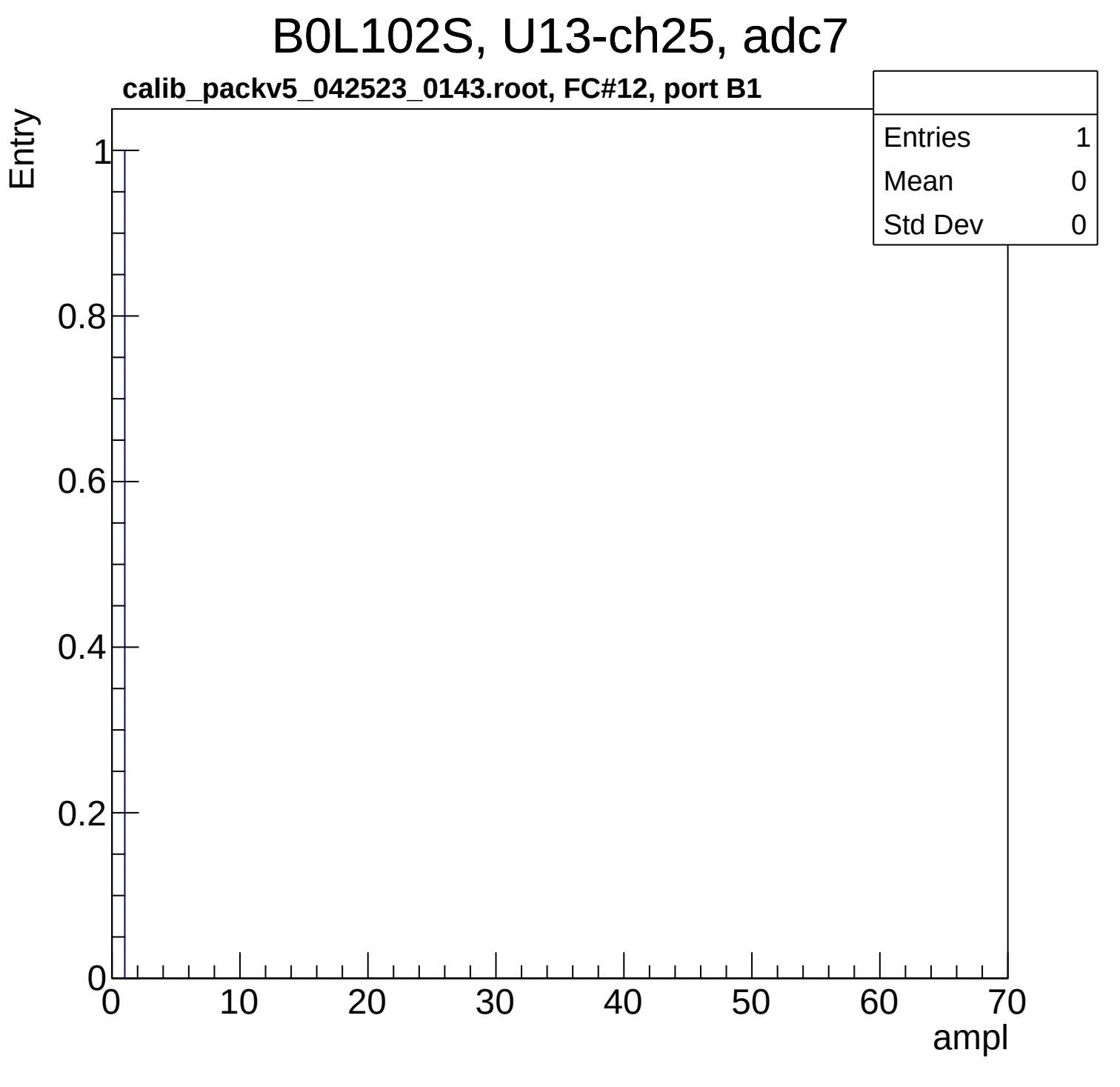
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch25, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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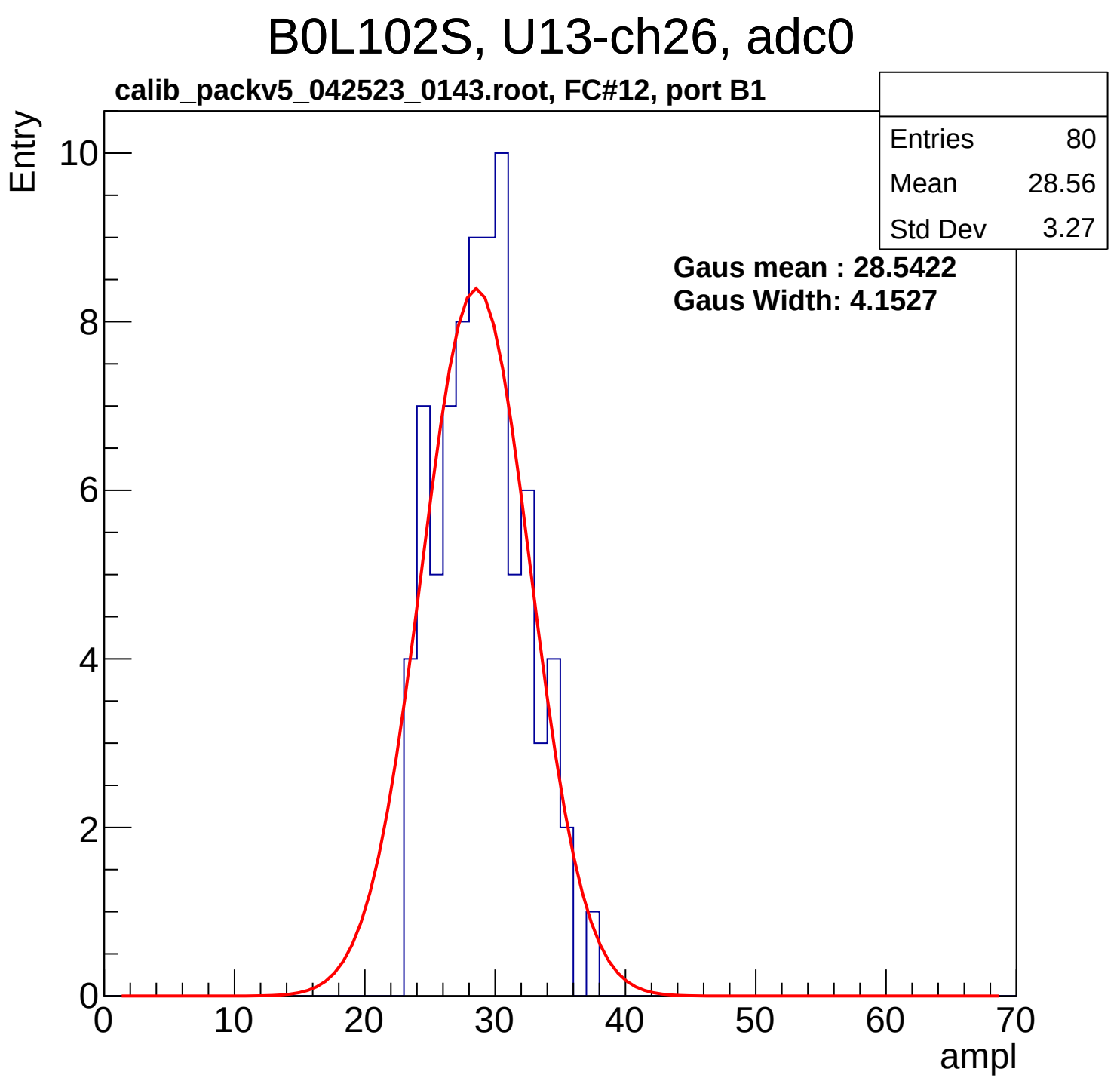
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch26, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 28.56
Std Dev 3.27
Gaus mean : 28.5422
Gaus Width: 4.1527
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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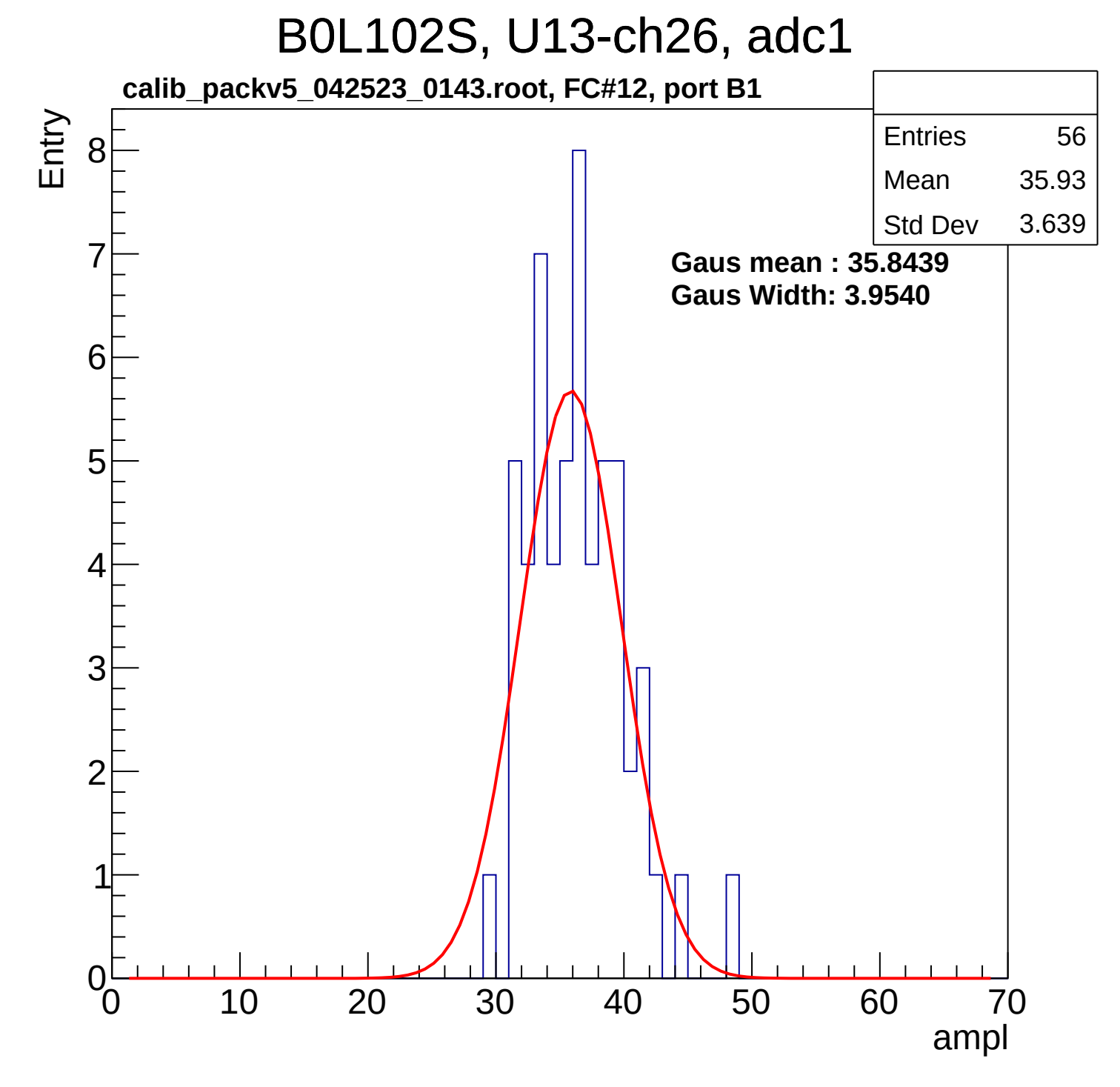
B0L102S, U13-ch26, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 35.93
Std Dev 3.639
Gaus mean : 35.8439
Gaus Width: 3.9540
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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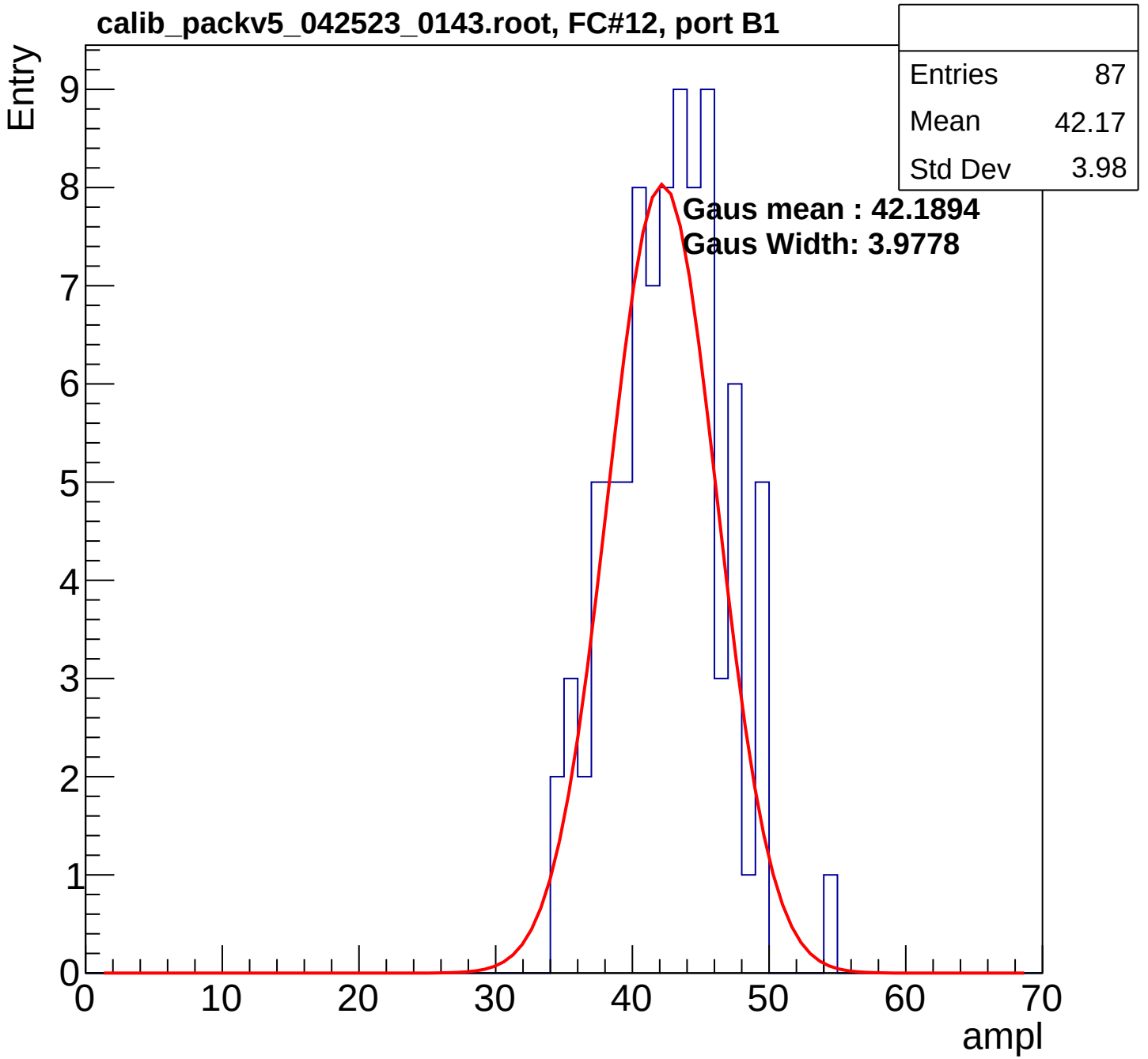
B0L102S, U13-ch26, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 42.17
Std Dev 3.98
Gaus mean : 42.1894
Gaus Width: 3.9778
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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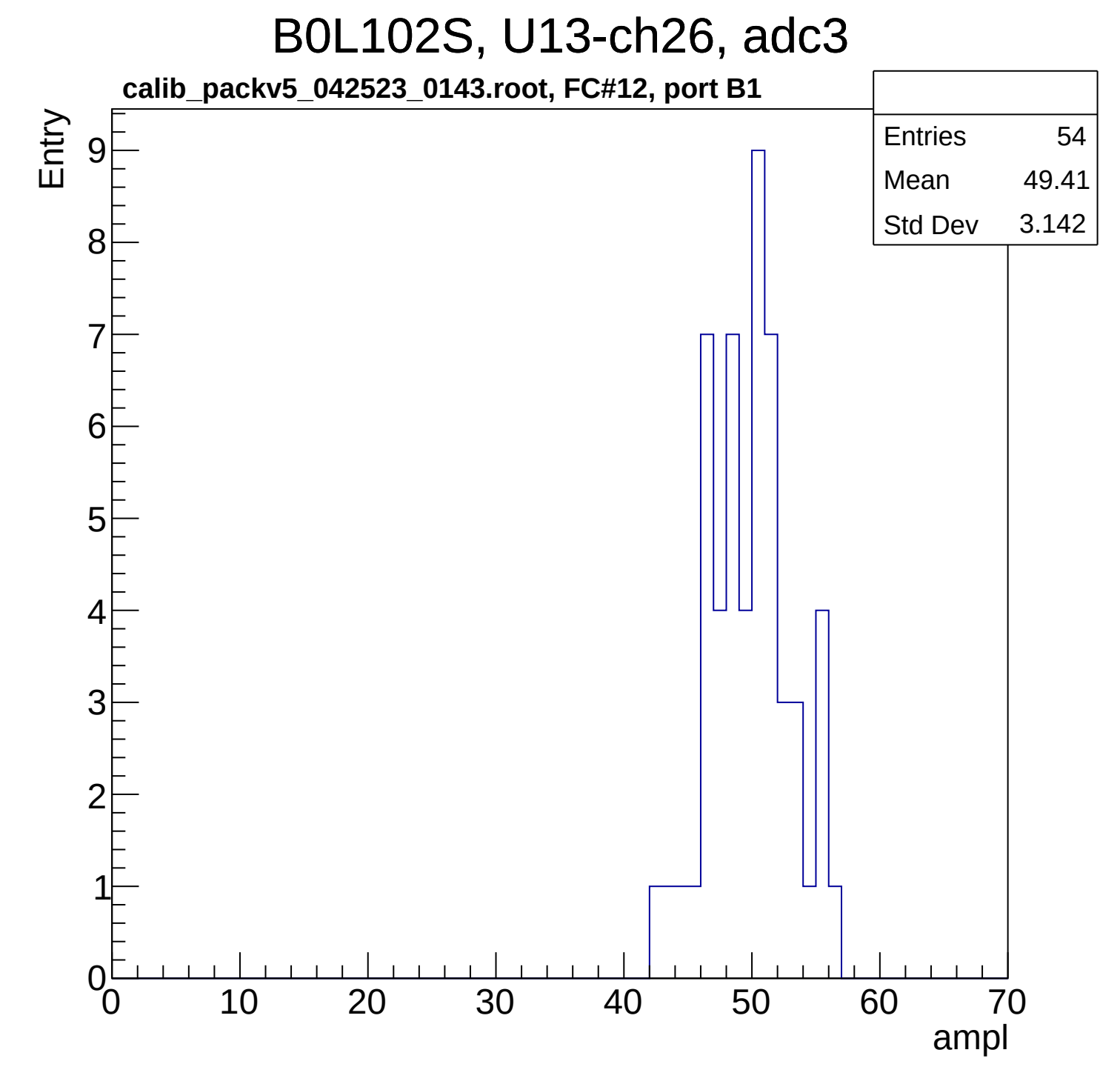
B0L102S, U13-ch26, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 49.41
Std Dev 3.142
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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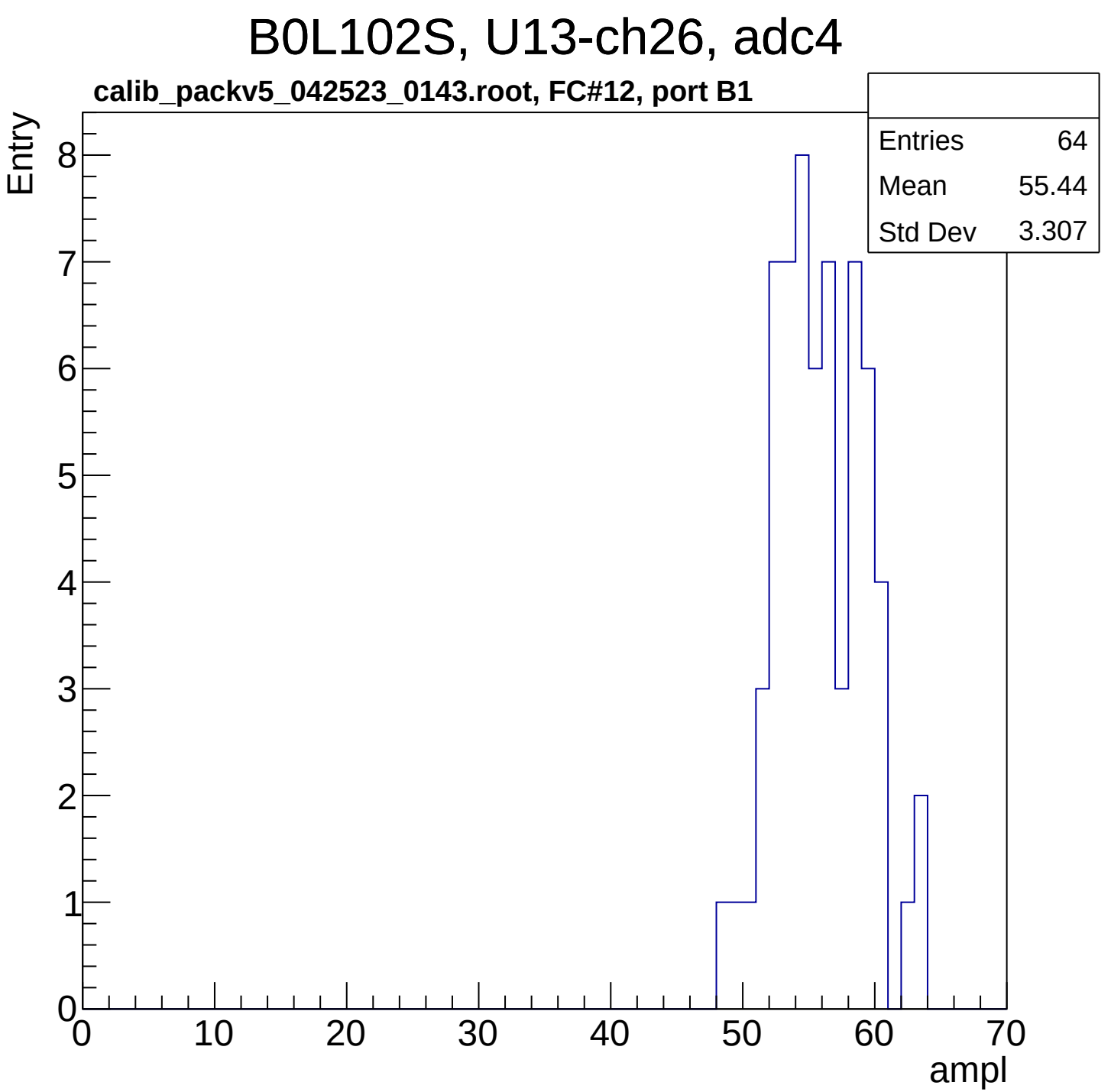
B0L102S, U13-ch26, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.44
Std Dev 3.307
Entry
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2
3
4
5
6
7
8
0
10
20
30
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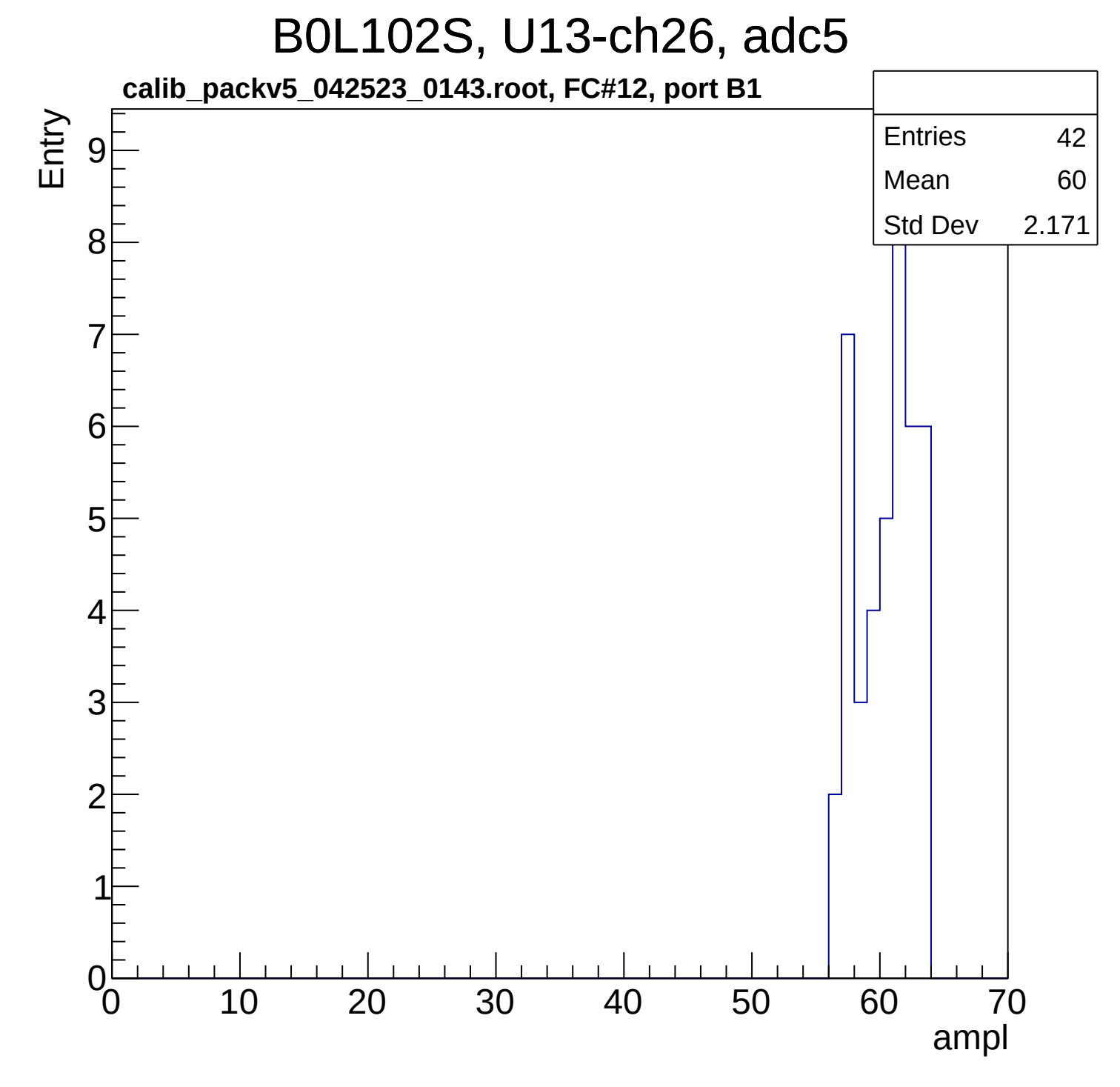
50
60
70
ampl


B0L102S, U13-ch26, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 60
Std Dev 2.171
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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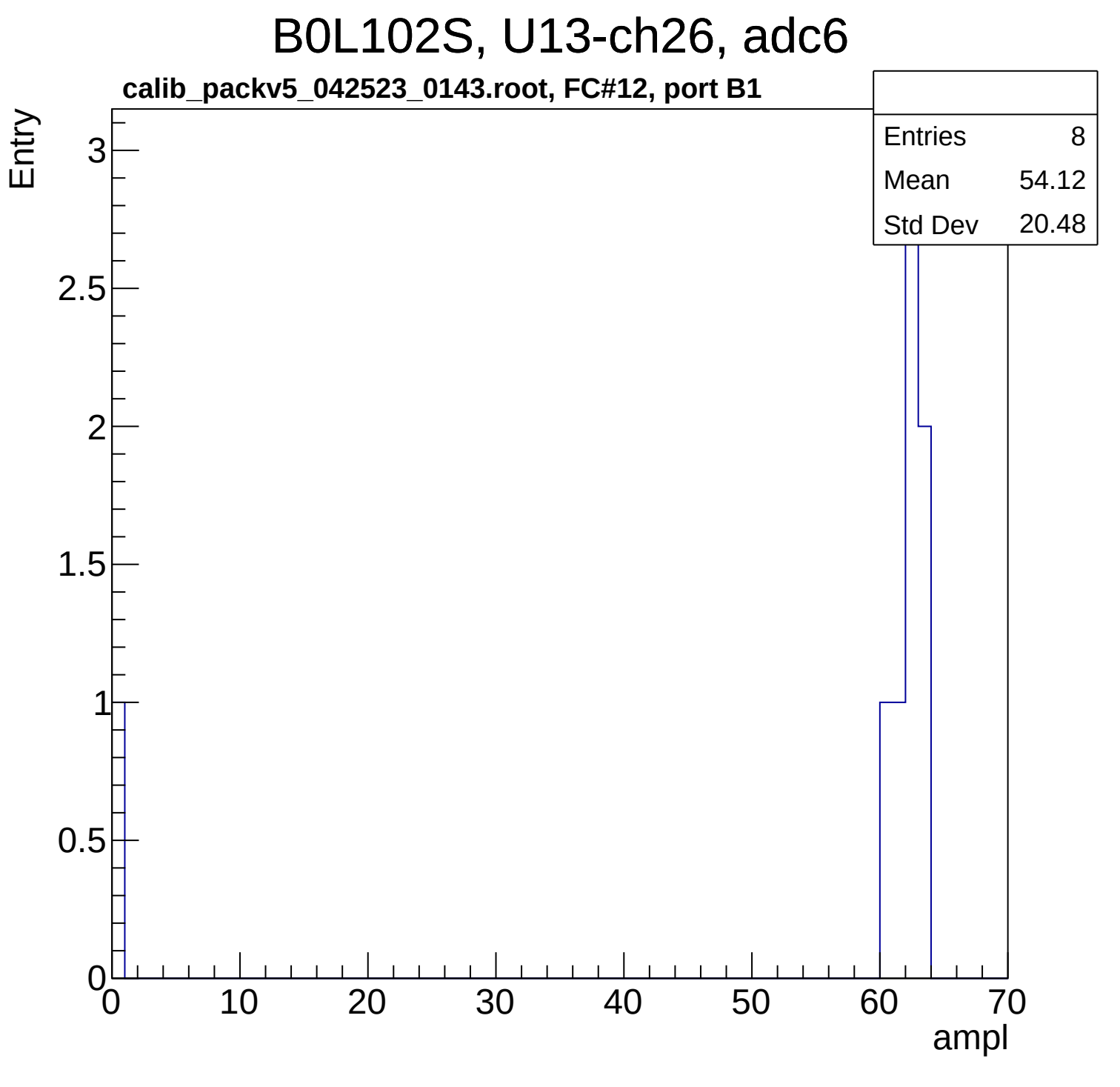
B0L102S, U13-ch26, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.12
Std Dev 20.48
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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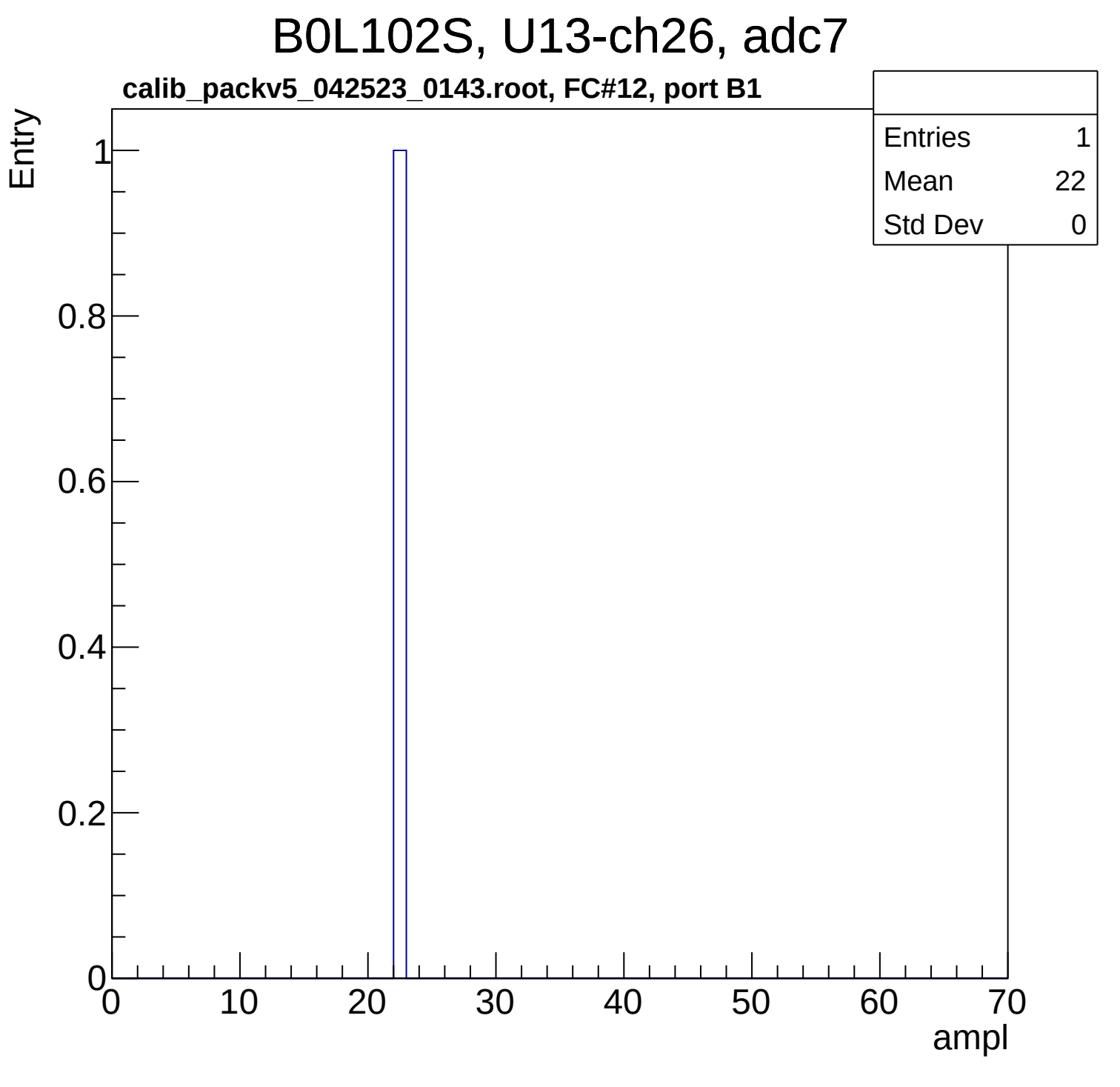
B0L102S, U13-ch26, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
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0
0
10
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50
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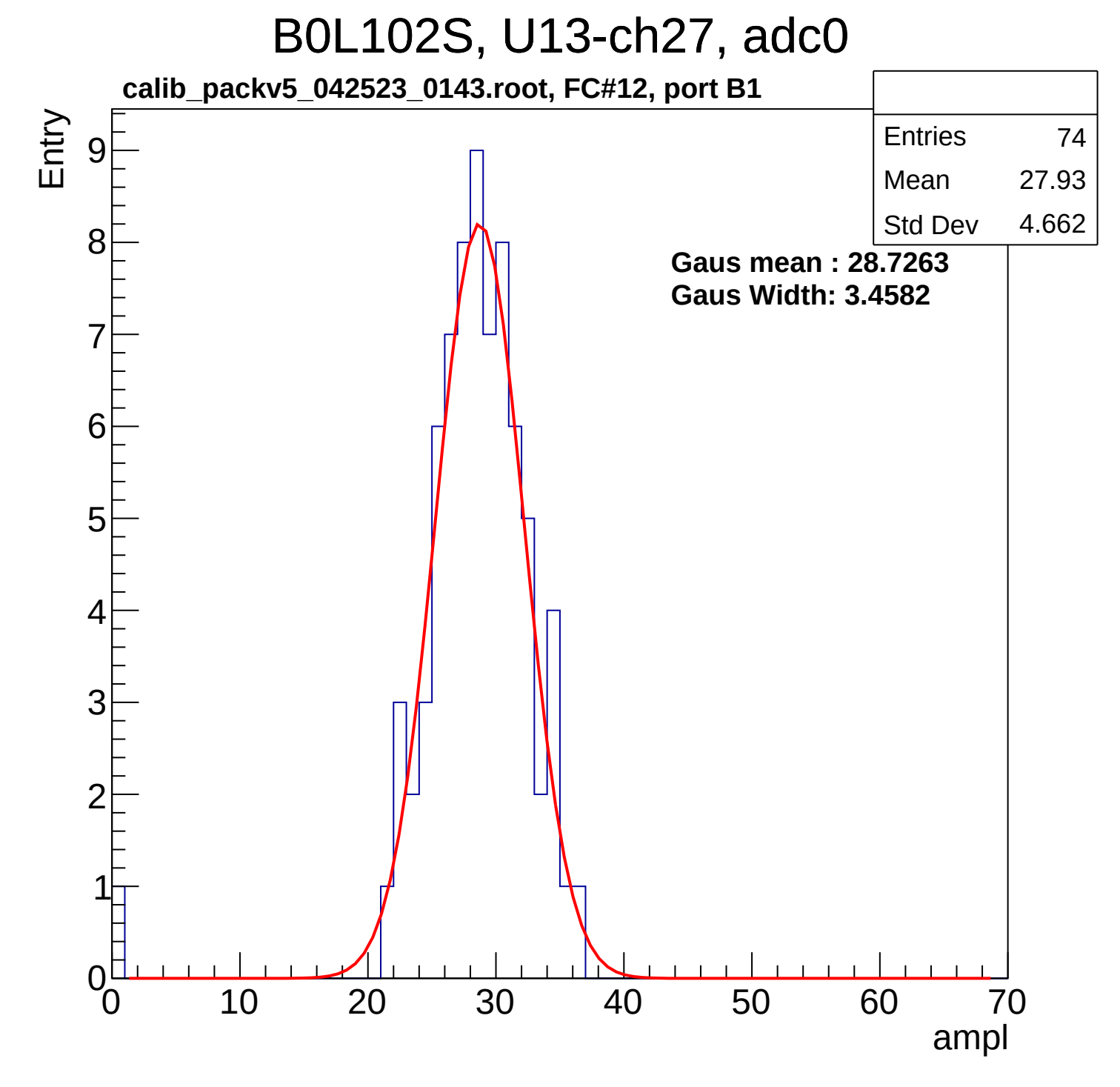
70
ampl


B0L102S, U13-ch27, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 27.93
Std Dev 4.662
Gaus mean : 28.7263
Gaus Width: 3.4582
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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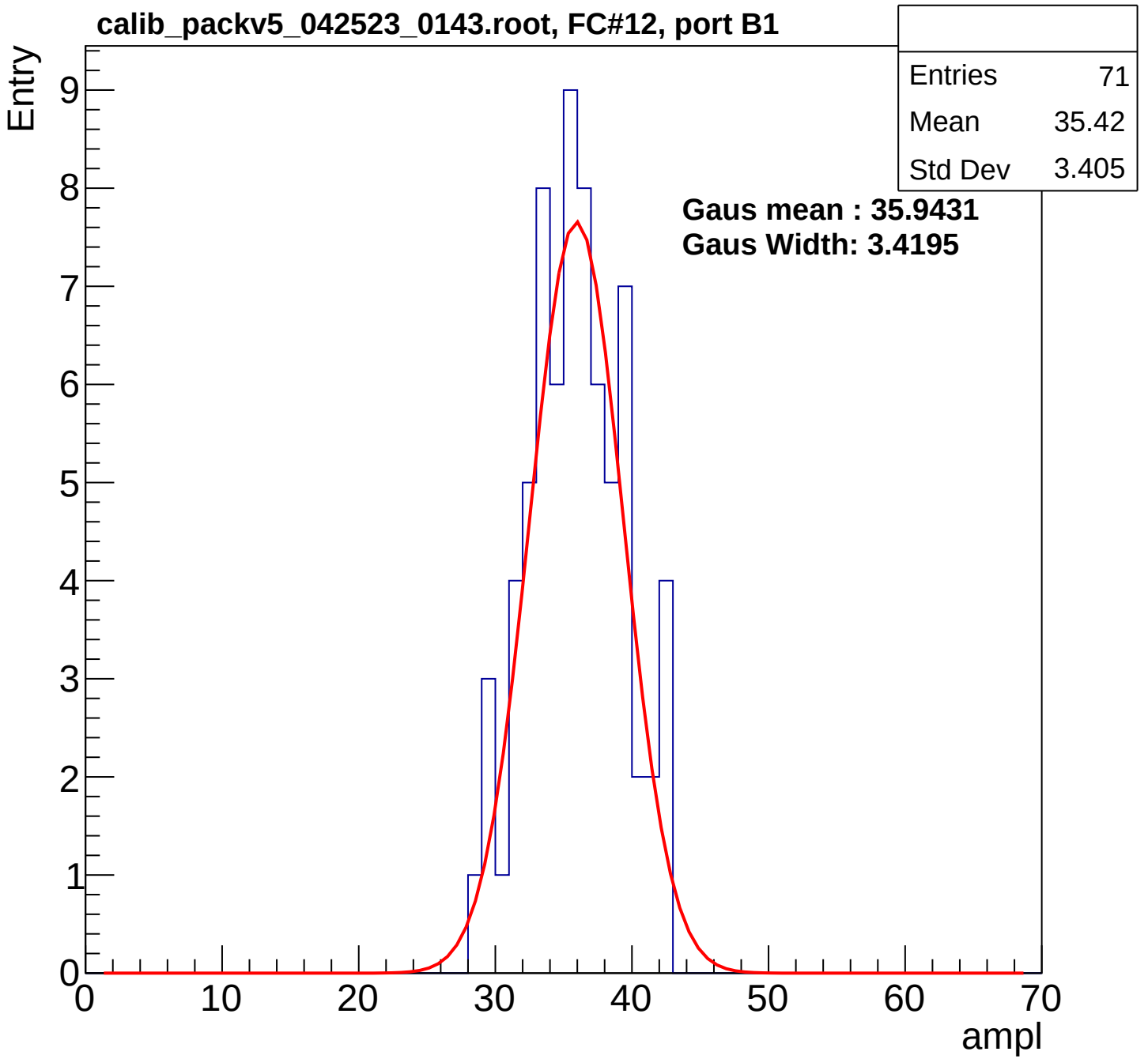
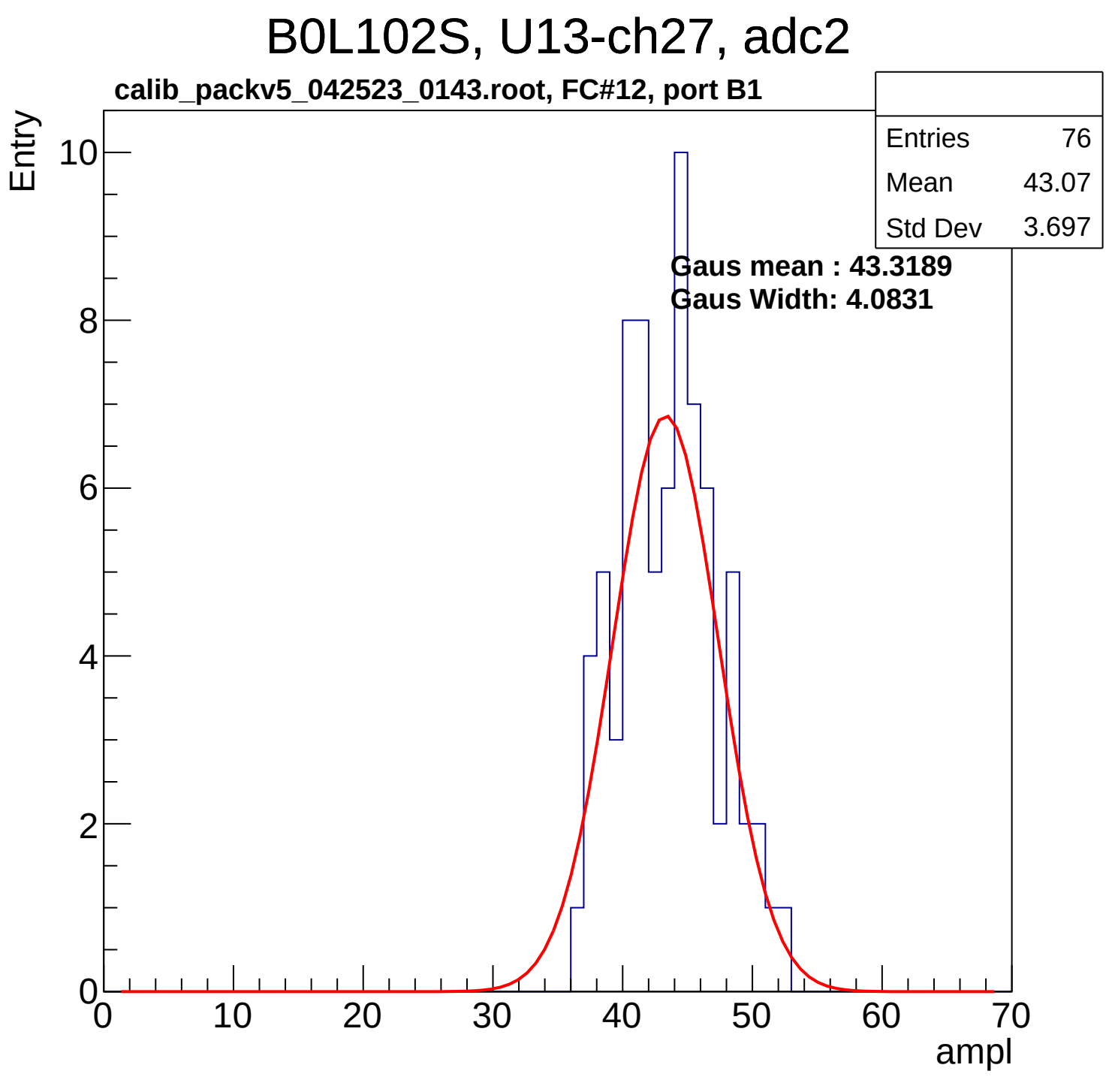

B0L102S, U13-ch27, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.42
Std Dev 3.405
Gaus mean : 35.9431
Gaus Width: 3.4195
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch27, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 43.07
Std Dev 3.697
Gaus mean : 43.3189
Gaus Width: 4.0831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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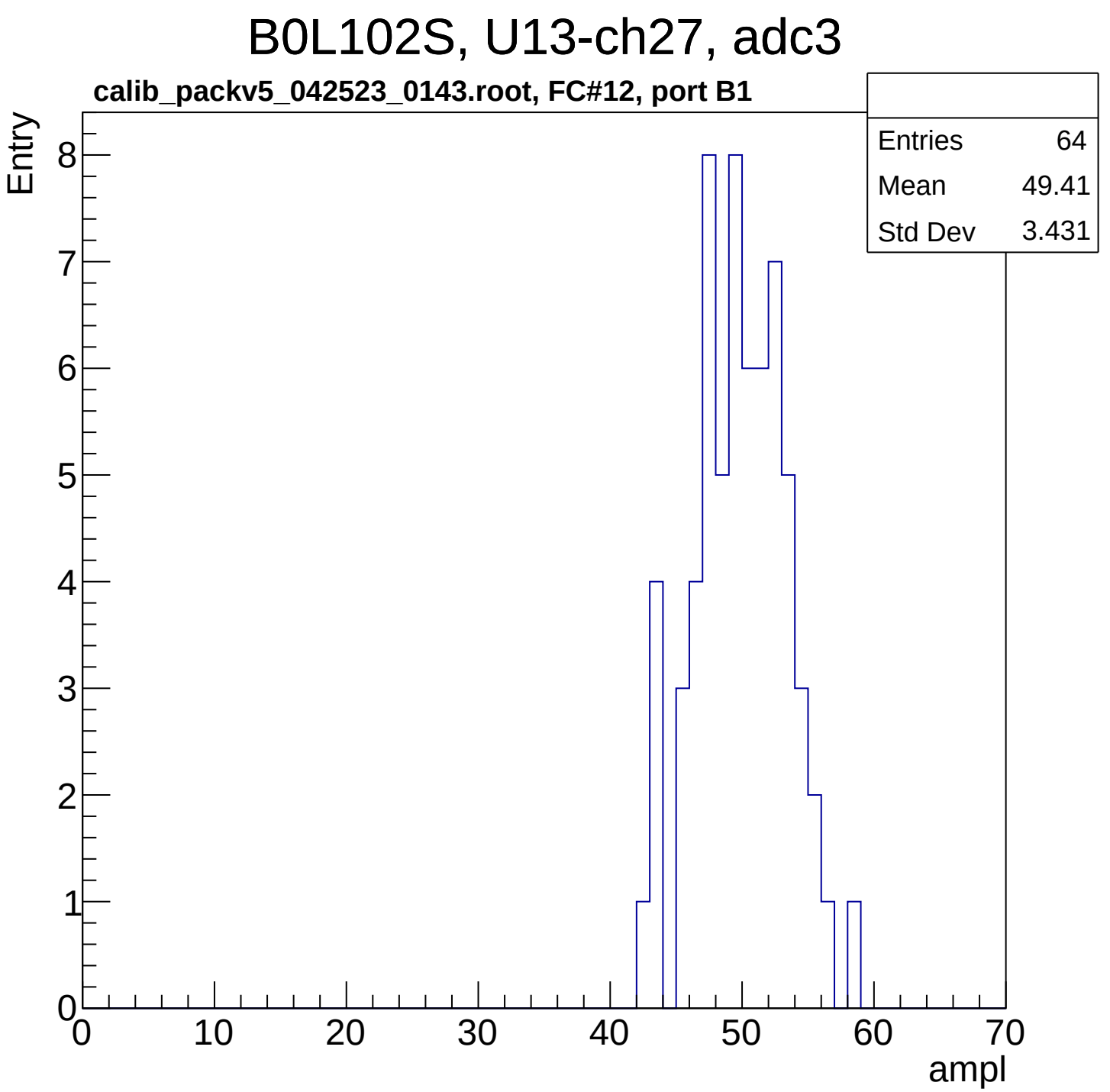
B0L102S, U13-ch27, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.41
Std Dev 3.431
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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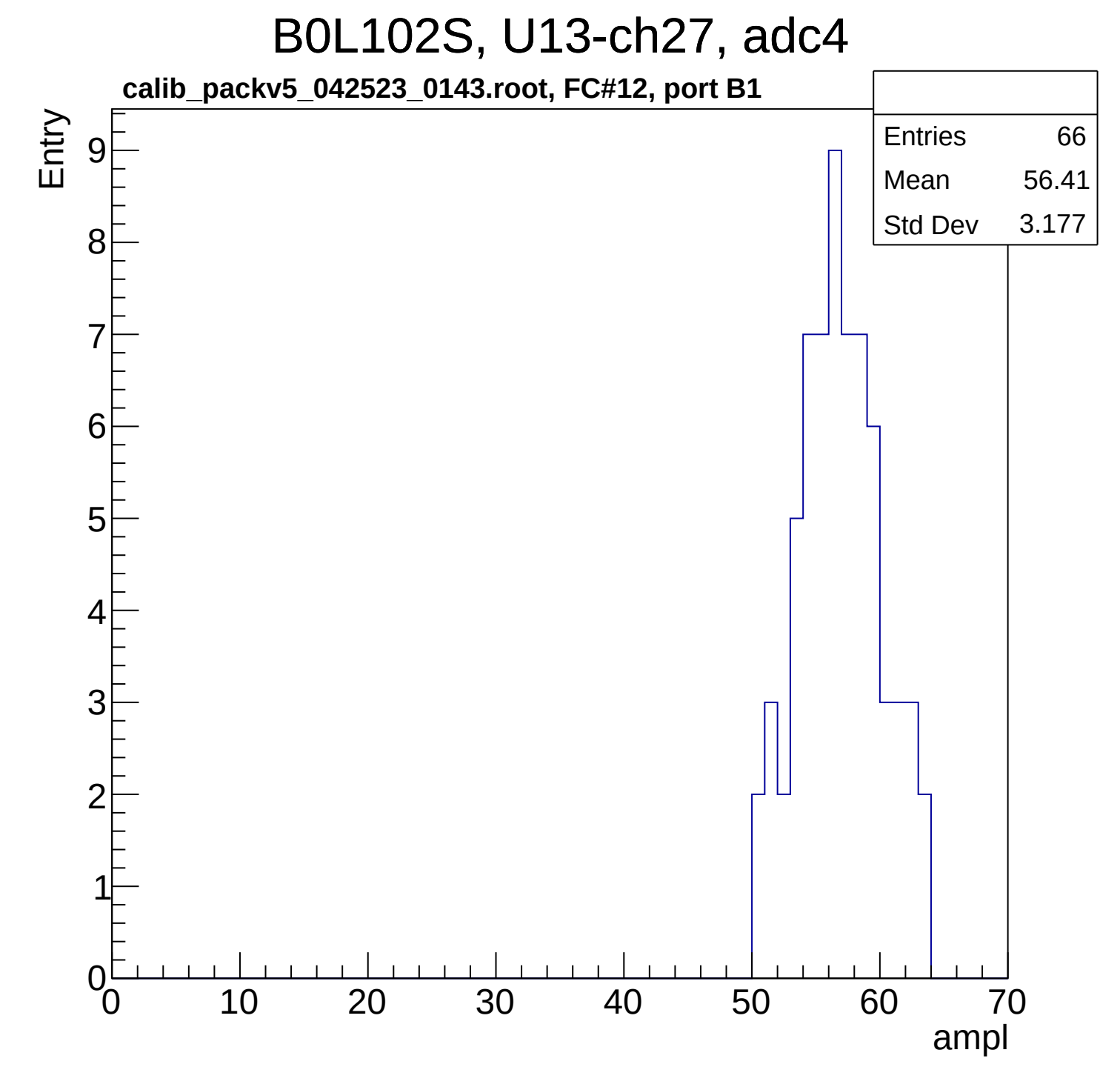
B0L102S, U13-ch27, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 56.41
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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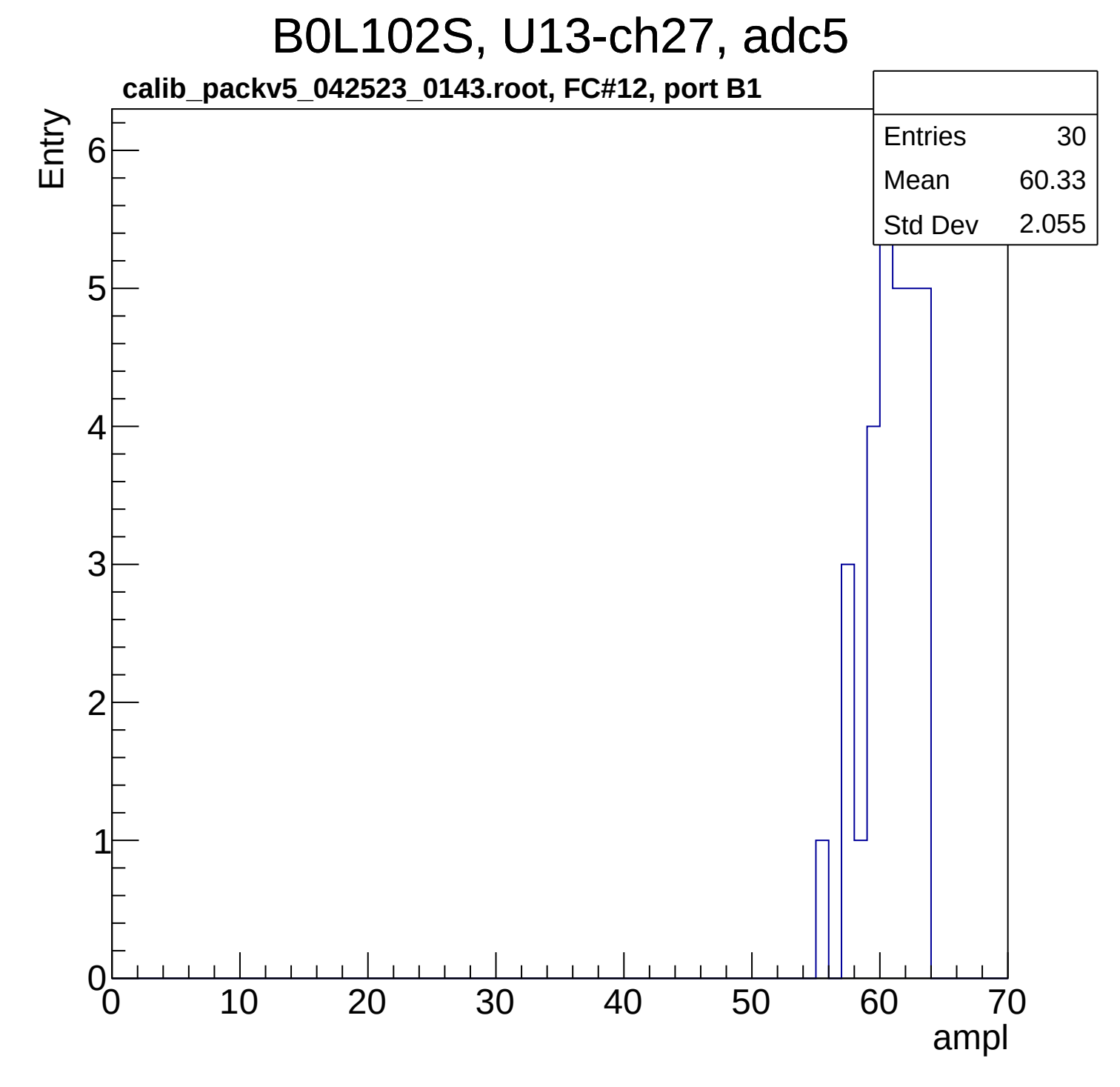
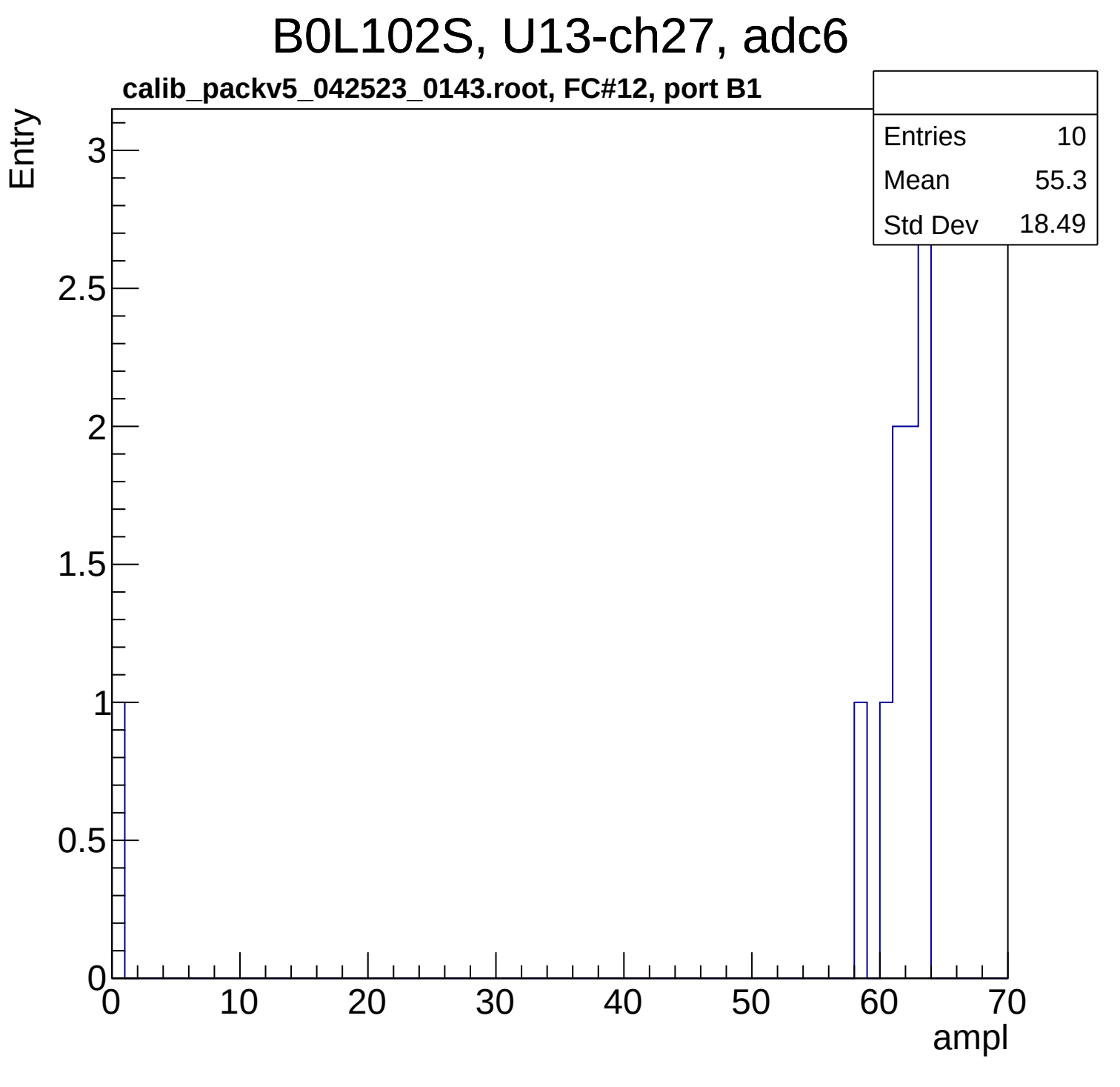

B0L102S, U13-ch27, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 60.33
Std Dev 2.055
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch27, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.3
Std Dev 18.49
Entry
ampl
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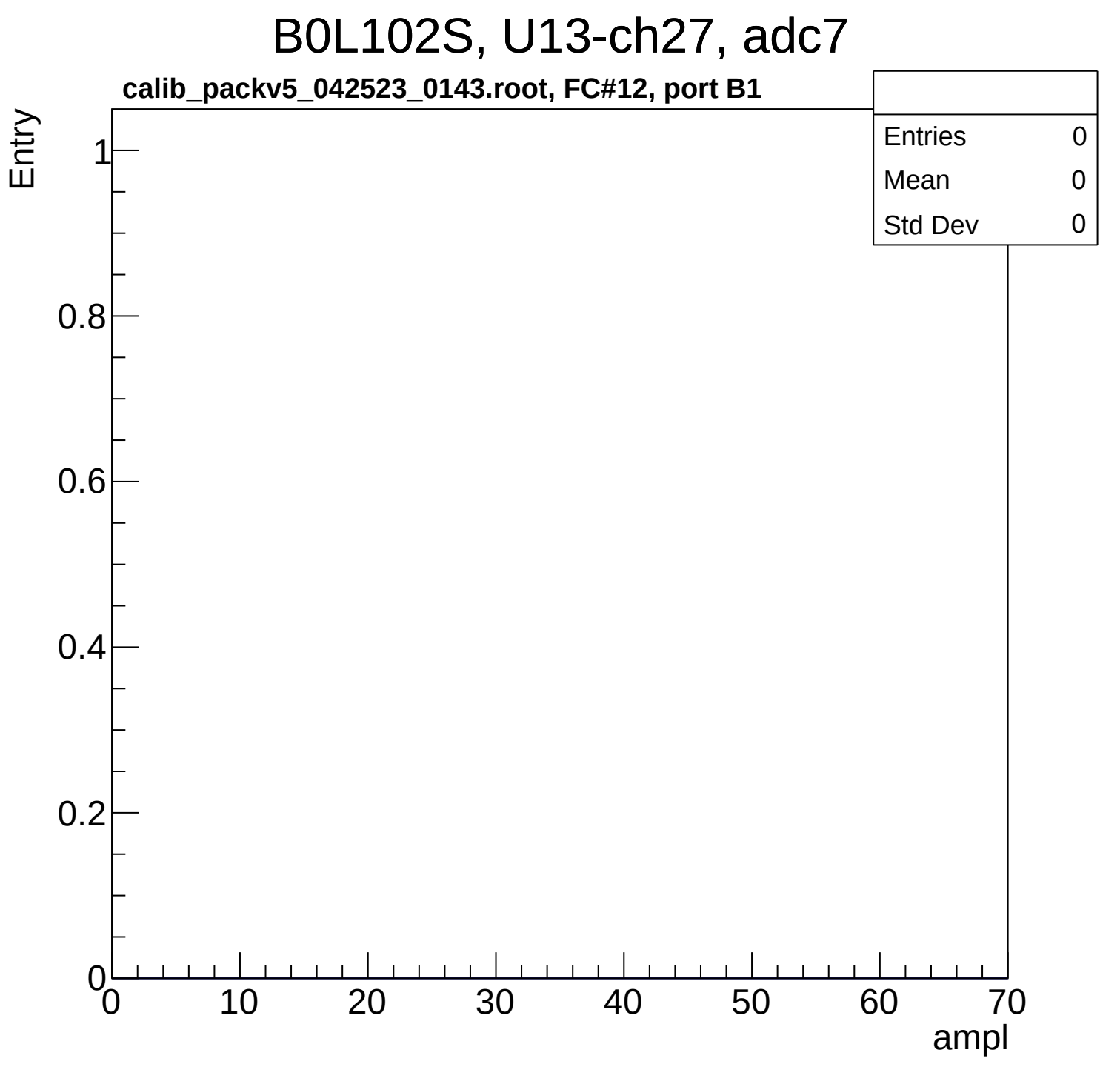
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch27, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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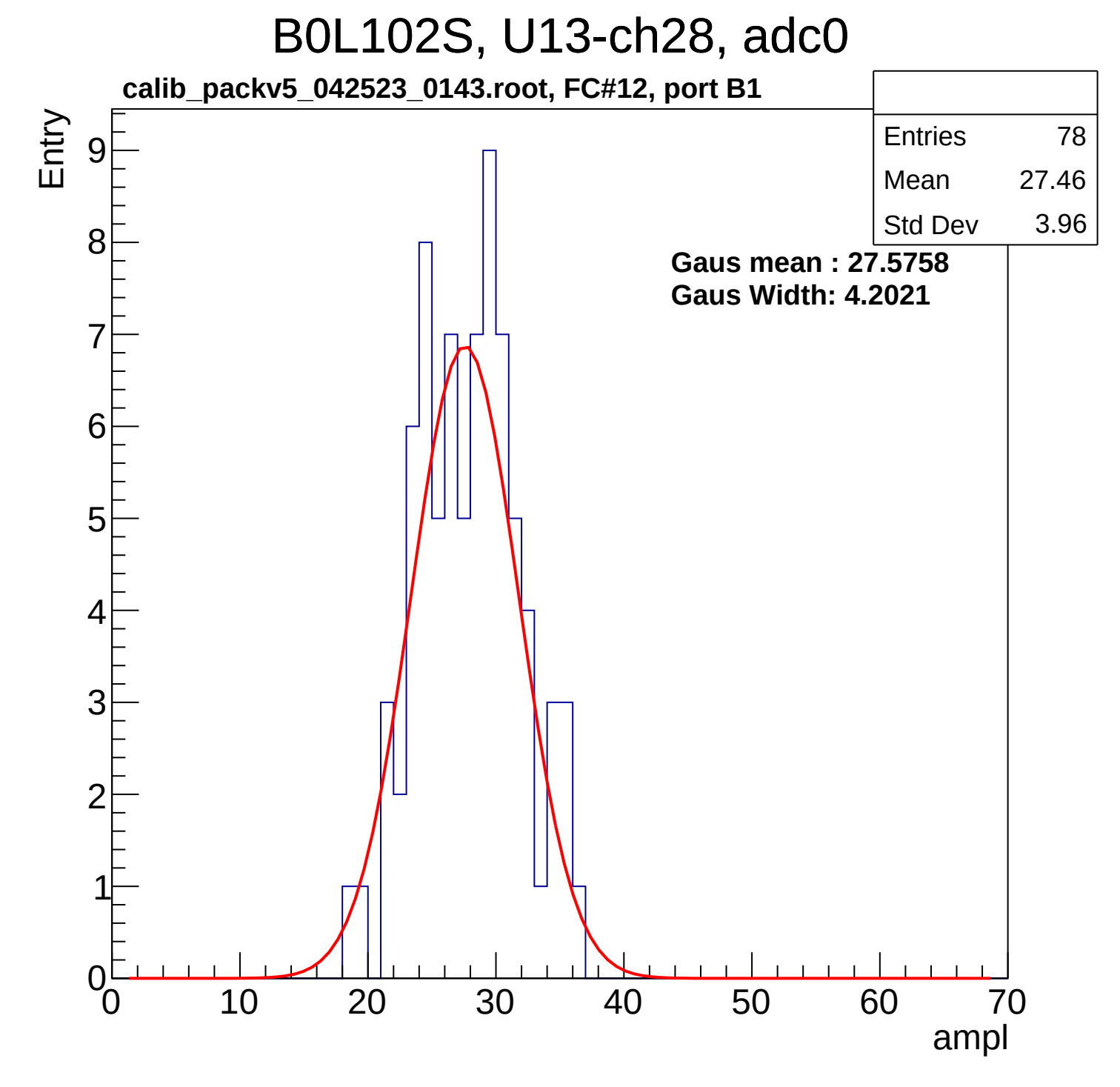
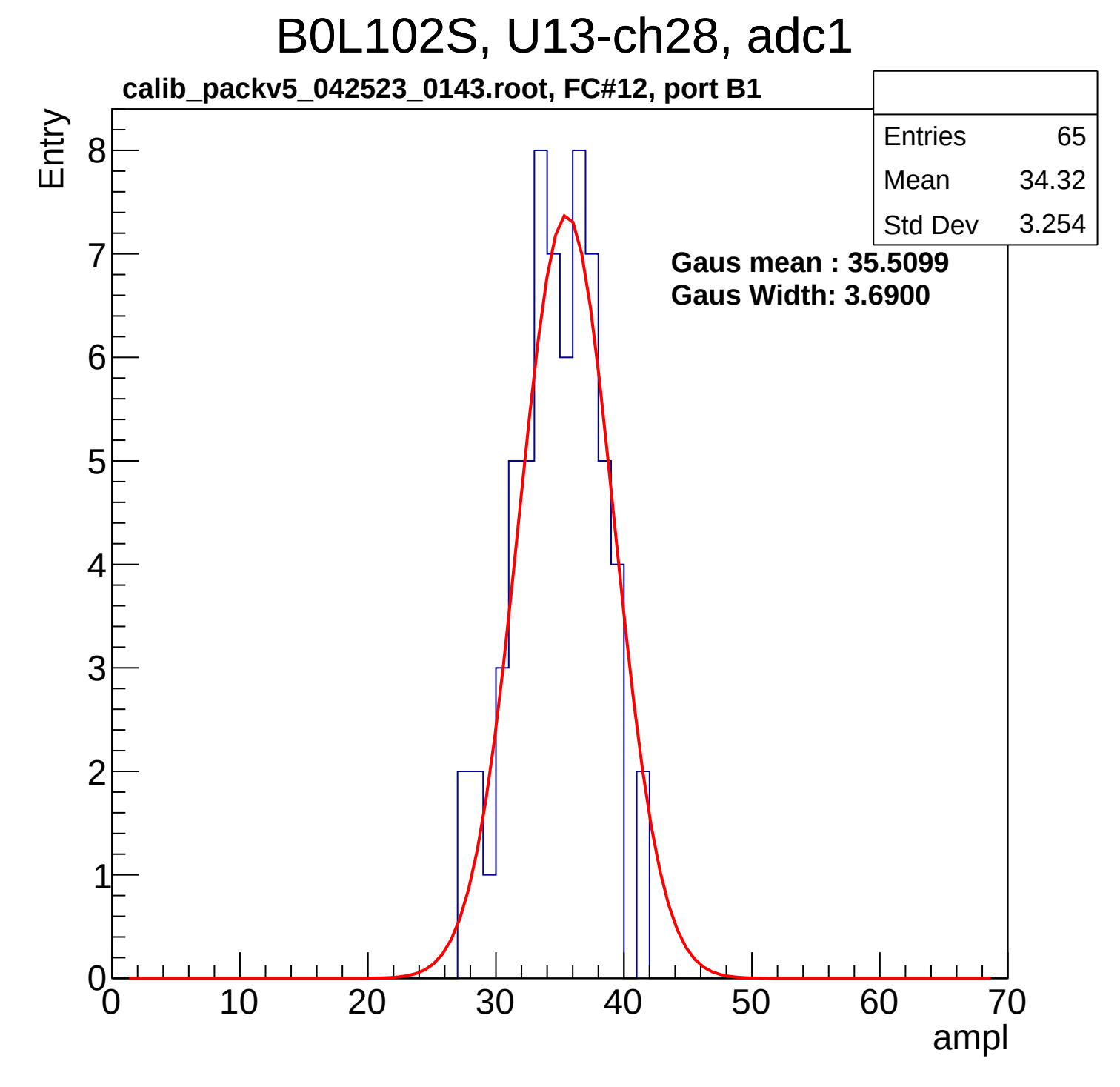
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch28, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 27.46
Std Dev 3.96
Gaus mean : 27.5758
Gaus Width: 4.2021
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch28, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 34.32
Std Dev 3.254
Gaus mean : 35.5099
Gaus Width: 3.6900
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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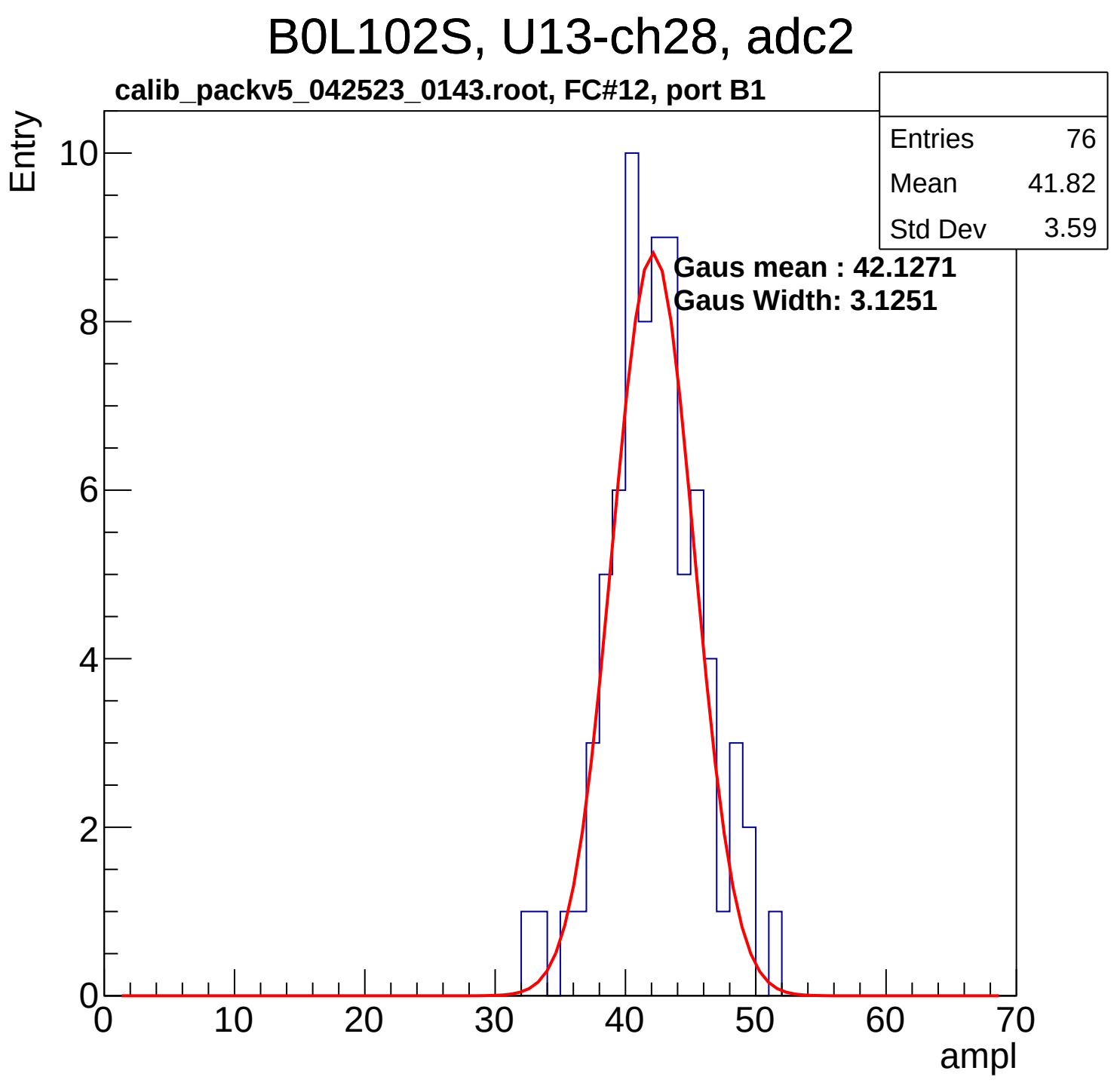
B0L102S, U13-ch28, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 41.82
Std Dev 3.59
Gaus mean : 42.1271
Gaus Width: 3.1251
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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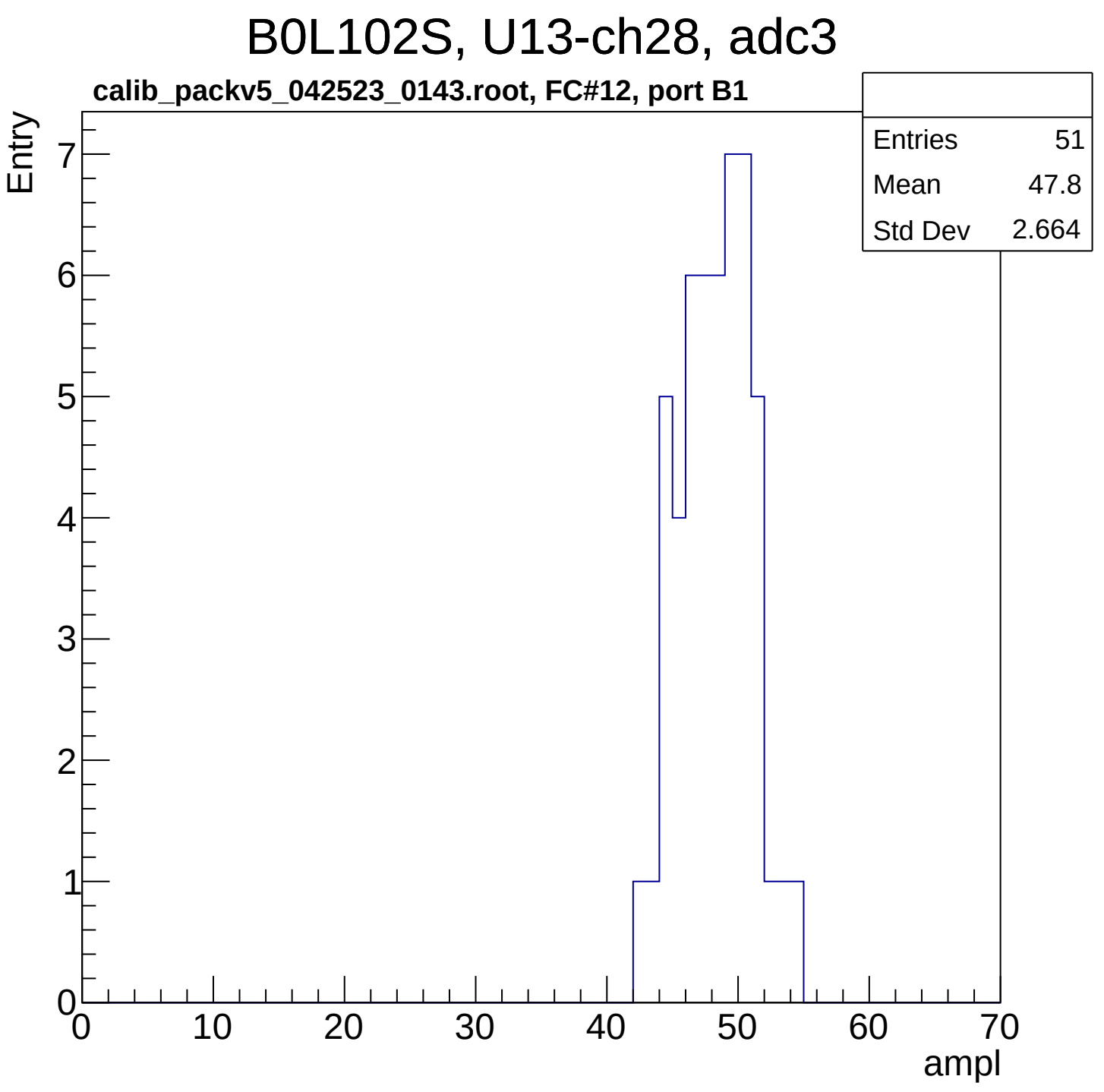
B0L102S, U13-ch28, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 47.8
Std Dev 2.664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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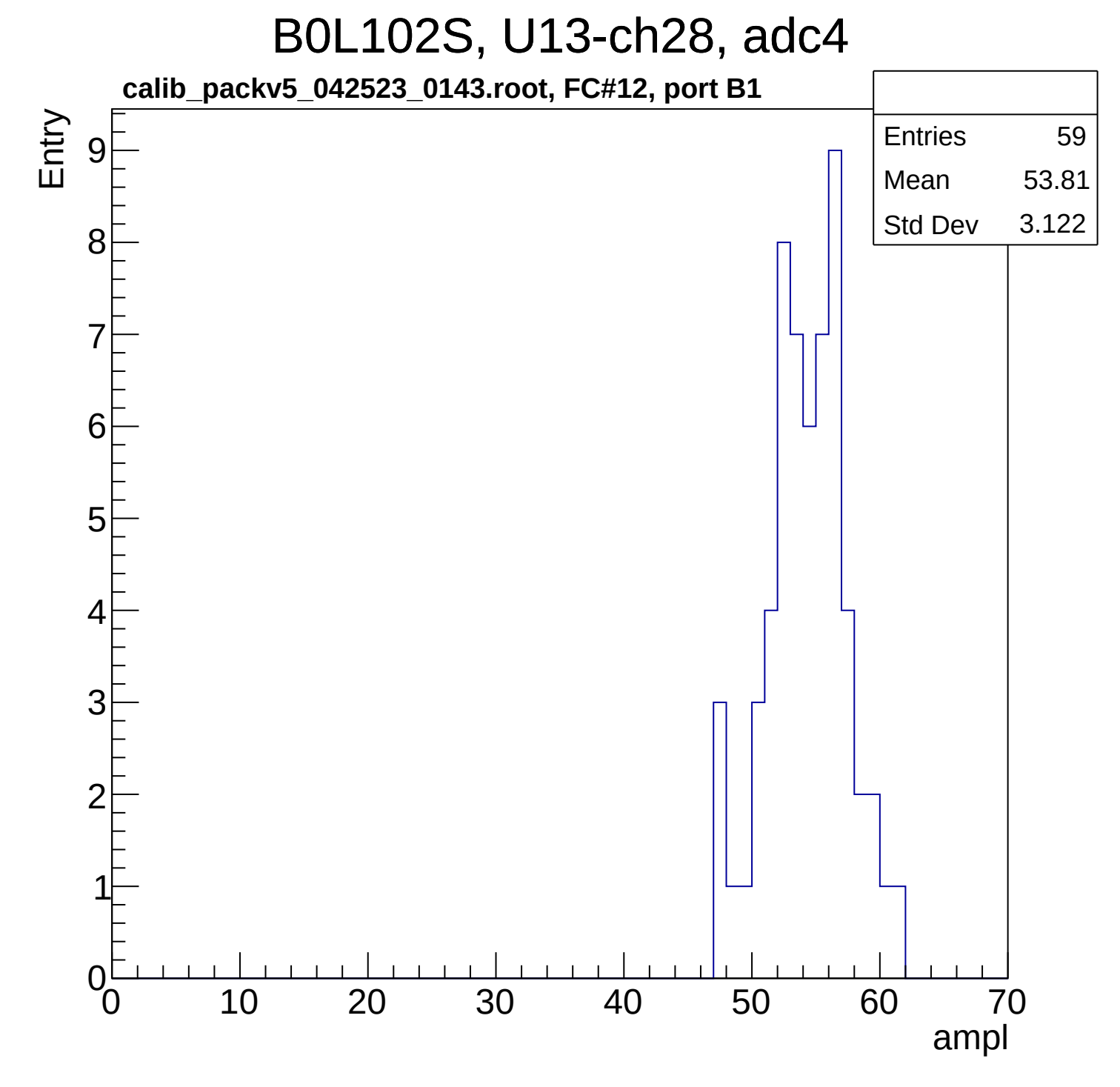
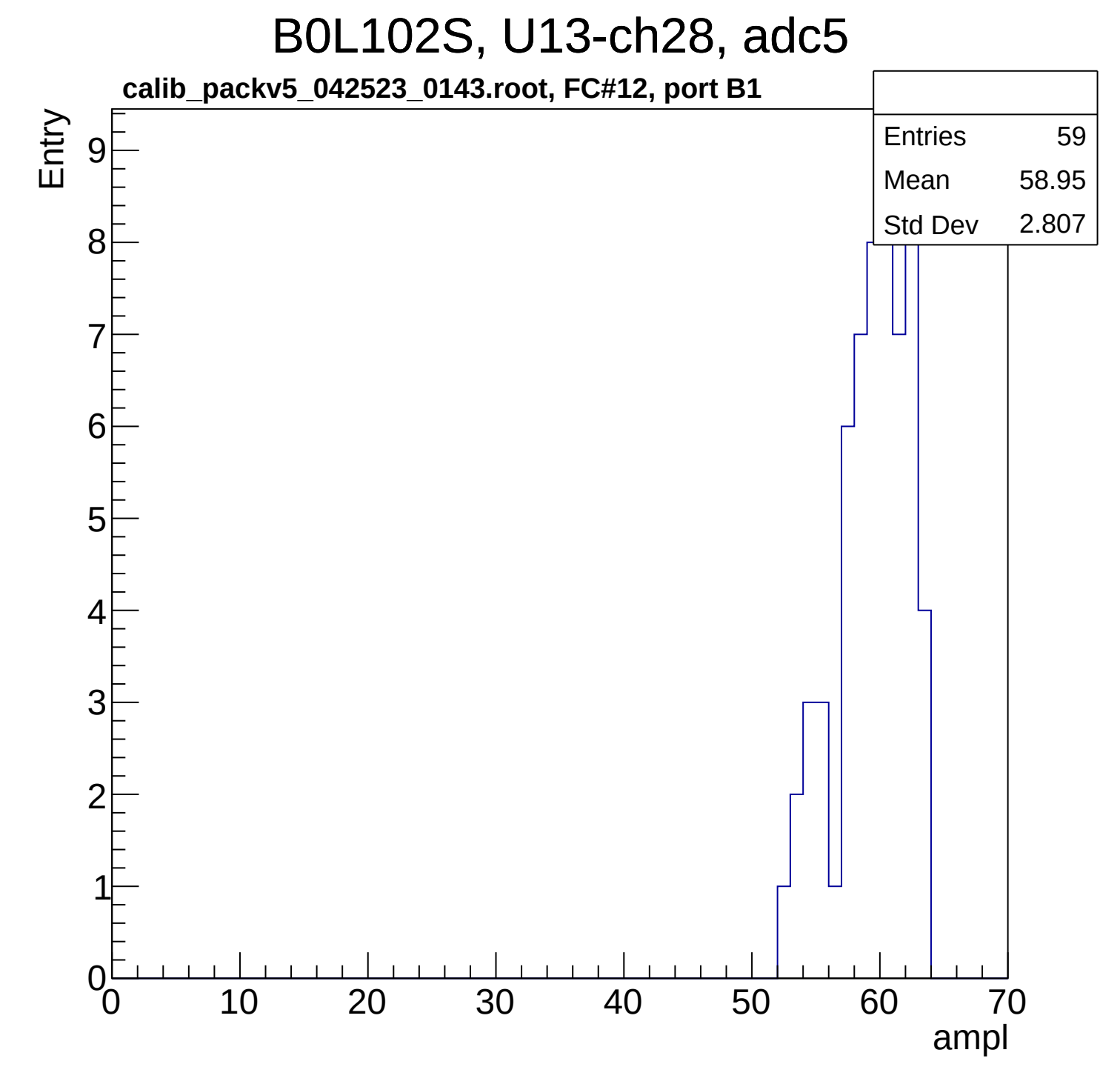

B0L102S, U13-ch28, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 53.81
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch28, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.95
Std Dev 2.807
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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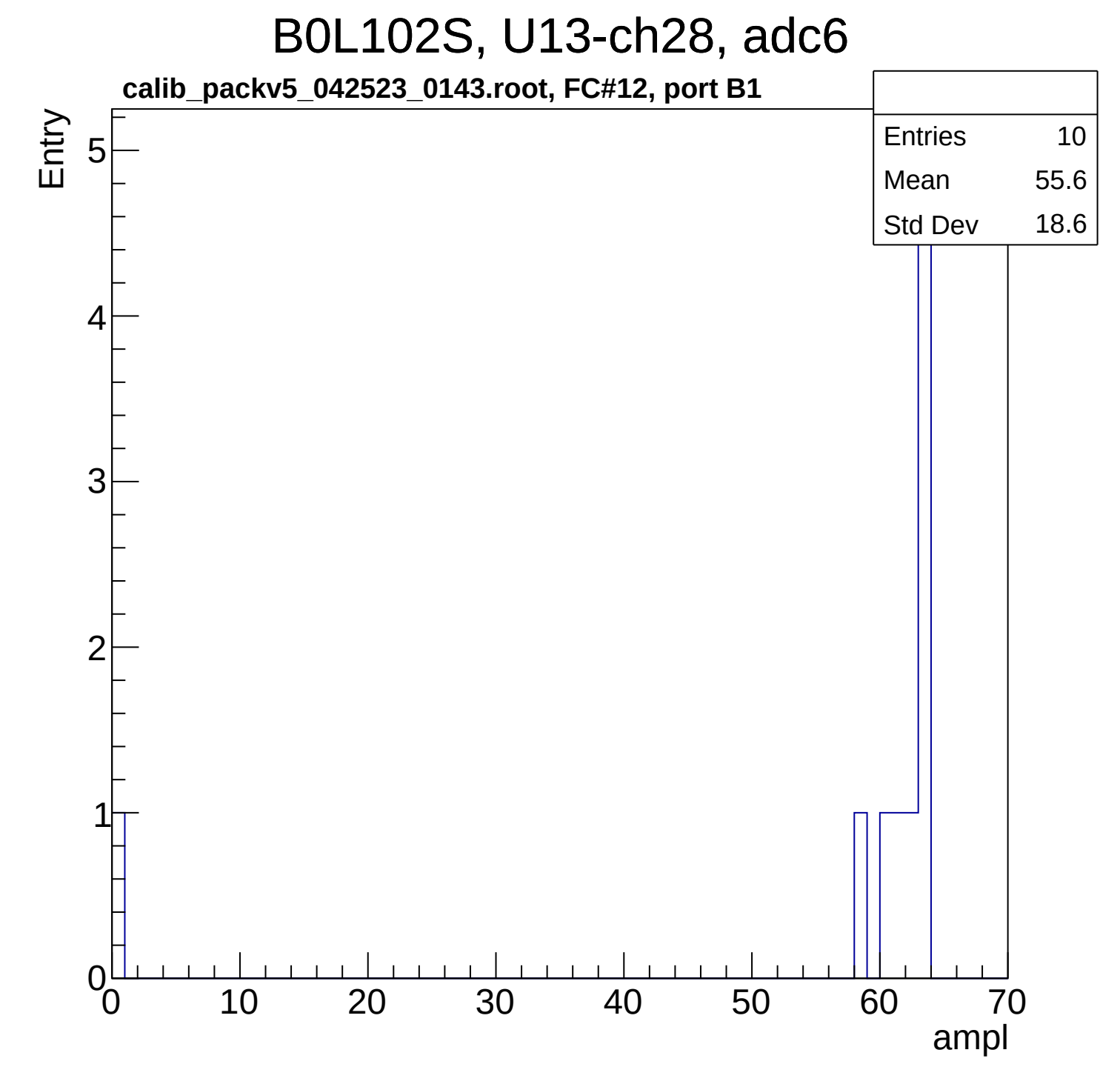
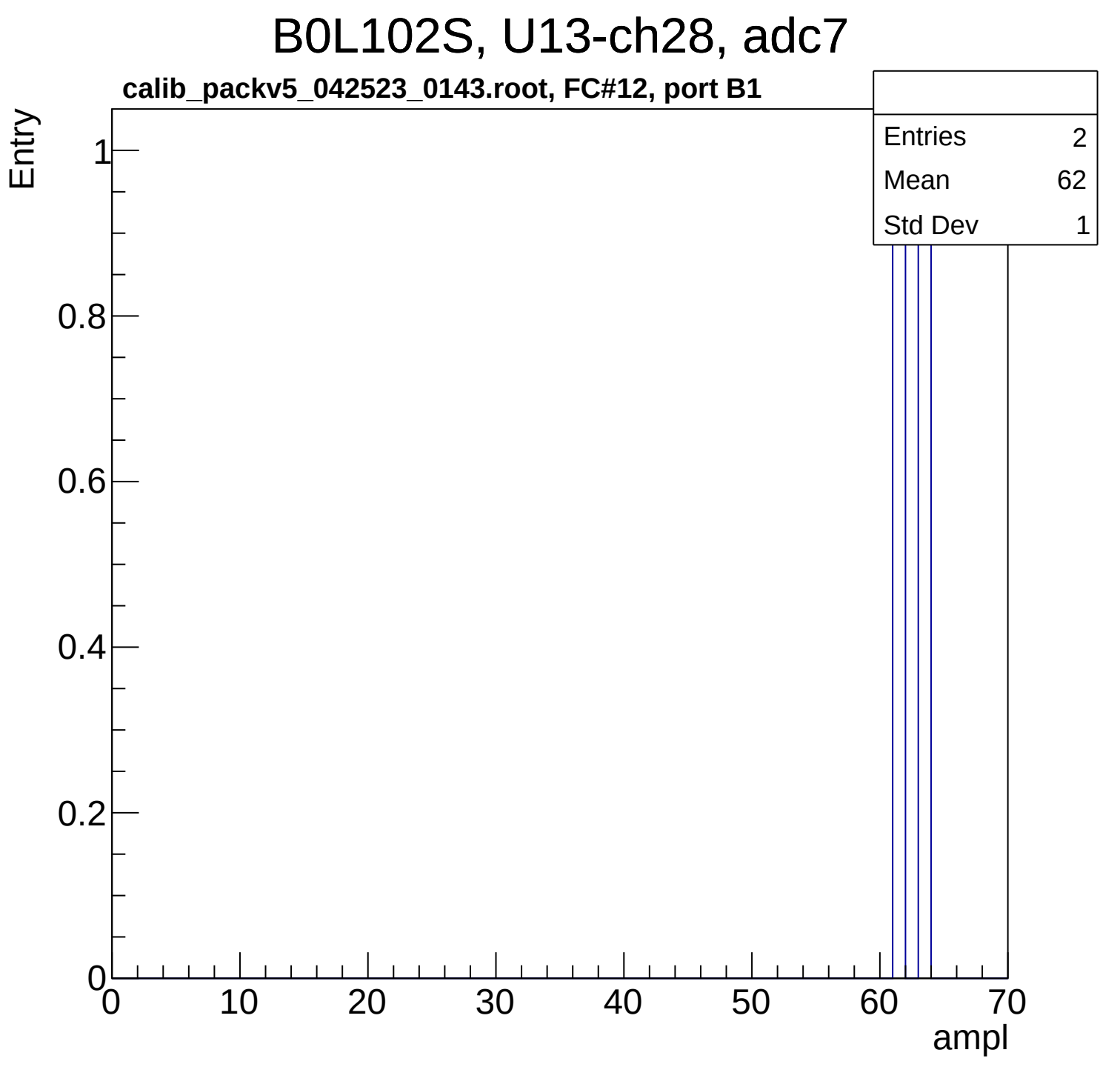
40
50
60
70


B0L102S, U13-ch28, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.6
Std Dev 18.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch28, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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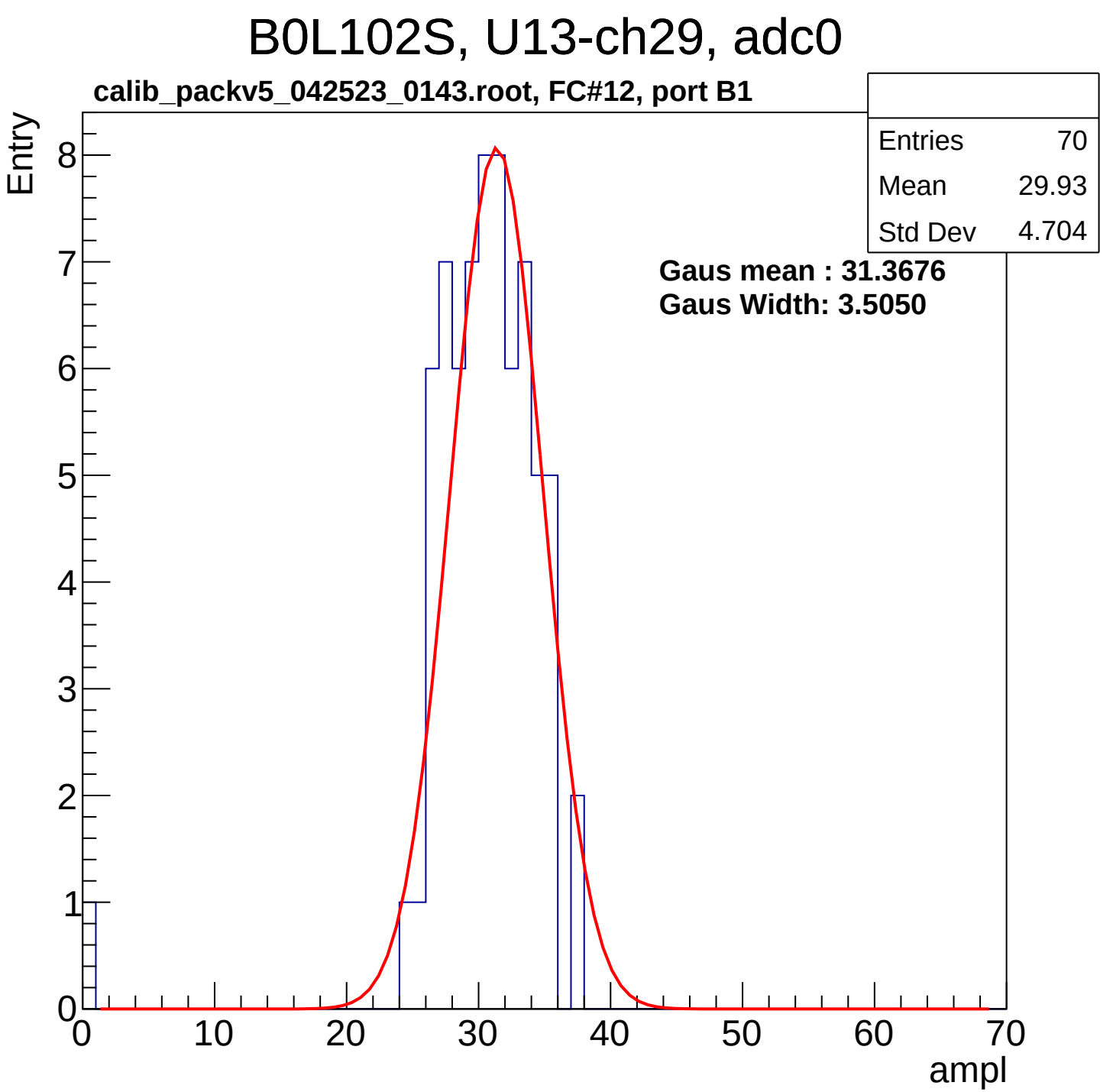
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch29, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 29.93
Std Dev 4.704
Gaus mean : 31.3676
Gaus Width: 3.5050
Entry
ampl
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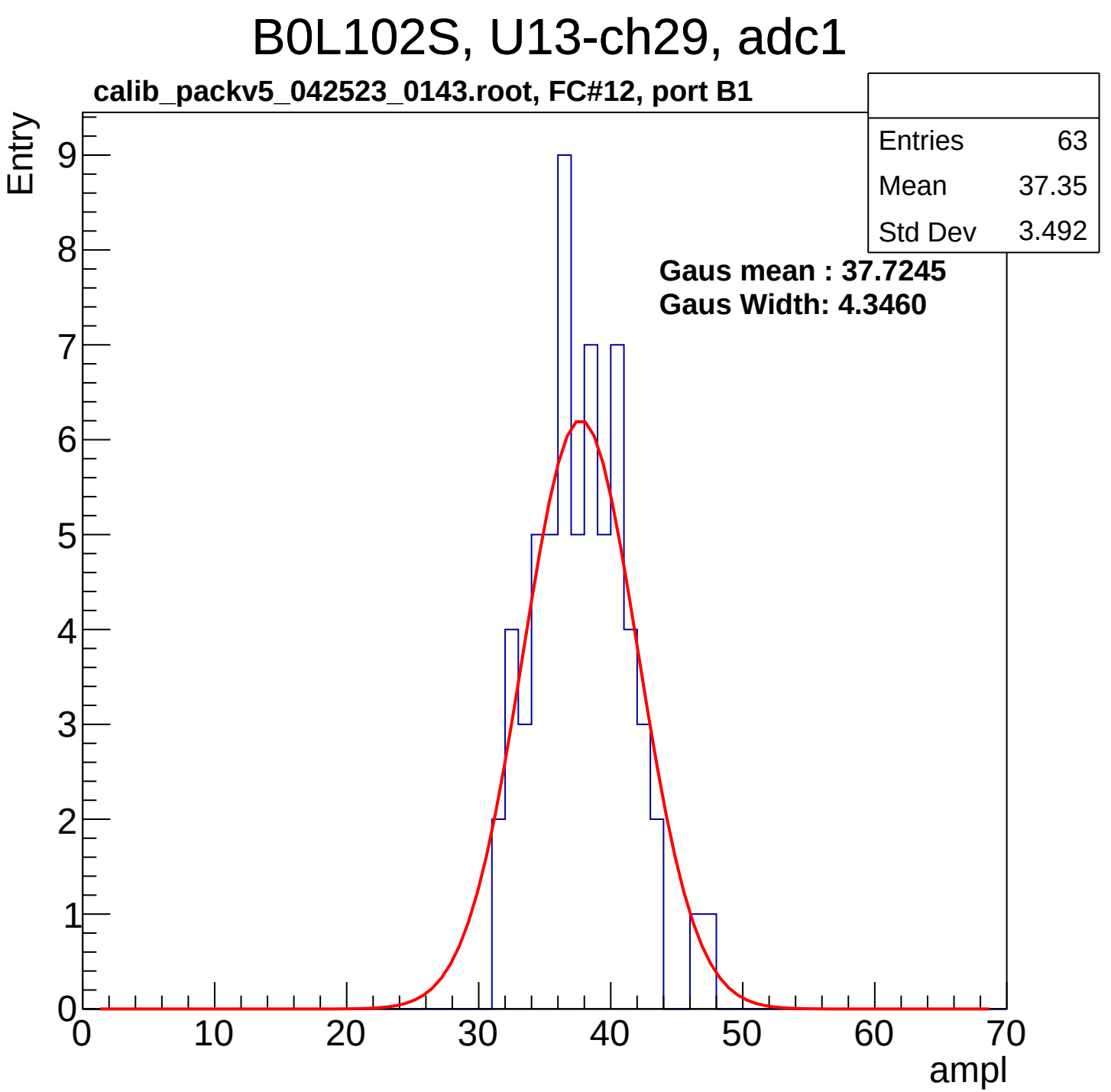
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch29, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 37.35
Std Dev 3.492
Gaus mean : 37.7245
Gaus Width: 4.3460
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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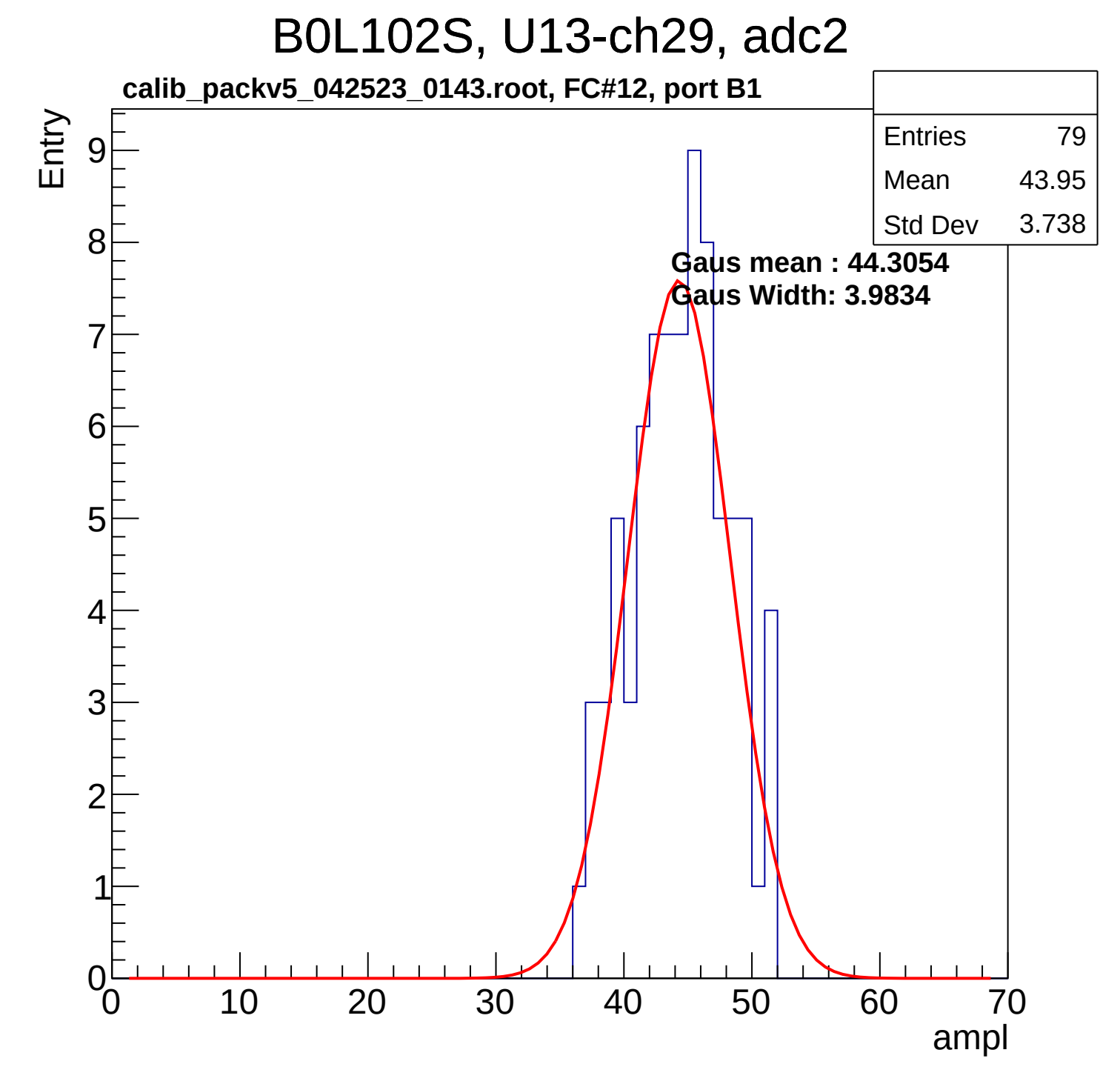
60
70


B0L102S, U13-ch29, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 43.95
Std Dev 3.738
Gaus mean : 44.3054
Gaus Width: 3.9834
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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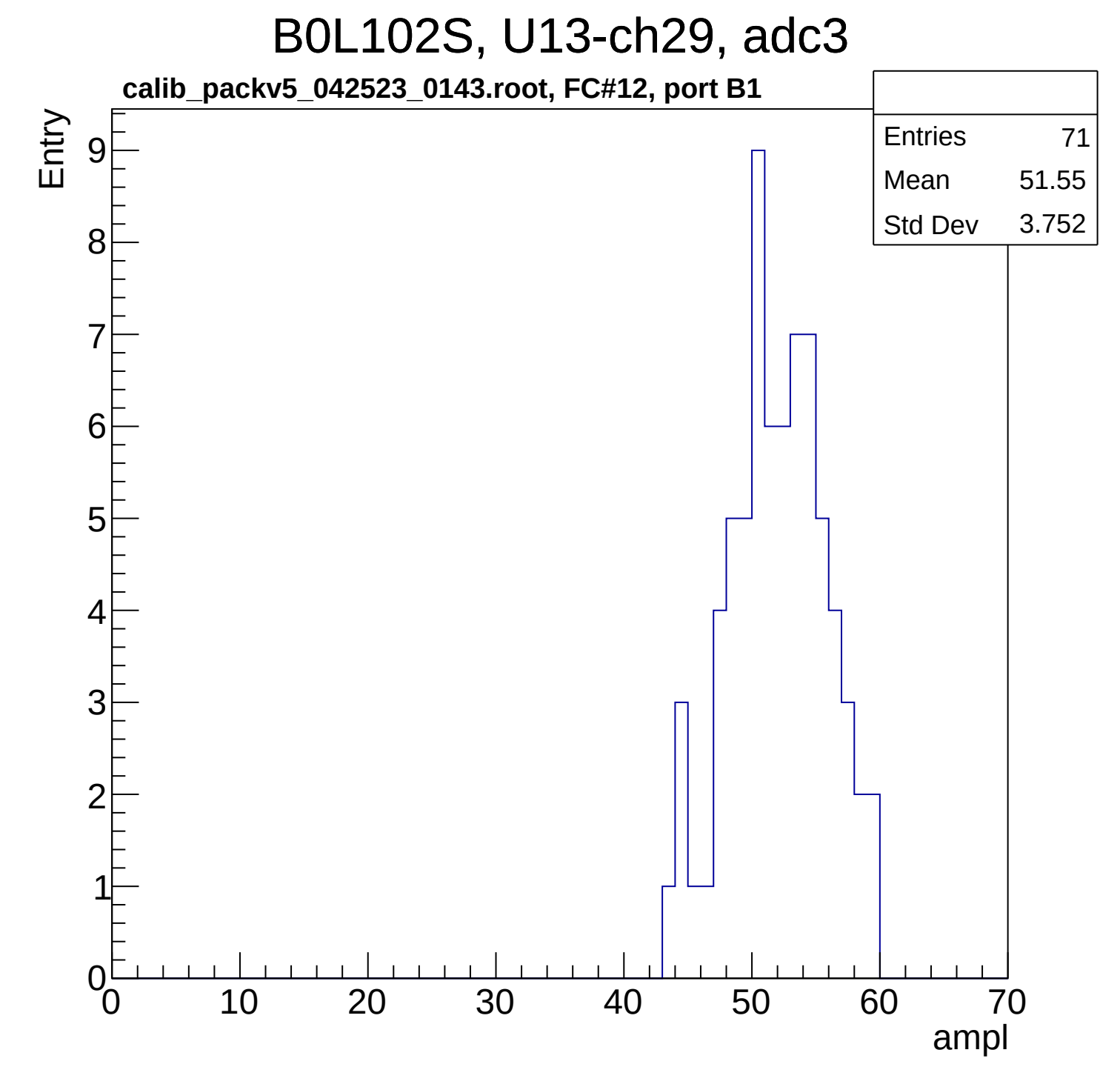
B0L102S, U13-ch29, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 51.55
Std Dev 3.752
Entry
ampl
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5
6
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0
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60
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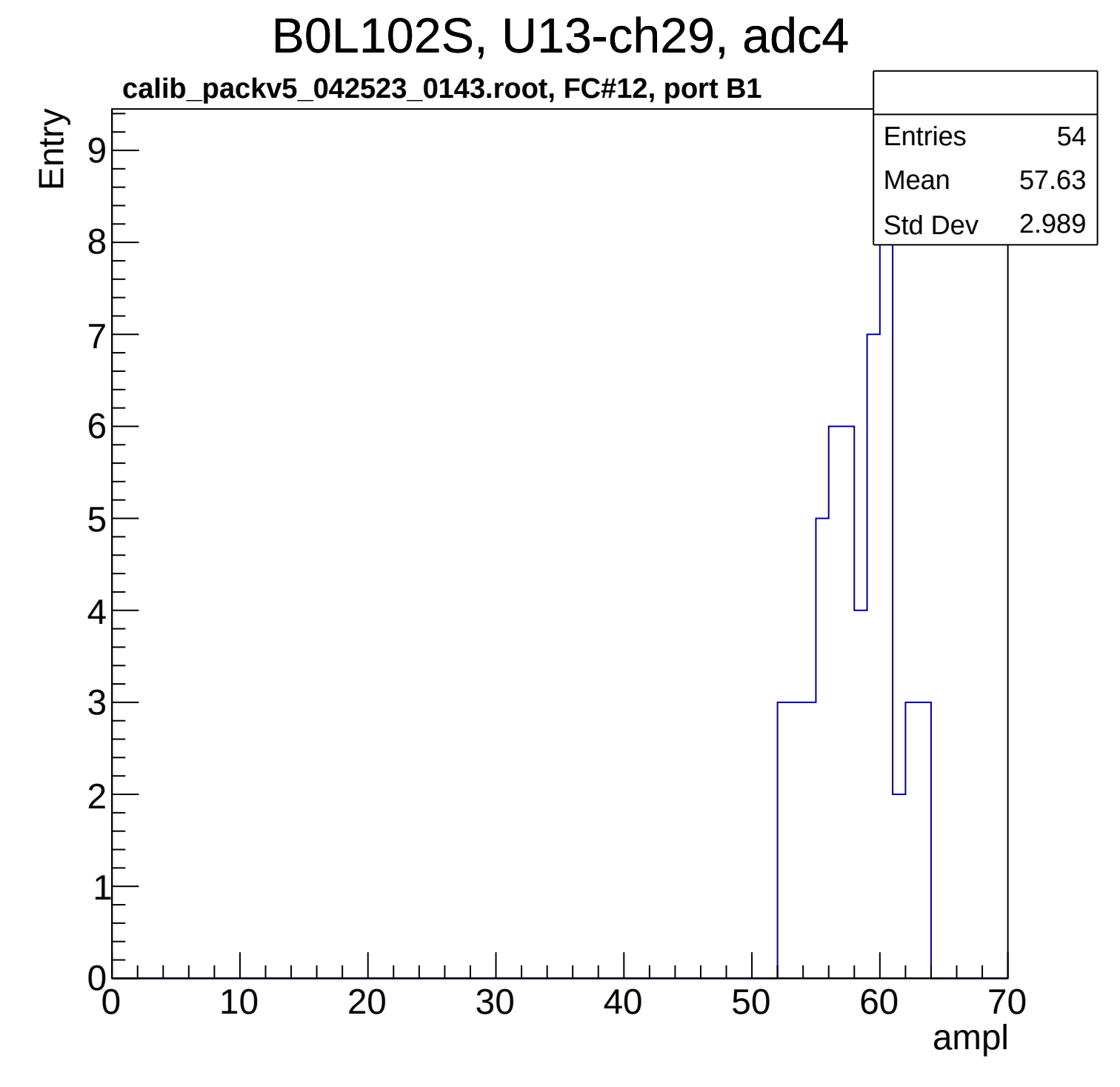
B0L102S, U13-ch29, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57.63
Std Dev 2.989
Entry
ampl
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3
4
5
6
7
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0
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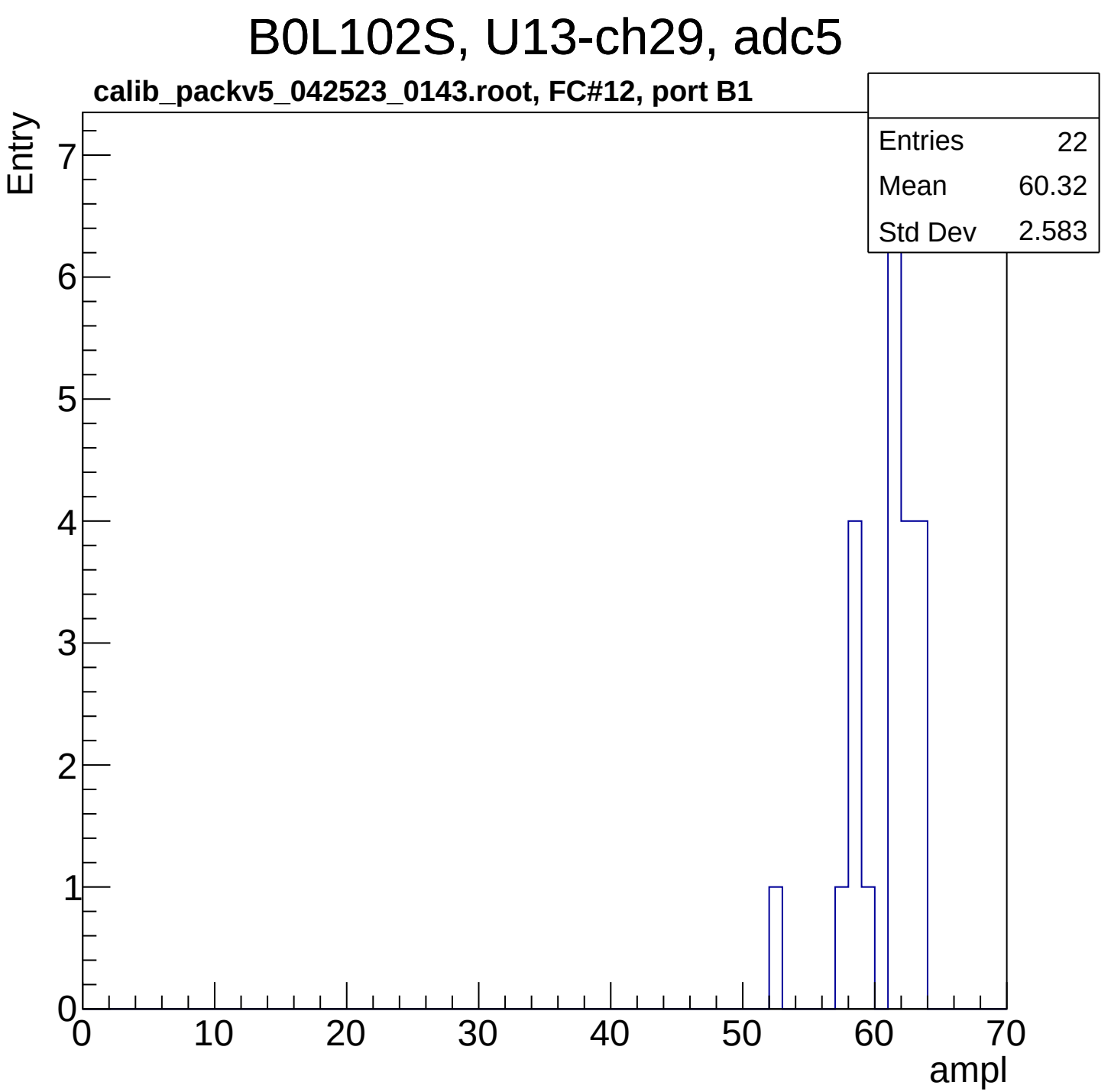
B0L102S, U13-ch29, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 60.32
Std Dev 2.583
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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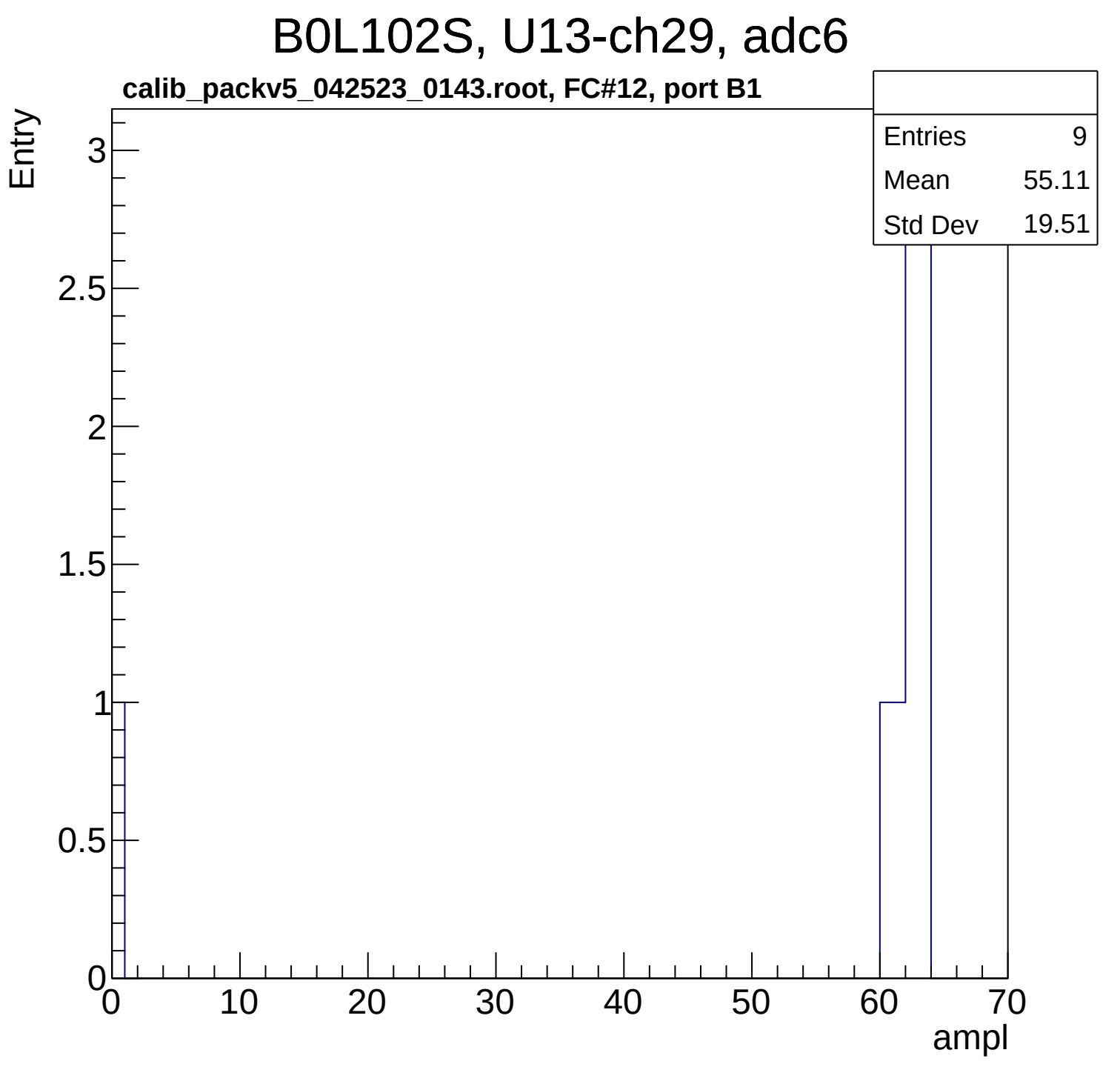
B0L102S, U13-ch29, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 55.11
Std Dev 19.51
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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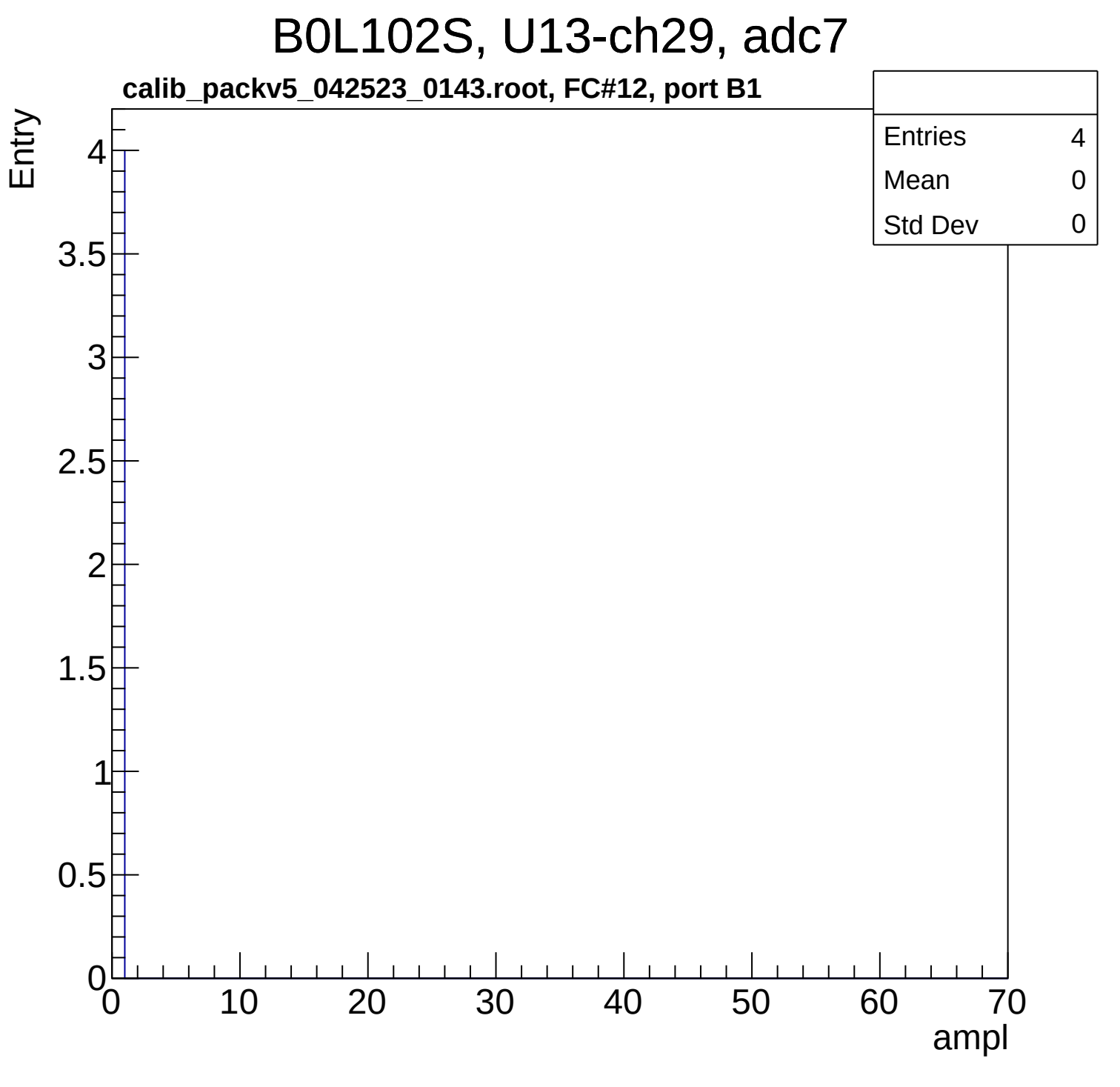
20
30
40
50
60
70


B0L102S, U13-ch29, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 0
Std Dev 0
Entry
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0.5
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1.5
2
2.5
3
3.5
4
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10
20
30
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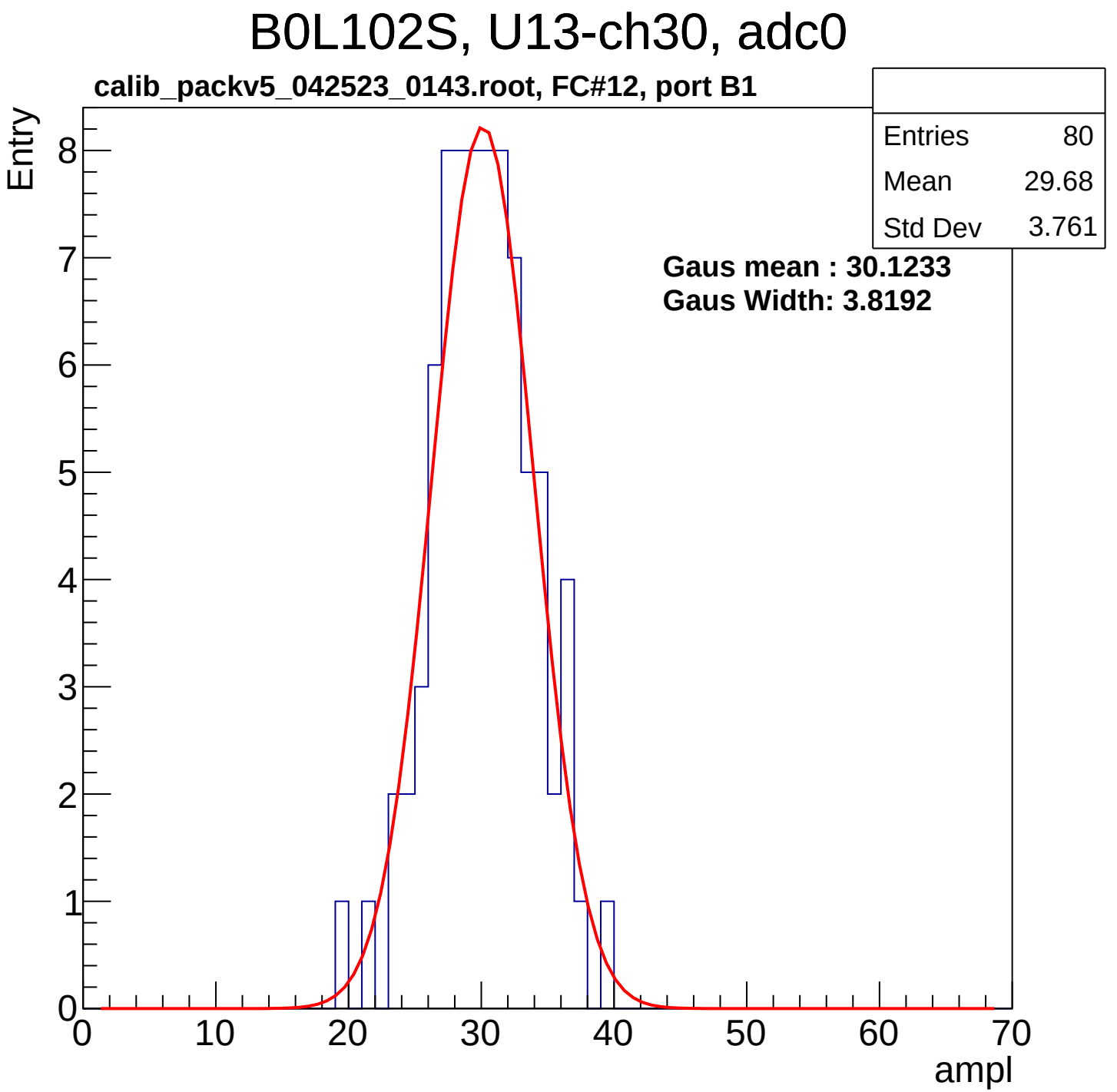
50
60
70
ampl


B0L102S, U13-ch30, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 29.68
Std Dev 3.761
Gaus mean : 30.1233
Gaus Width: 3.8192
Entry
ampl
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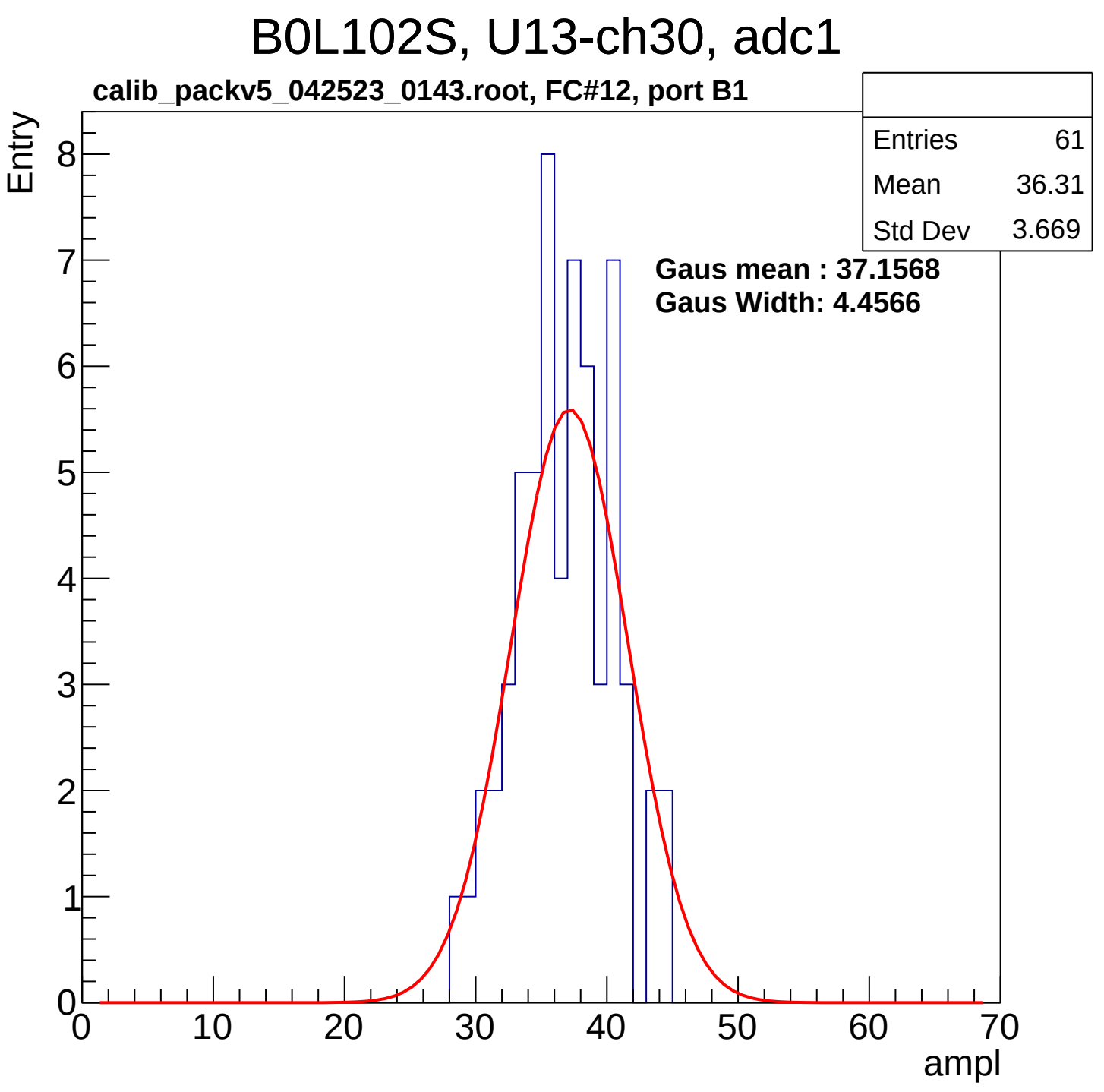
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch30, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.31
Std Dev 3.669
Gaus mean : 37.1568
Gaus Width: 4.4566
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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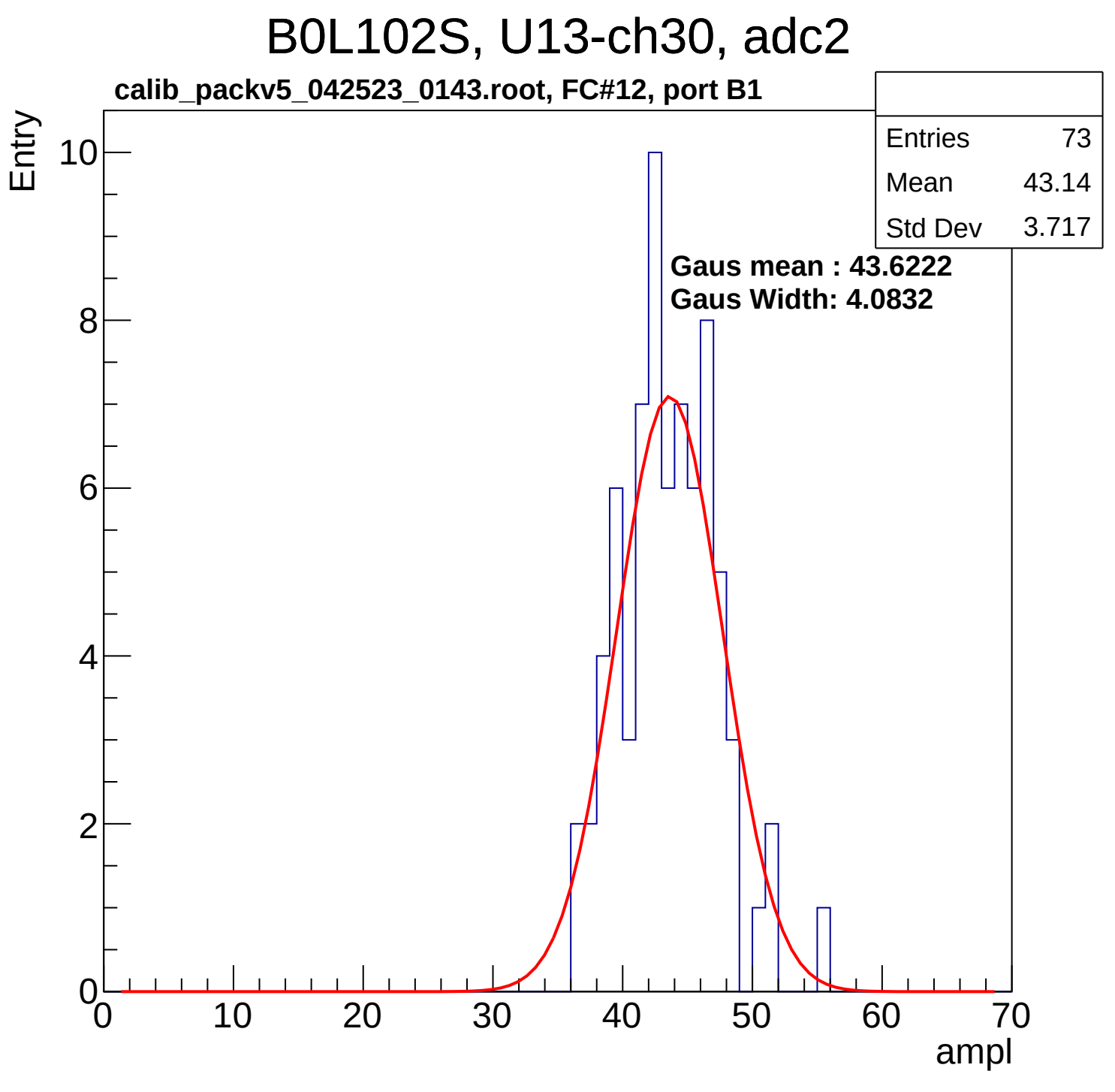
B0L102S, U13-ch30, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 43.14
Std Dev 3.717
Gaus mean : 43.6222
Gaus Width: 4.0832
Entry
ampl
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6
8
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0
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20
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40
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60
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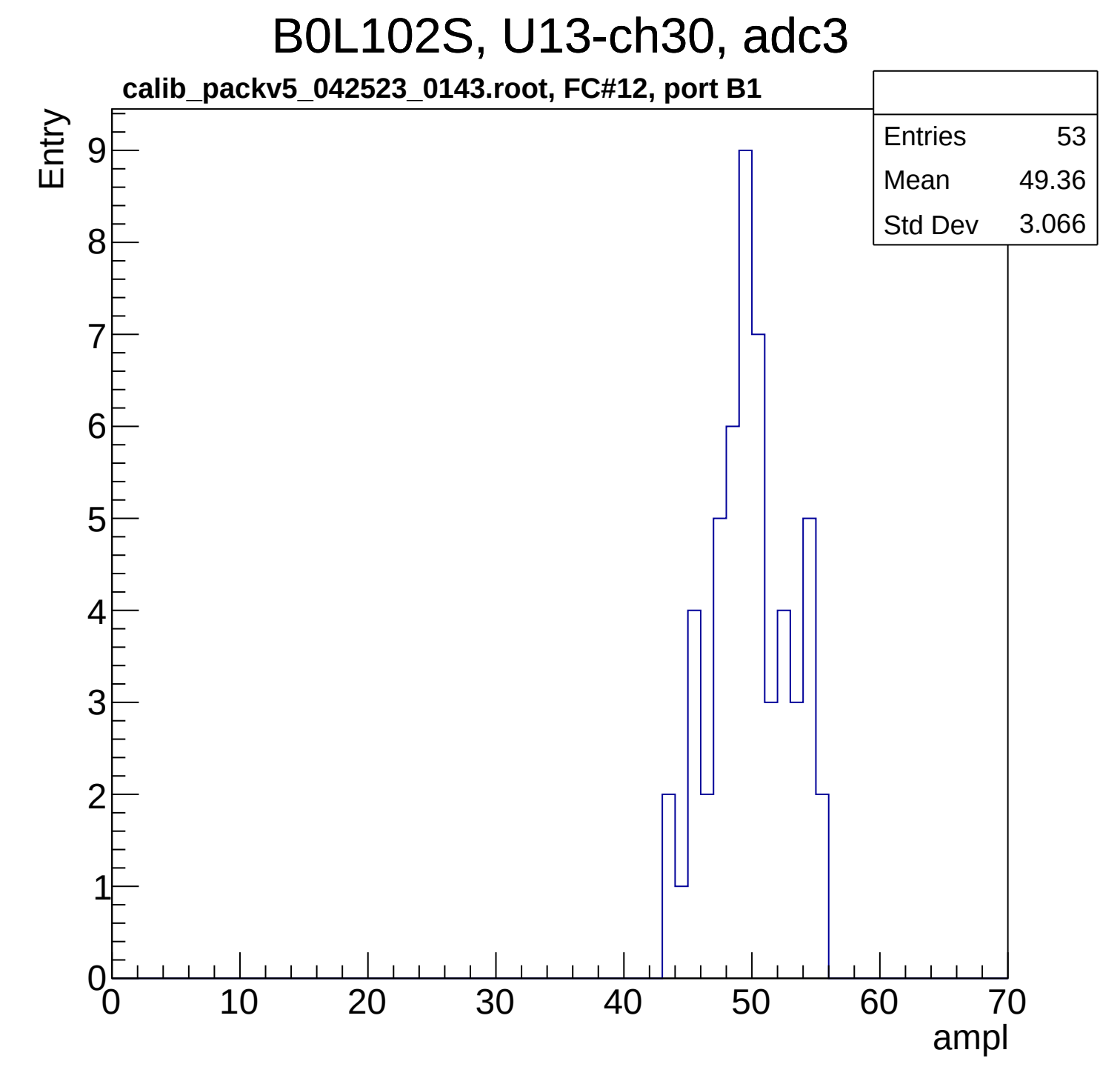
B0L102S, U13-ch30, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 49.36
Std Dev 3.066
Entry
ampl
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3
4
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6
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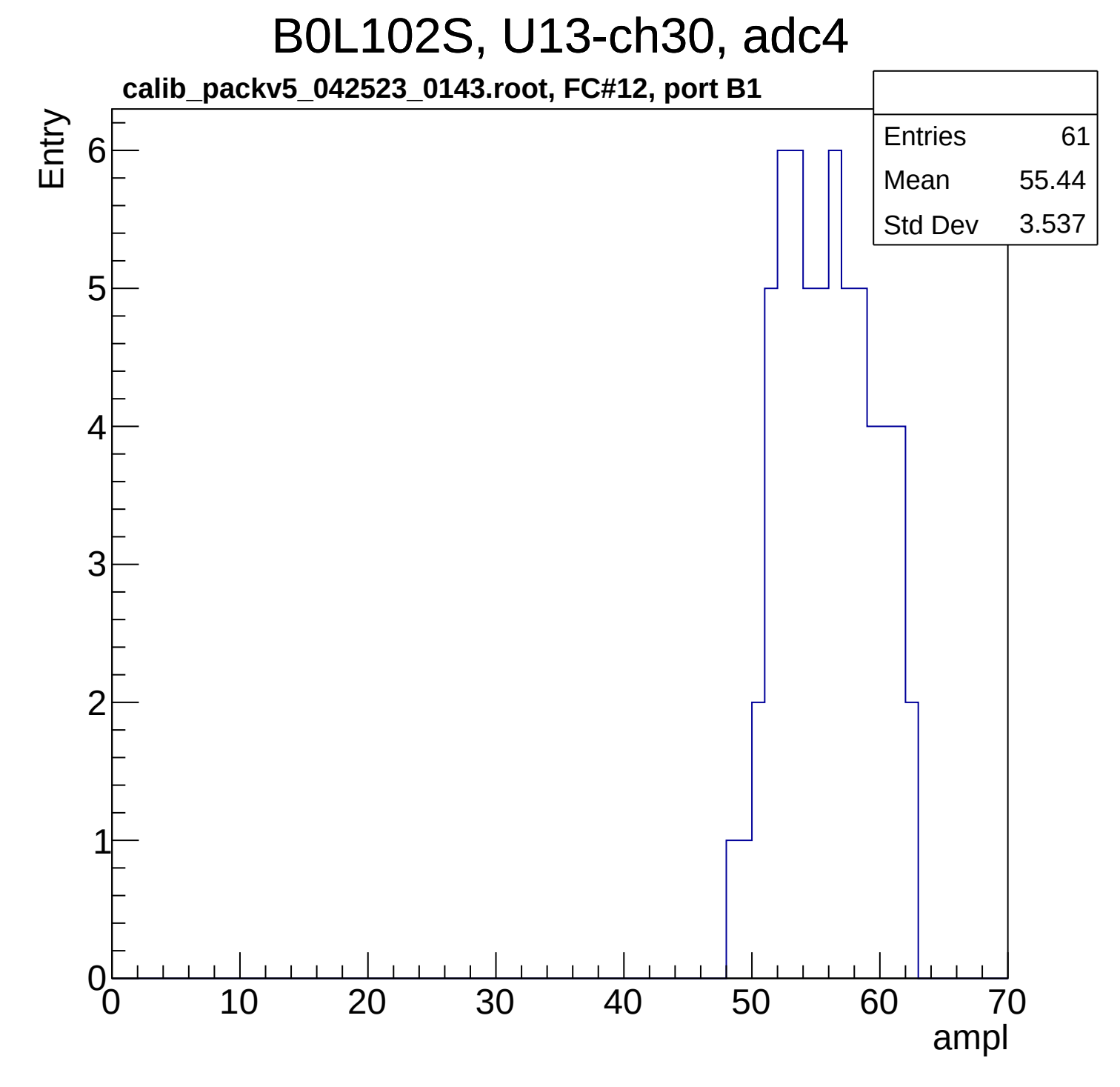
B0L102S, U13-ch30, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.44
Std Dev 3.537
Entry
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2
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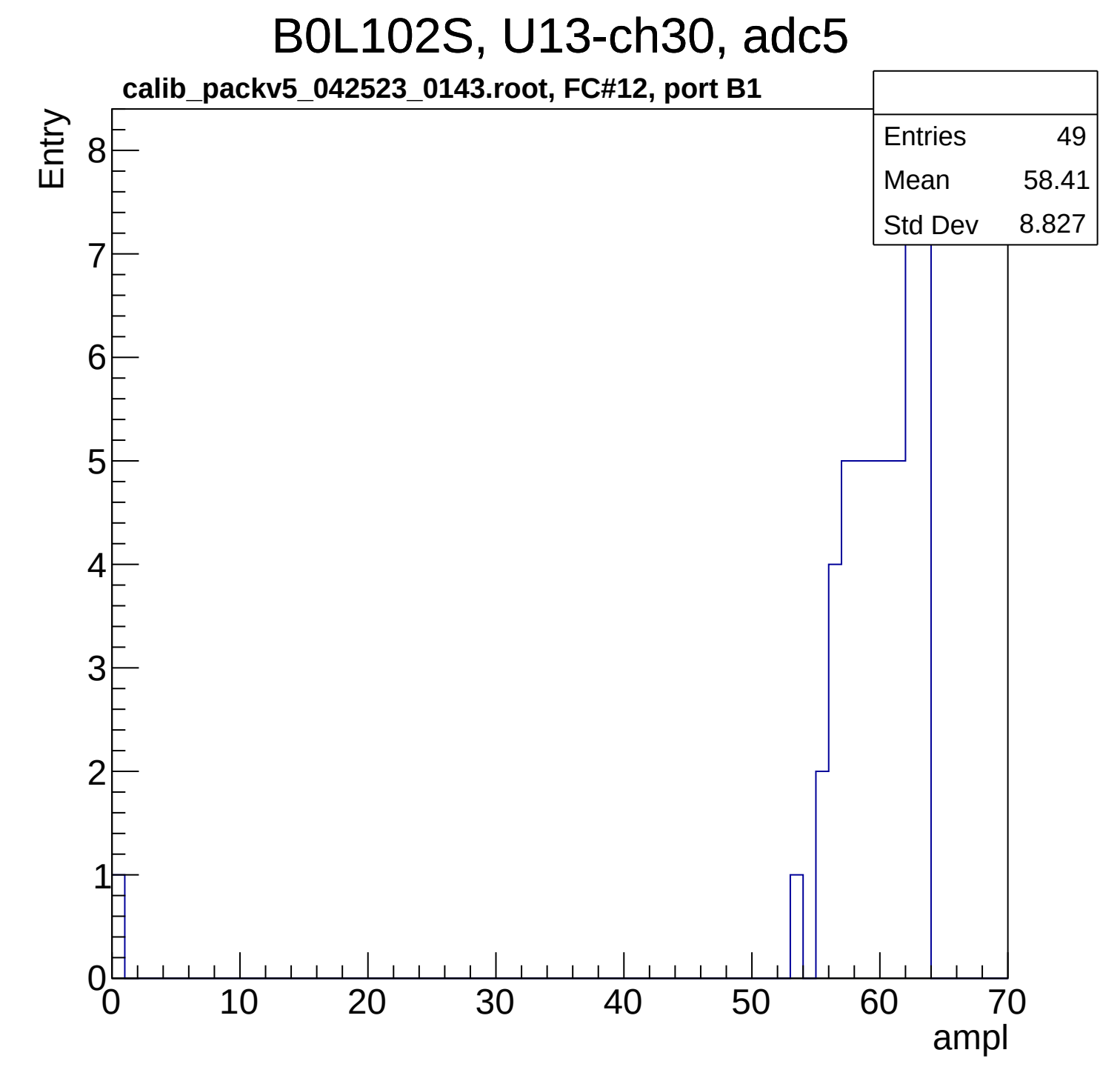
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch30, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 58.41
Std Dev 8.827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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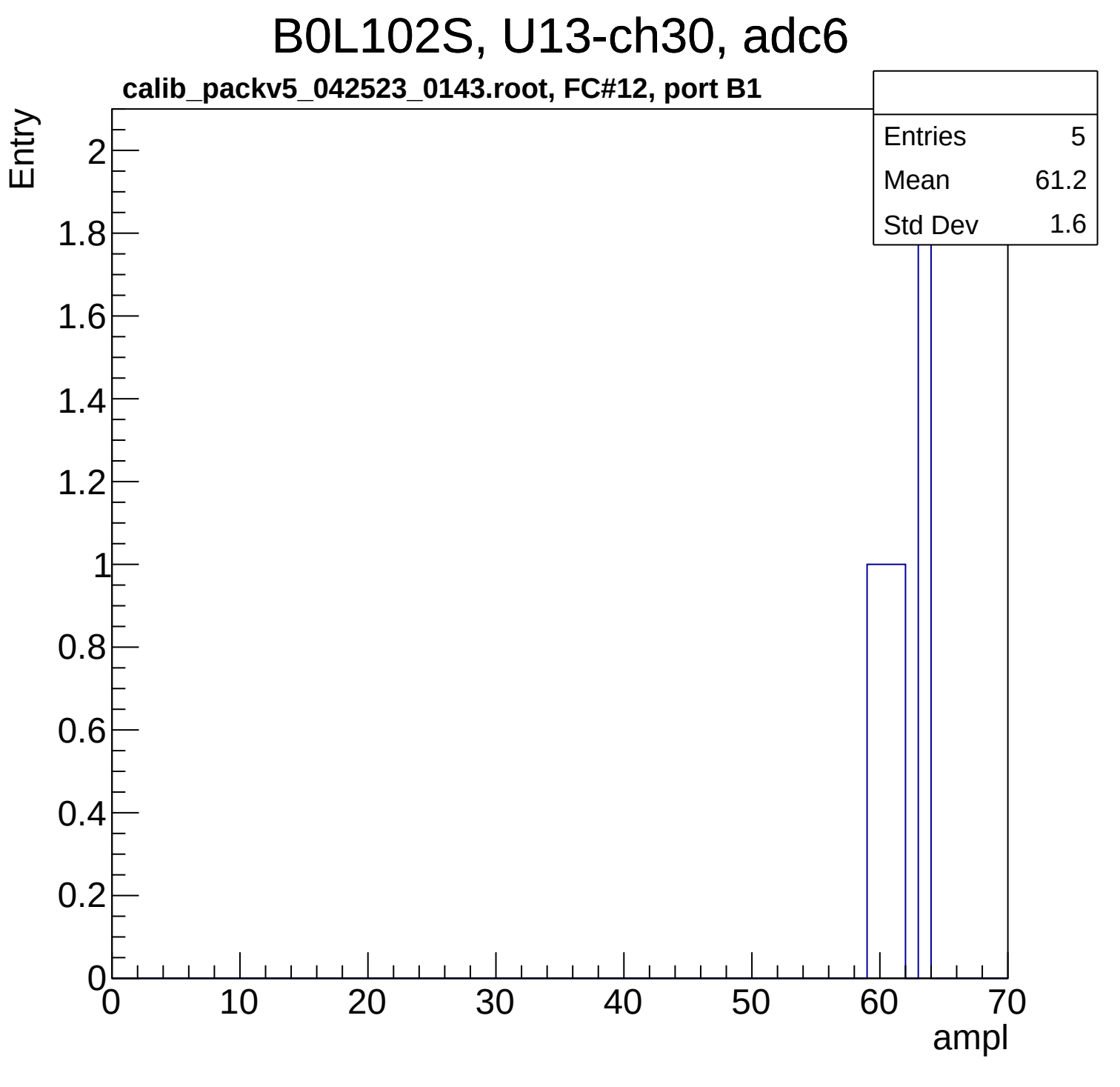
B0L102S, U13-ch30, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.2
Std Dev 1.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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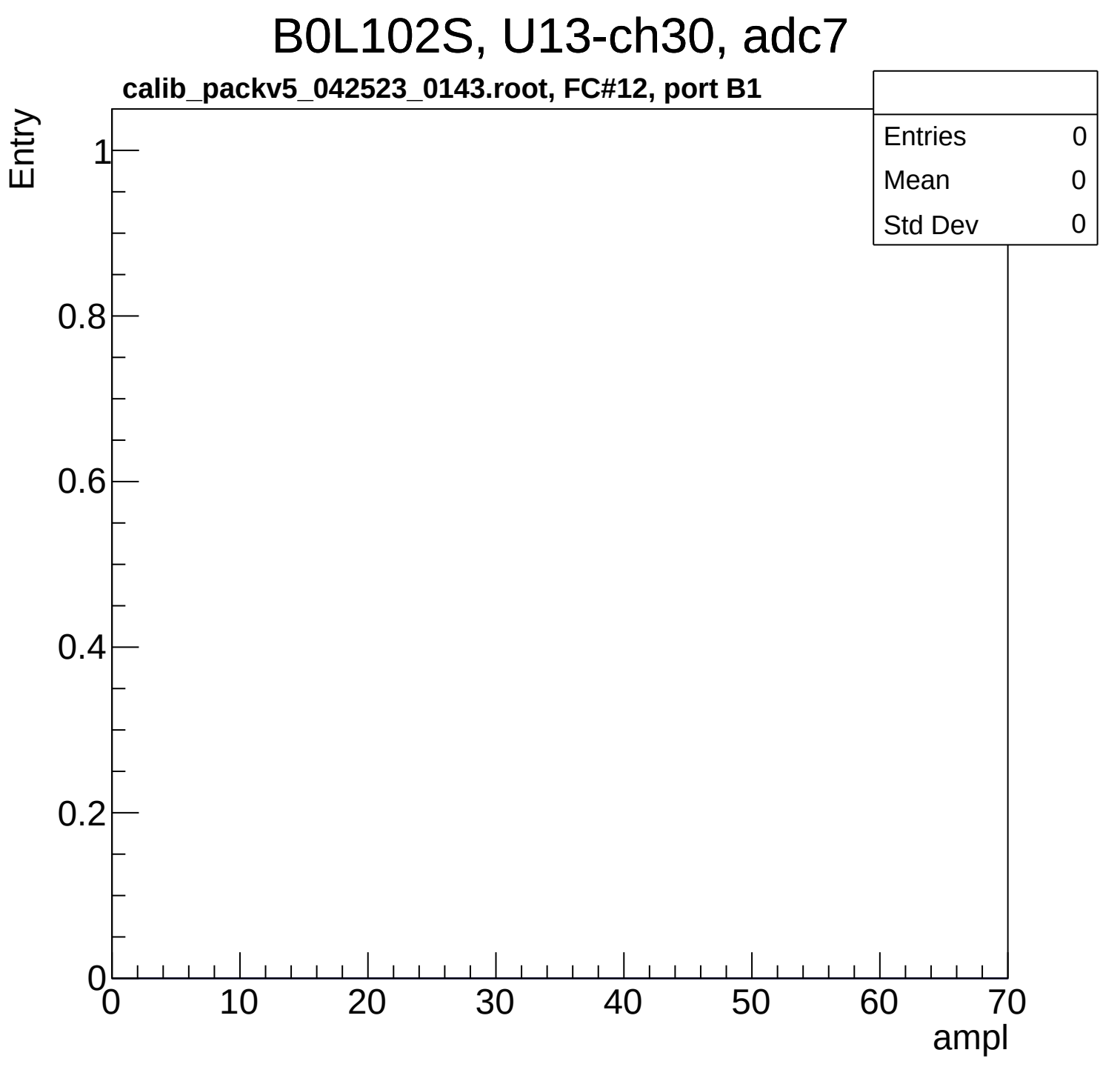
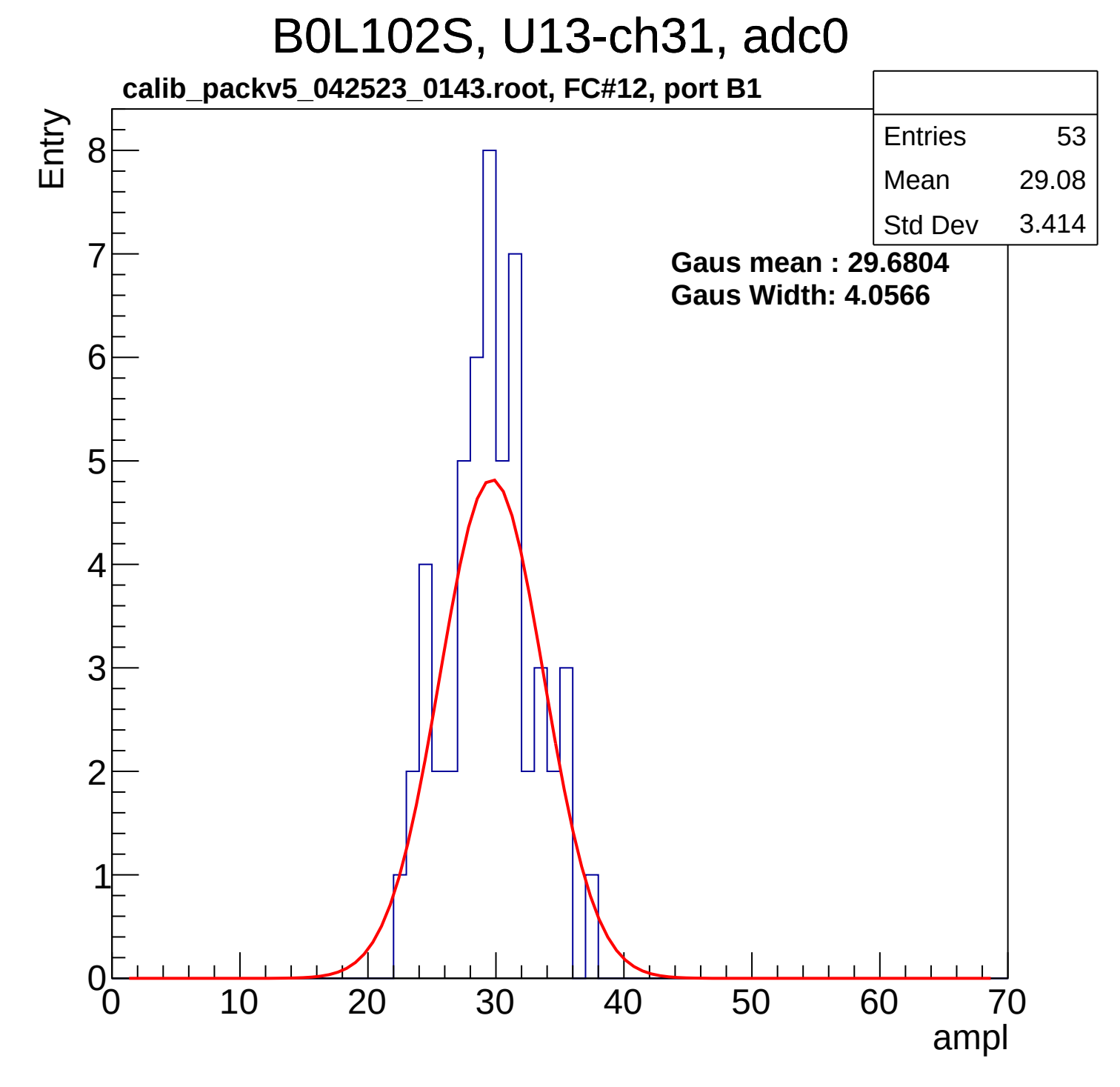
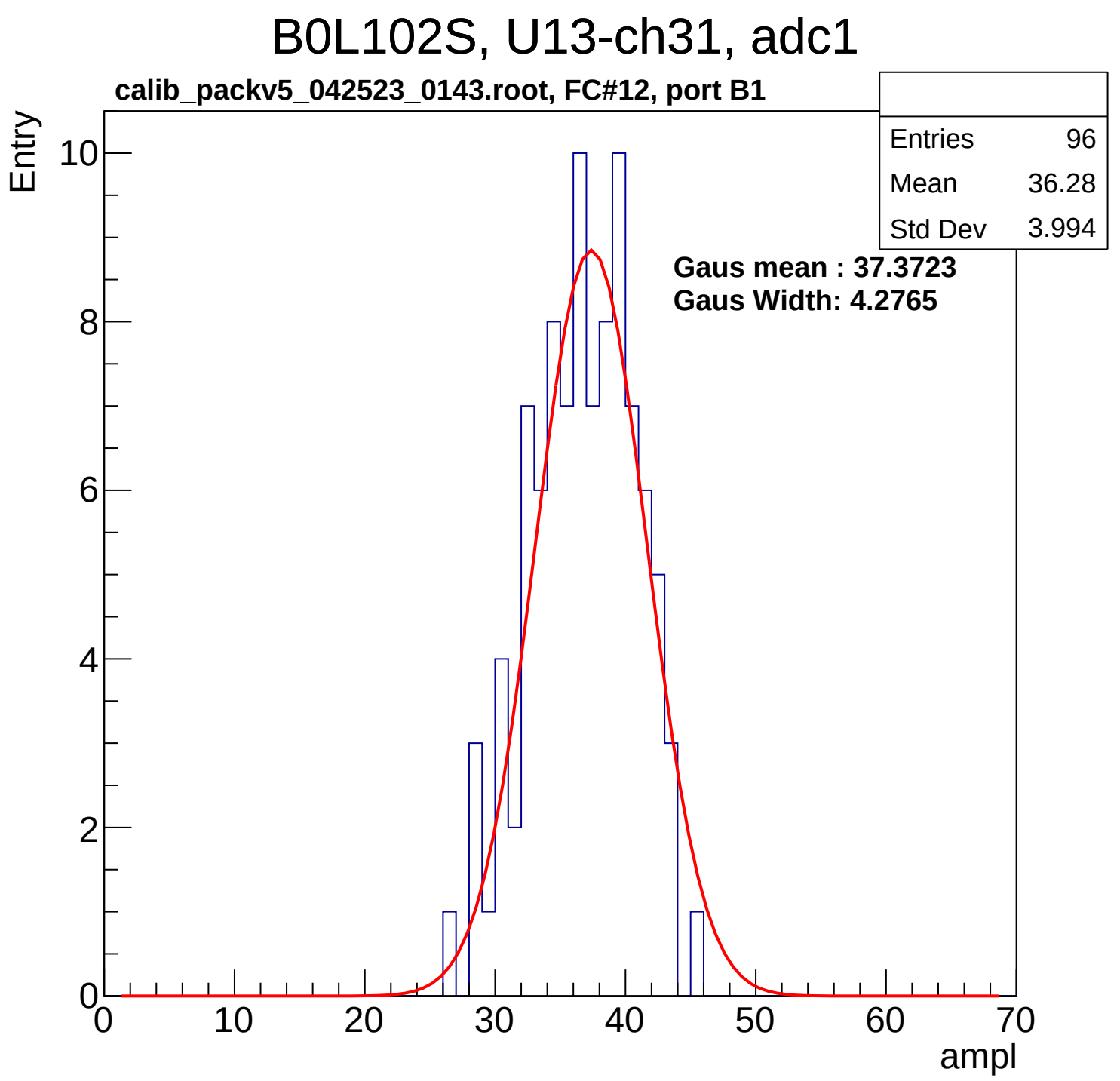

B0L102S, U13-ch30, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch31, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 29.08
Std Dev 3.414
Gaus mean : 29.6804
Gaus Width: 4.0566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch31, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 96
Mean 36.28
Std Dev 3.994
Gaus mean : 37.3723
Gaus Width: 4.2765
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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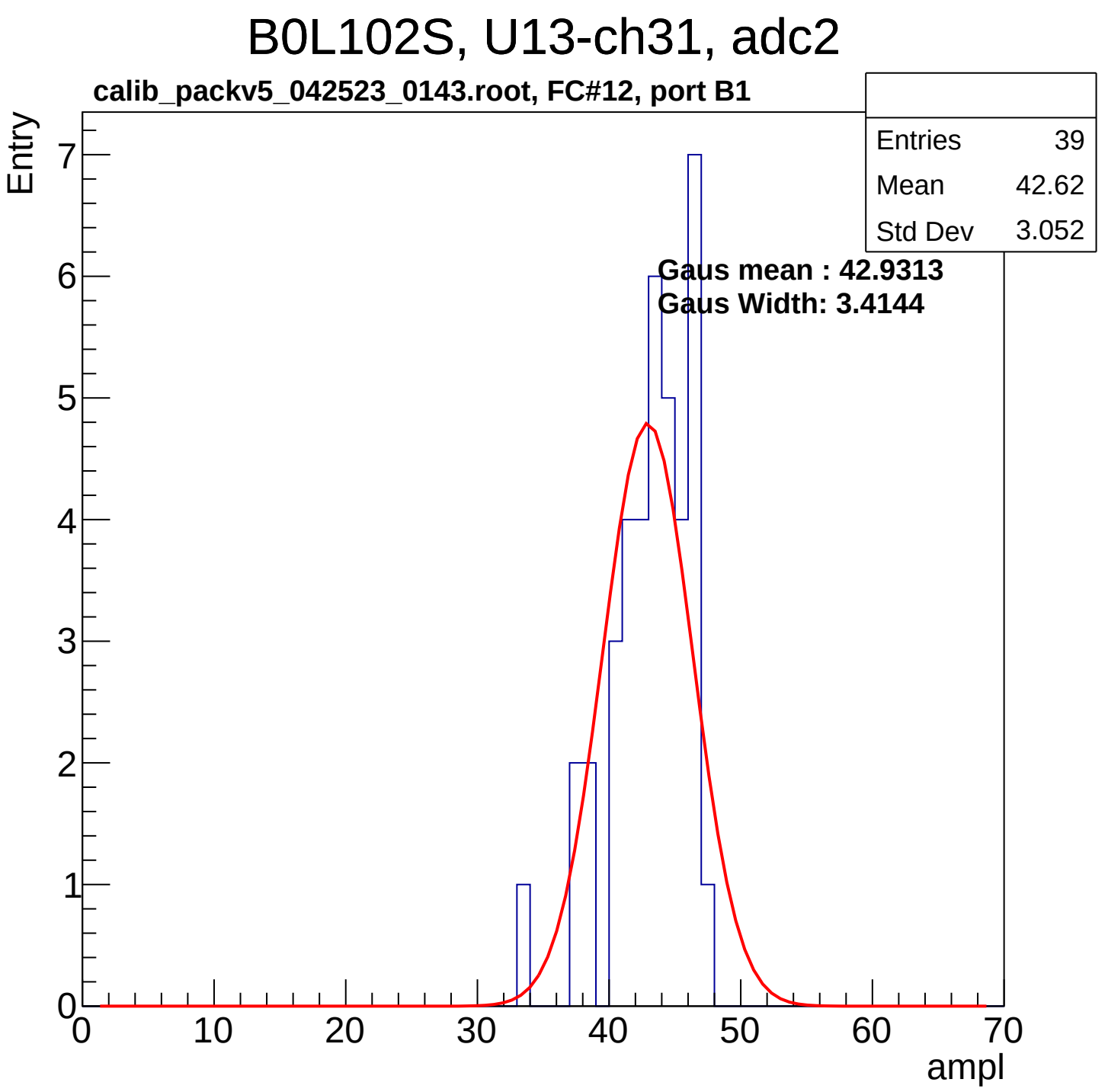
B0L102S, U13-ch31, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 42.62
Std Dev 3.052
Gaus mean : 42.9313
Gaus Width: 3.4144
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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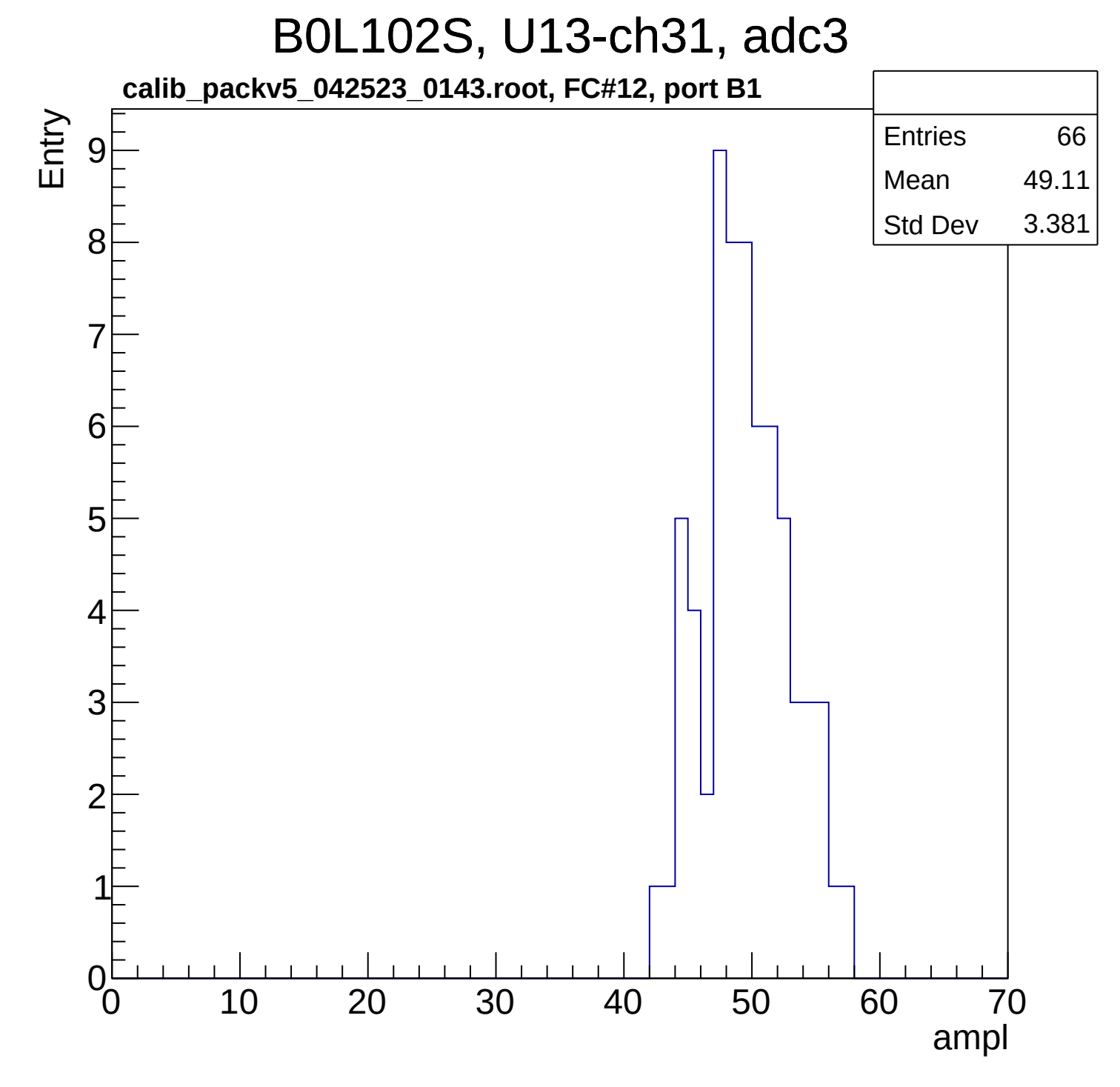
B0L102S, U13-ch31, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.11
Std Dev 3.381
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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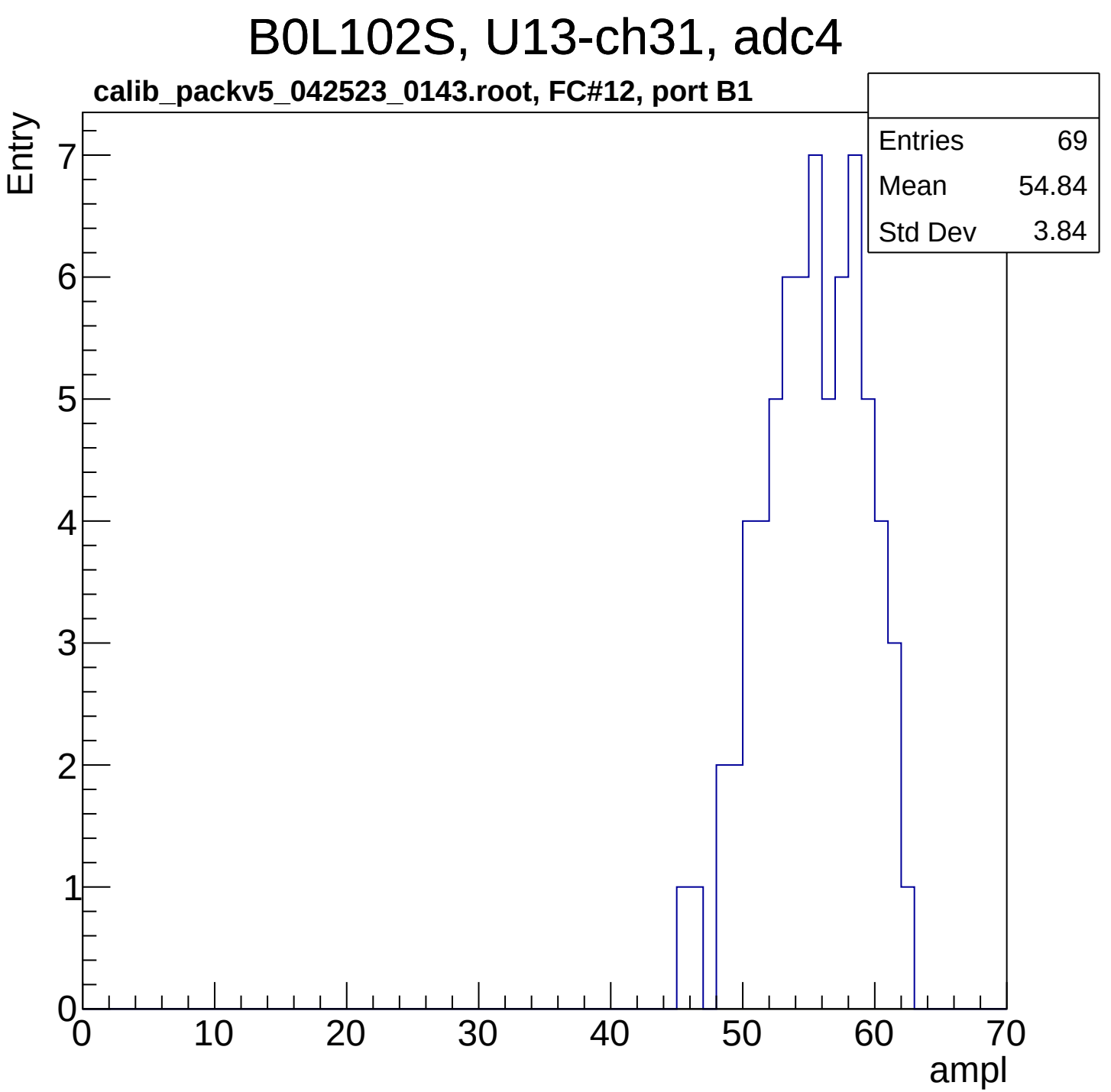
B0L102S, U13-ch31, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 54.84
Std Dev 3.84
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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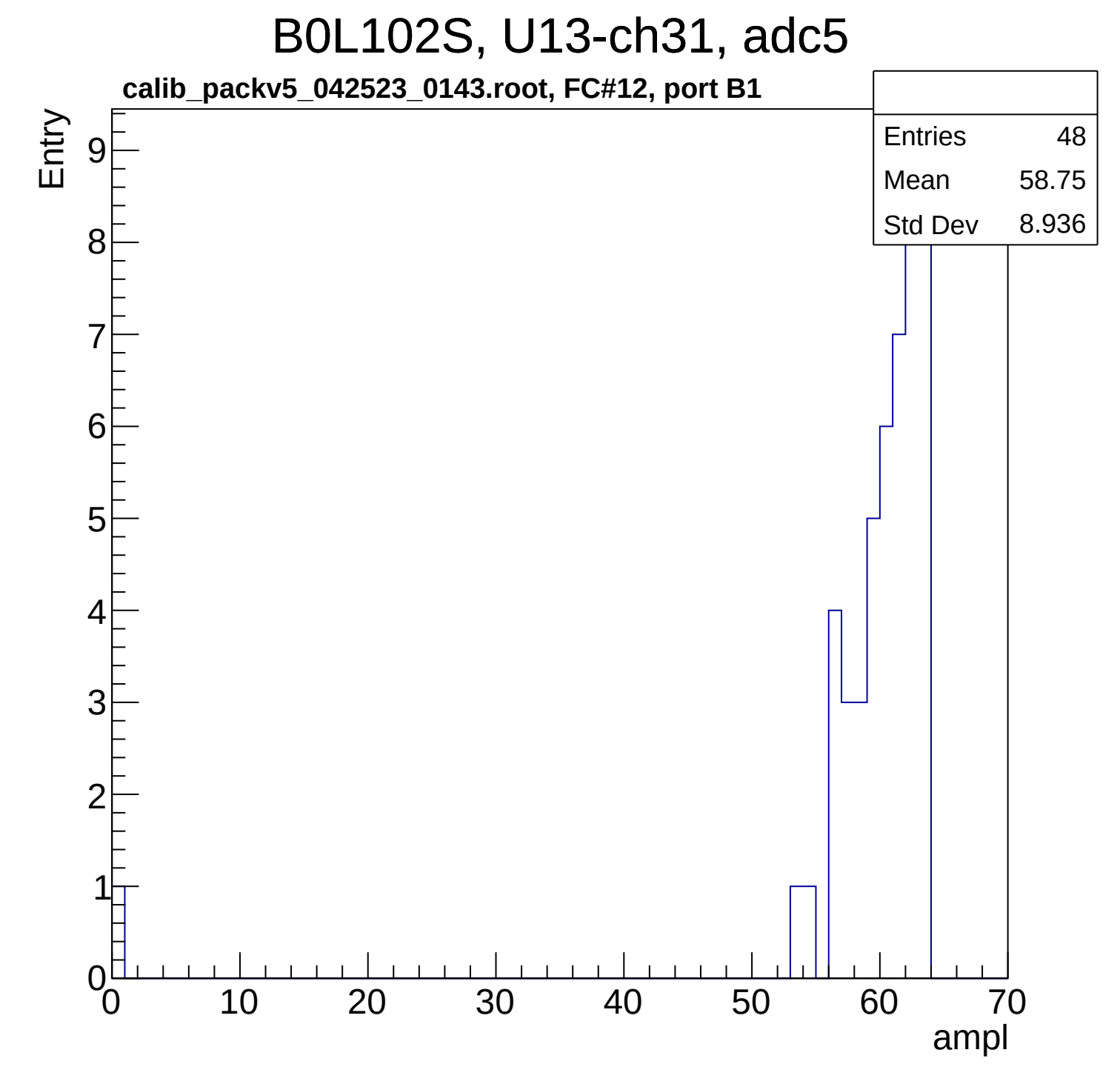
B0L102S, U13-ch31, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.75
Std Dev 8.936
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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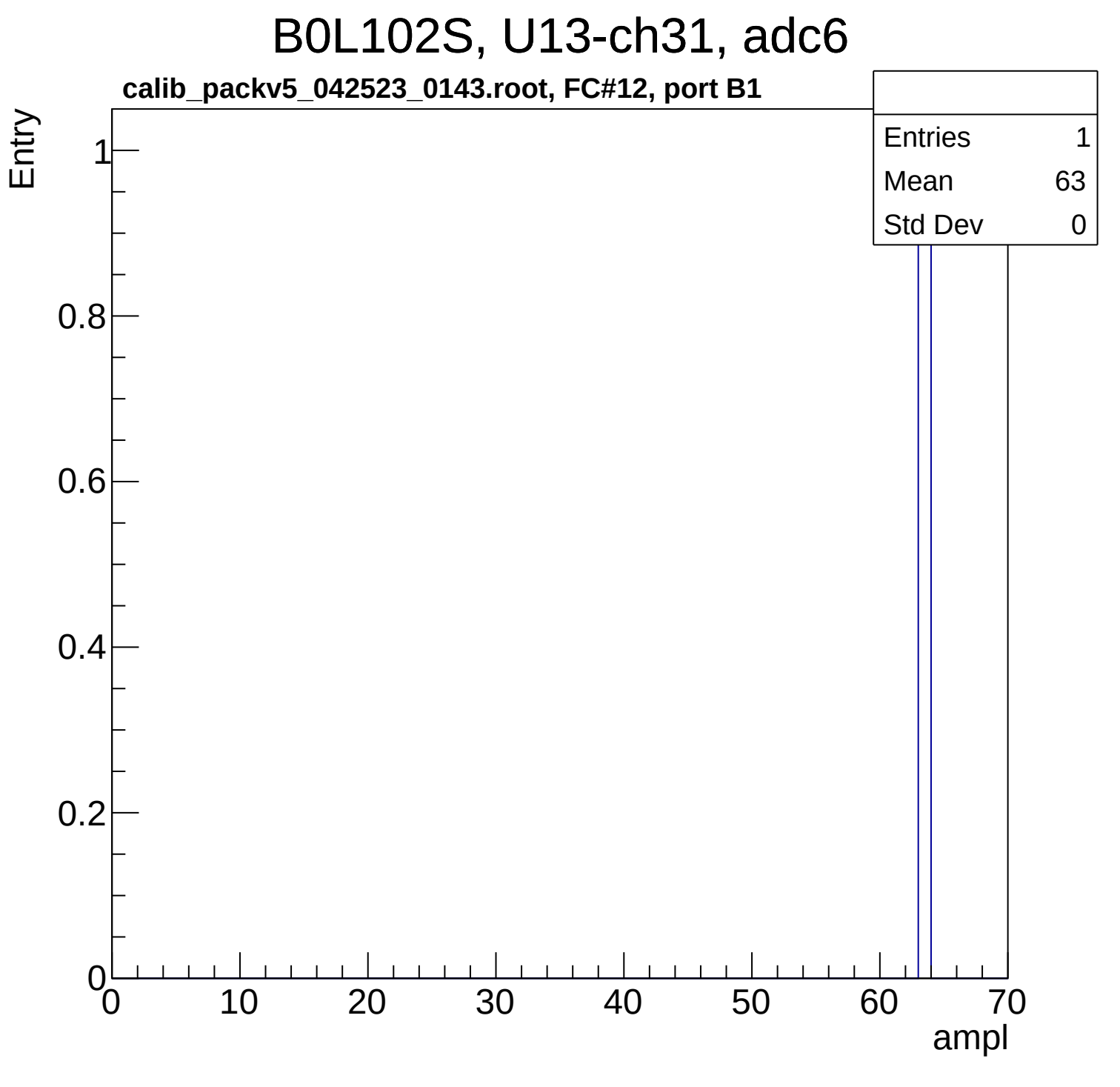
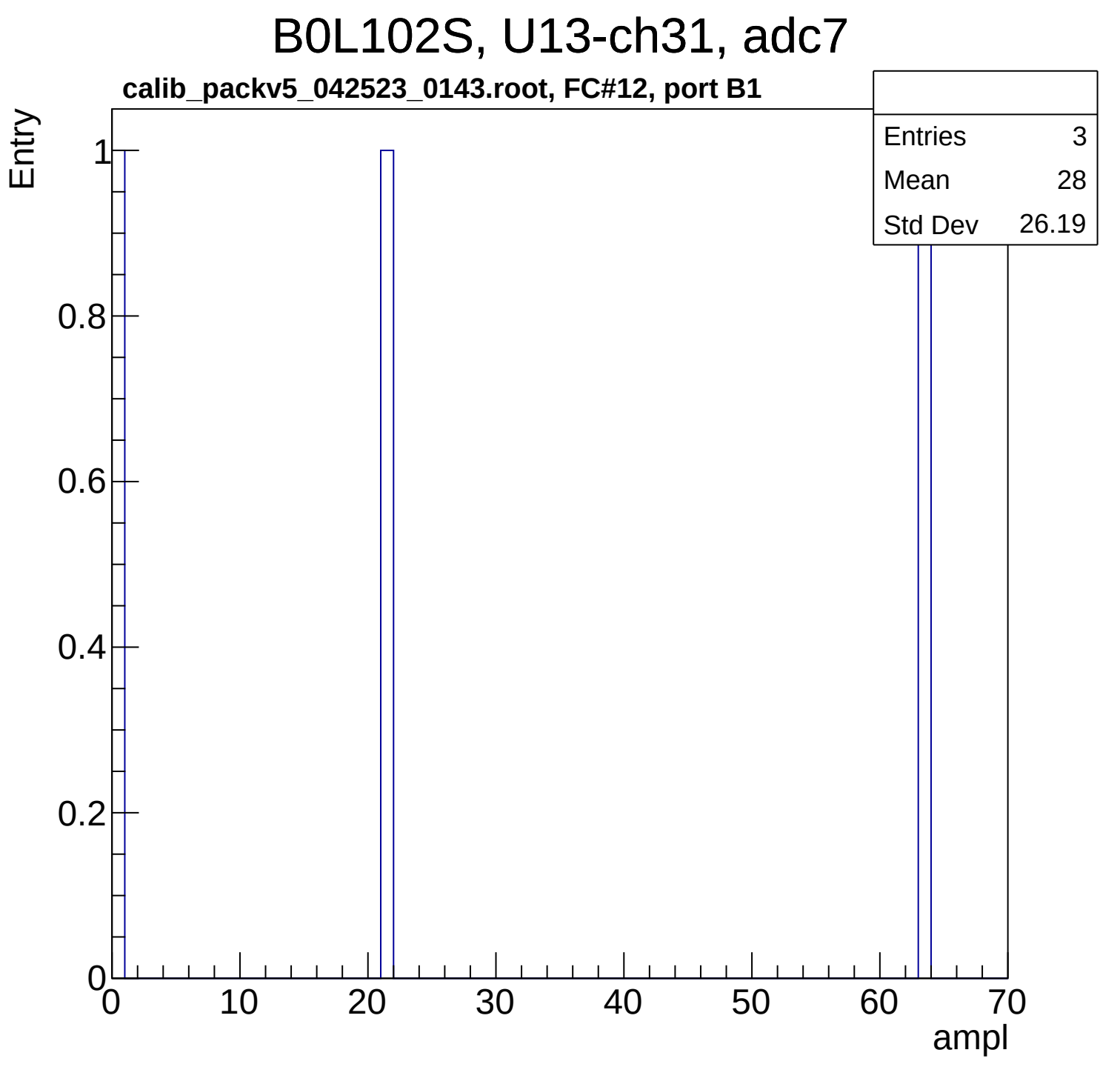
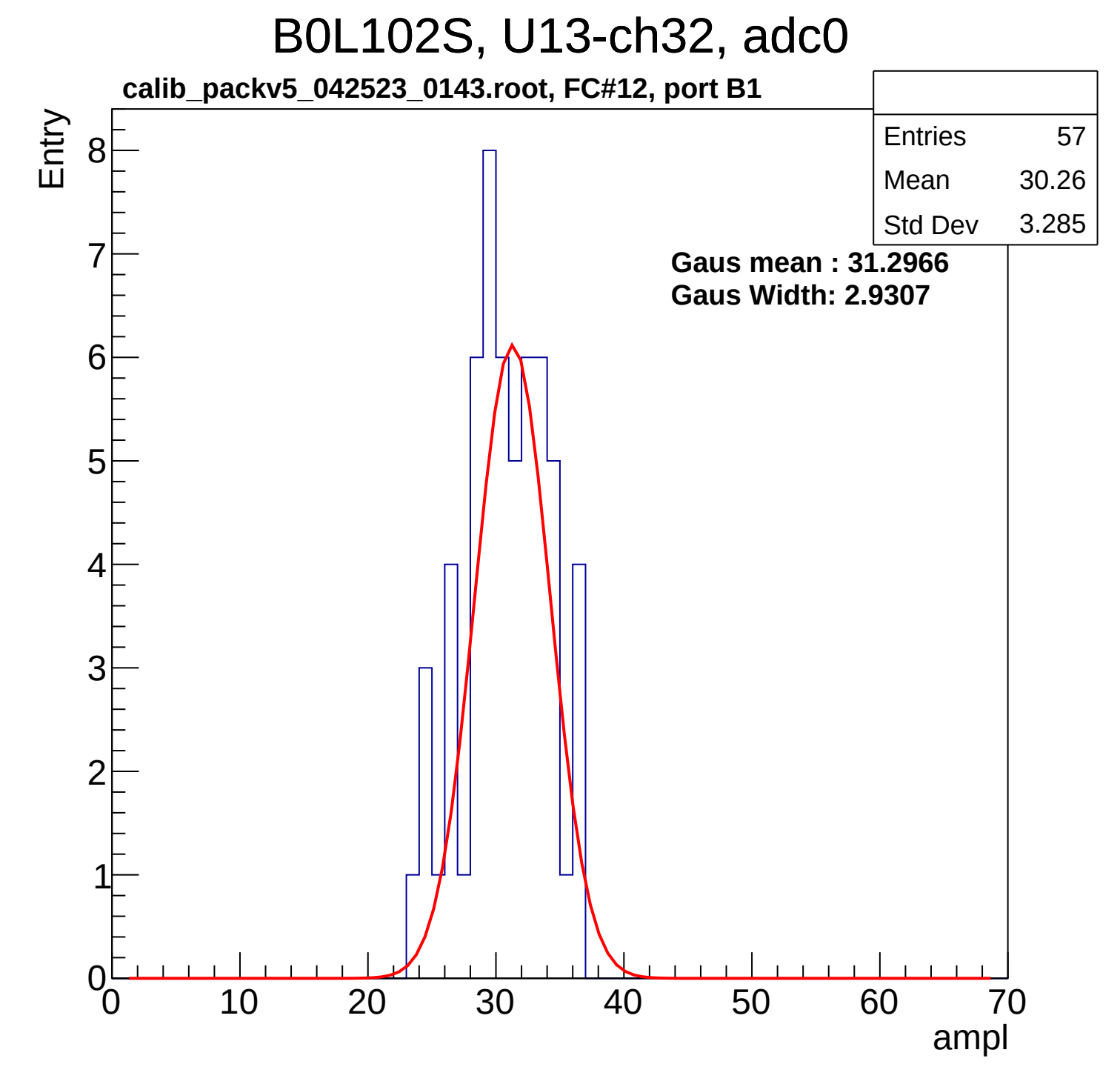
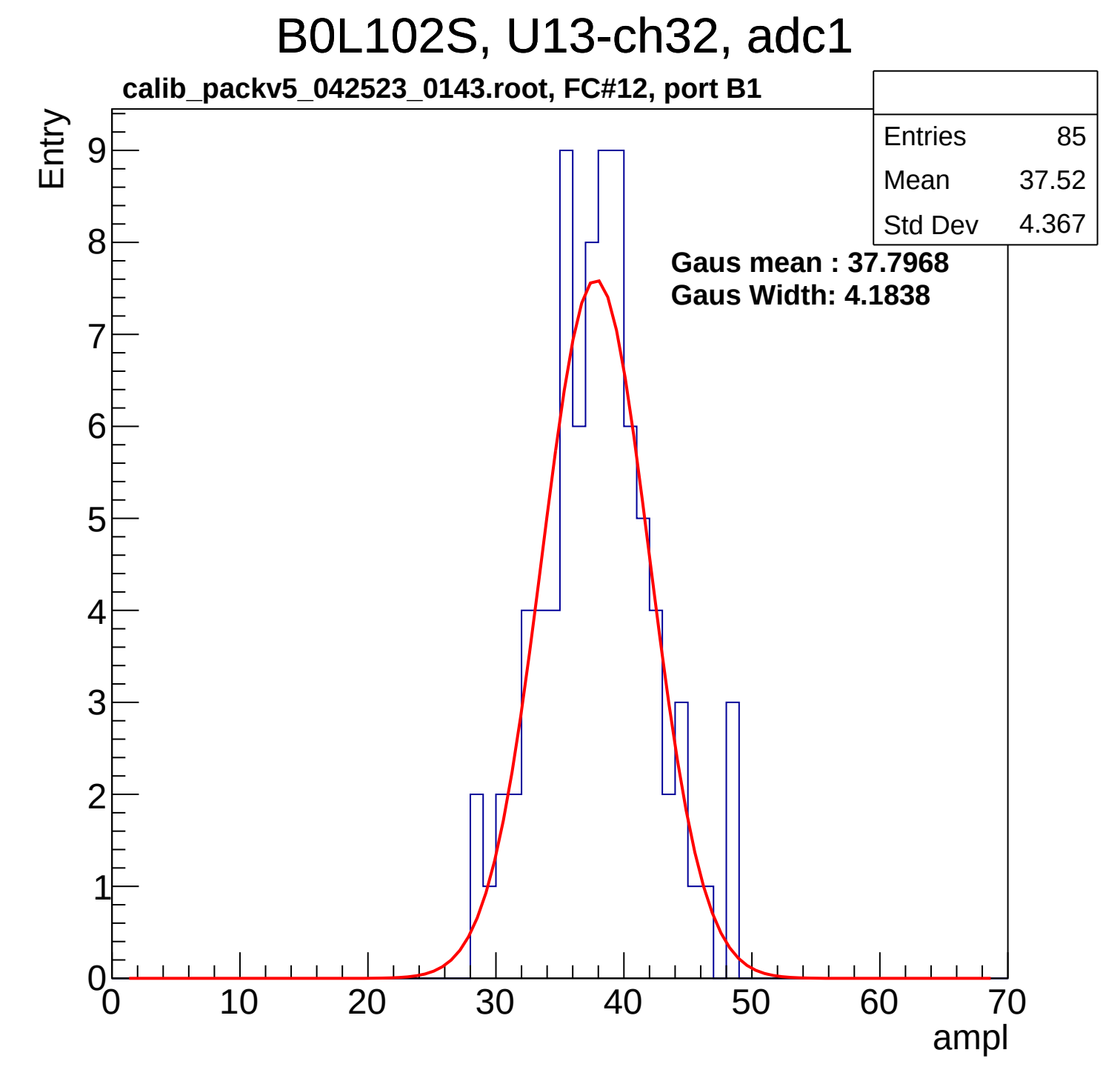

B0L102S, U13-ch31, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch31, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 28
Std Dev 26.19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch32, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 30.26
Std Dev 3.285
Gaus mean : 31.2966
Gaus Width: 2.9307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch32, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 37.52
Std Dev 4.367
Gaus mean : 37.7968
Gaus Width: 4.1838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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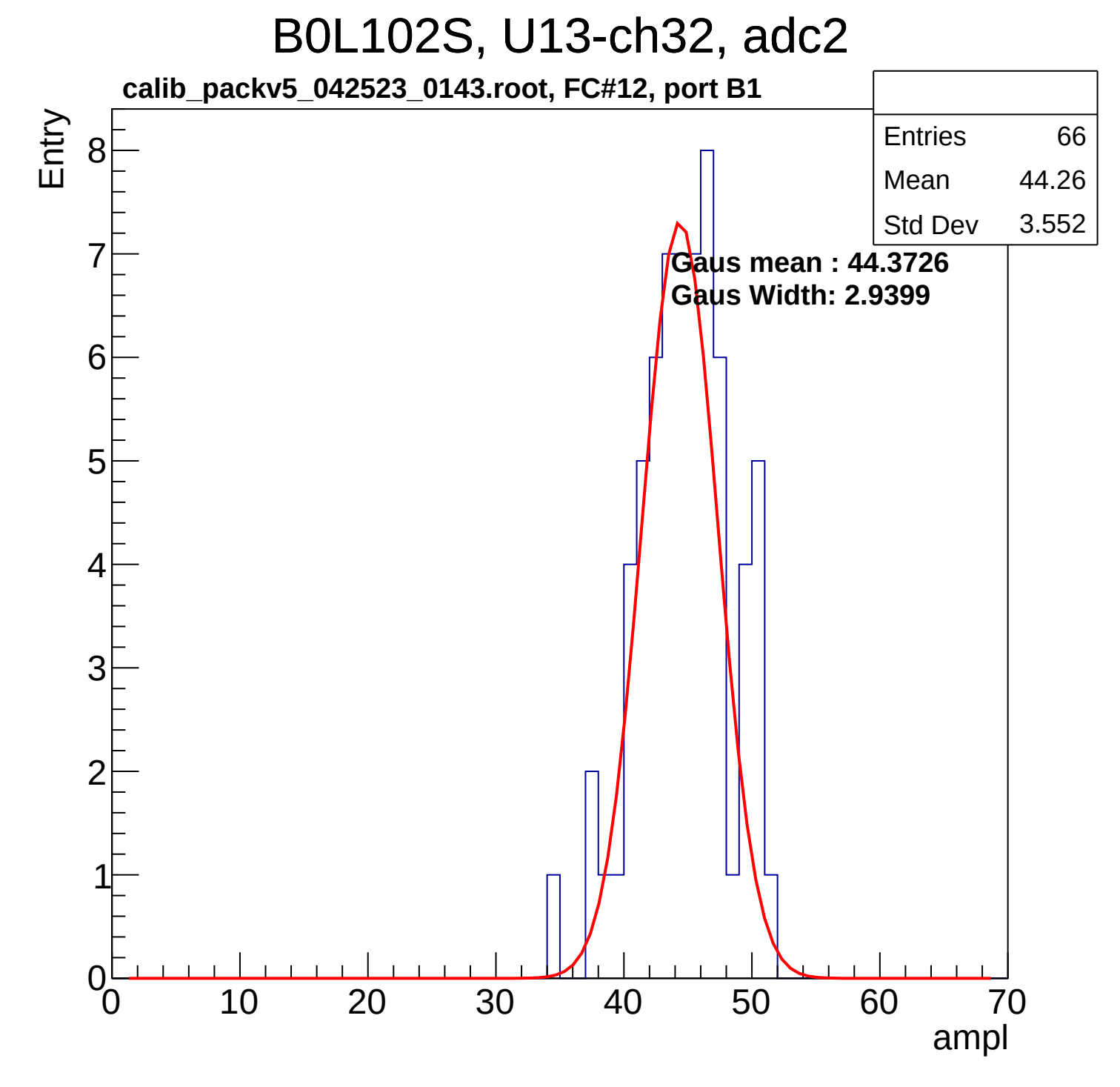
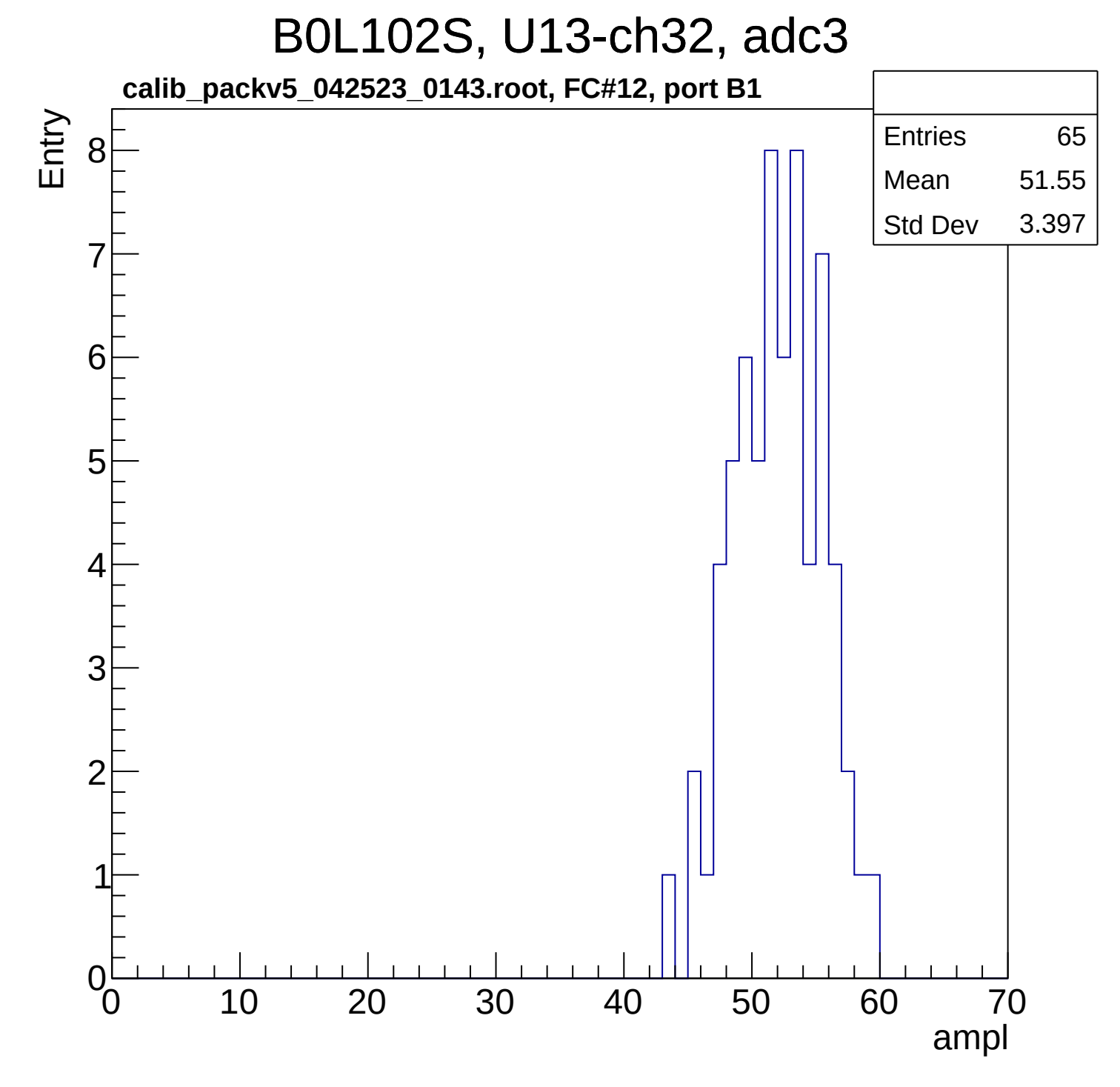
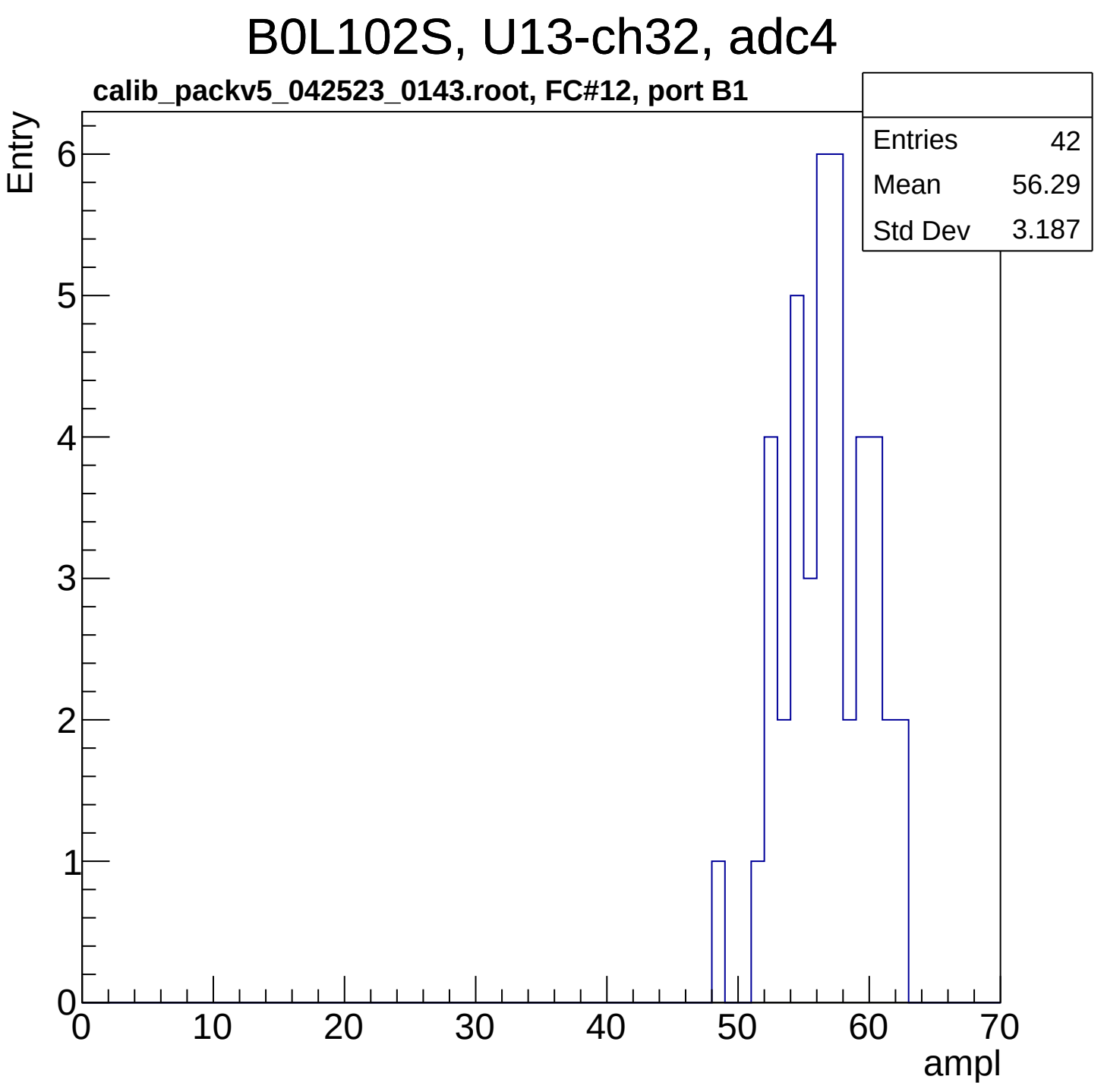
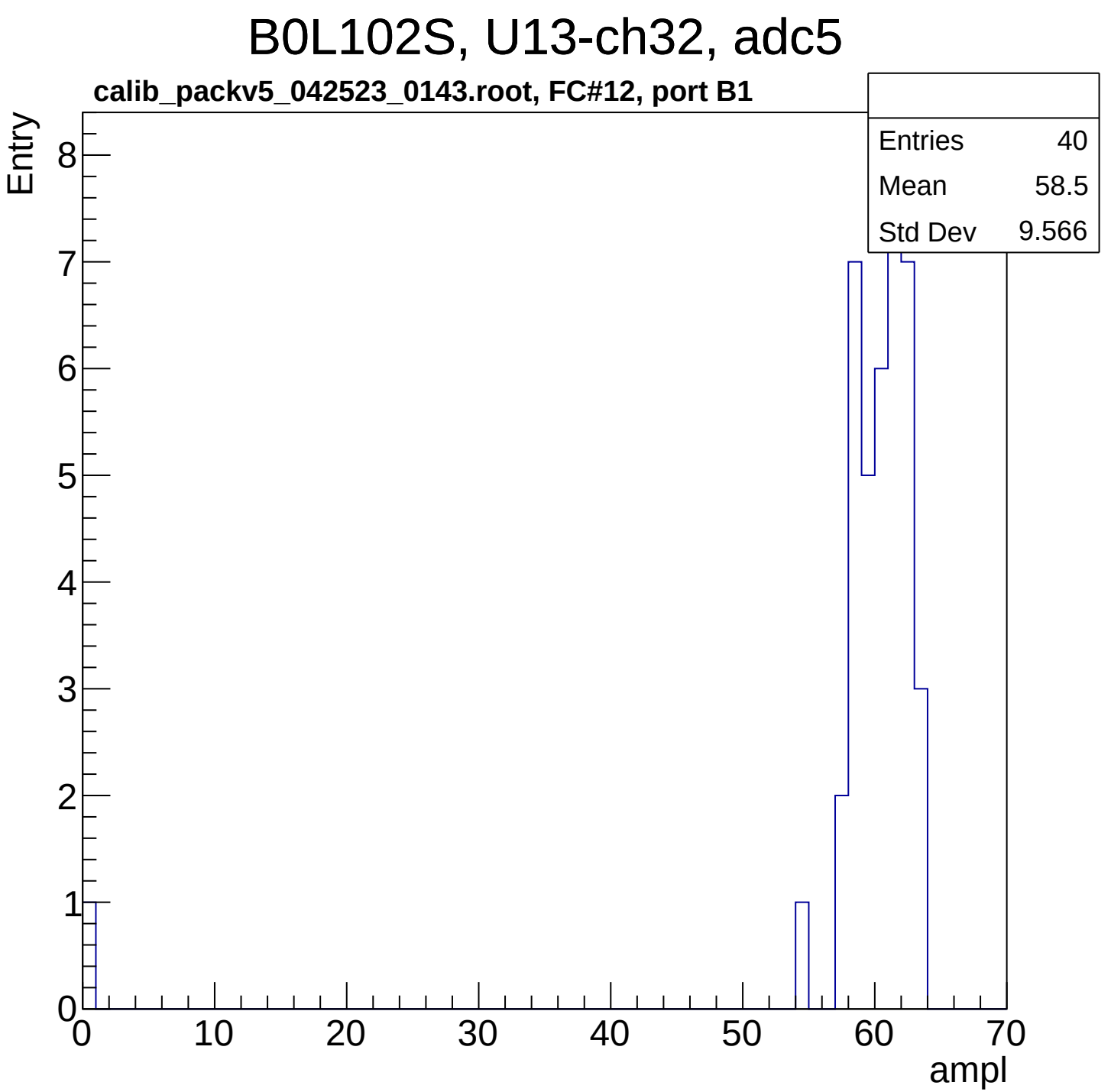

B0L102S, U13-ch32, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 44.26
Std Dev 3.552
Gaus mean : 44.3726
Gaus Width: 2.9399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch32, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 51.55
Std Dev 3.397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch32, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 56.29
Std Dev 3.187
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch32, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.5
Std Dev 9.566
Entry
ampl
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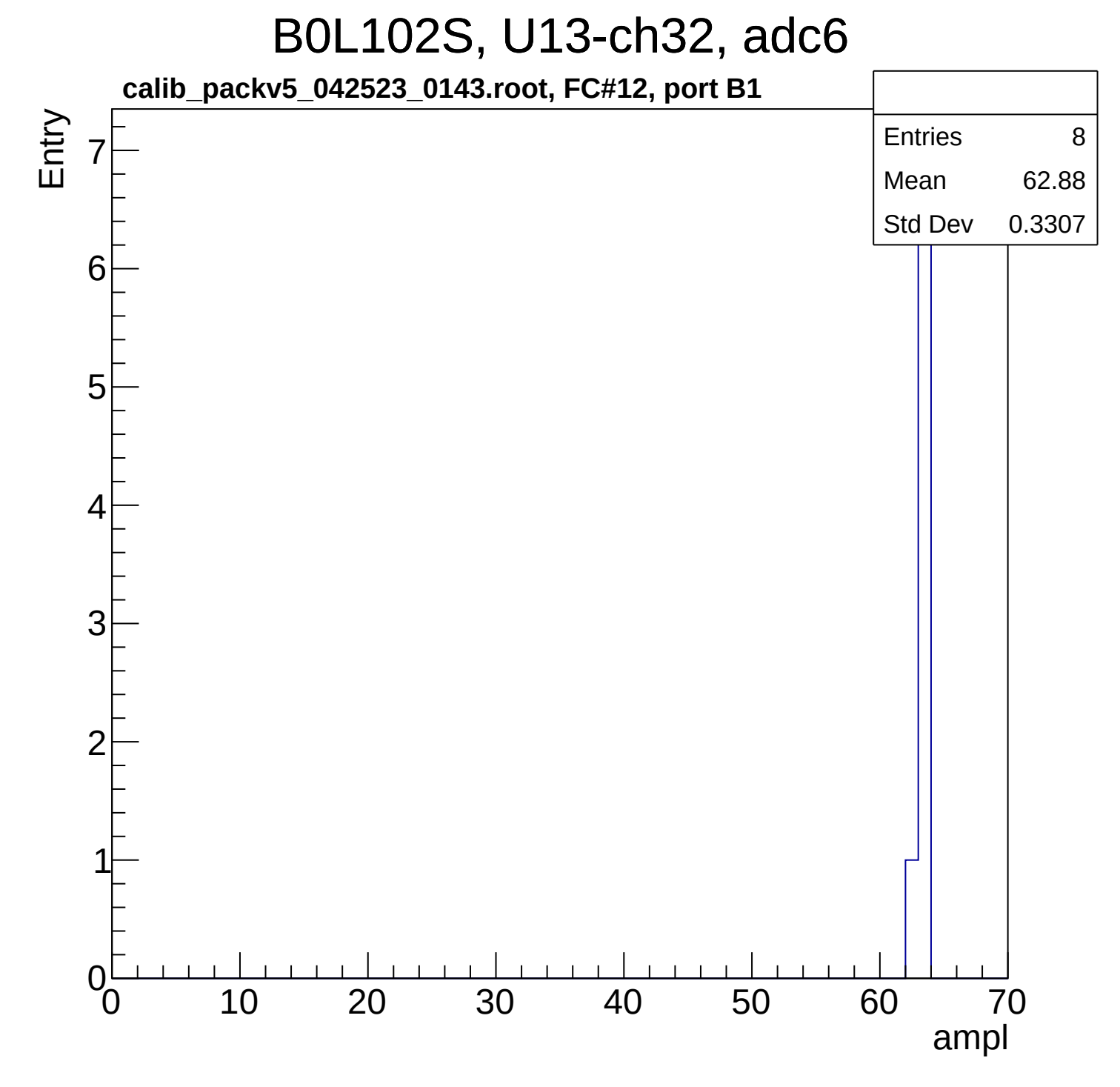
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch32, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.88
Std Dev 0.3307
Entry
ampl
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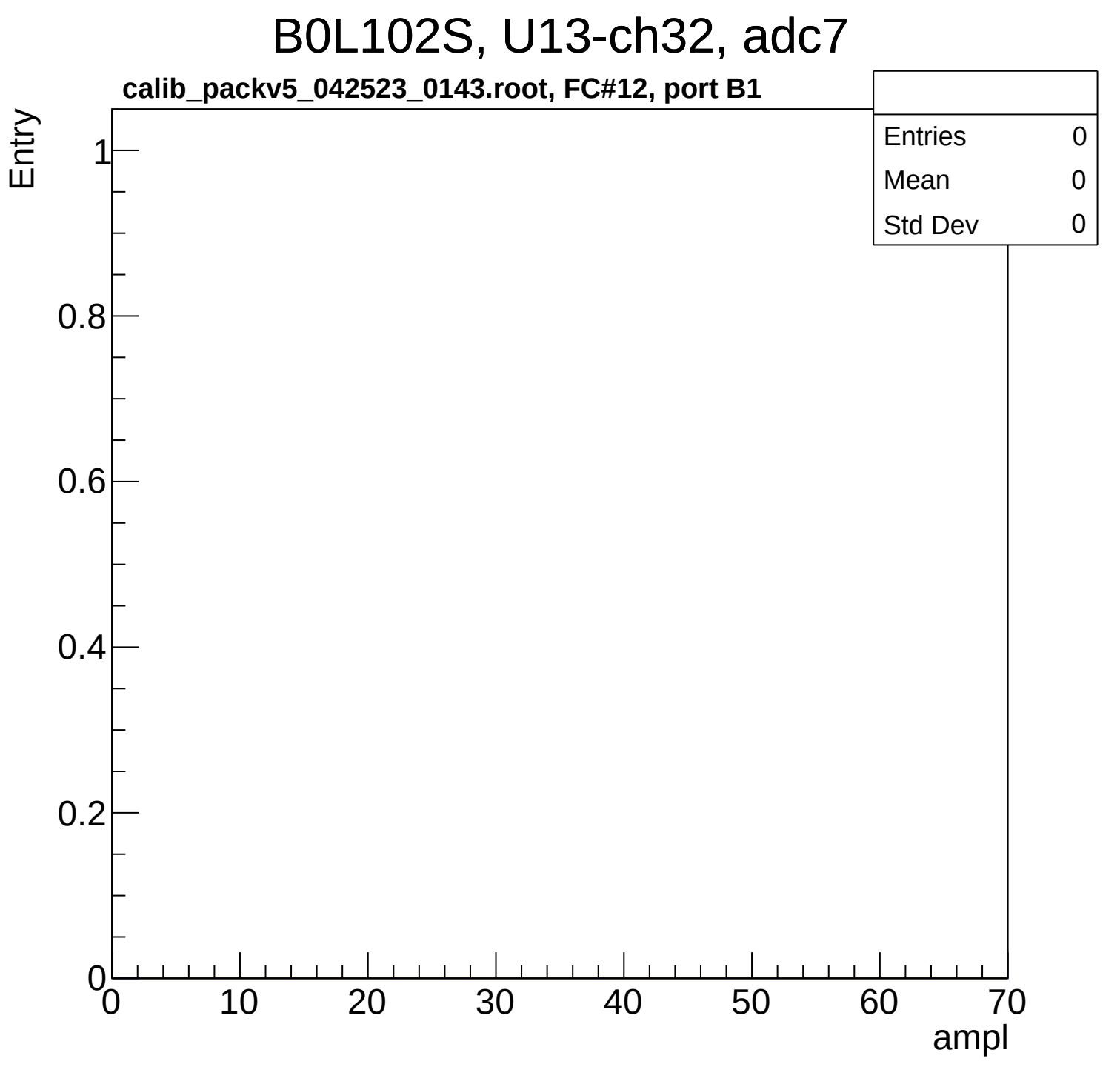
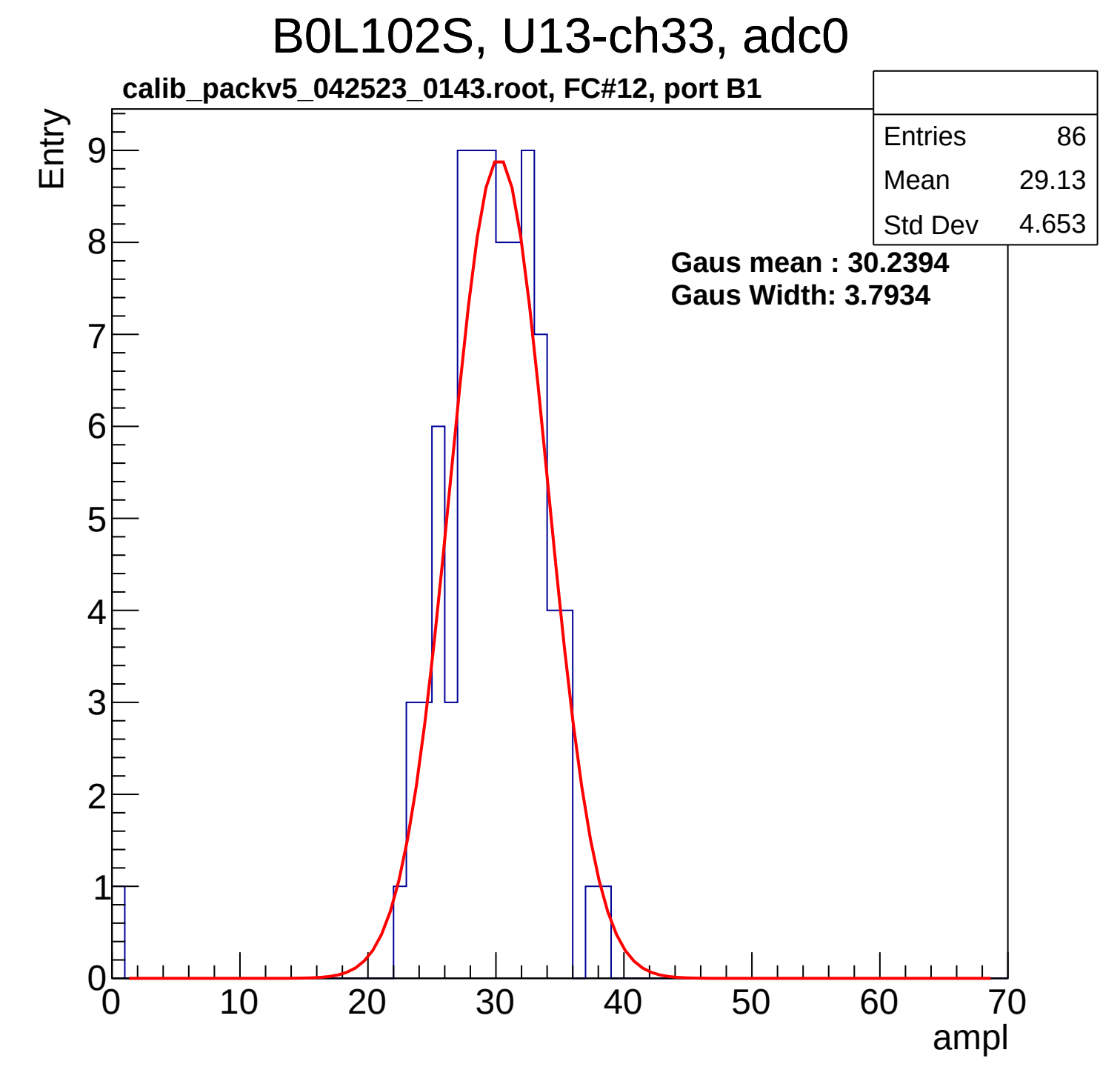
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch32, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch33, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 29.13
Std Dev 4.653
Gaus mean : 30.2394
Gaus Width: 3.7934
Entry
ampl
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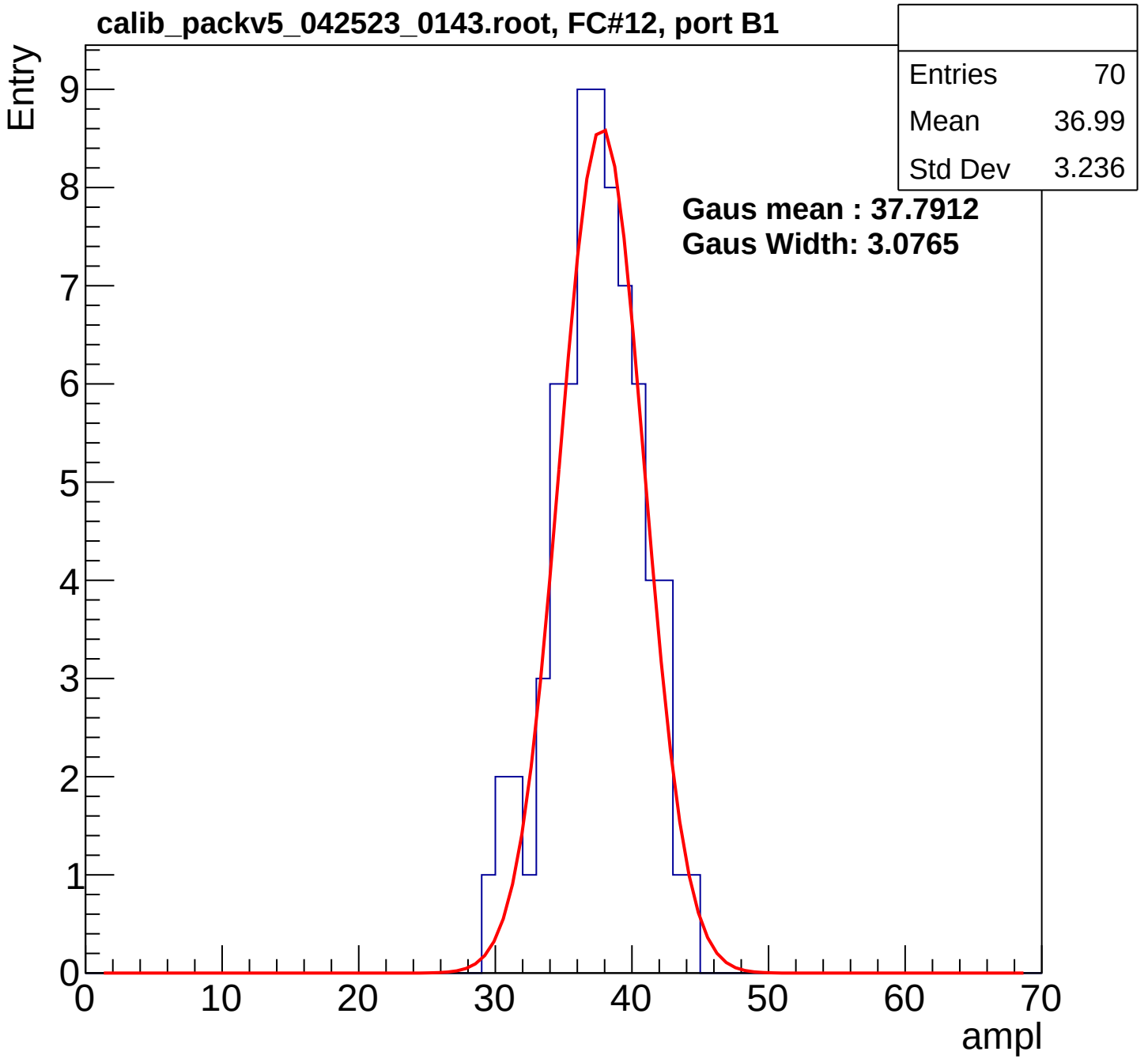
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch33, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 36.99
Std Dev 3.236
Gaus mean : 37.7912
Gaus Width: 3.0765
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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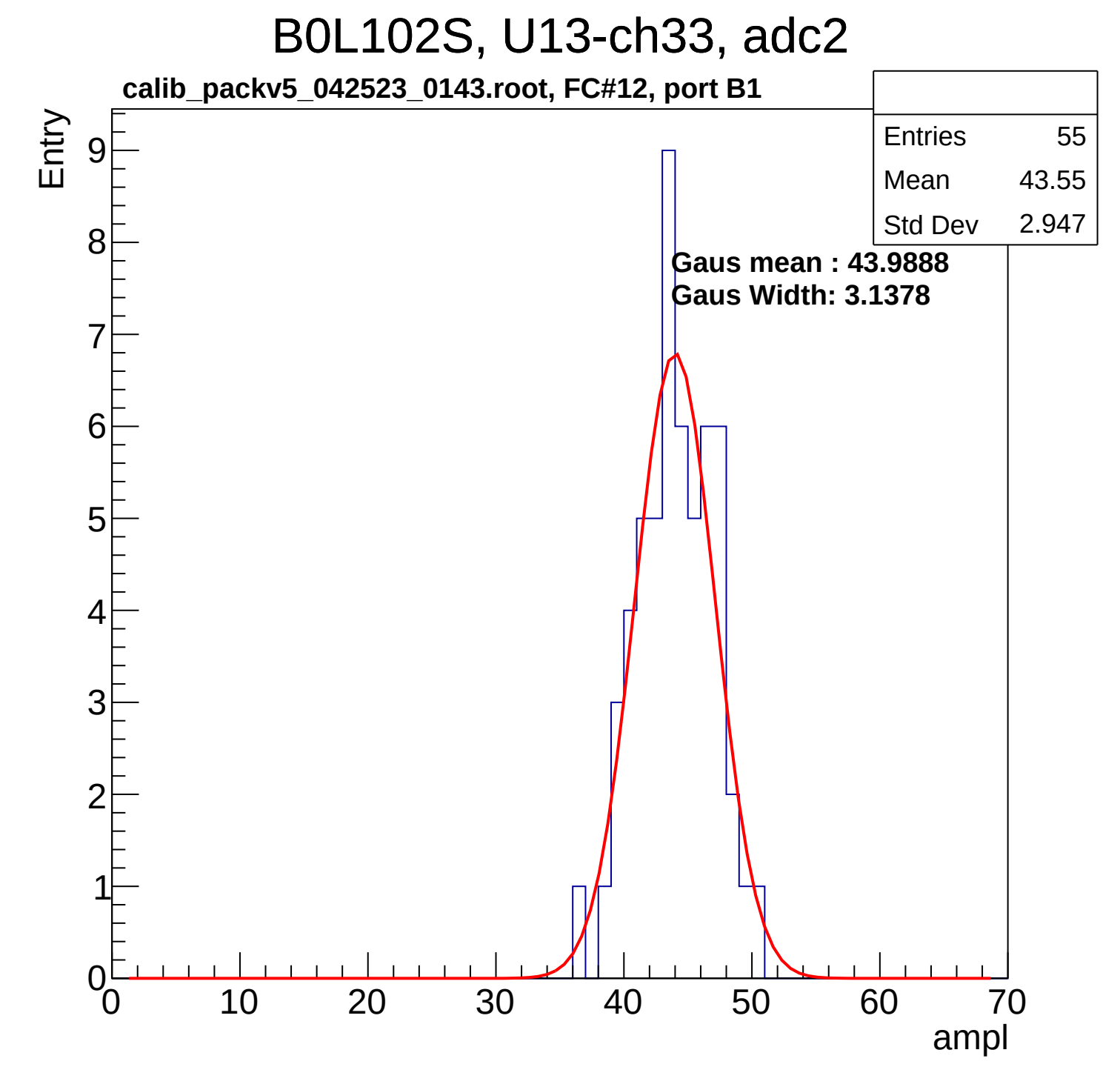
40
50
60
70


B0L102S, U13-ch33, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 43.55
Std Dev 2.947
Gaus mean : 43.9888
Gaus Width: 3.1378
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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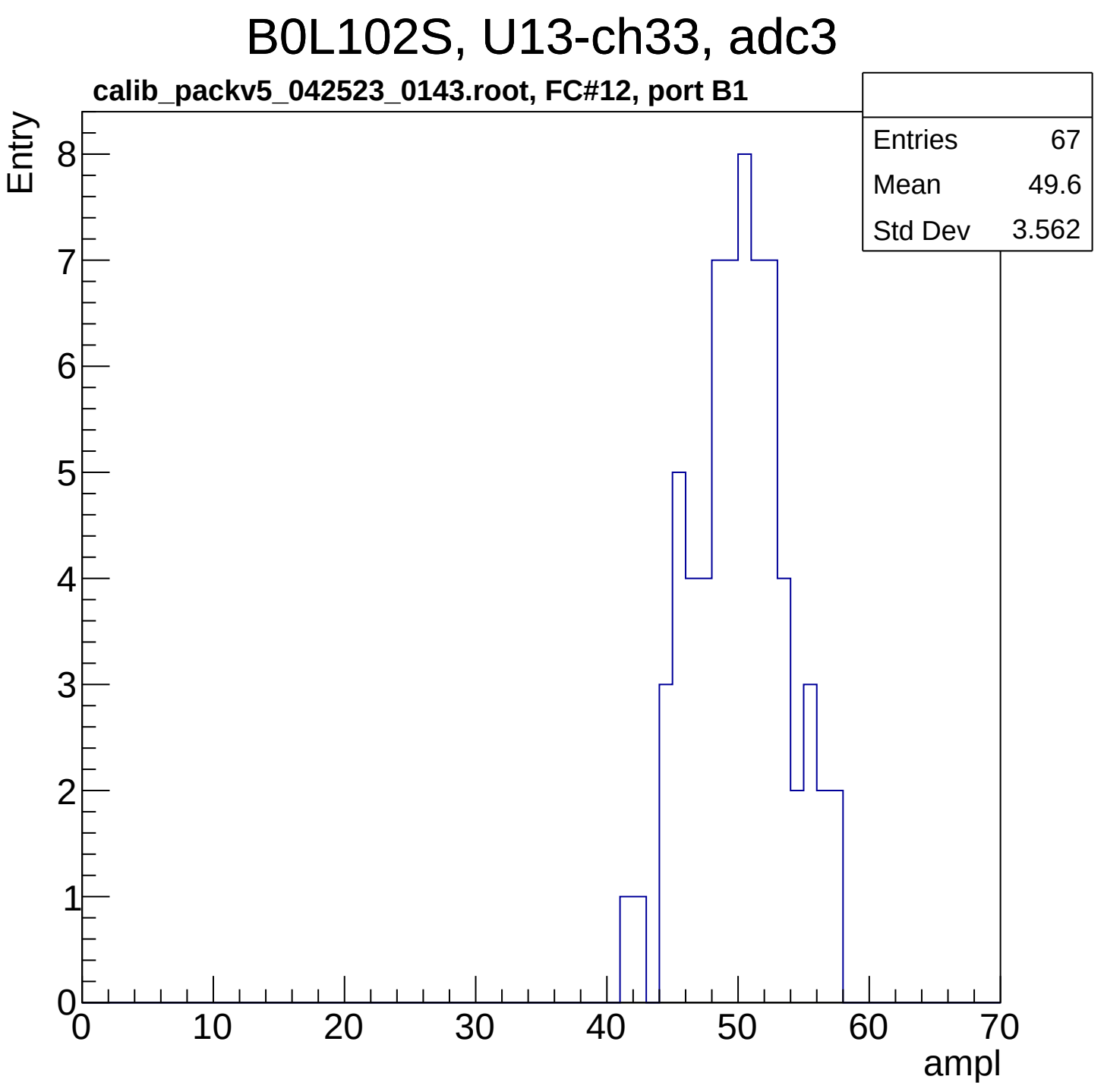
B0L102S, U13-ch33, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.6
Std Dev 3.562
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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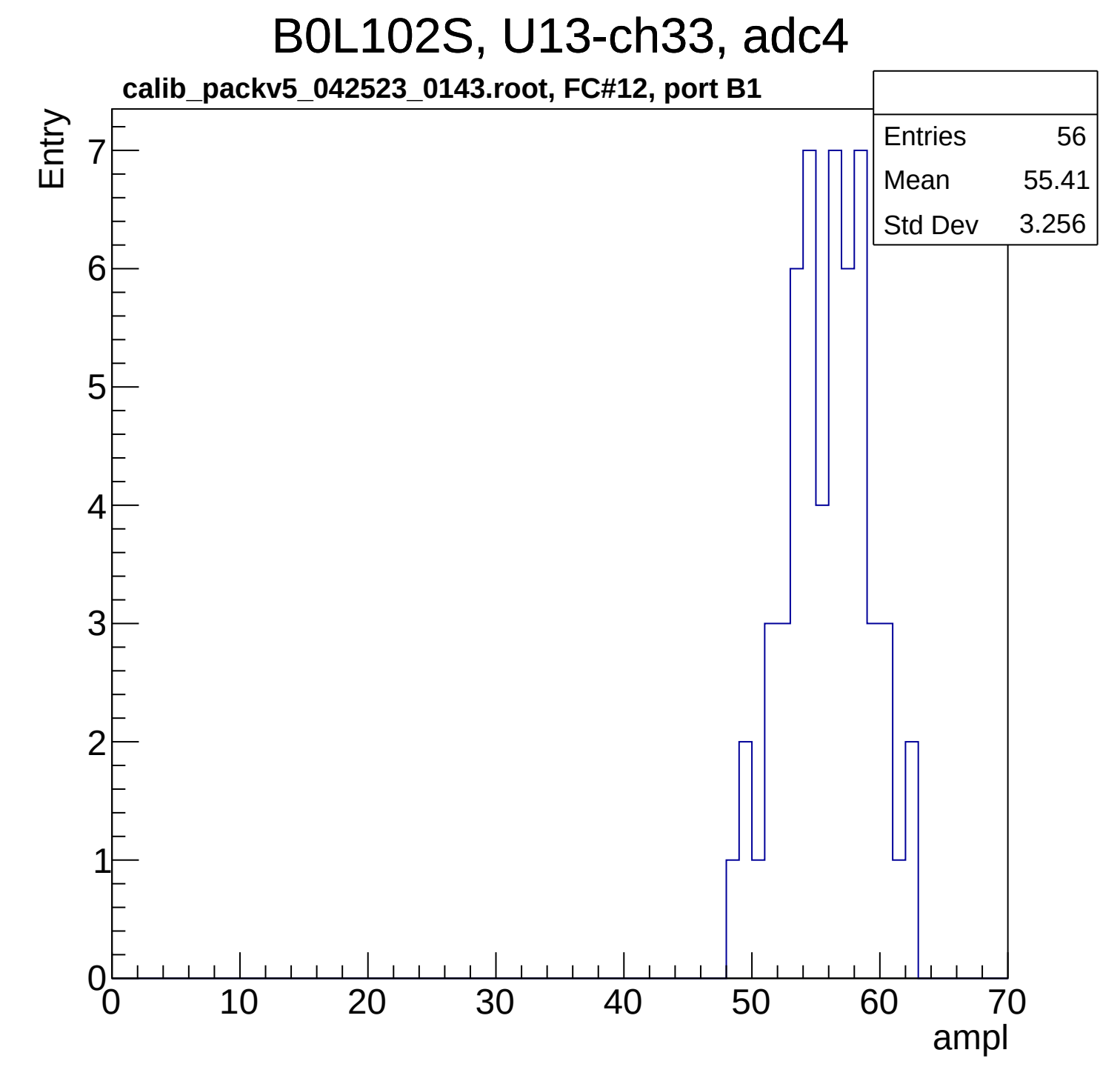
B0L102S, U13-ch33, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.41
Std Dev 3.256
Entry
ampl
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0
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60
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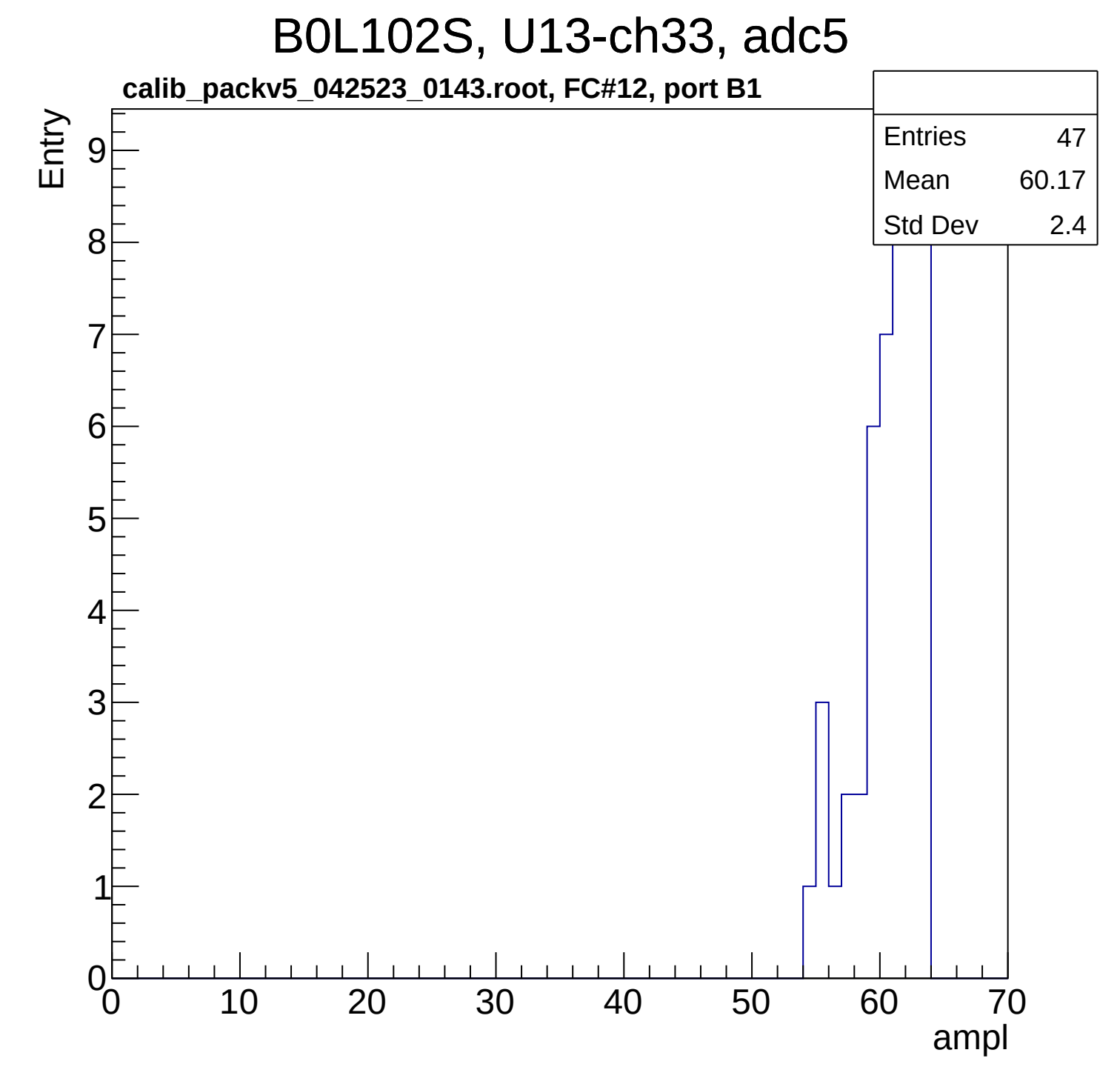
B0L102S, U13-ch33, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 60.17
Std Dev 2.4
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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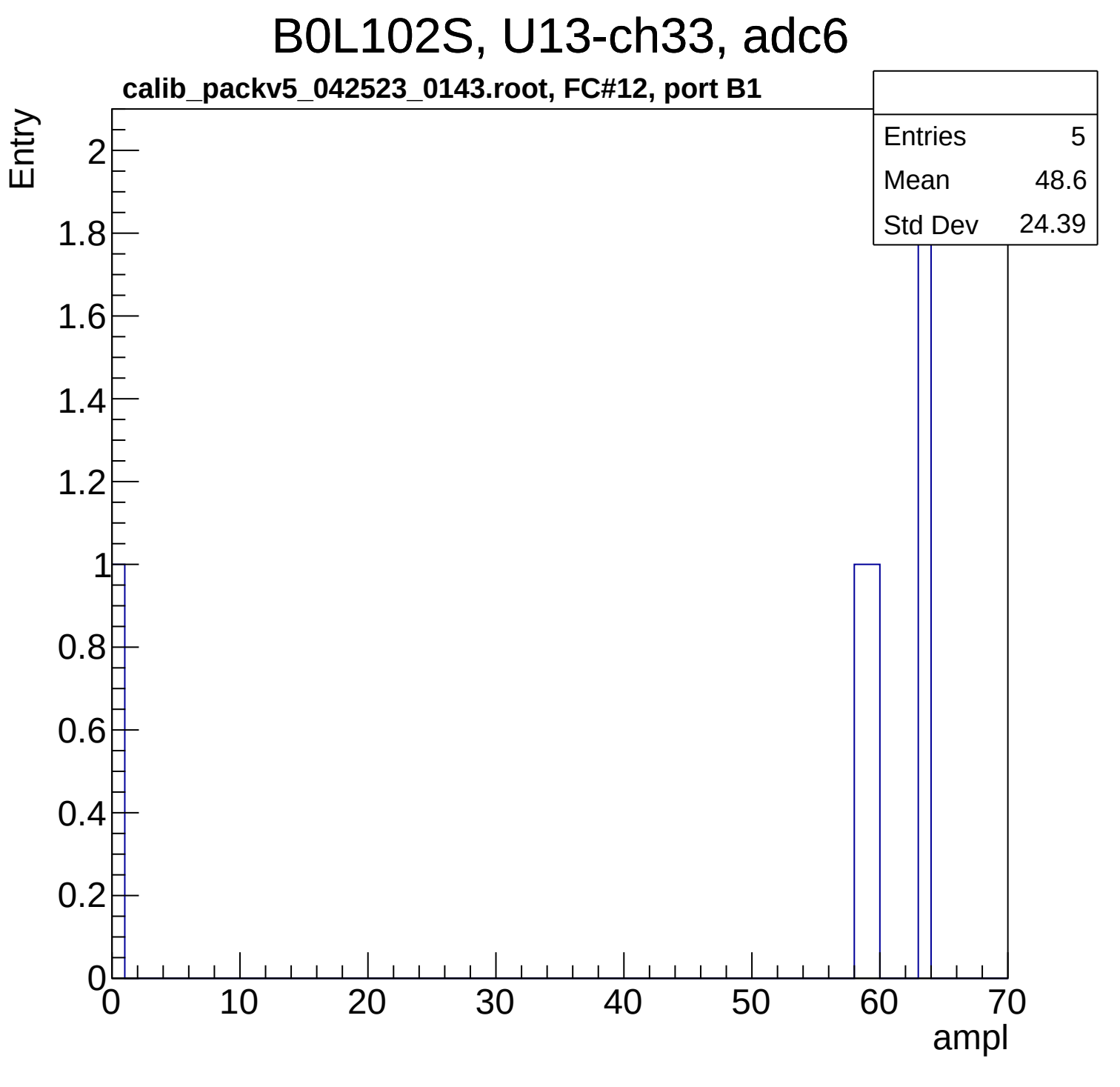
B0L102S, U13-ch33, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 48.6
Std Dev 24.39
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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50
60
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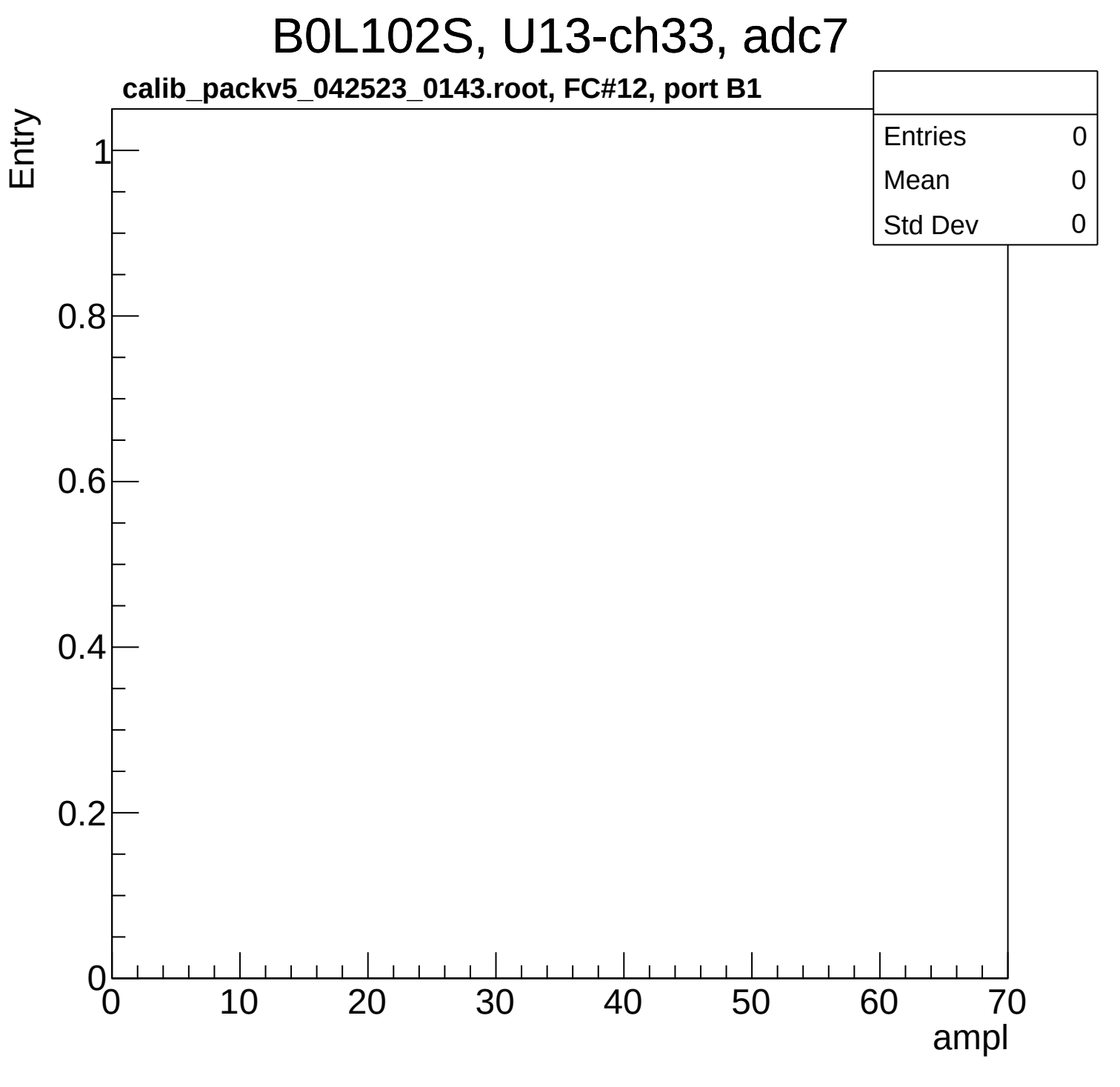
B0L102S, U13-ch33, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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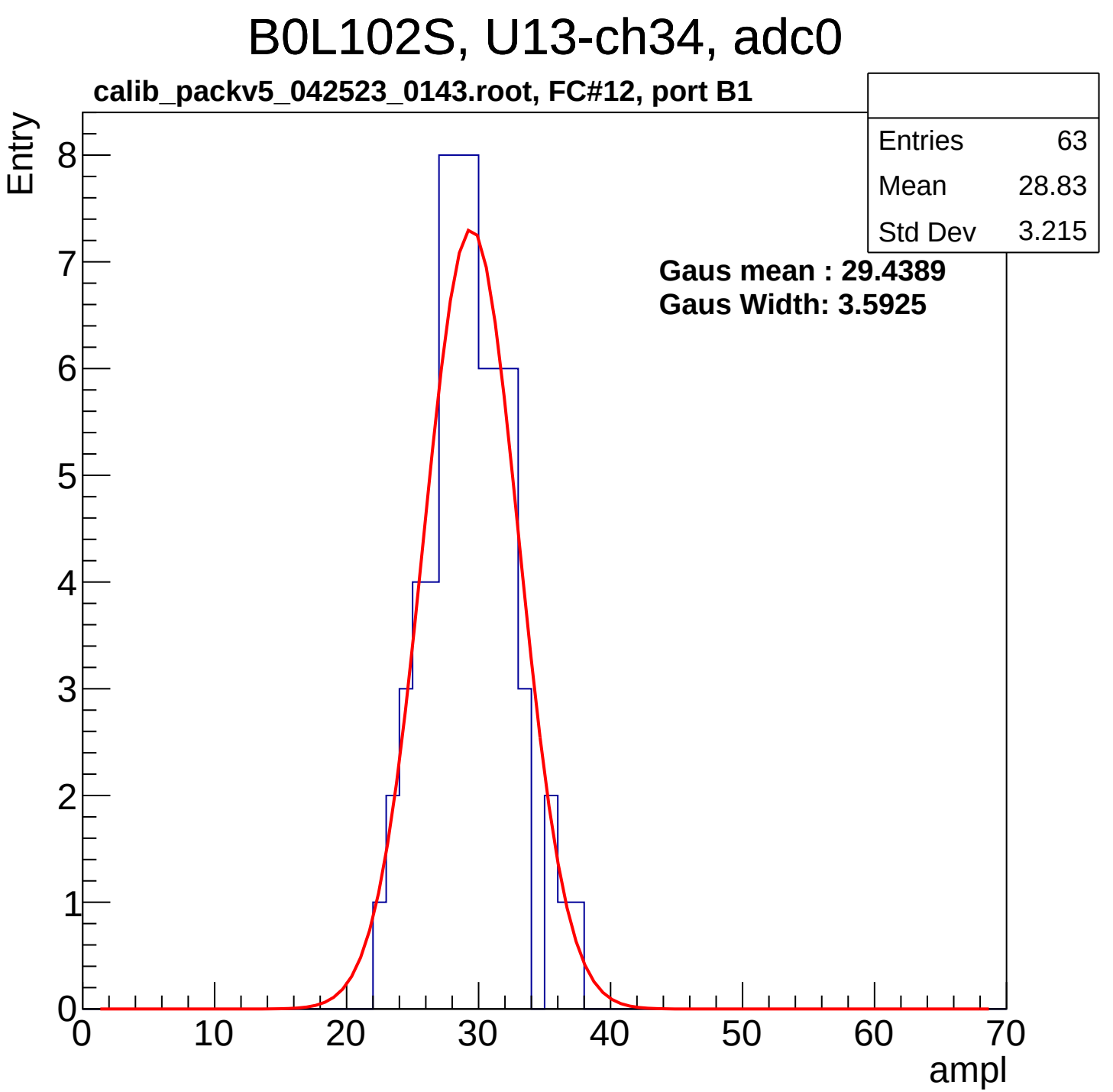
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch34, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.83
Std Dev 3.215
Gaus mean : 29.4389
Gaus Width: 3.5925
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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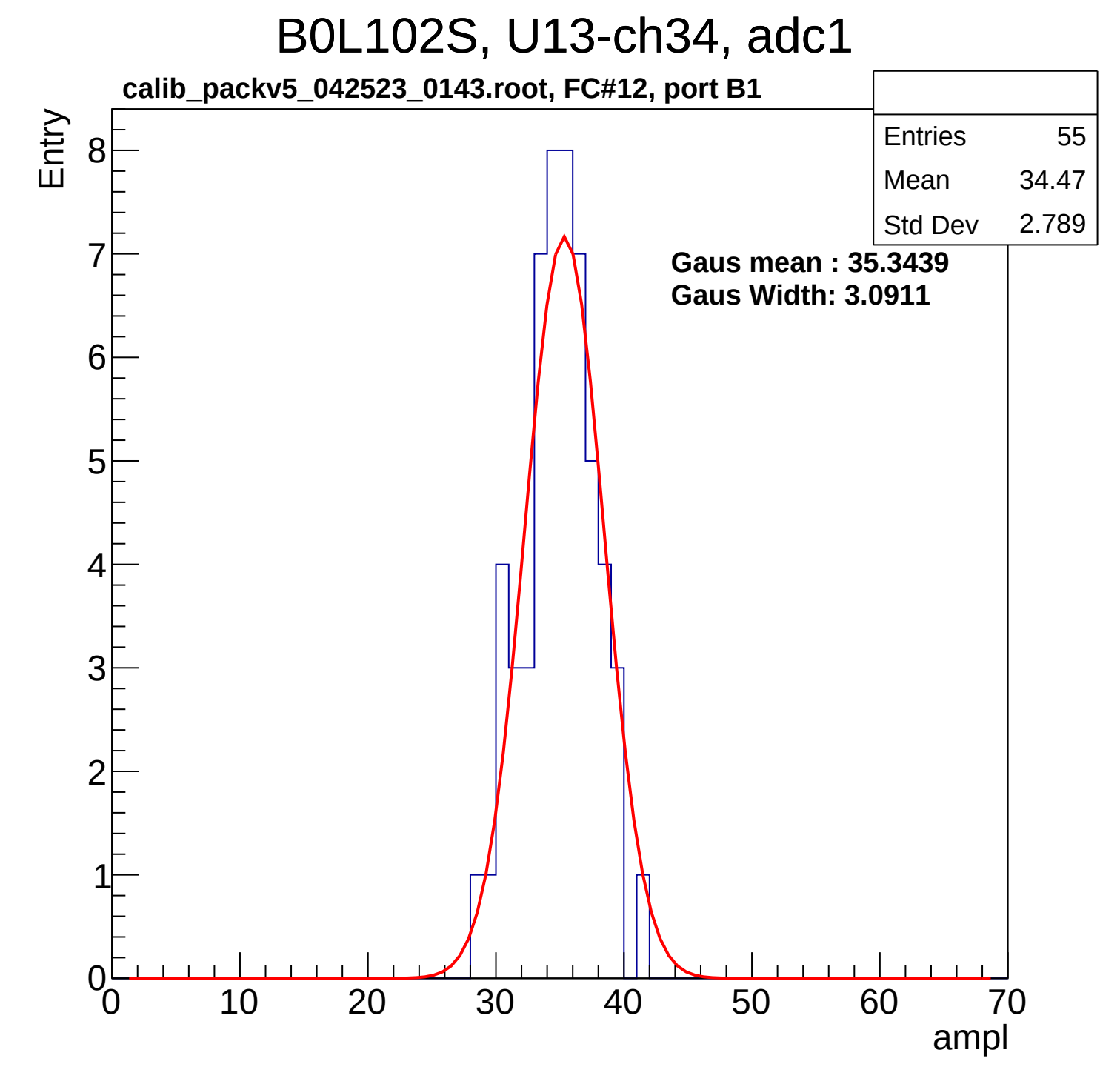
40
50
60
70


B0L102S, U13-ch34, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 34.47
Std Dev 2.789
Gaus mean : 35.3439
Gaus Width: 3.0911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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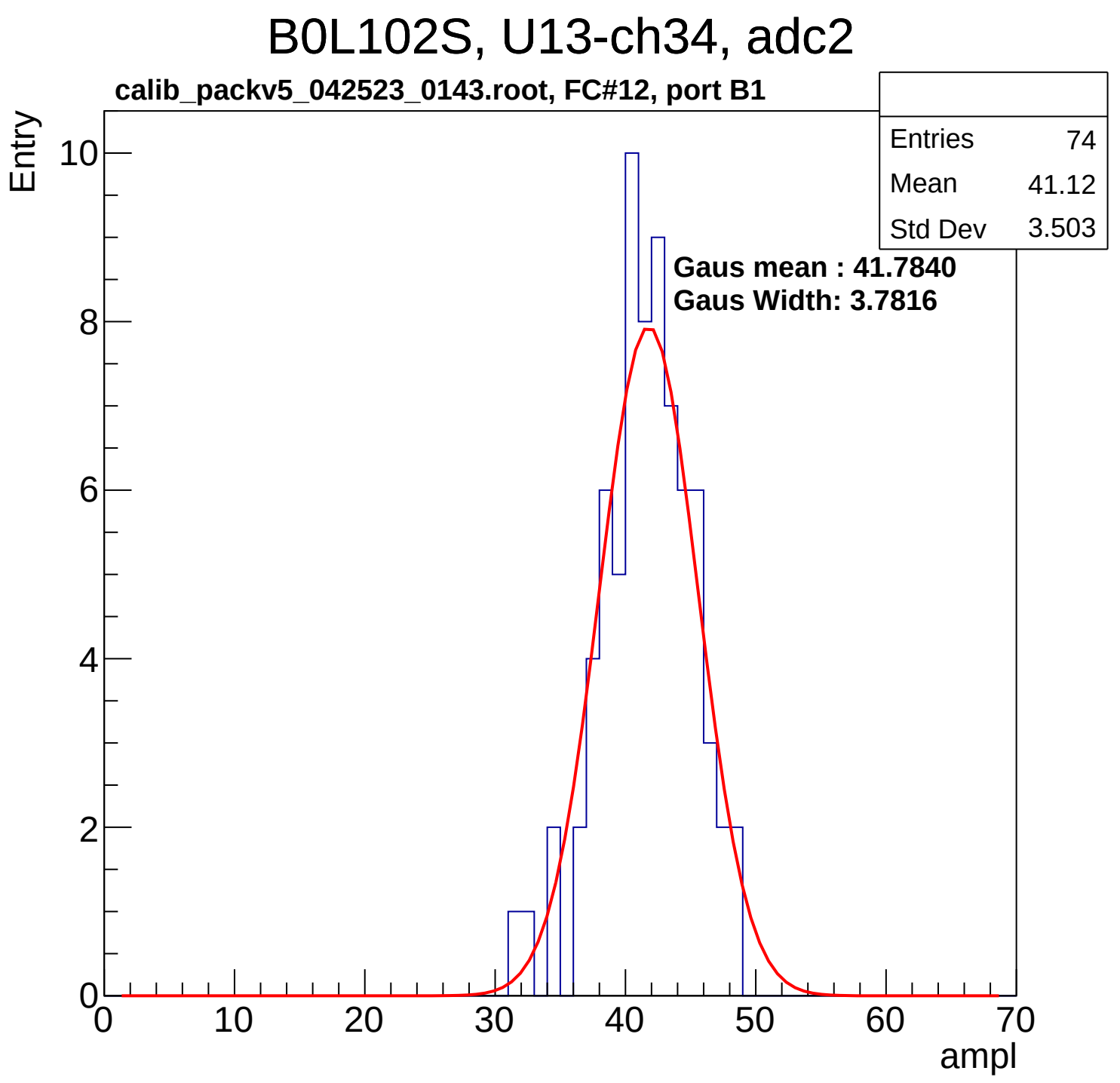
B0L102S, U13-ch34, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 41.12
Std Dev 3.503
Gaus mean : 41.7840
Gaus Width: 3.7816
Entry
ampl
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4
6
8
10
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10
20
30
40
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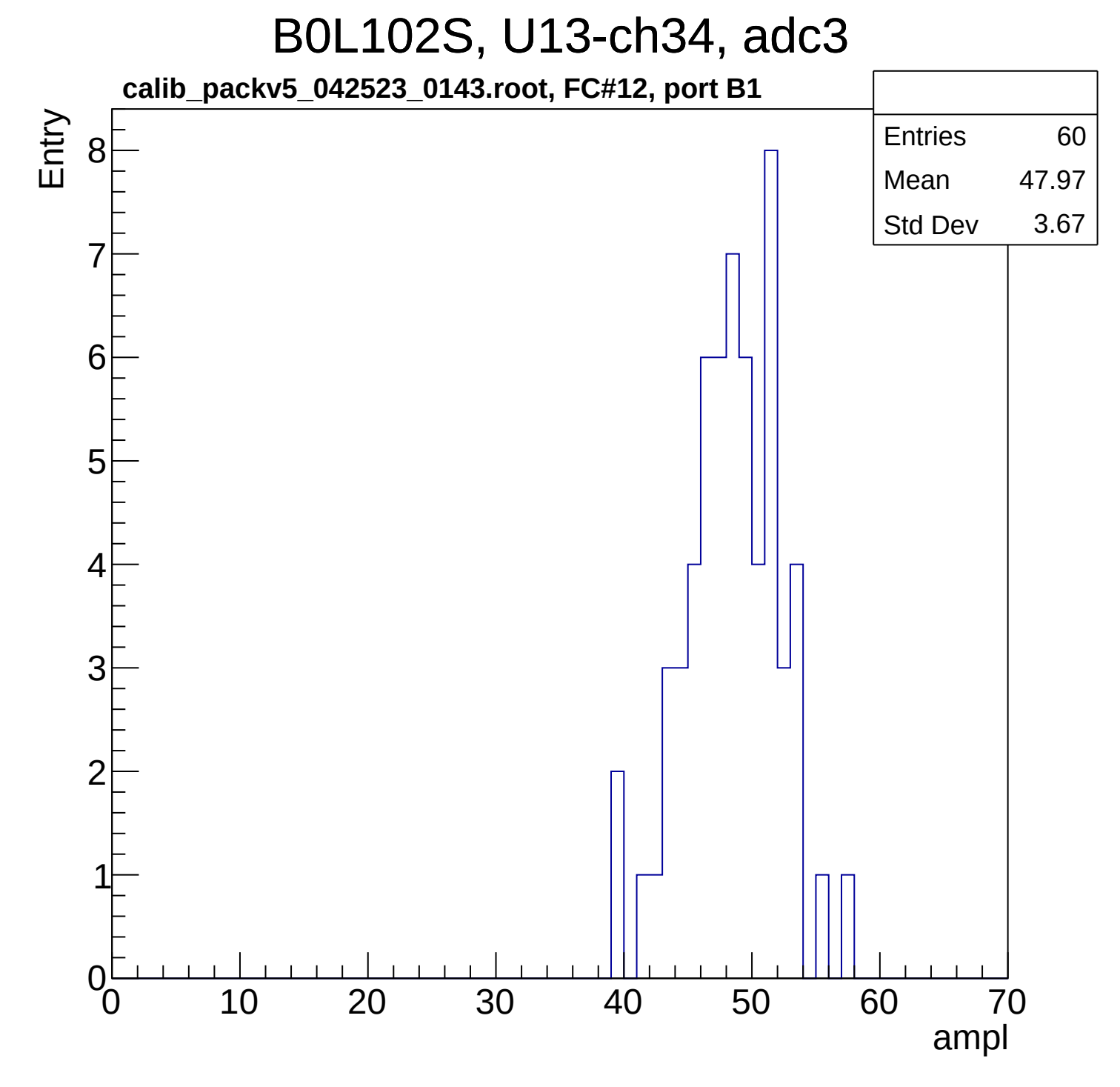
B0L102S, U13-ch34, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 47.97
Std Dev 3.67
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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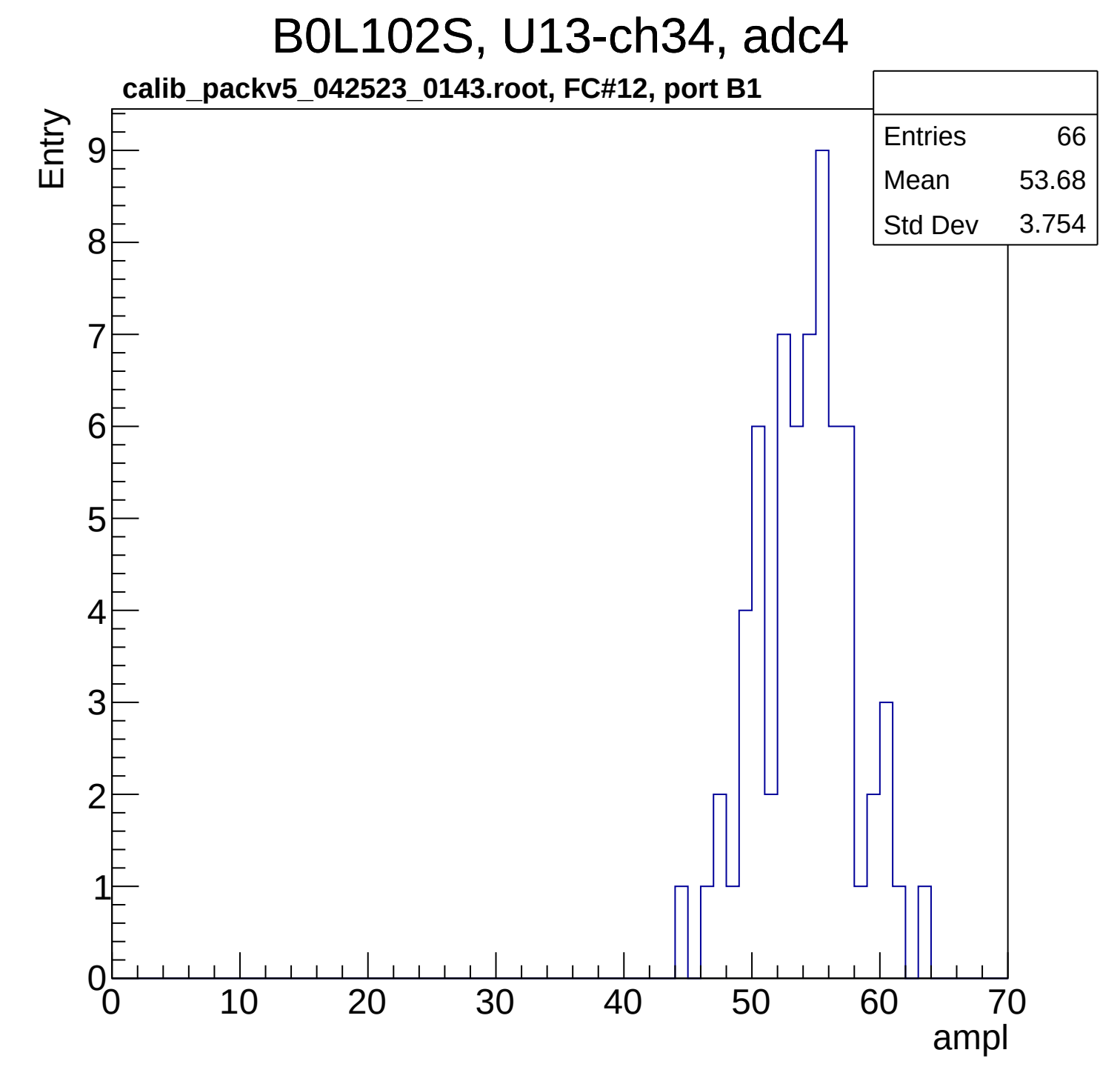
B0L102S, U13-ch34, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 53.68
Std Dev 3.754
Entry
ampl
0
1
2
3
4
5
6
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8
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0
10
20
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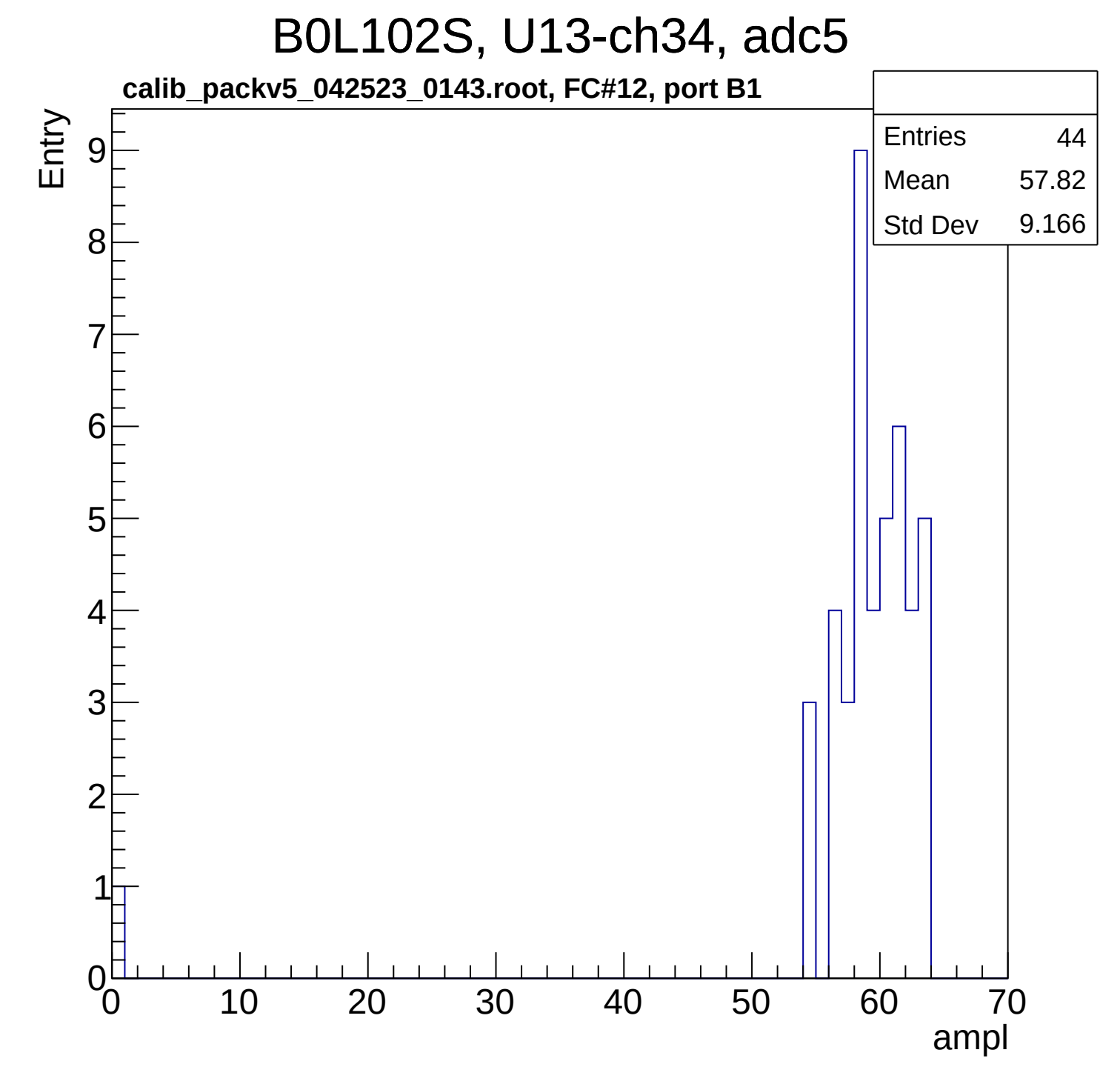
60
70


B0L102S, U13-ch34, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 57.82
Std Dev 9.166
Entry
ampl
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1
2
3
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5
6
7
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60
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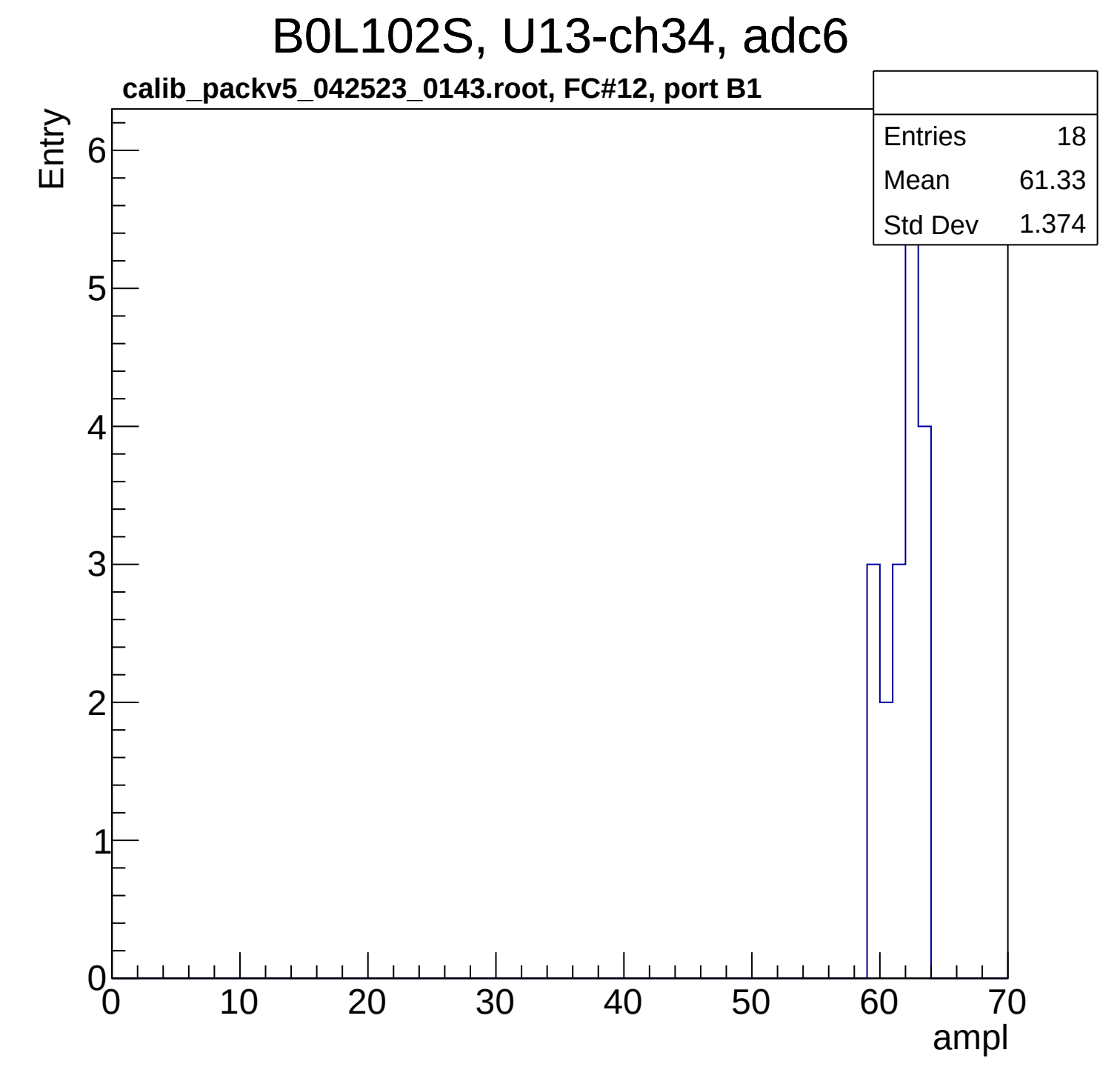
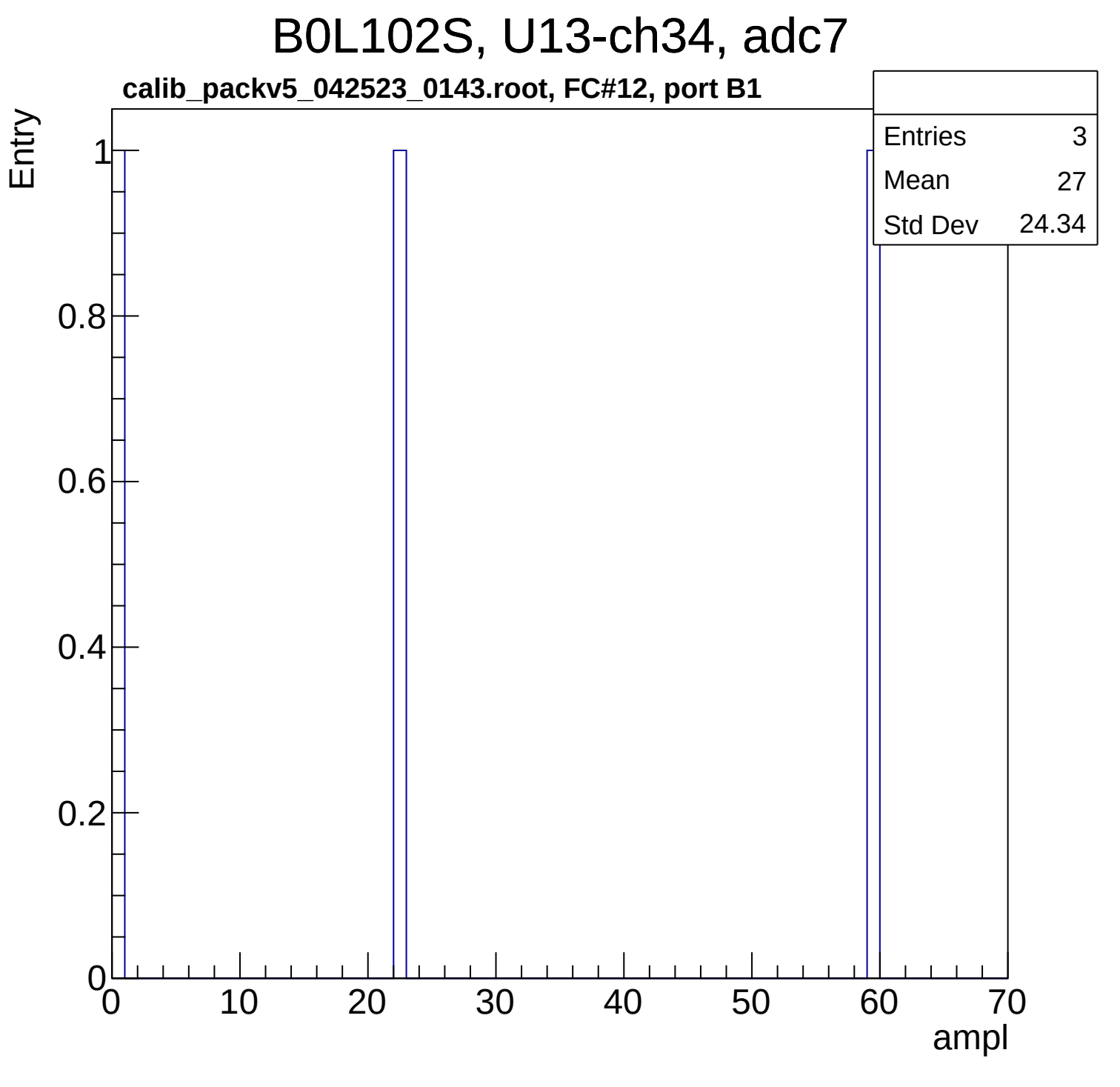

B0L102S, U13-ch34, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 18
Mean 61.33
Std Dev 1.374
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch34, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27
Std Dev 24.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
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60
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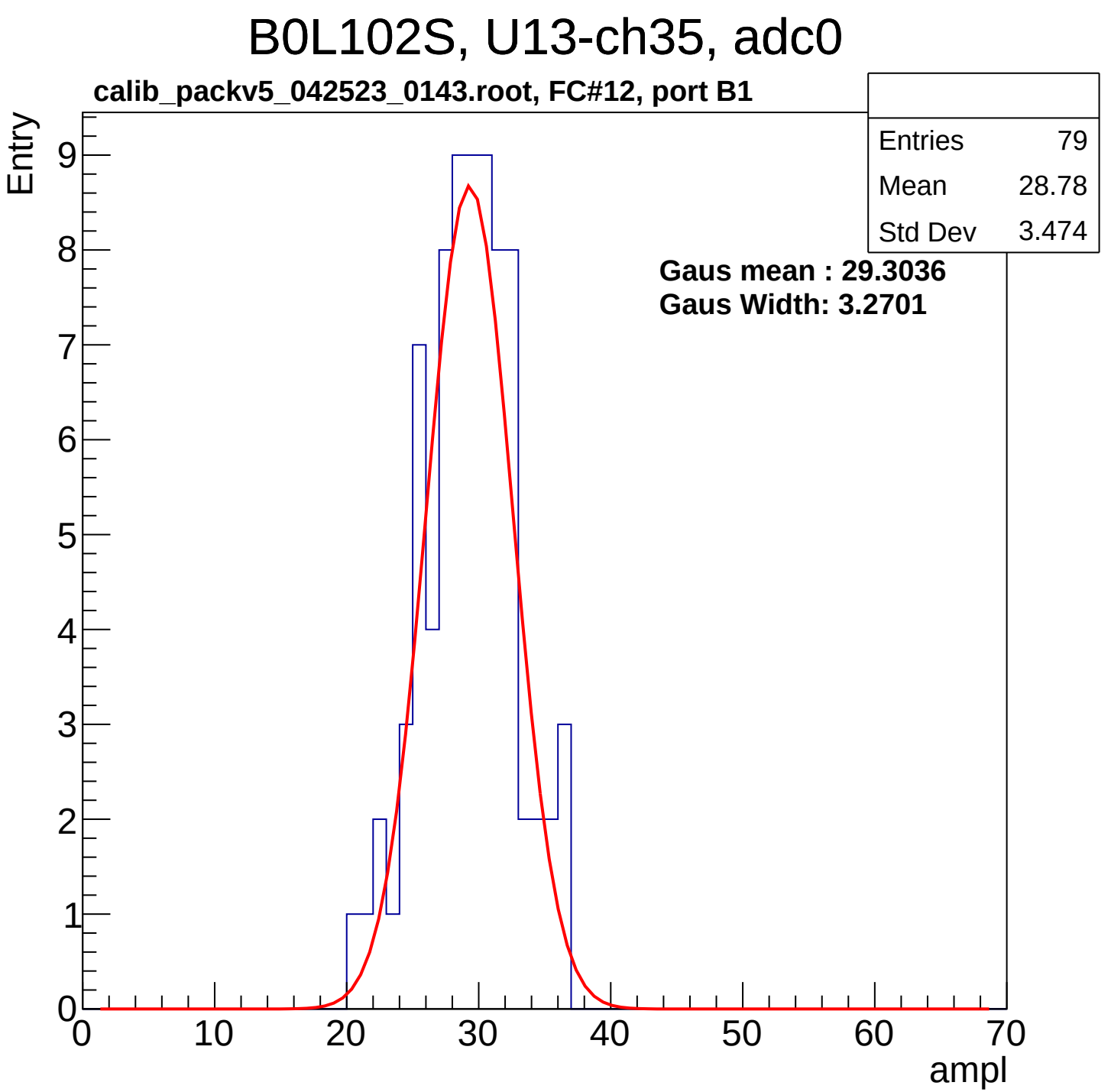
B0L102S, U13-ch35, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 28.78
Std Dev 3.474
Gaus mean : 29.3036
Gaus Width: 3.2701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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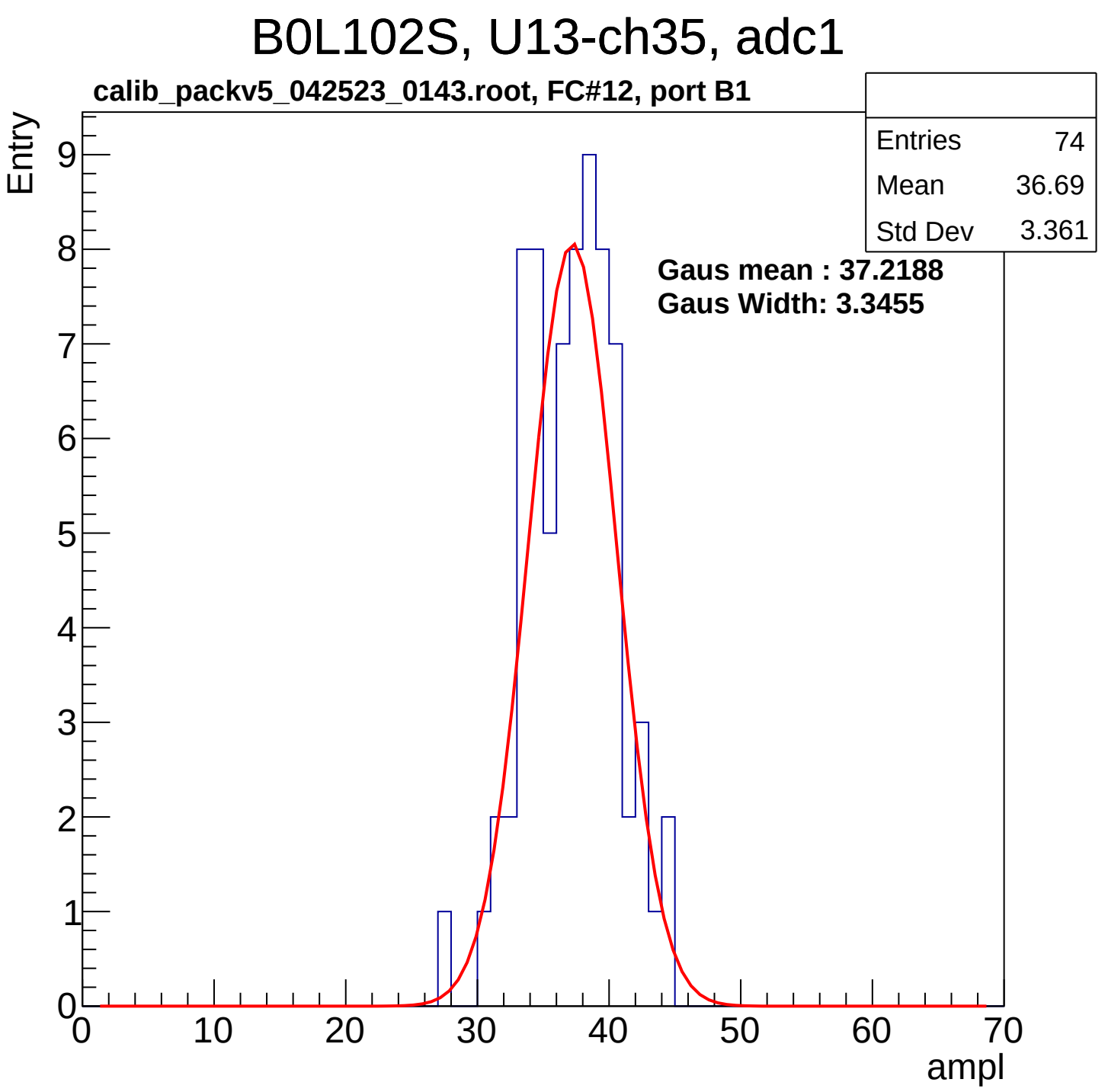
B0L102S, U13-ch35, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.69
Std Dev 3.361
Gaus mean : 37.2188
Gaus Width: 3.3455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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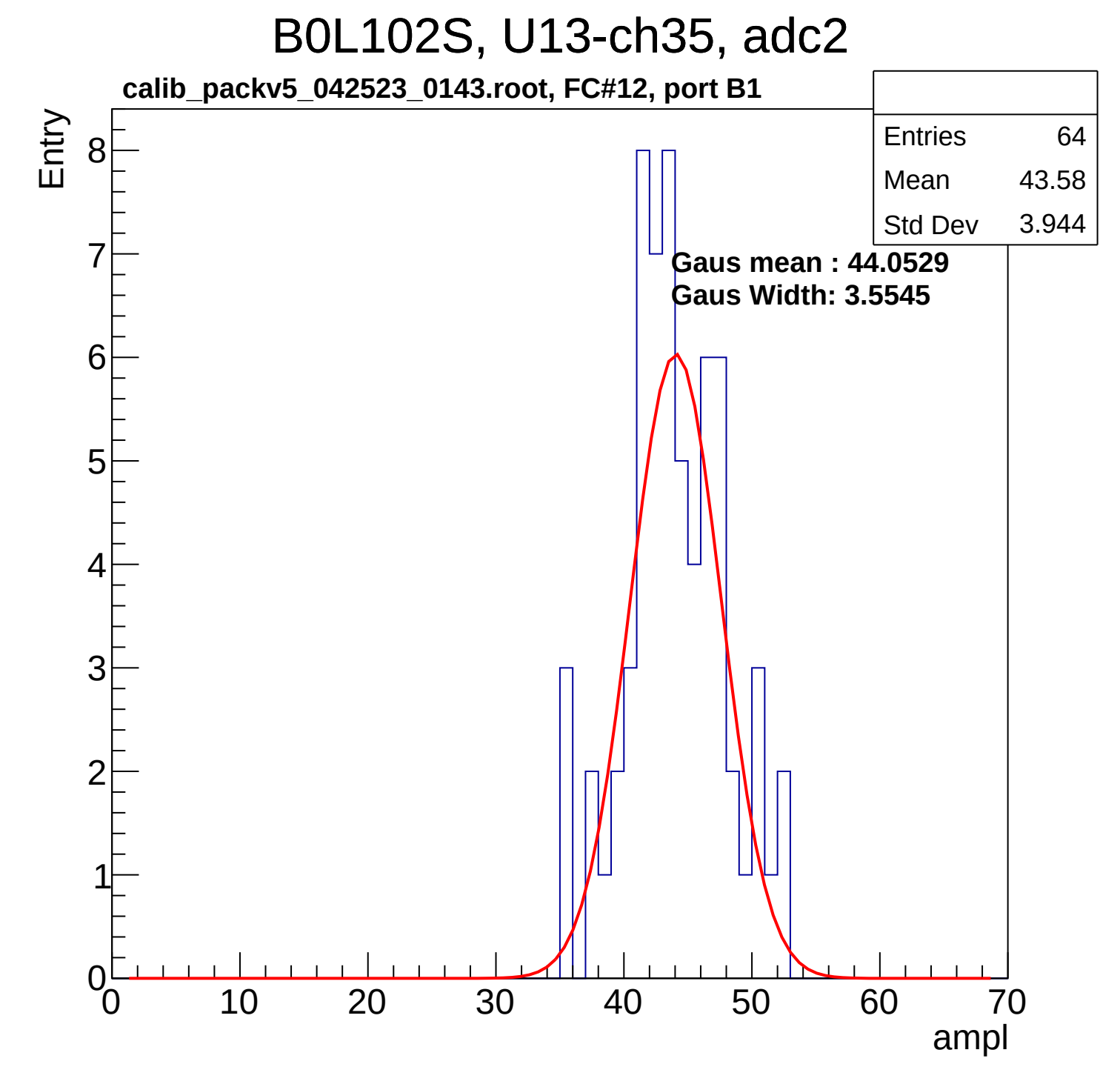
B0L102S, U13-ch35, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.58
Std Dev 3.944
Gaus mean : 44.0529
Gaus Width: 3.5545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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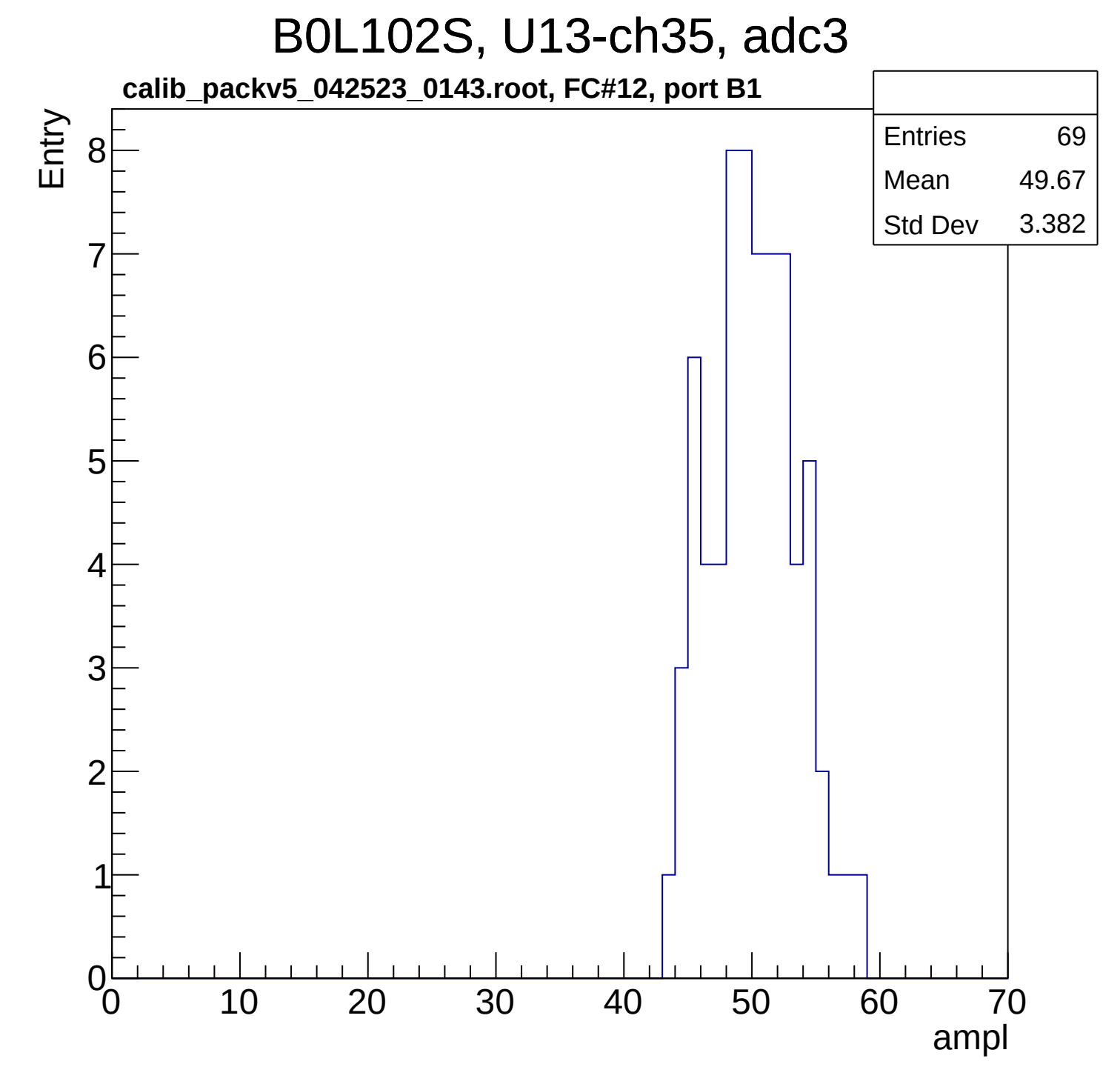
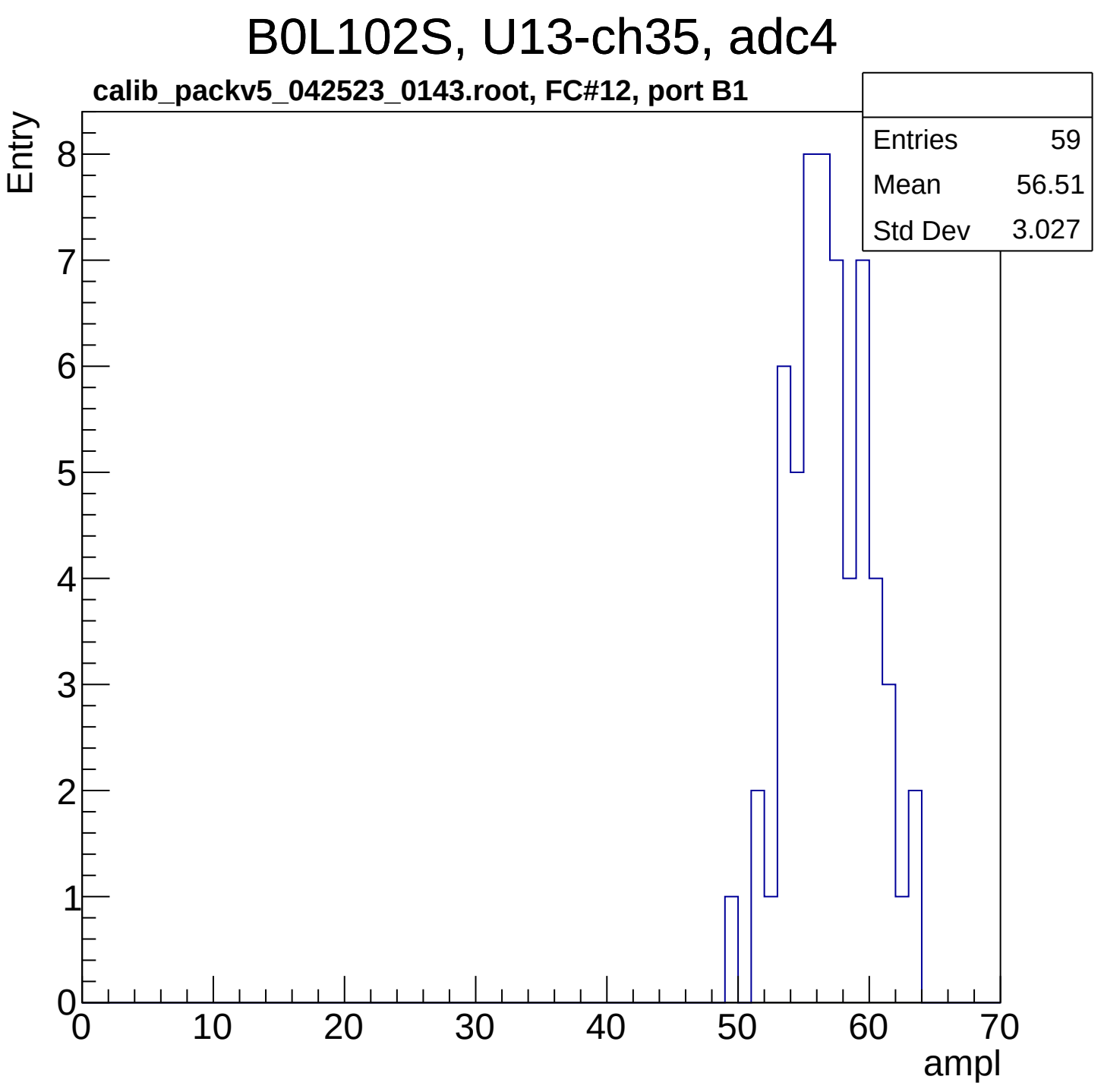
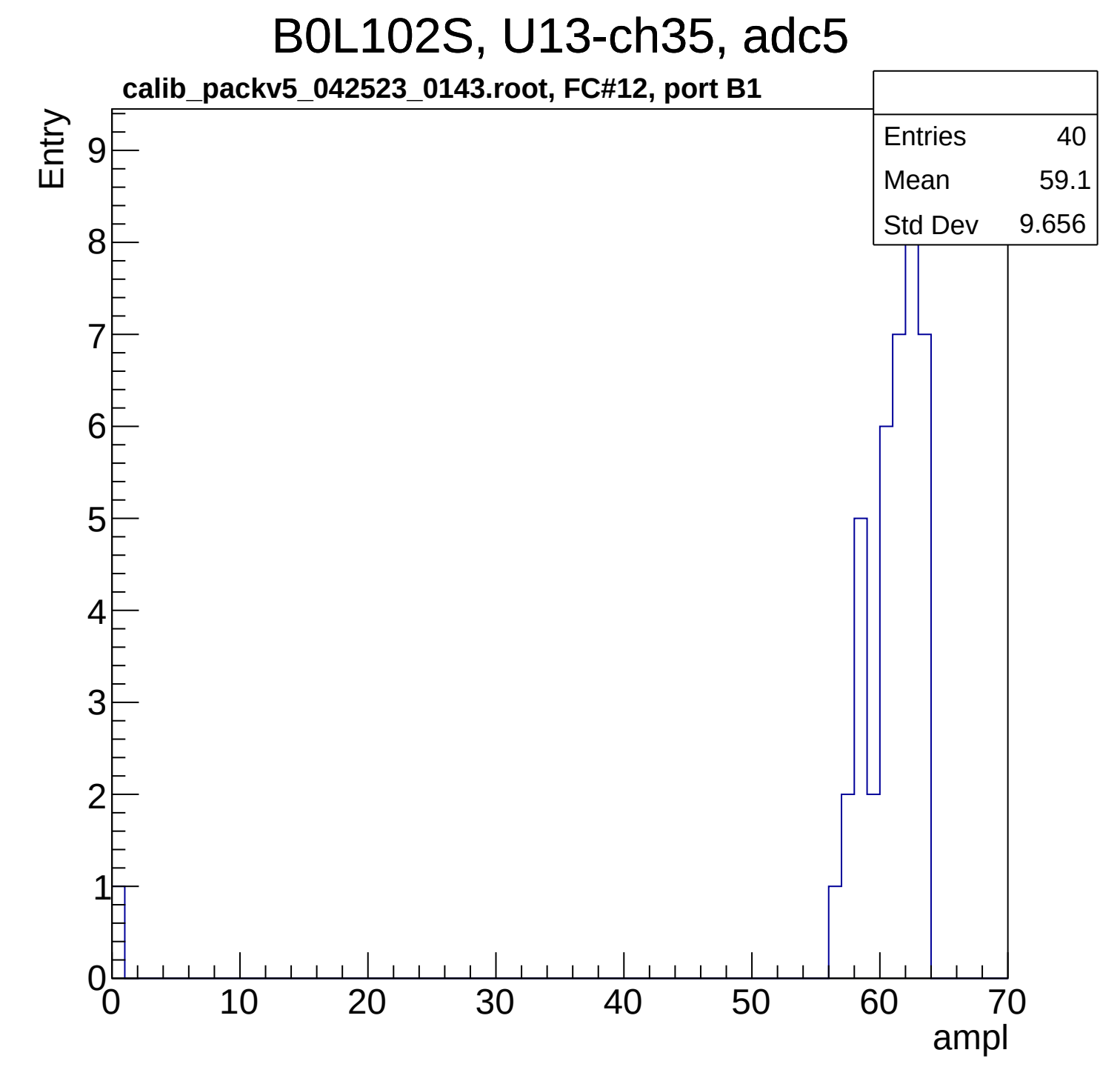
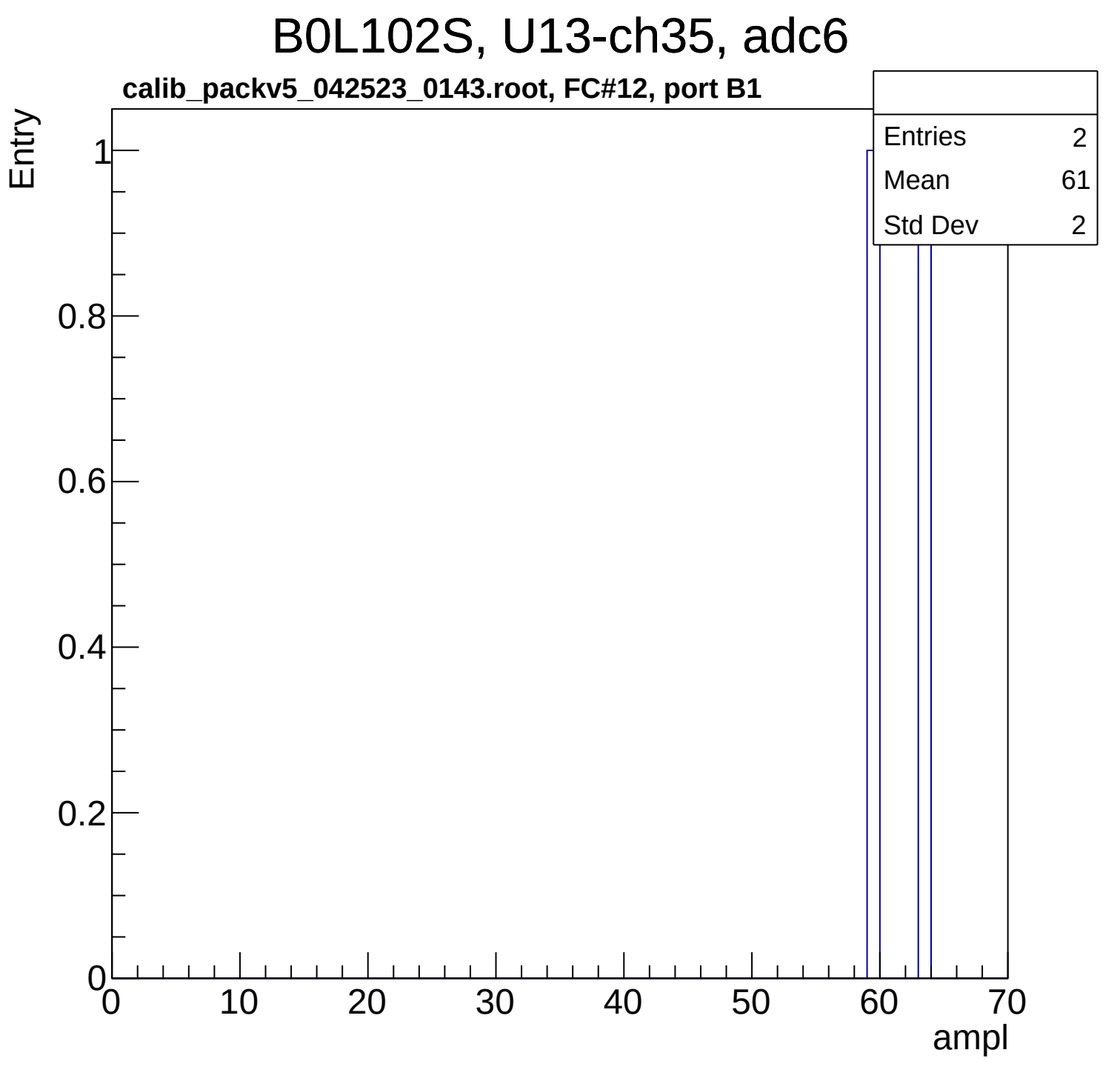
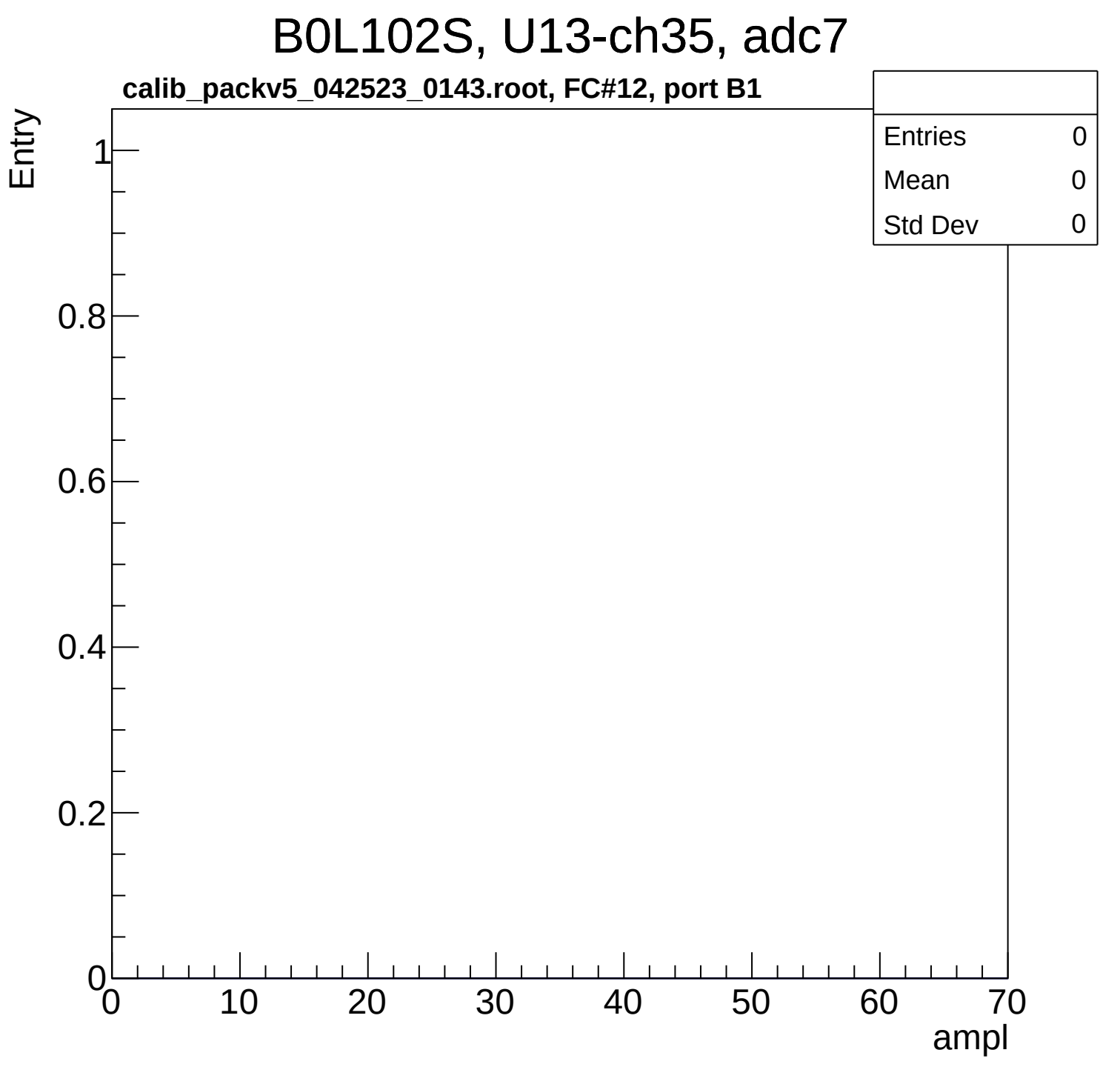
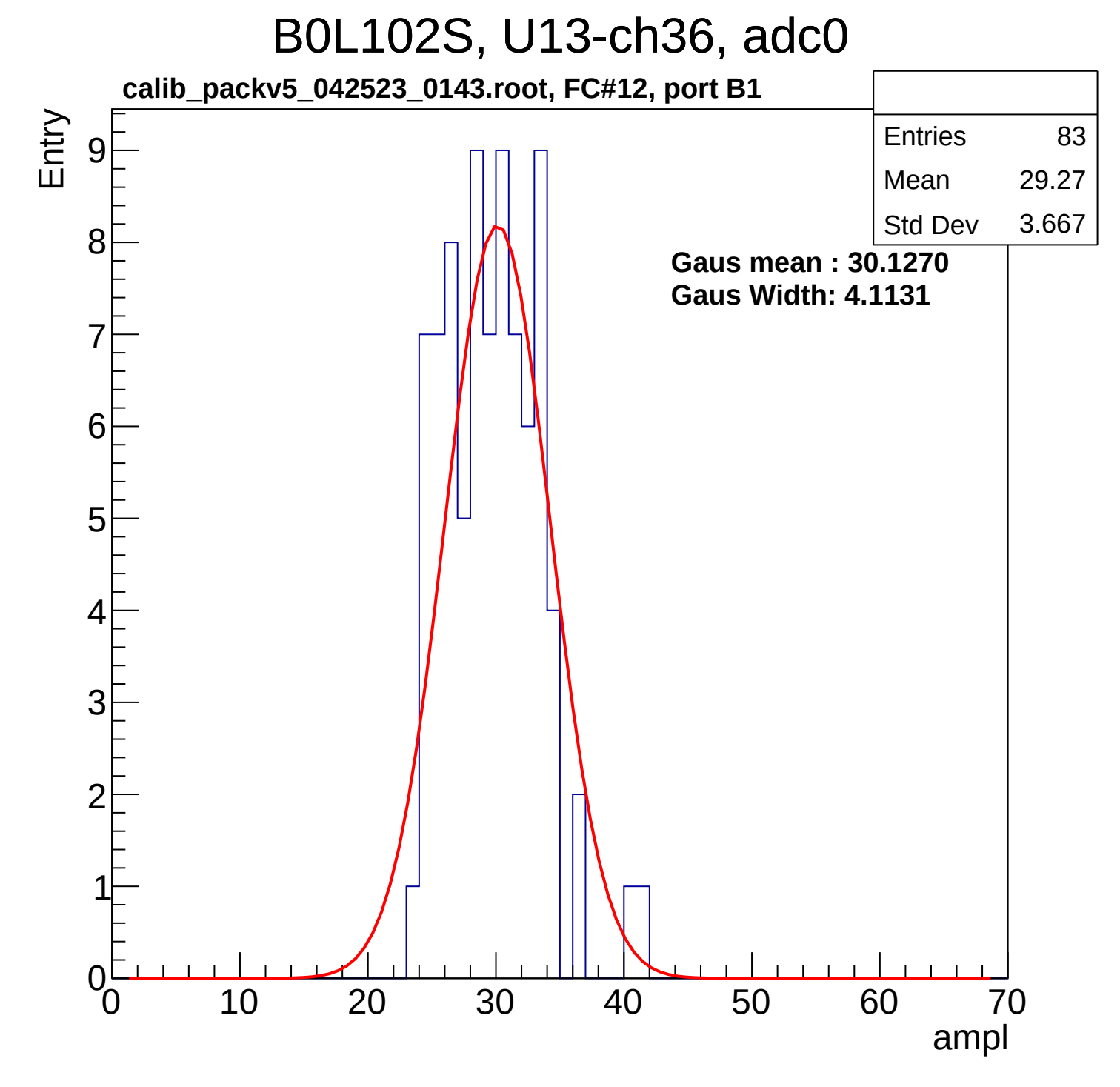

B0L102S, U13-ch35, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.67
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

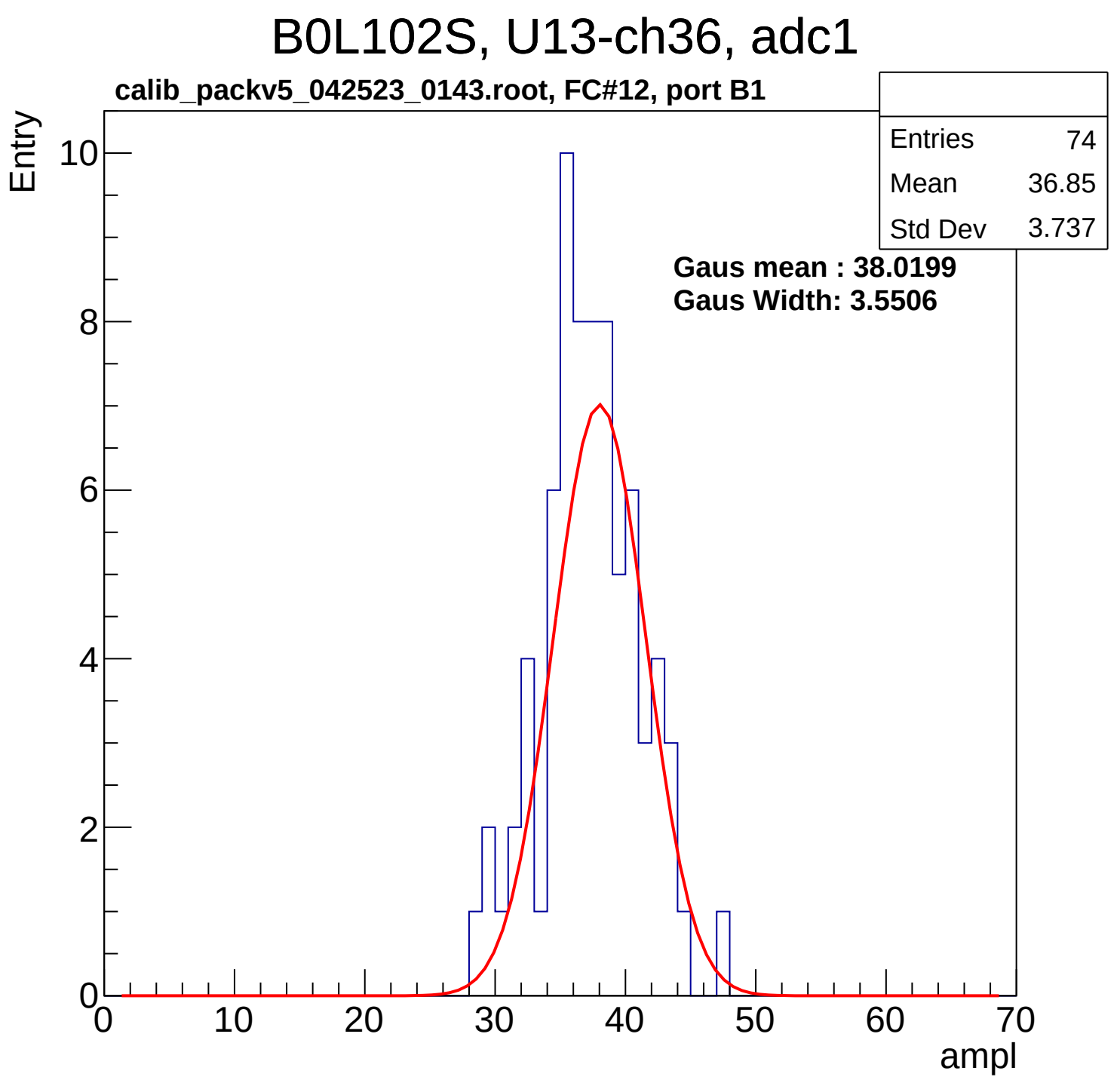

B0L102S, U13-ch35, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.51
Std Dev 3.027
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

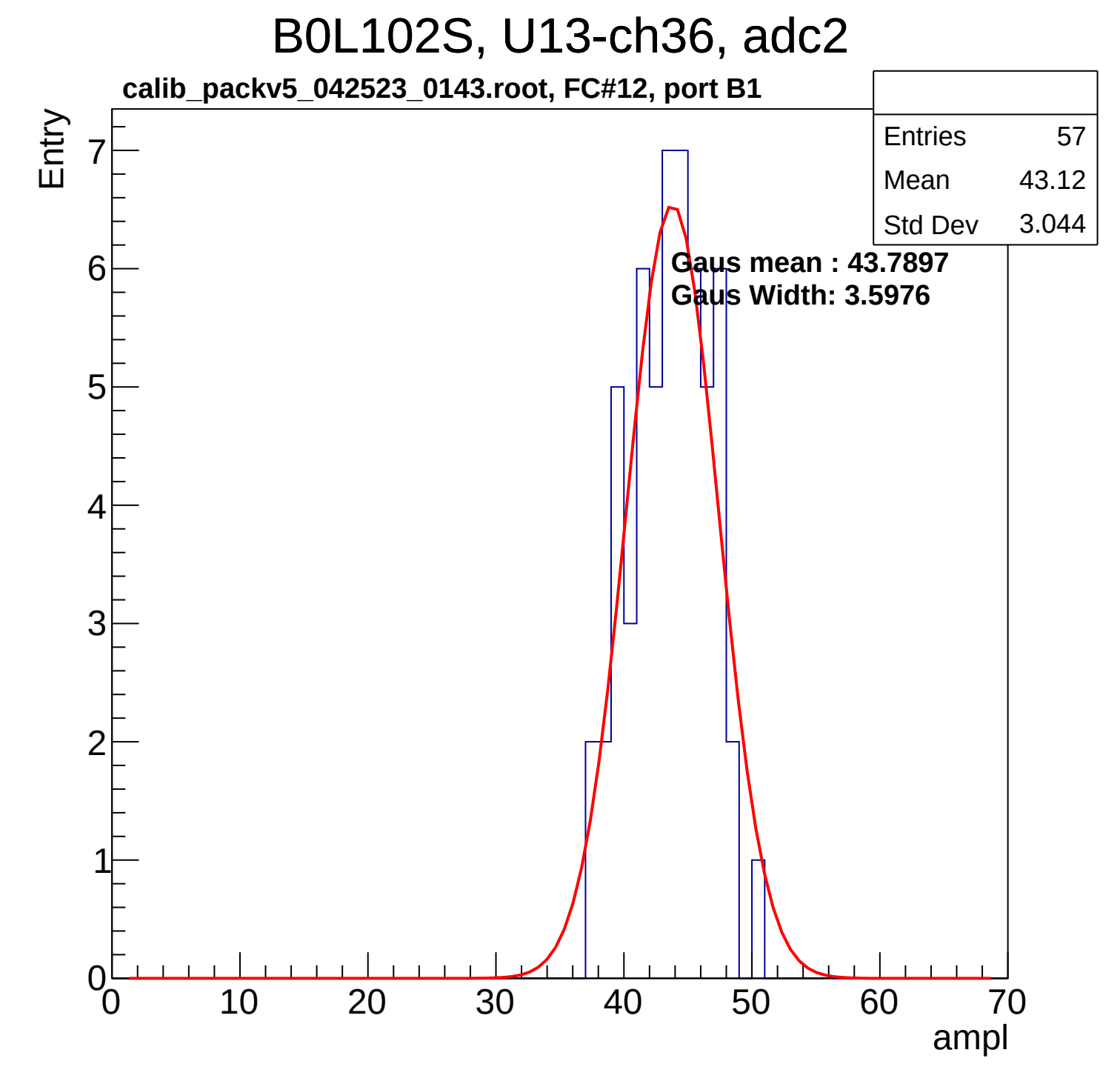

B0L102S, U13-ch35, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 59.1
Std Dev 9.656
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch35, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch35, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch36, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 29.27
Std Dev 3.667
Gaus mean : 30.1270
Gaus Width: 4.1131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch36, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.85
Std Dev 3.737
Gaus mean : 38.0199
Gaus Width: 3.5506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch36, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.12
Std Dev 3.044
Gaus mean : 43.7897
Gaus Width: 3.5976
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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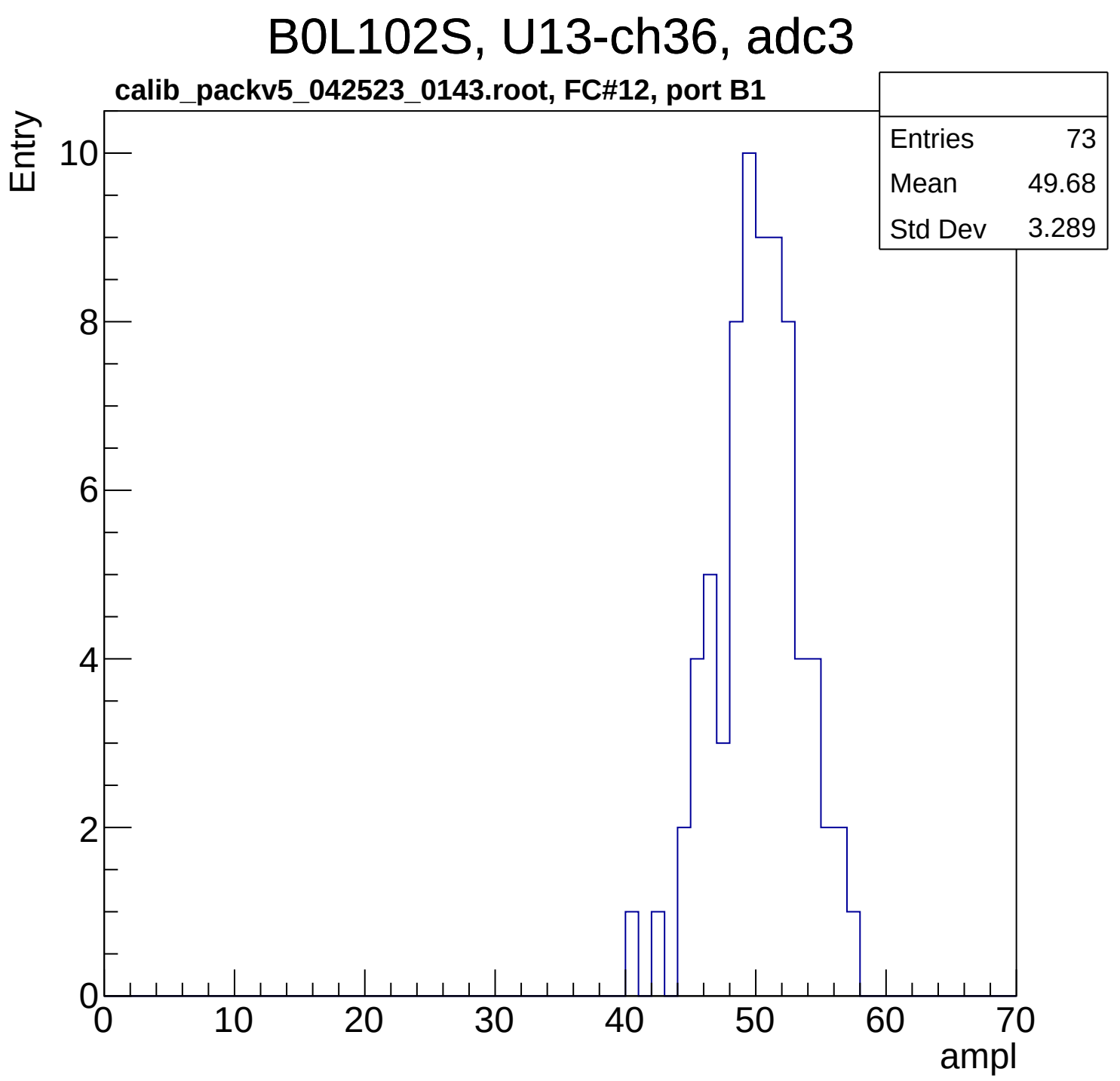
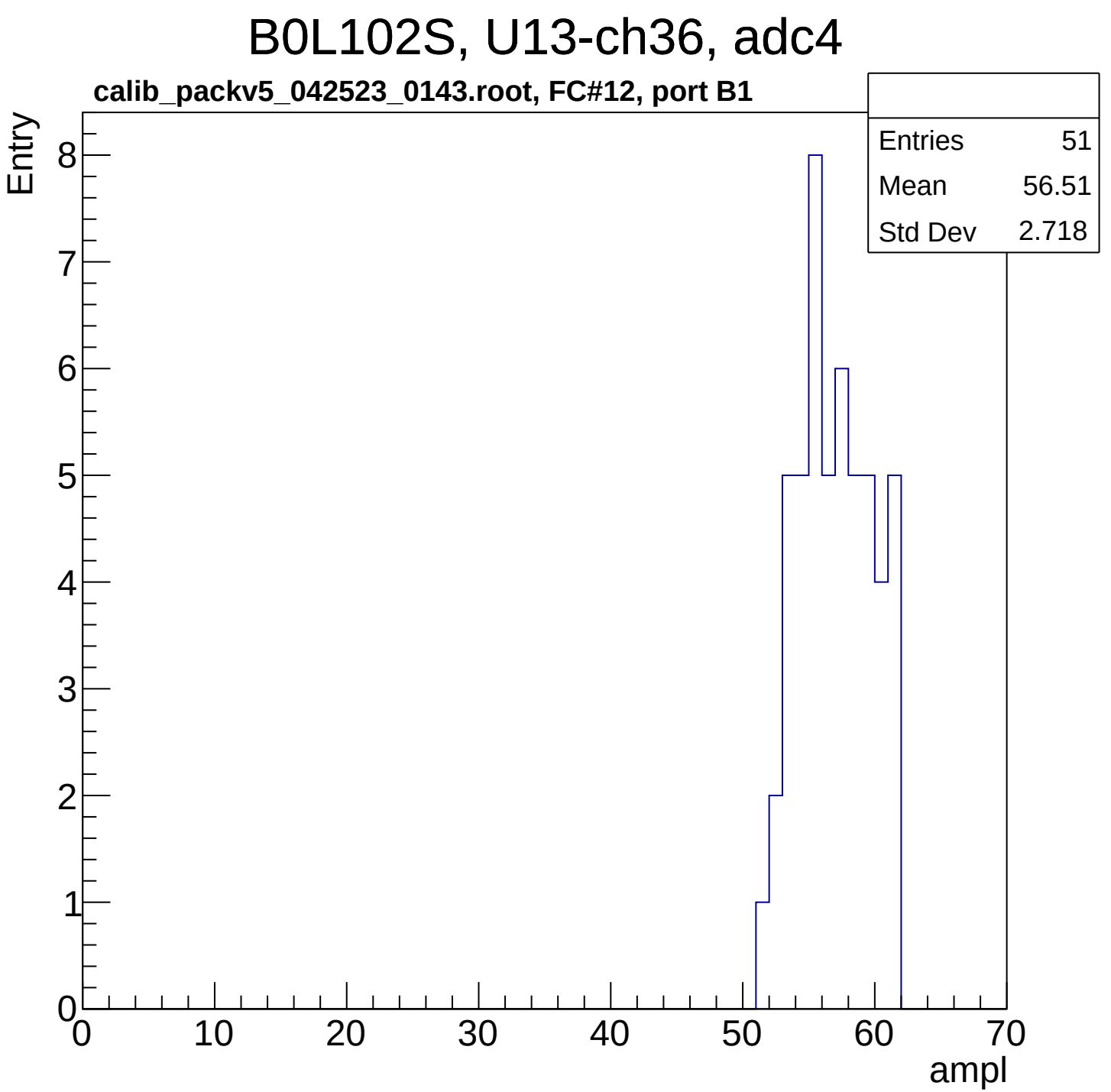
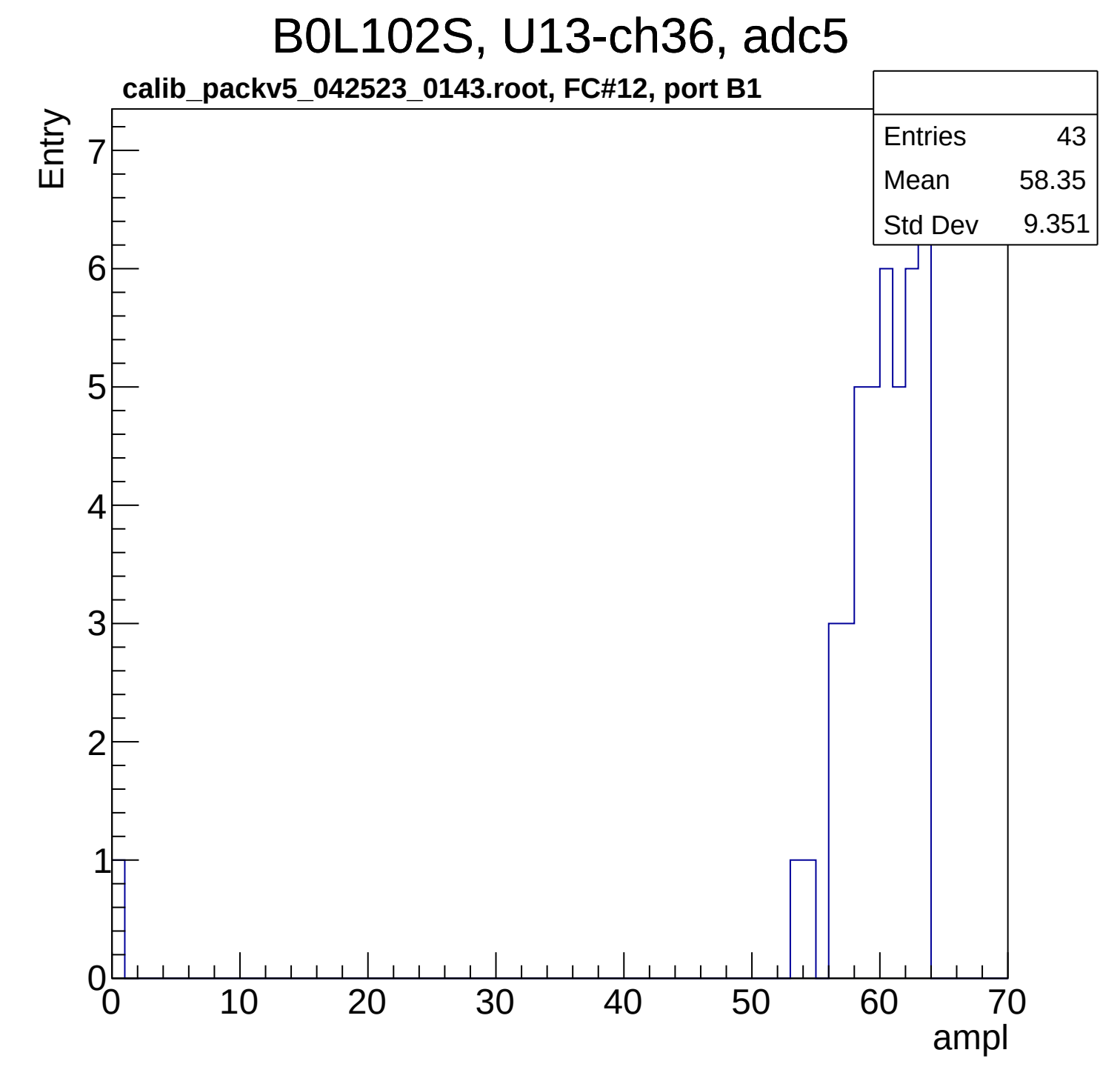
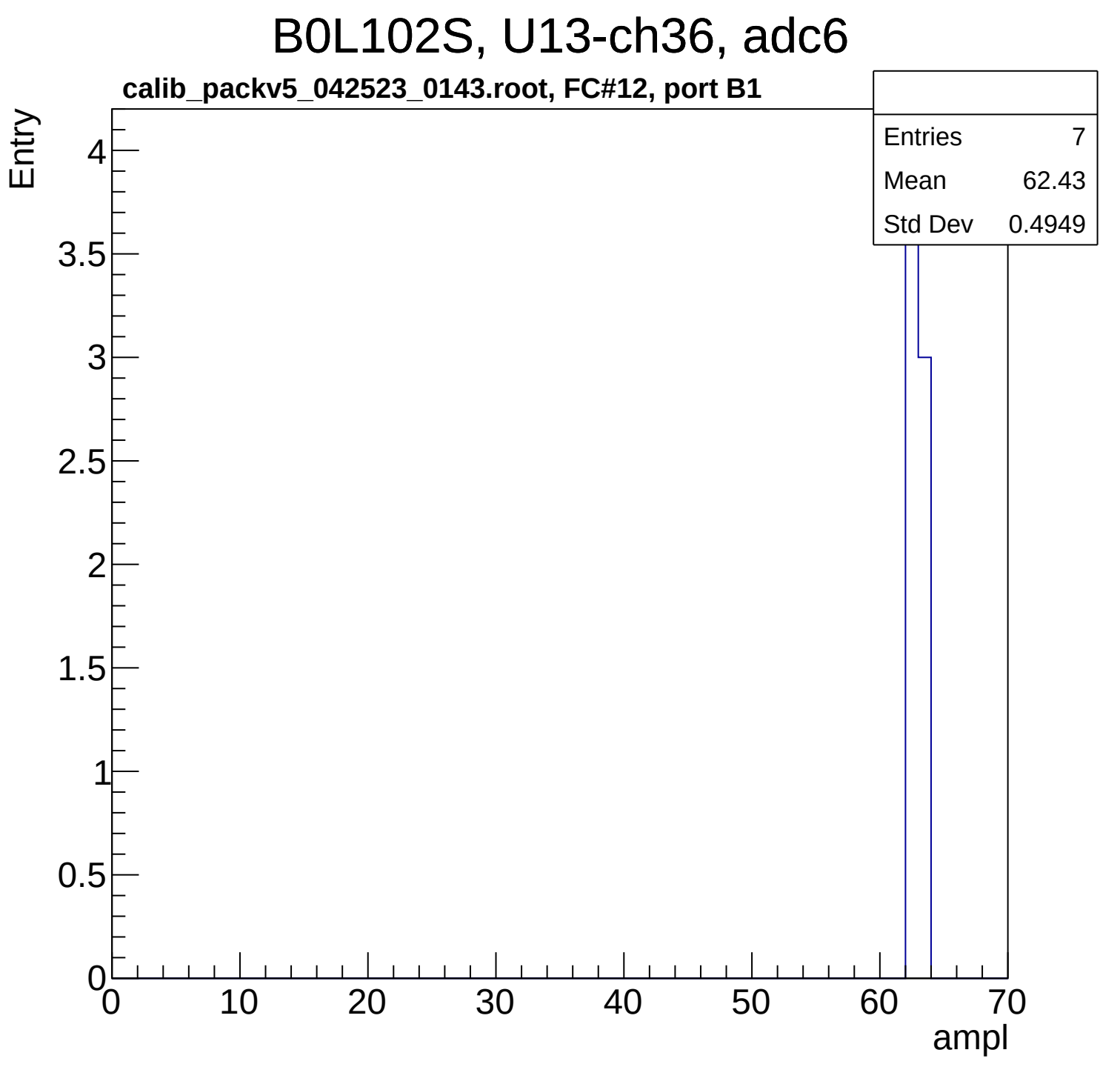
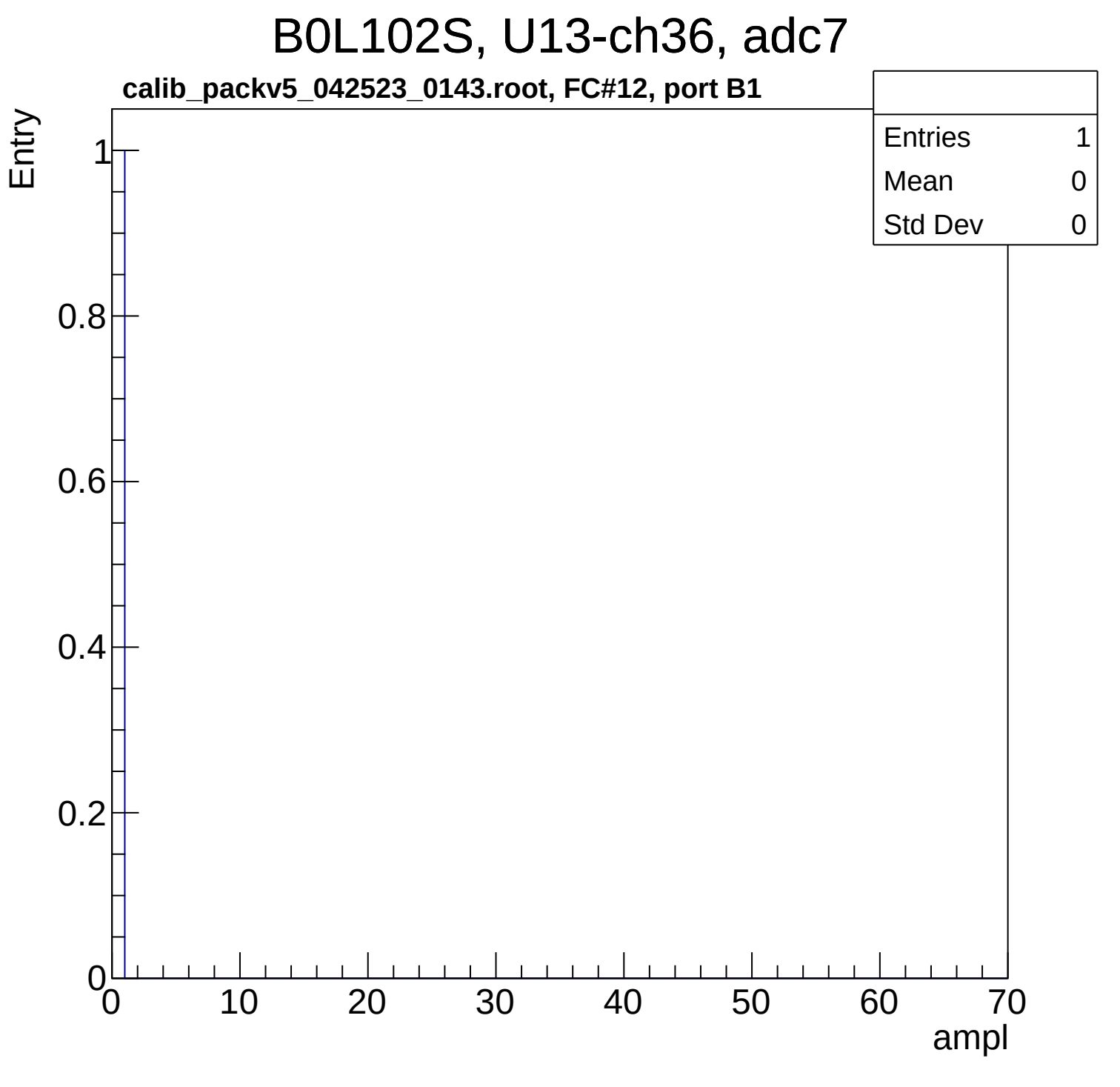
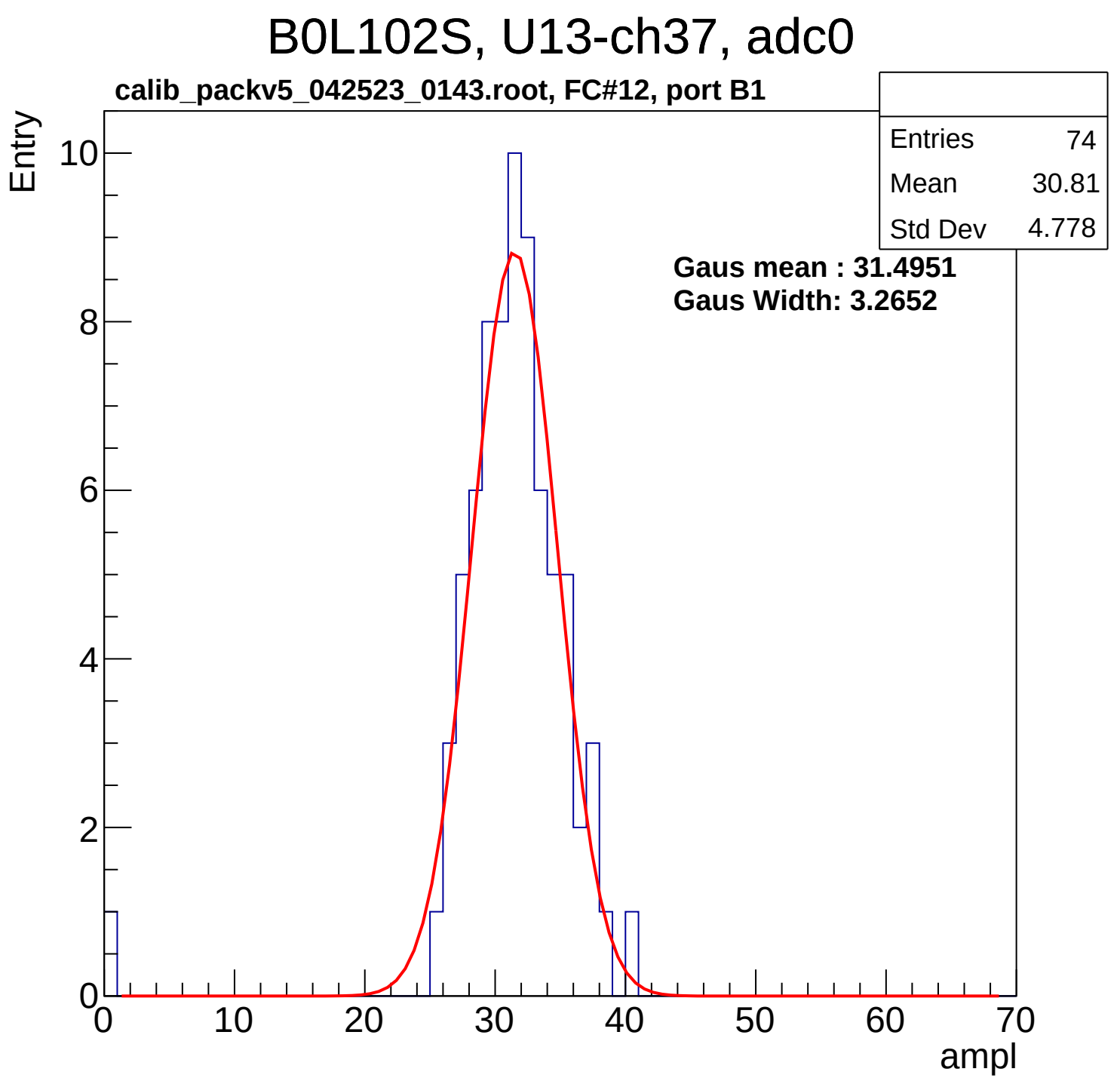

B0L102S, U13-ch36, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 49.68
Std Dev 3.289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

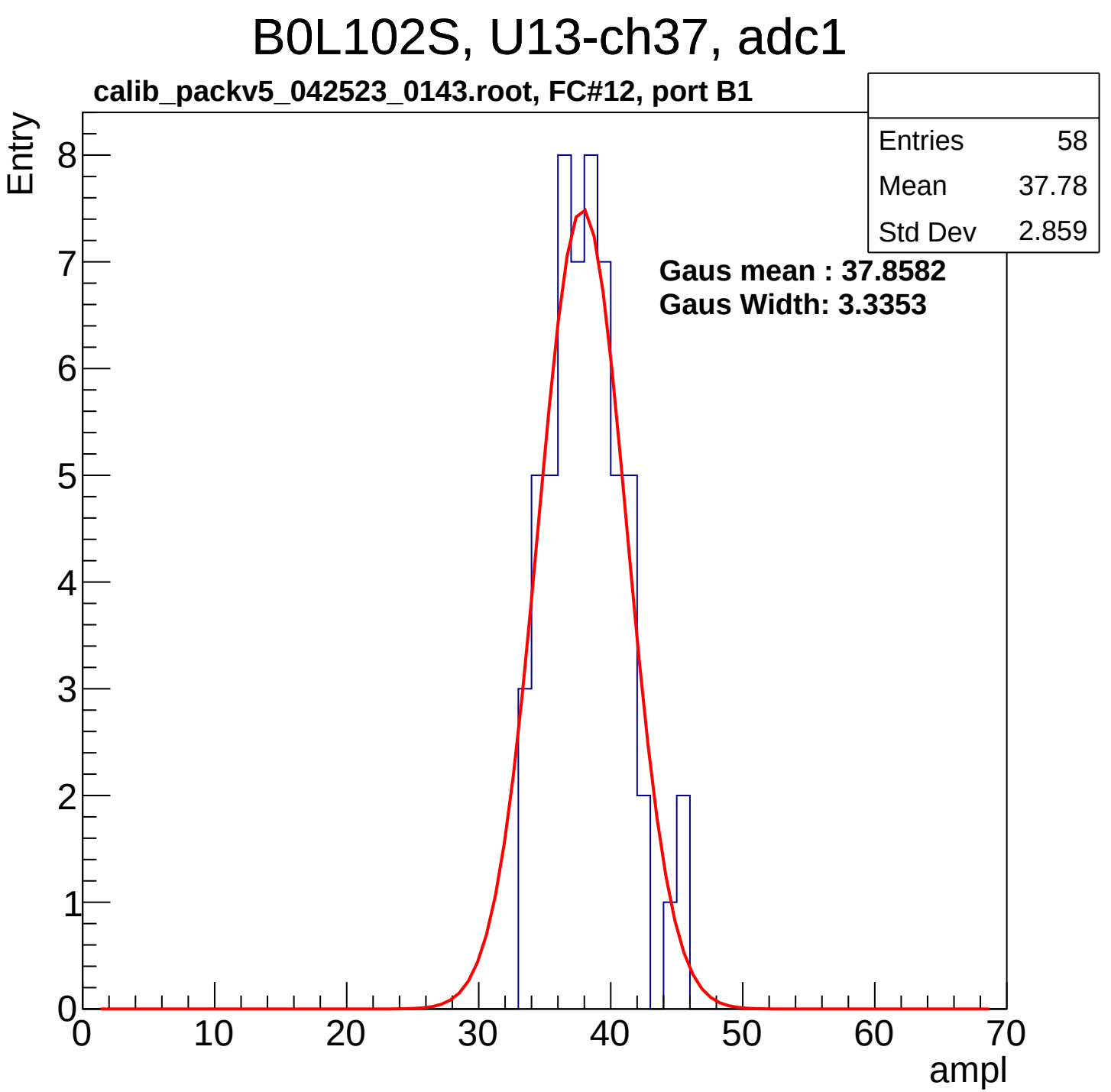

B0L102S, U13-ch36, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 56.51
Std Dev 2.718
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch36, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.35
Std Dev 9.351
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch36, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.43
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch36, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch37, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 30.81
Std Dev 4.778
Gaus mean : 31.4951
Gaus Width: 3.2652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch37, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 37.78
Std Dev 2.859
Gaus mean : 37.8582
Gaus Width: 3.3353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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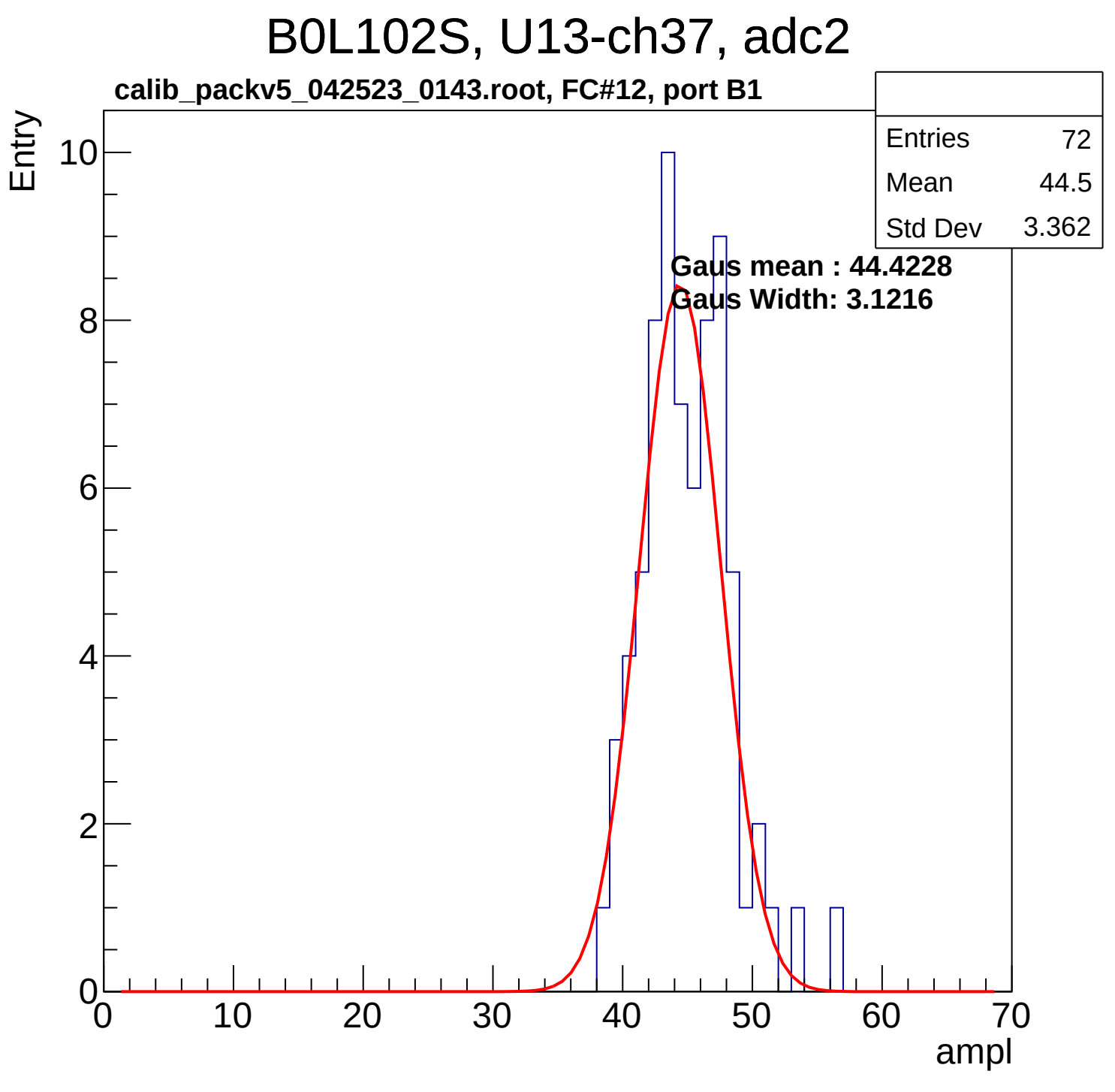
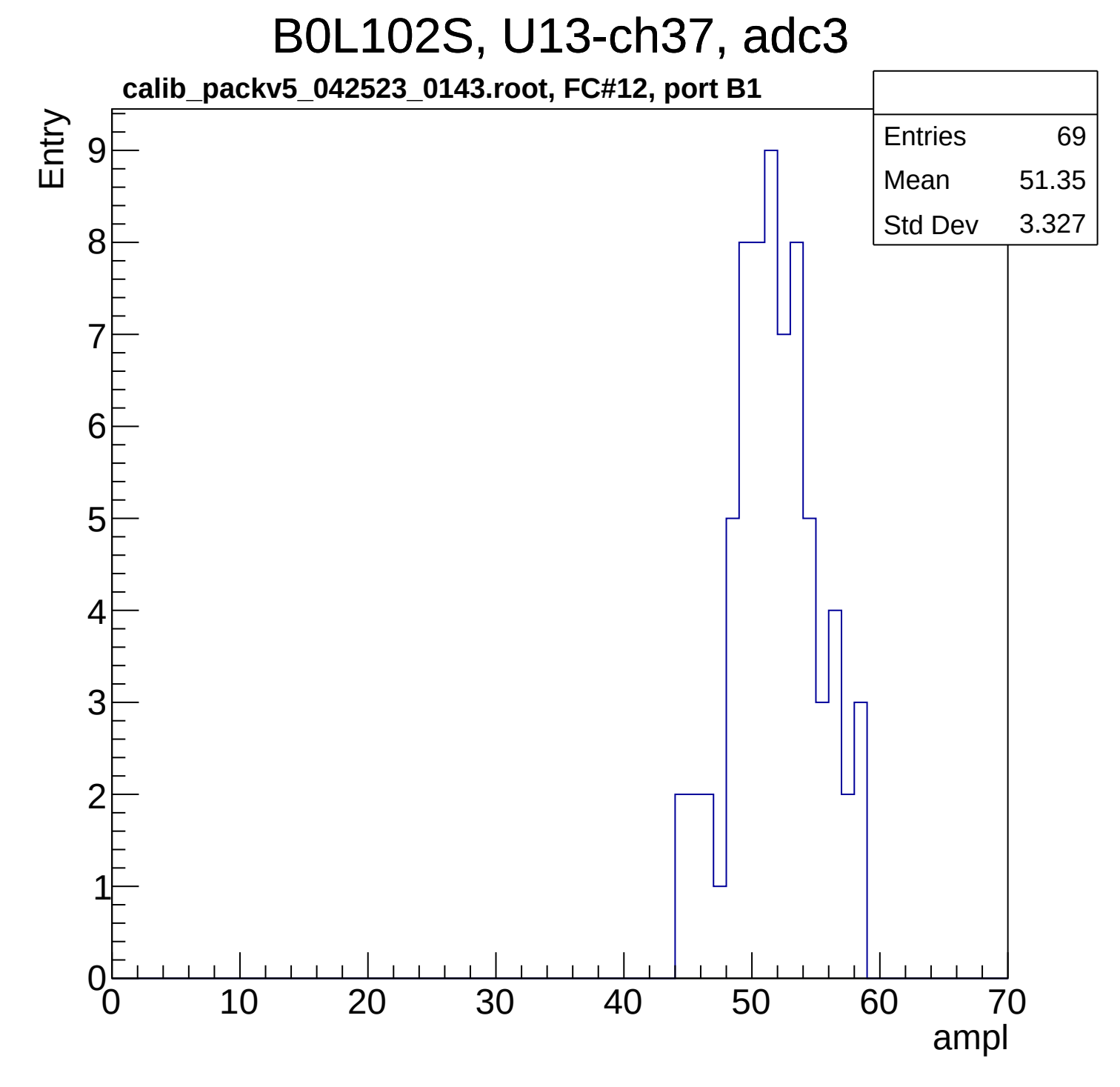
40
50
60
70


B0L102S, U13-ch37, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 44.5
Std Dev 3.362
Gaus mean : 44.4228
Gaus Width: 3.1216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch37, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 51.35
Std Dev 3.327
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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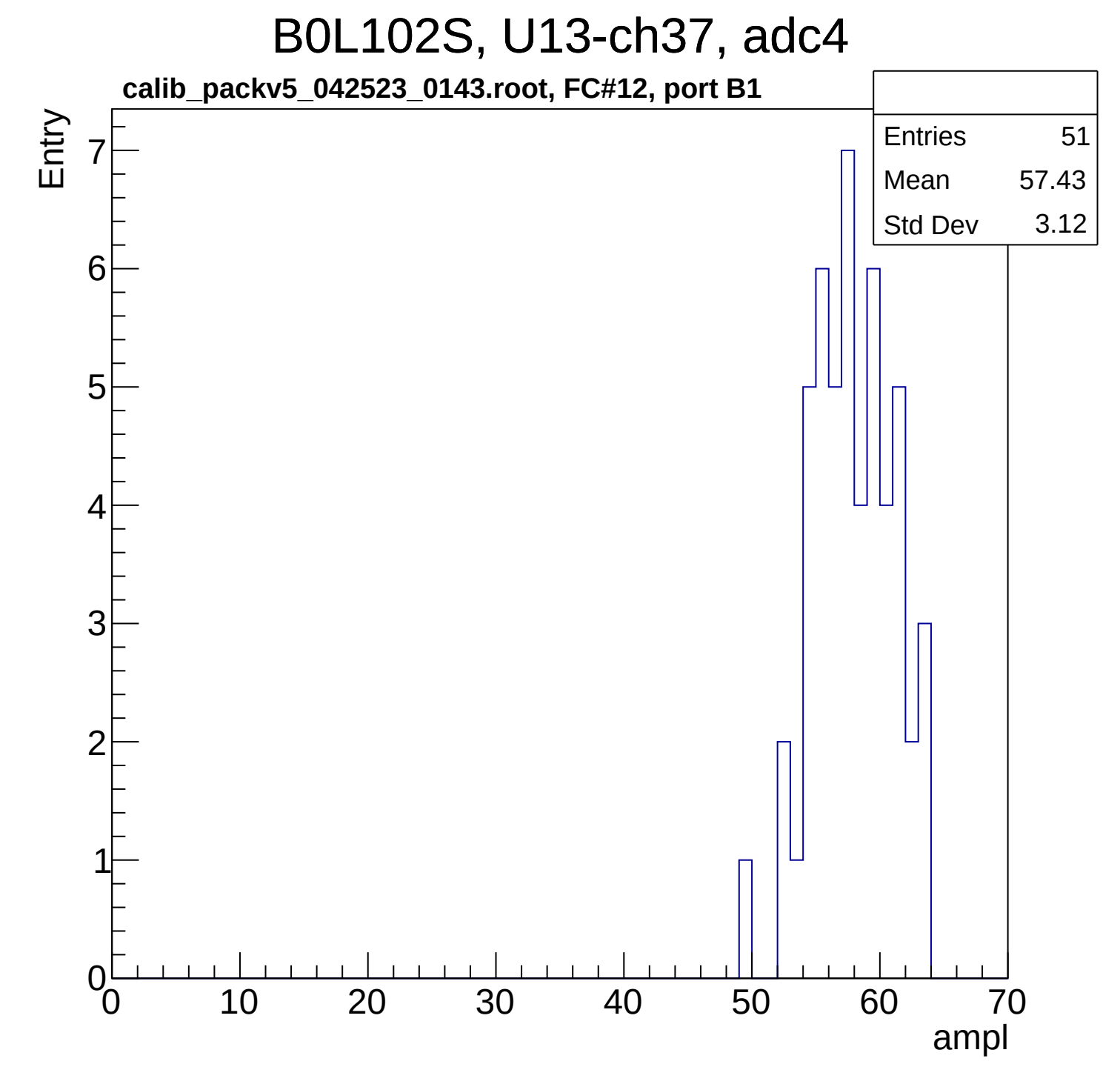
B0L102S, U13-ch37, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 57.43
Std Dev 3.12
Entry
ampl
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1
2
3
4
5
6
7
0
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40
50
60
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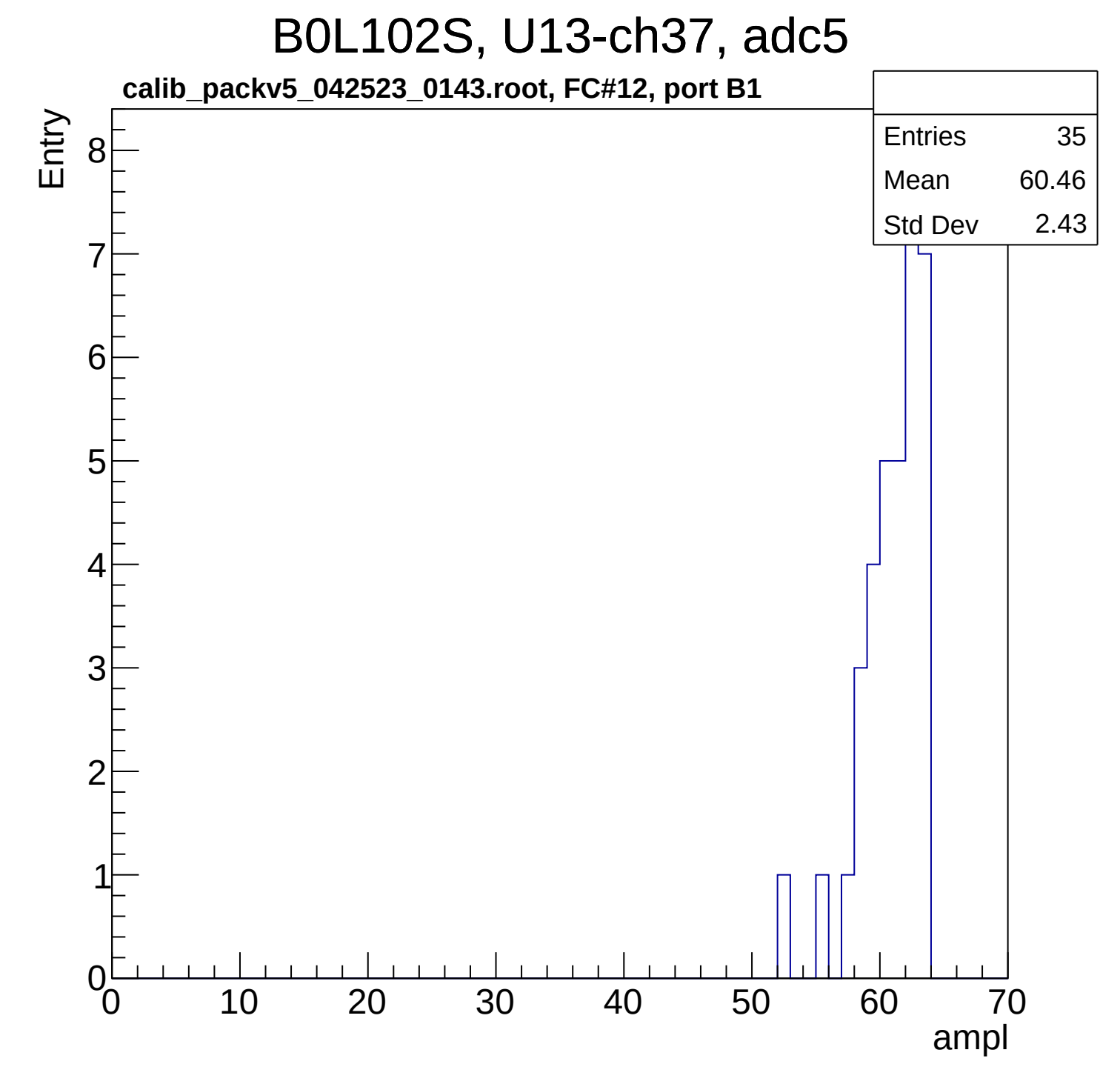
B0L102S, U13-ch37, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 60.46
Std Dev 2.43
Entry
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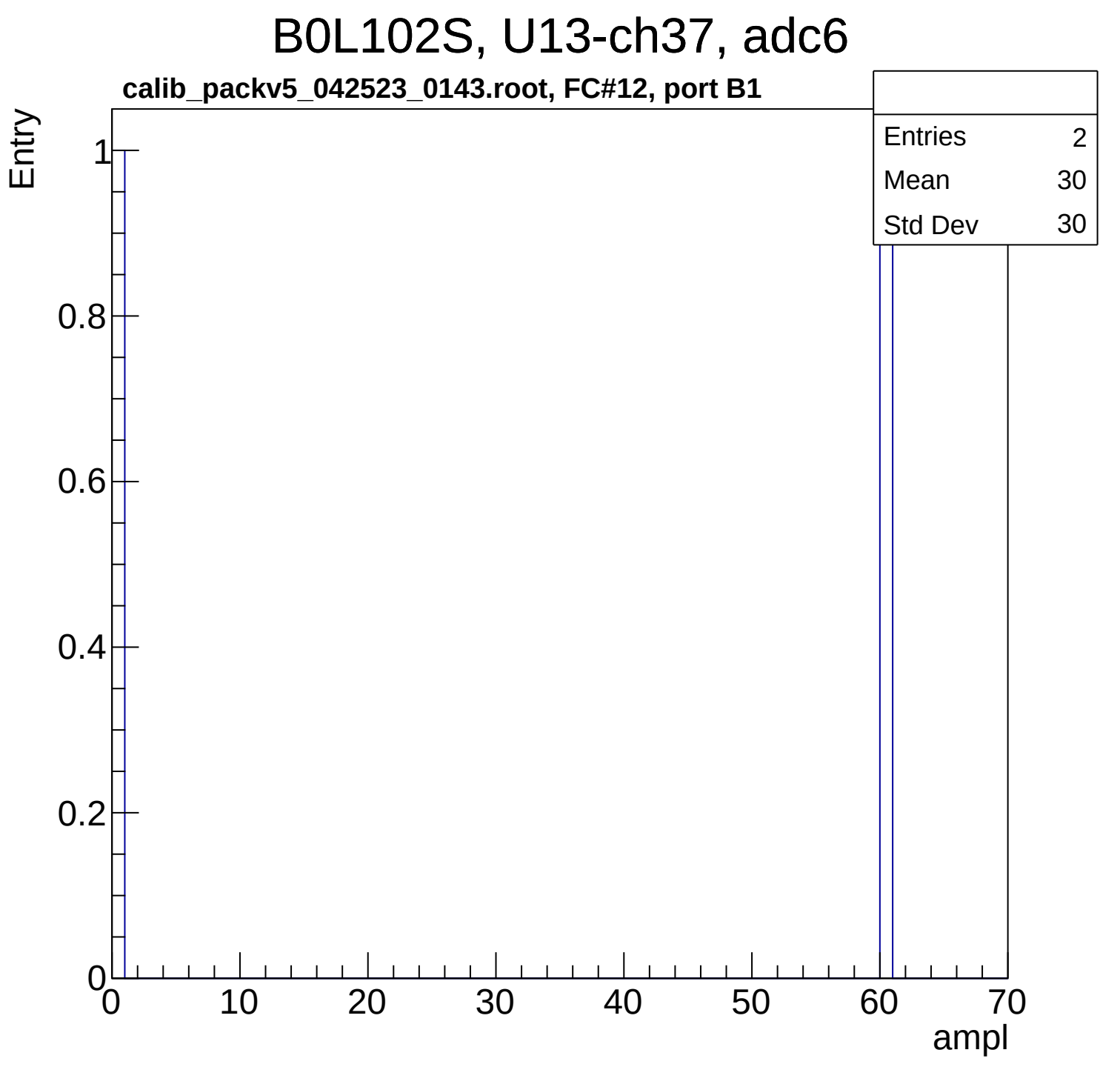
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch37, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30
Std Dev 30
Entry
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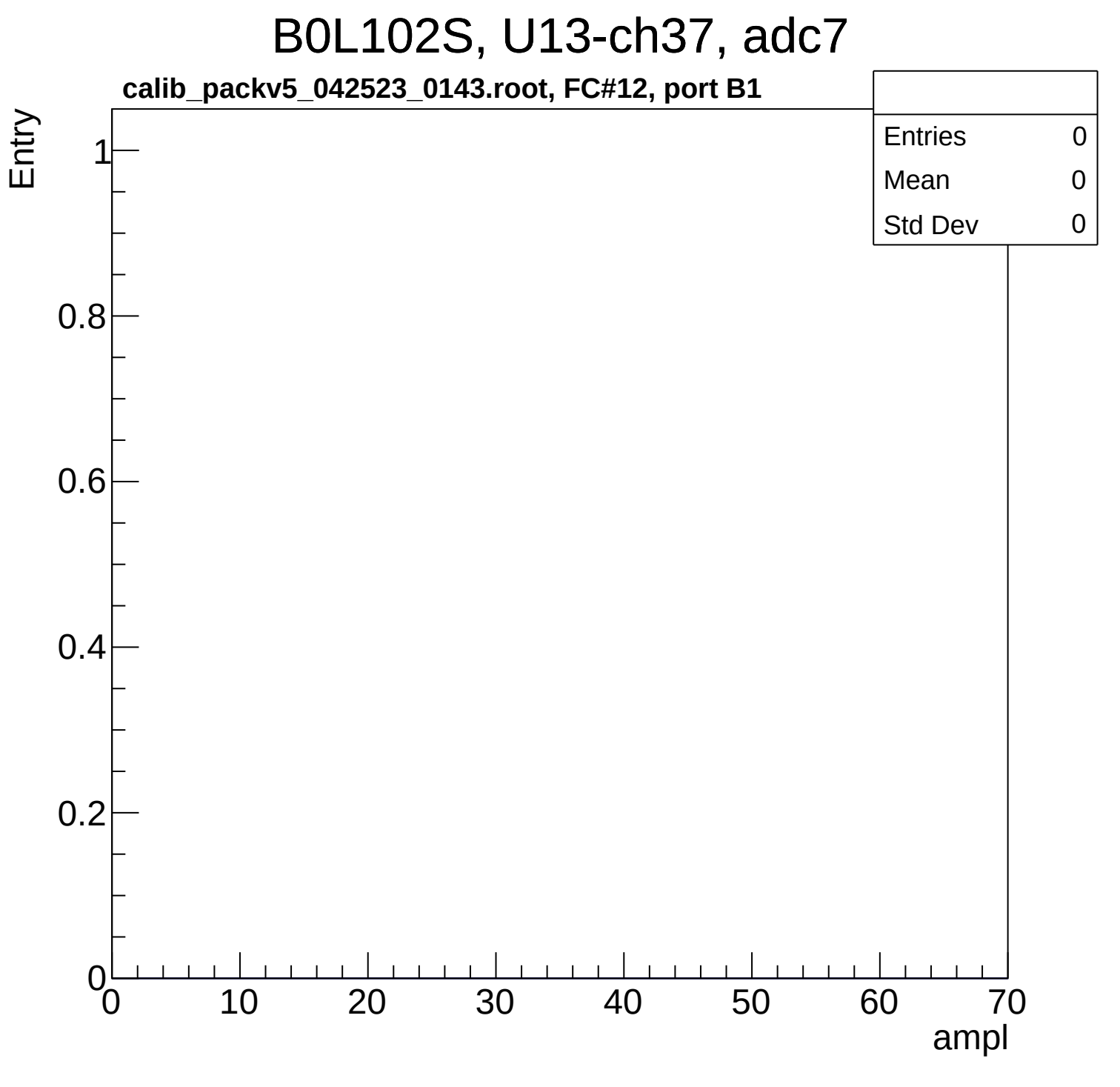
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch37, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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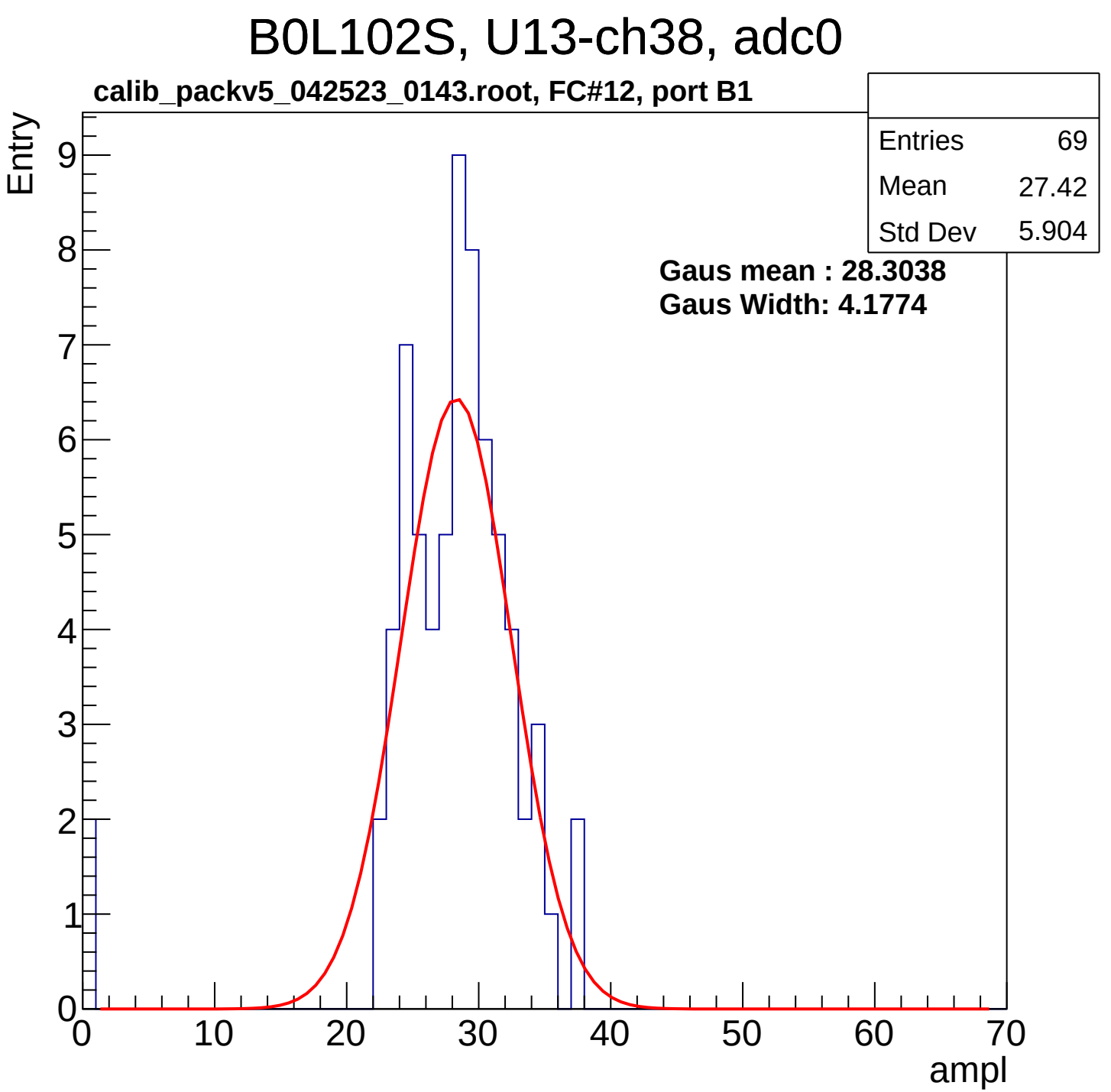
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch38, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 27.42
Std Dev 5.904
Gaus mean : 28.3038
Gaus Width: 4.1774
Entry
ampl
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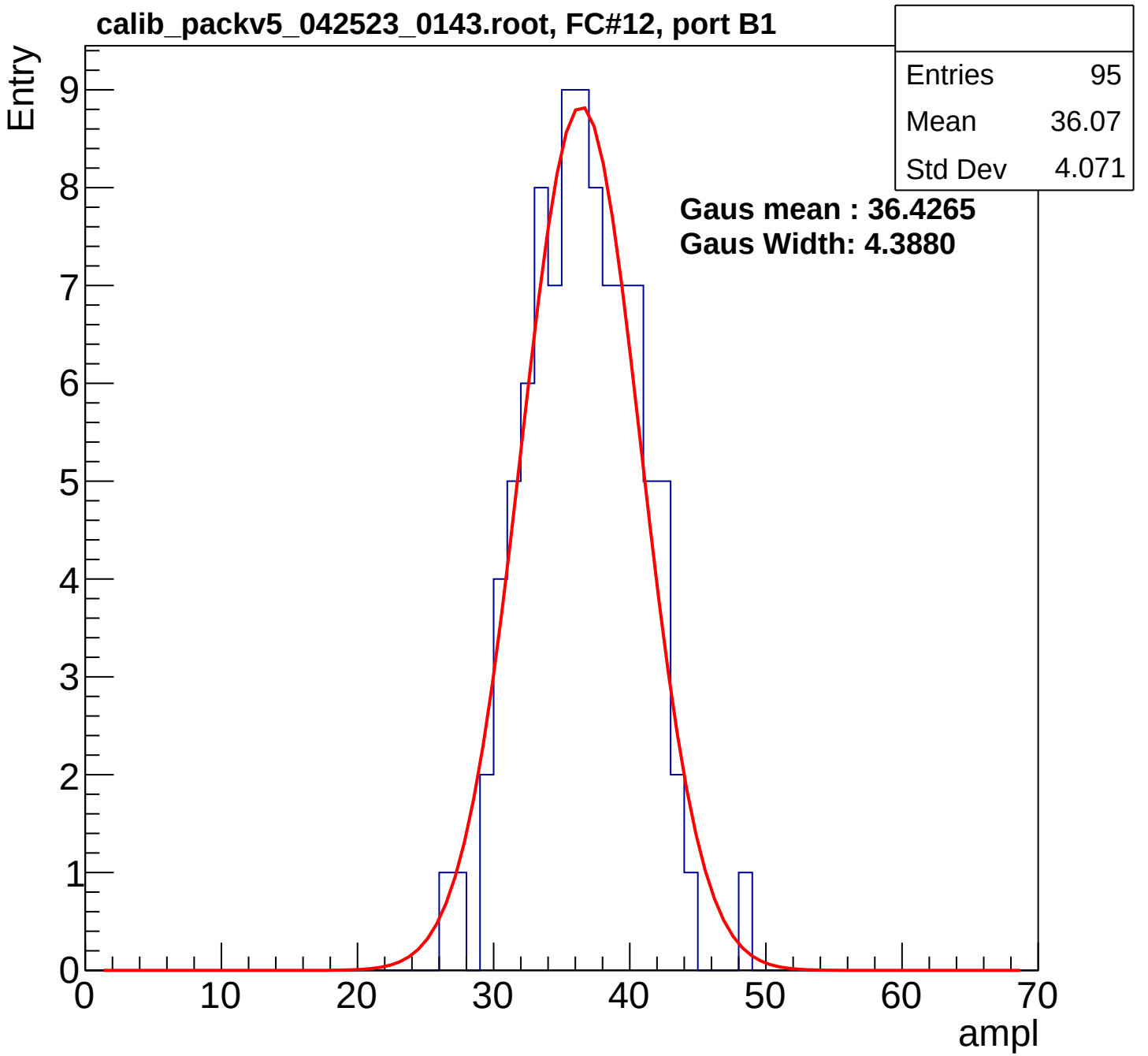
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch38, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 95
Mean 36.07
Std Dev 4.071
Gaus mean : 36.4265
Gaus Width: 4.3880
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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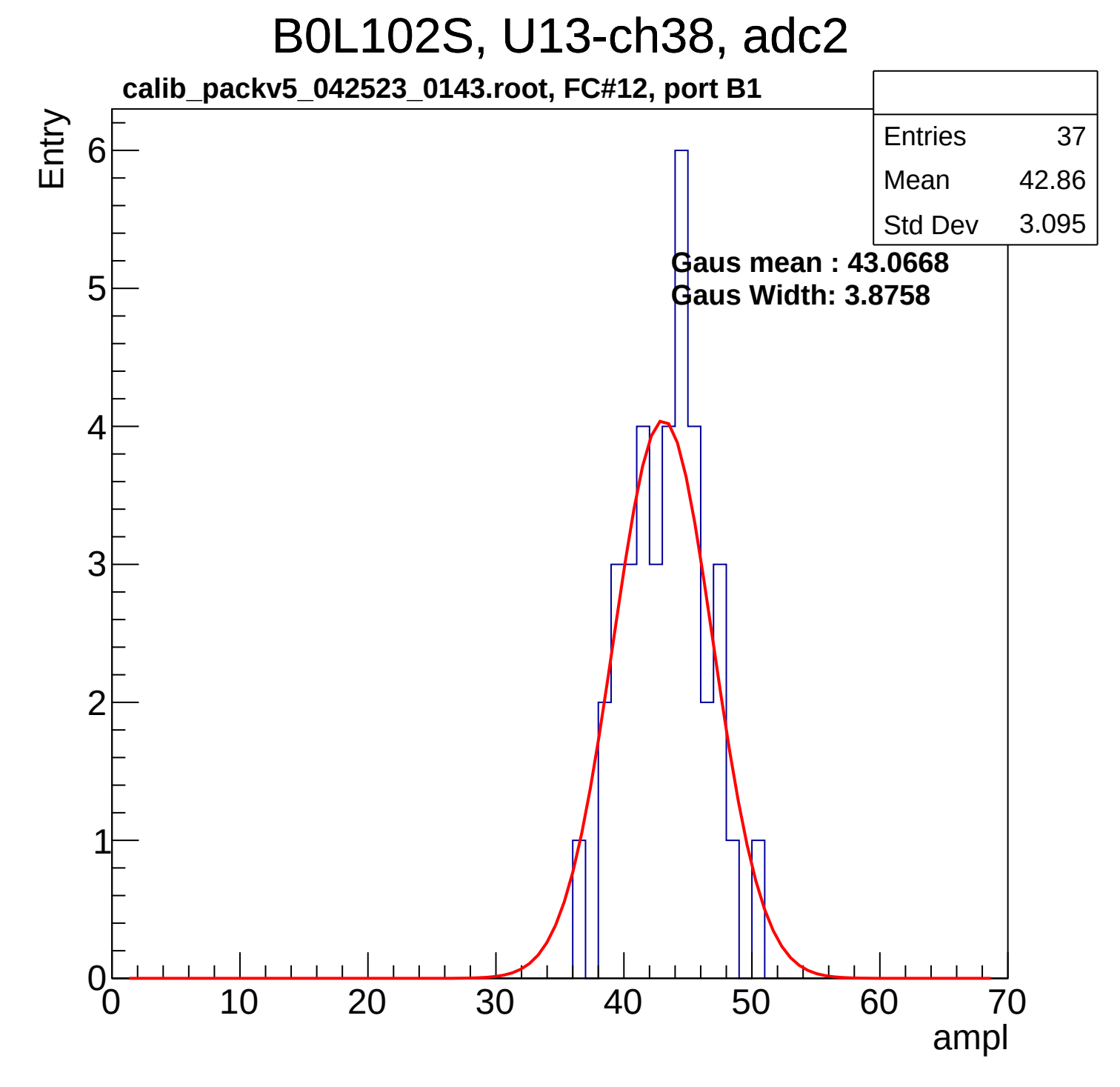
B0L102S, U13-ch38, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 42.86
Std Dev 3.095
Gaus mean : 43.0668
Gaus Width: 3.8758
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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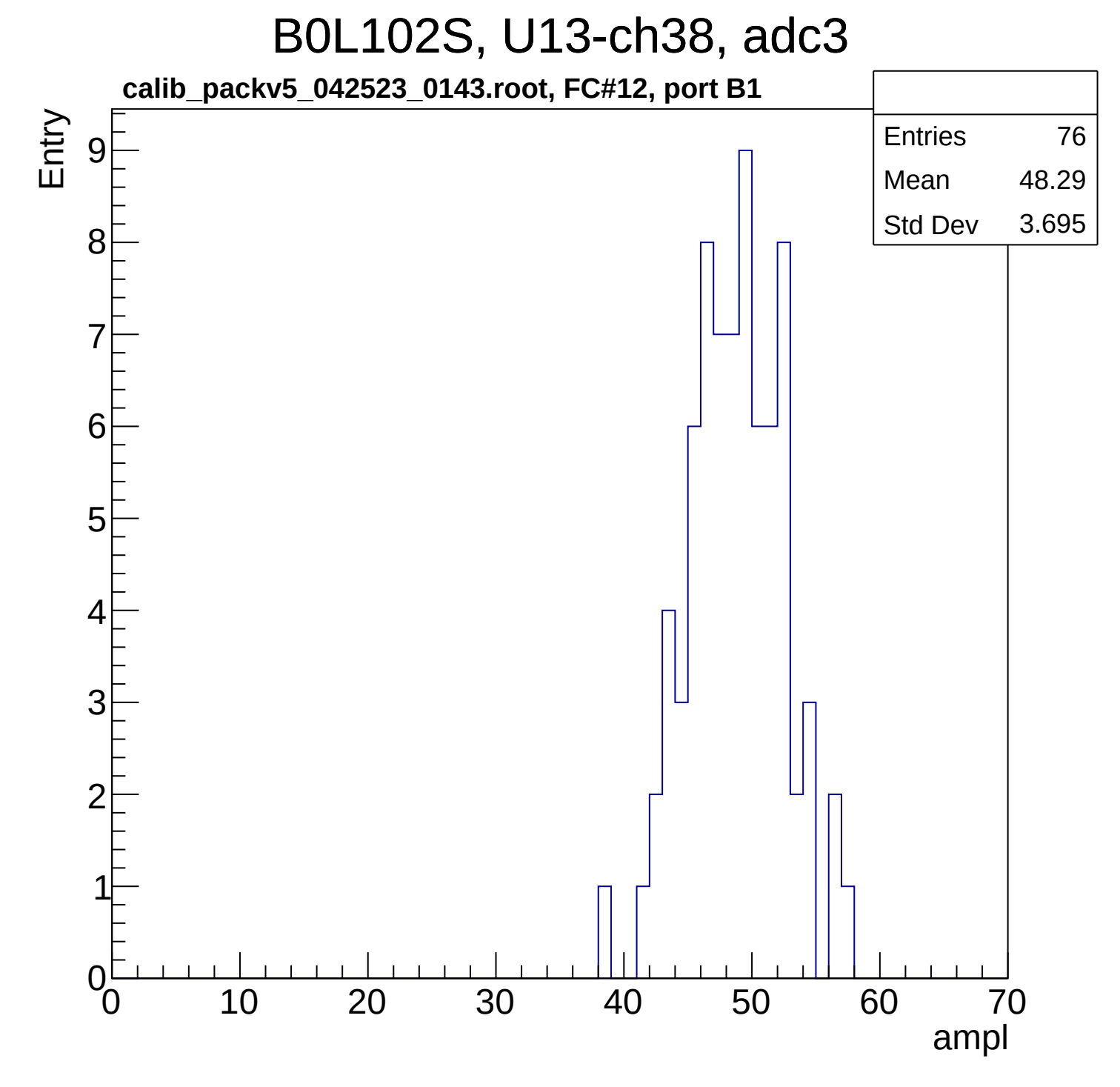
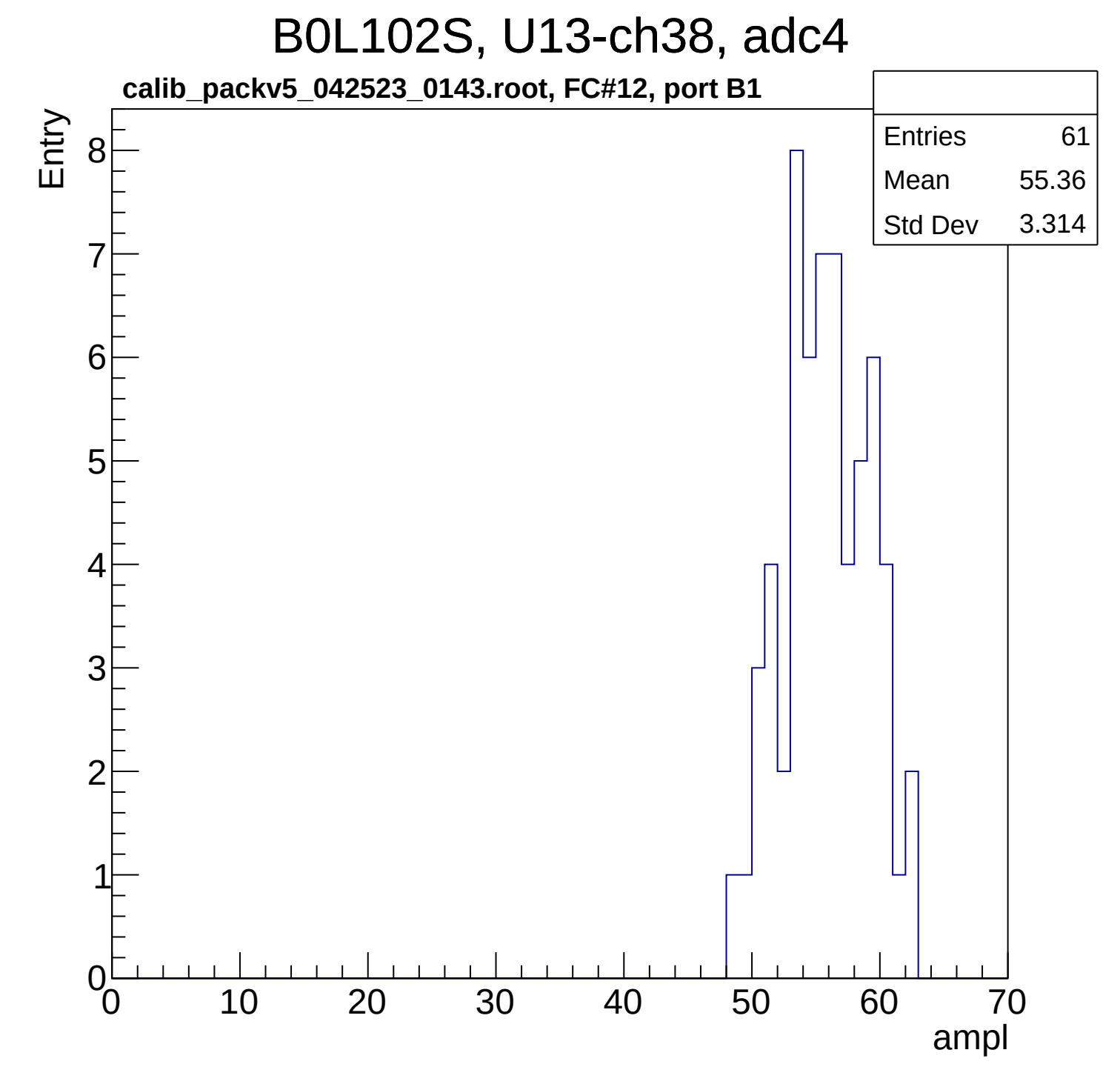

B0L102S, U13-ch38, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.29
Std Dev 3.695
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch38, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.36
Std Dev 3.314
Entry
ampl
0
1
2
3
4
5
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7
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0
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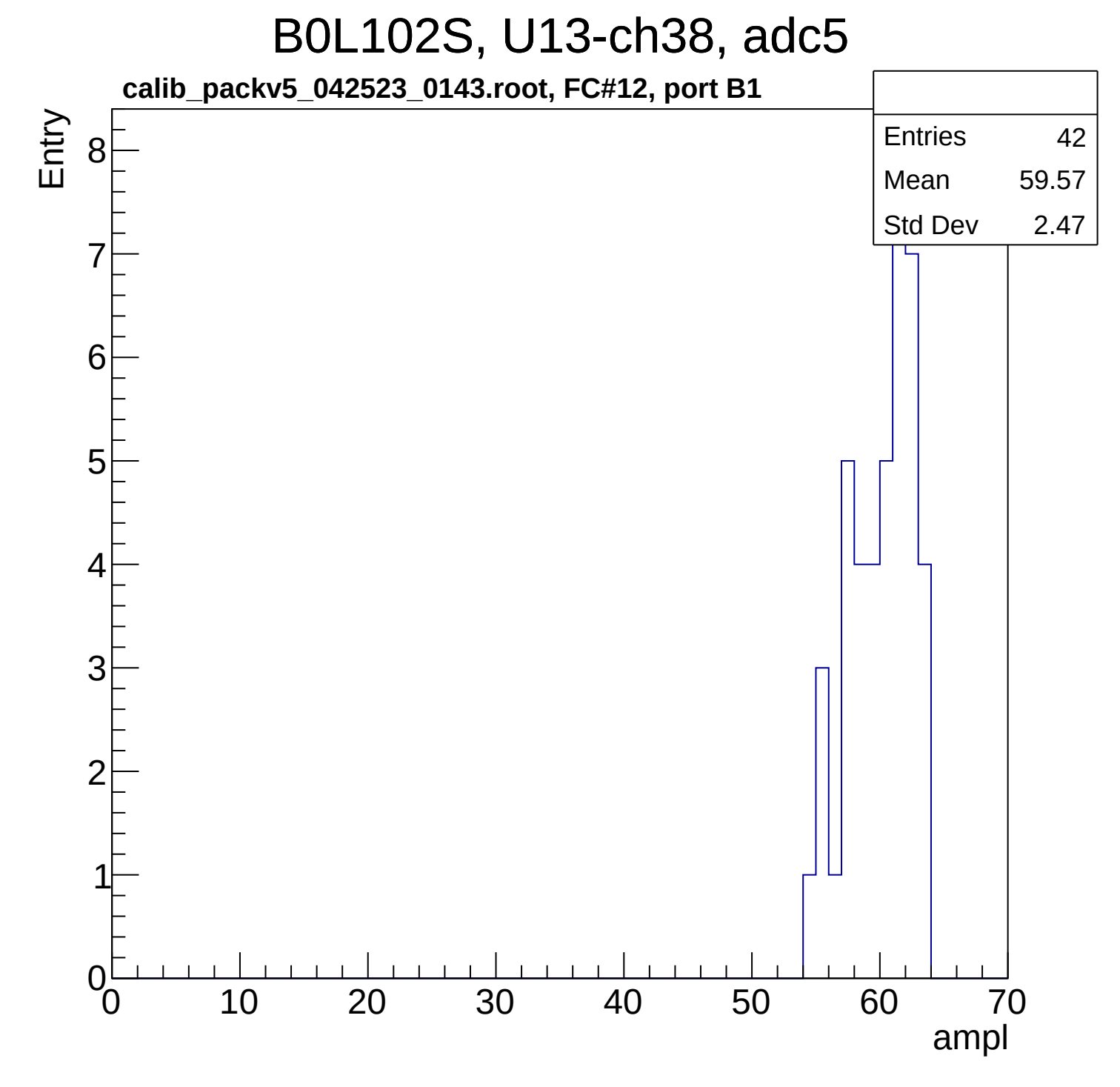
B0L102S, U13-ch38, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 59.57
Std Dev 2.47
Entry
ampl
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1
2
3
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0
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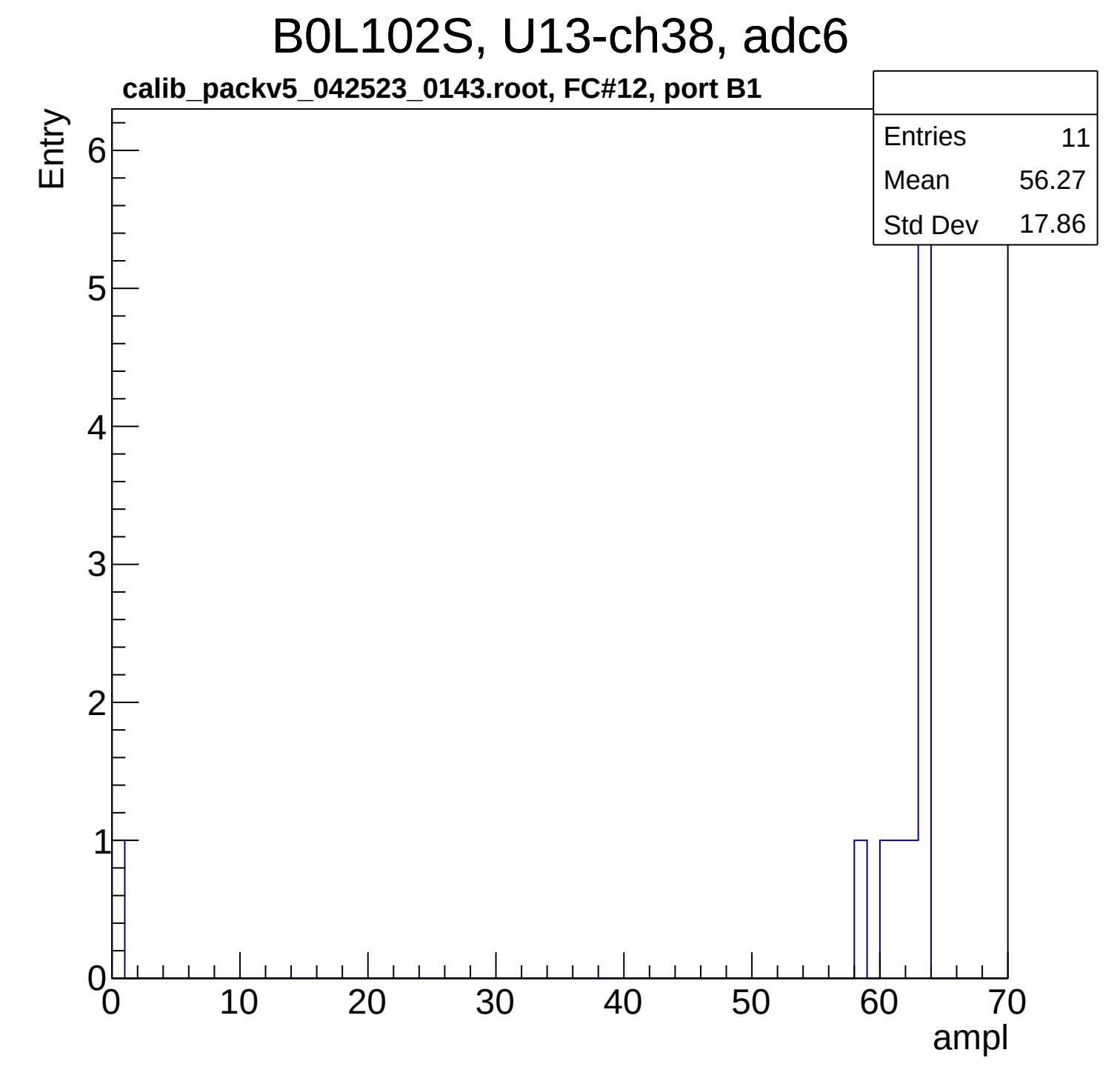
B0L102S, U13-ch38, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 56.27
Std Dev 17.86
Entry
0
1
2
3
4
5
6
0
10
20
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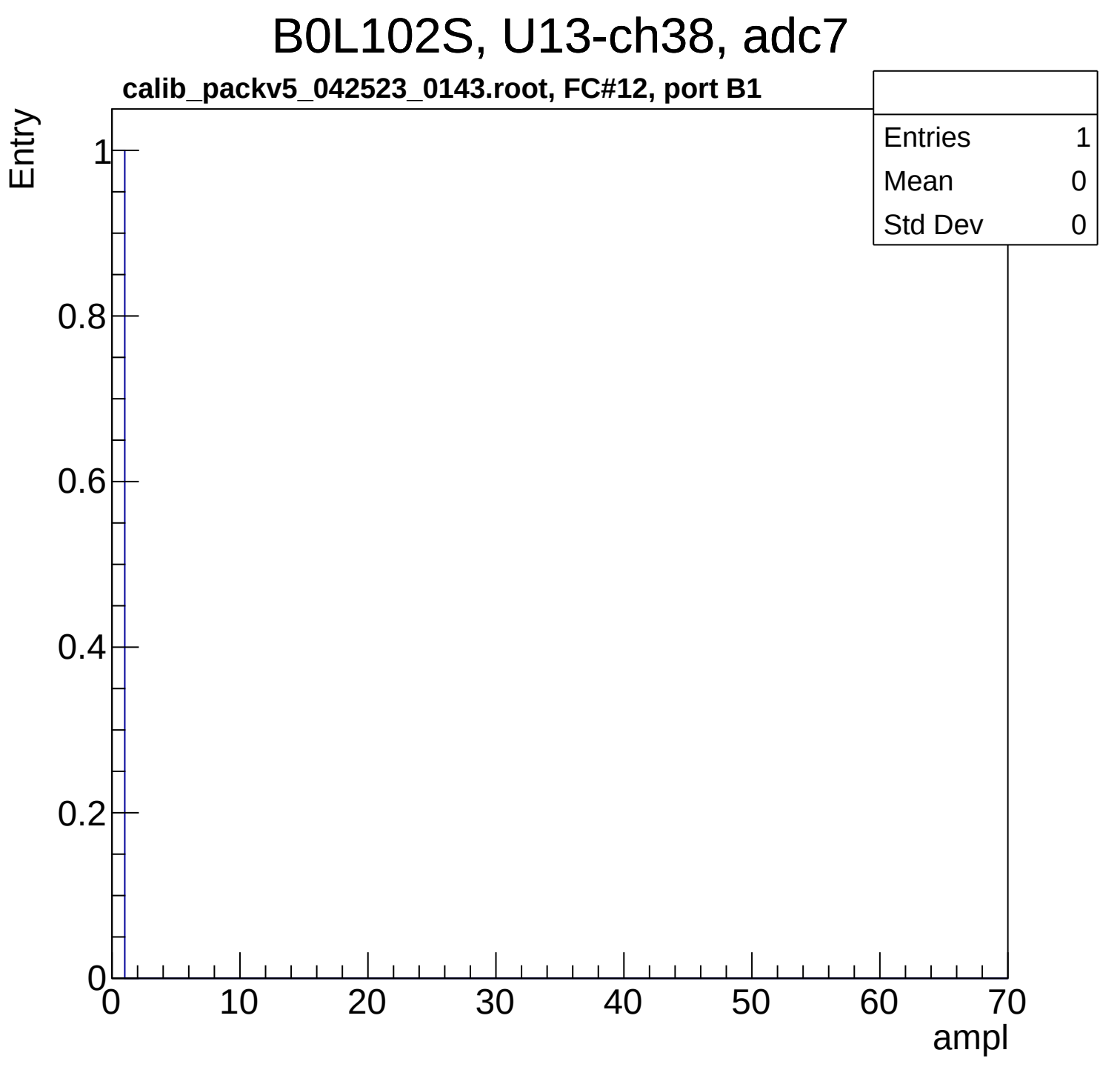
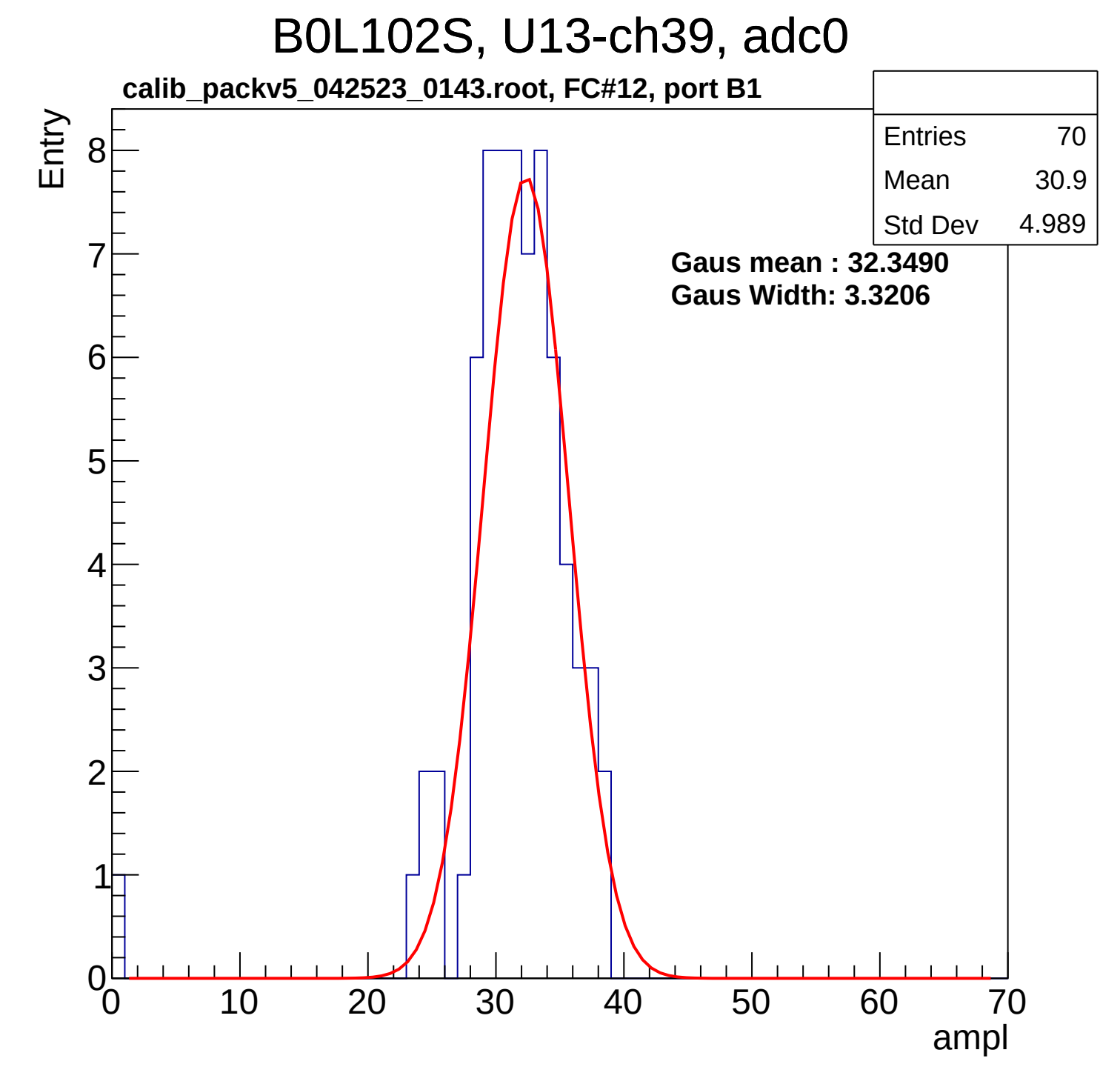
40
50
60
70
ampl


B0L102S, U13-ch38, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch39, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 30.9
Std Dev 4.989
Gaus mean : 32.3490
Gaus Width: 3.3206
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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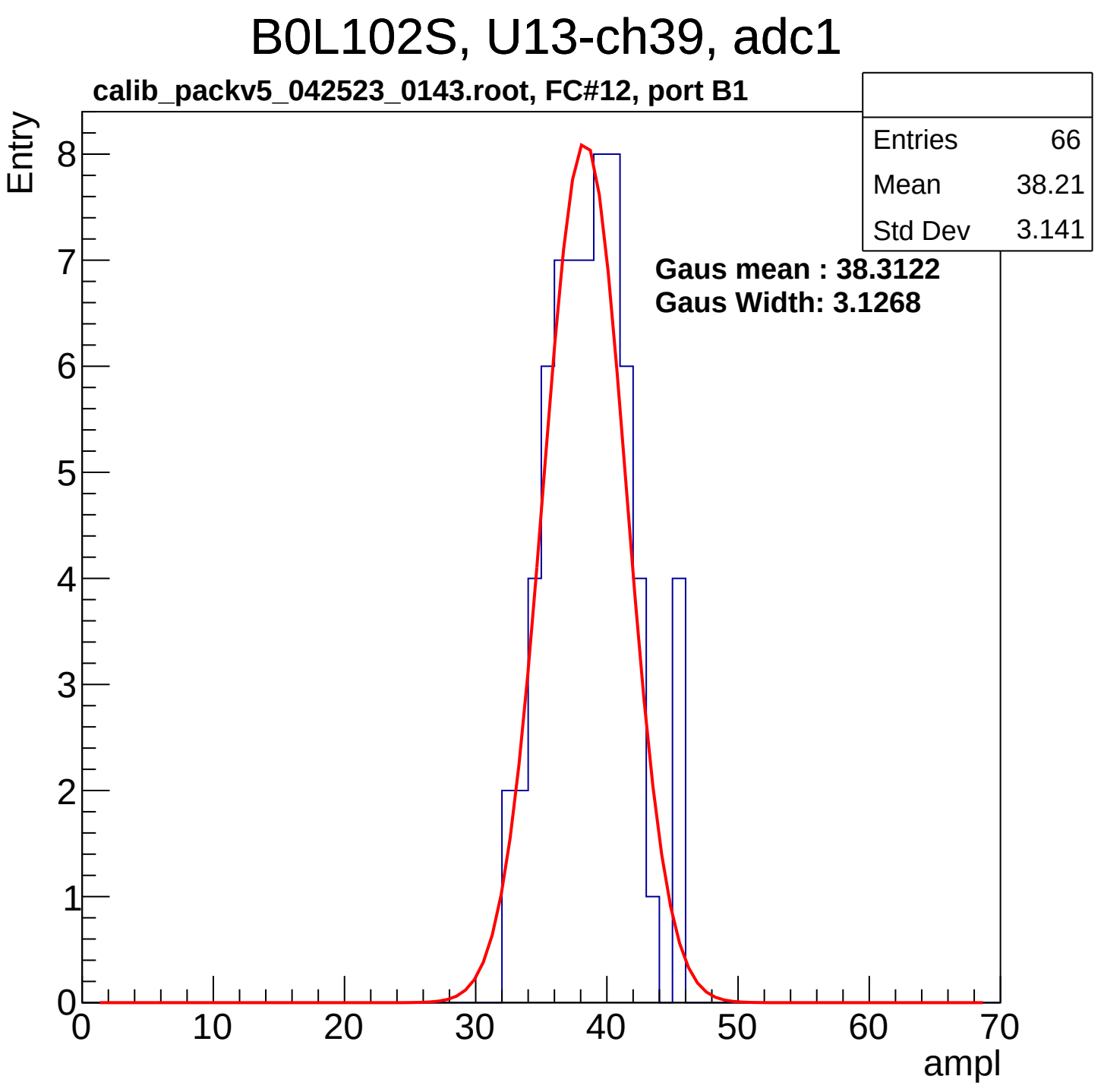
B0L102S, U13-ch39, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 38.21
Std Dev 3.141
Gaus mean : 38.3122
Gaus Width: 3.1268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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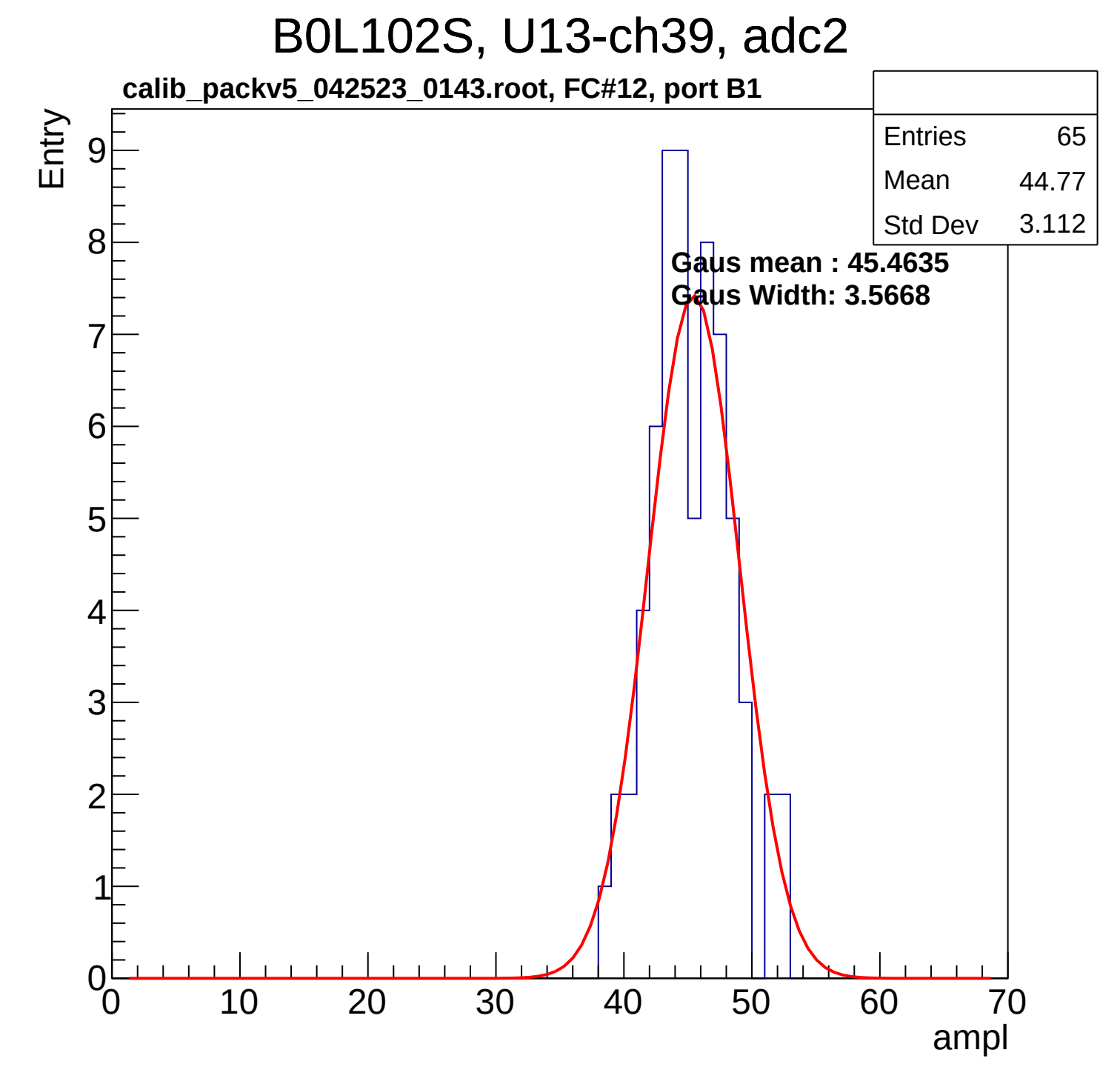
B0L102S, U13-ch39, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 44.77
Std Dev 3.112
Gaus mean : 45.4635
Gaus Width: 3.5668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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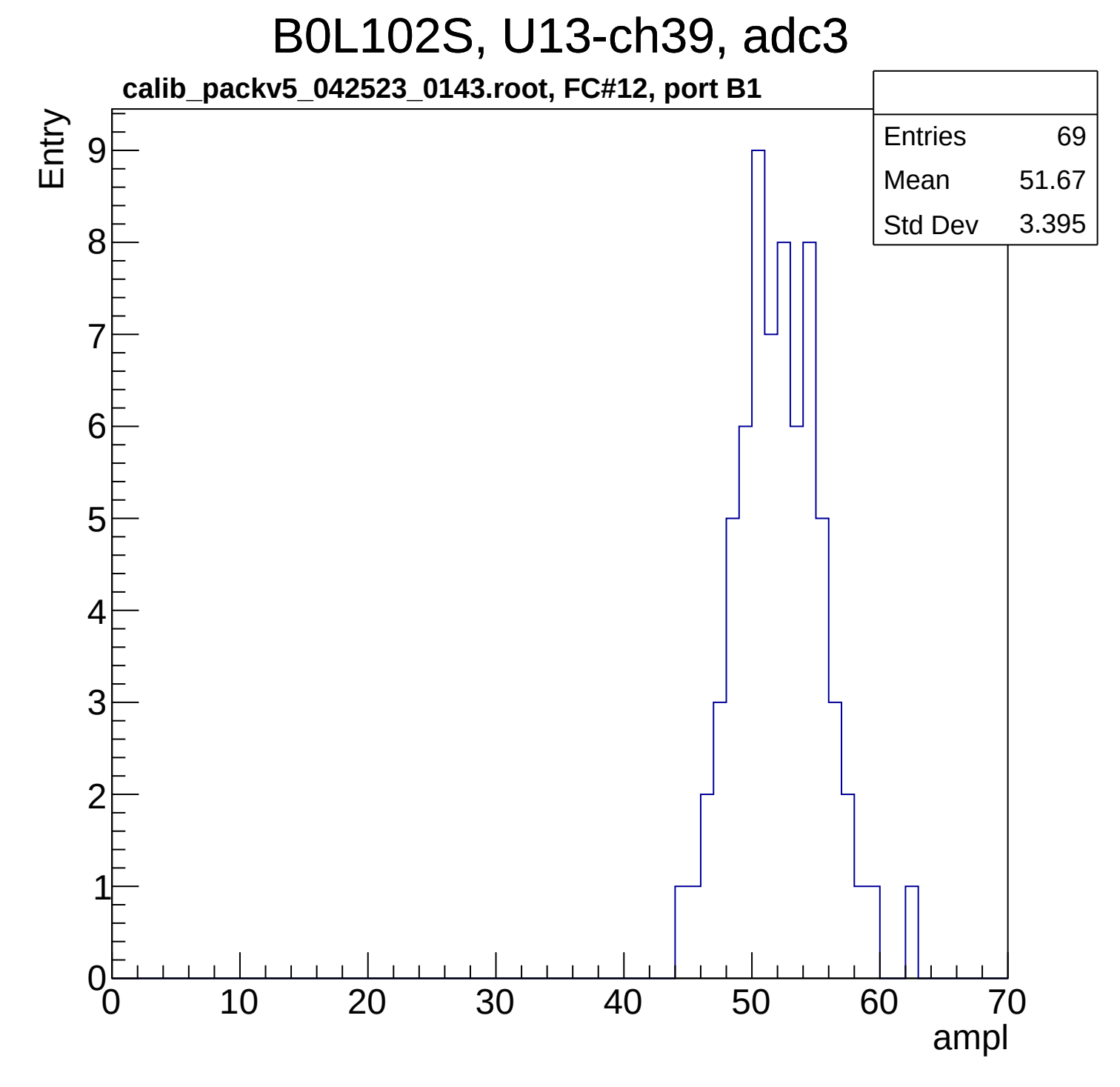
B0L102S, U13-ch39, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 51.67
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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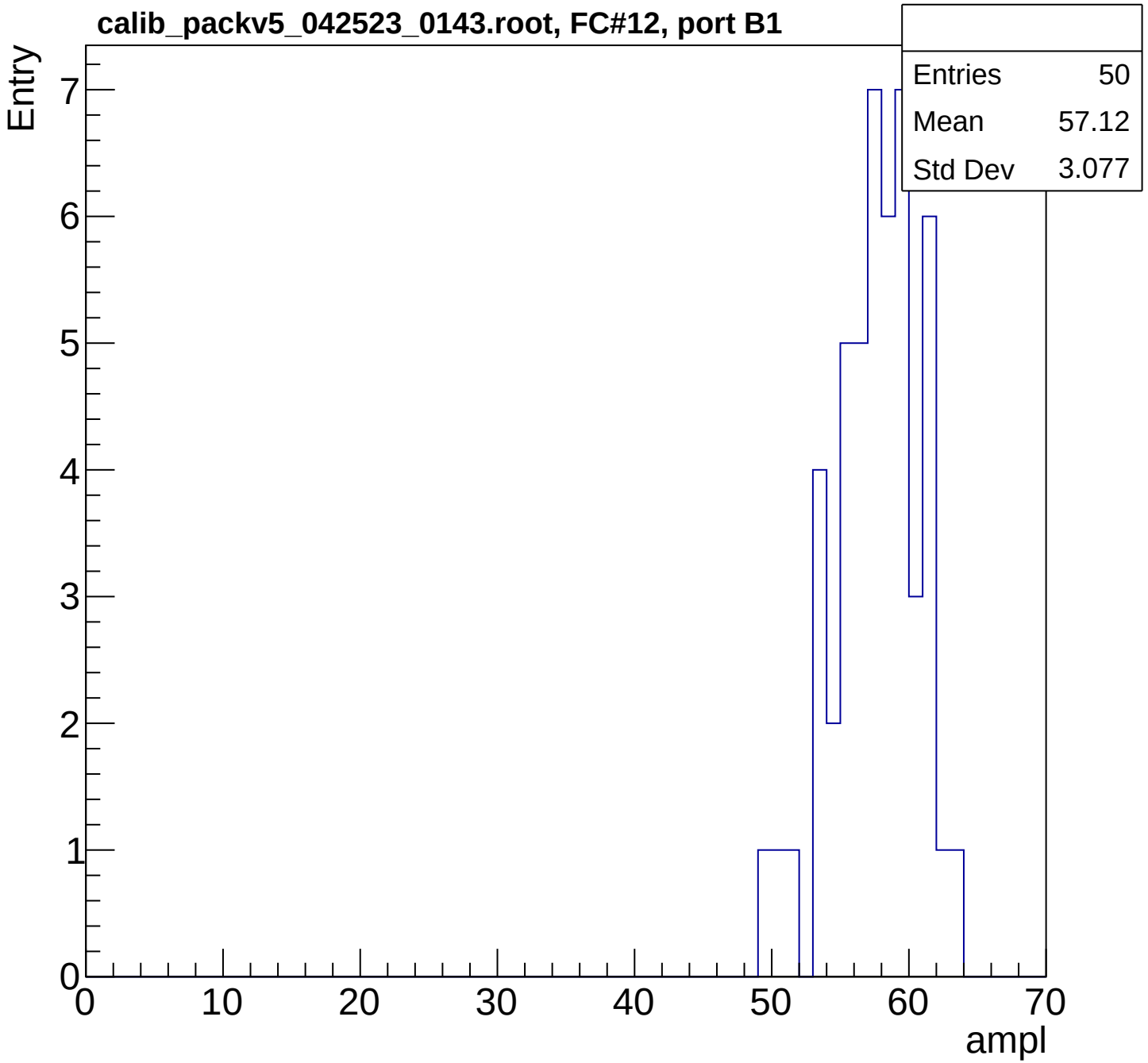
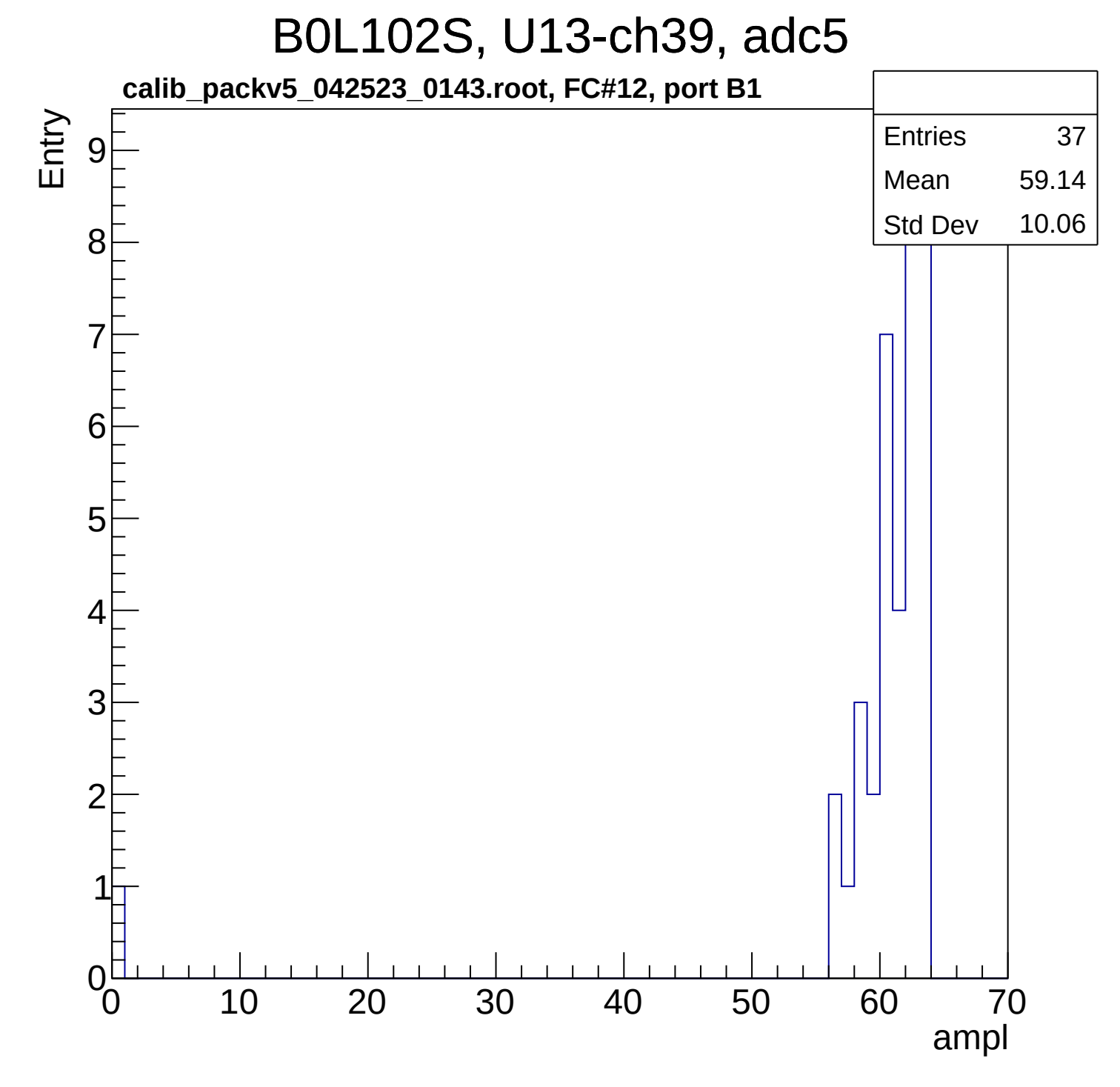

B0L102S, U13-ch39, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 57.12
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch39, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 59.14
Std Dev 10.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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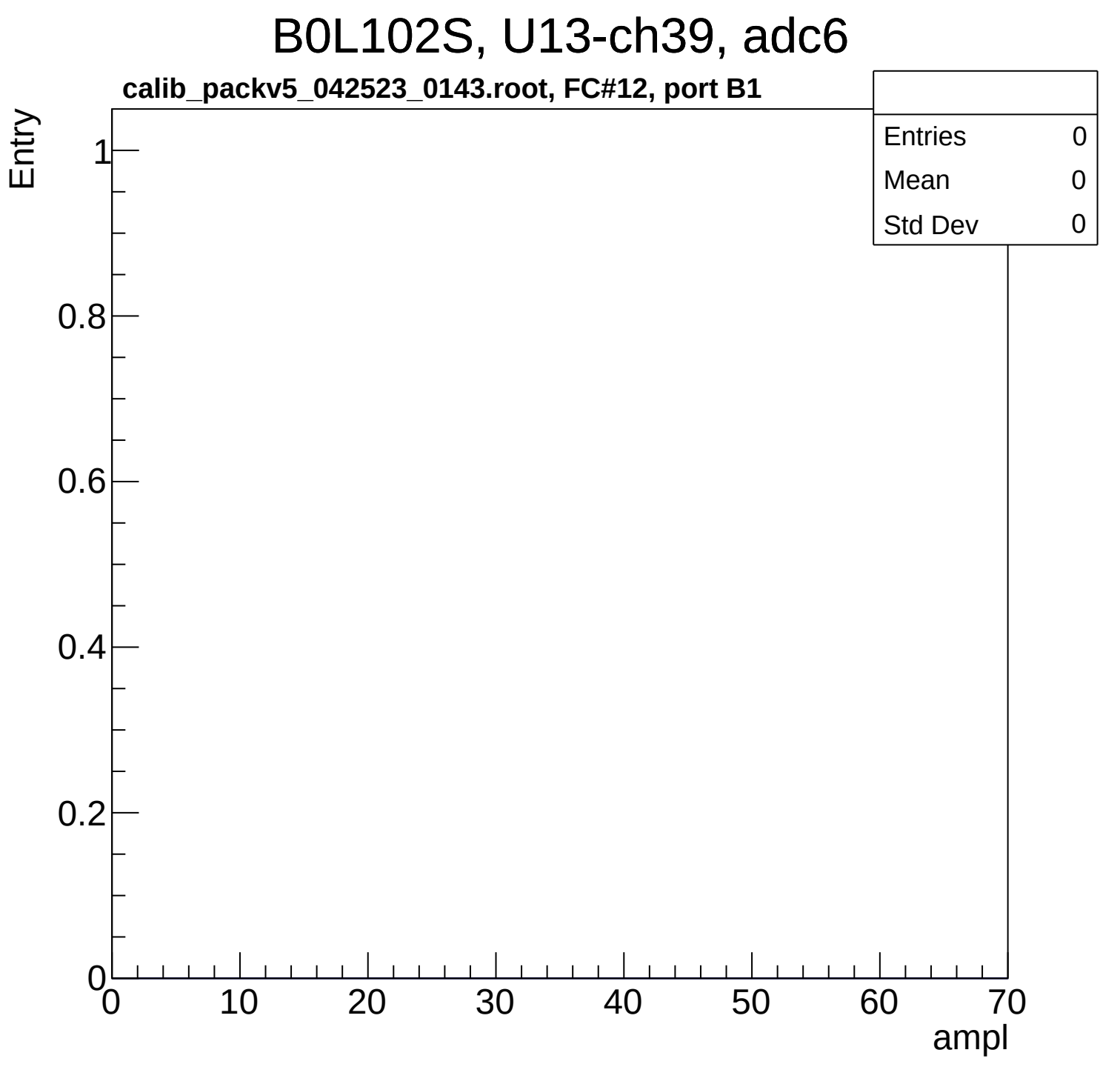
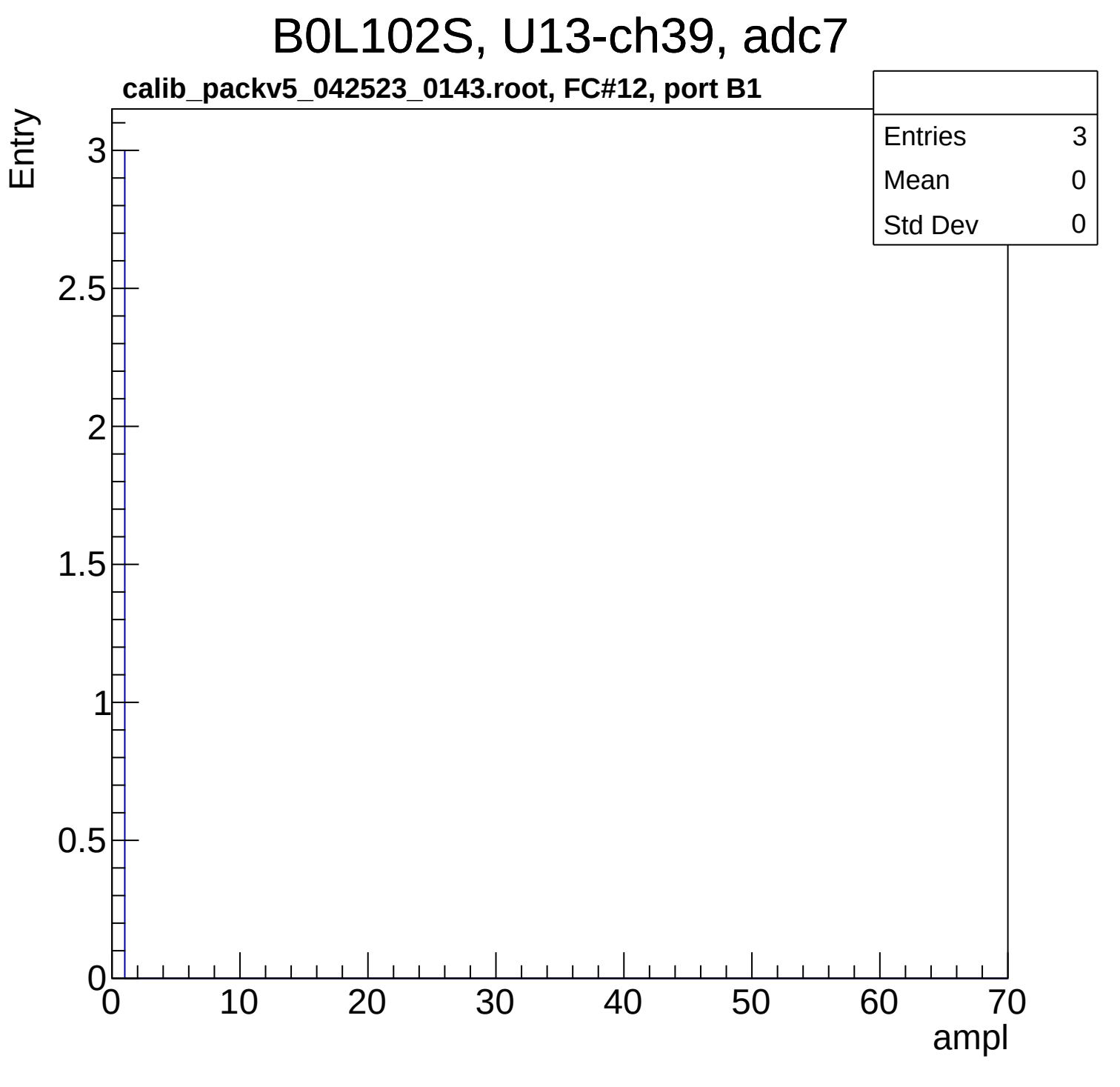

B0L102S, U13-ch39, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch39, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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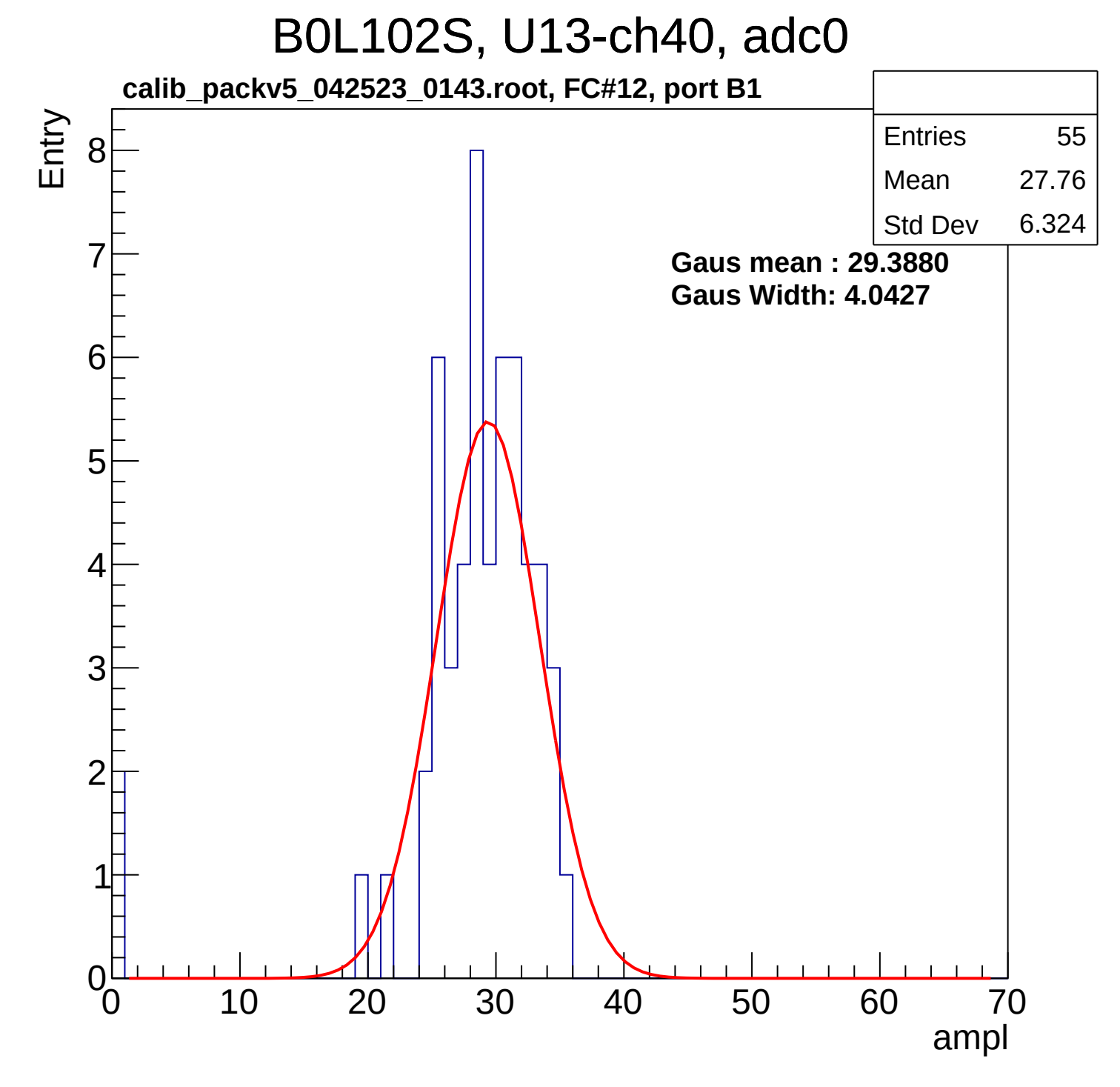
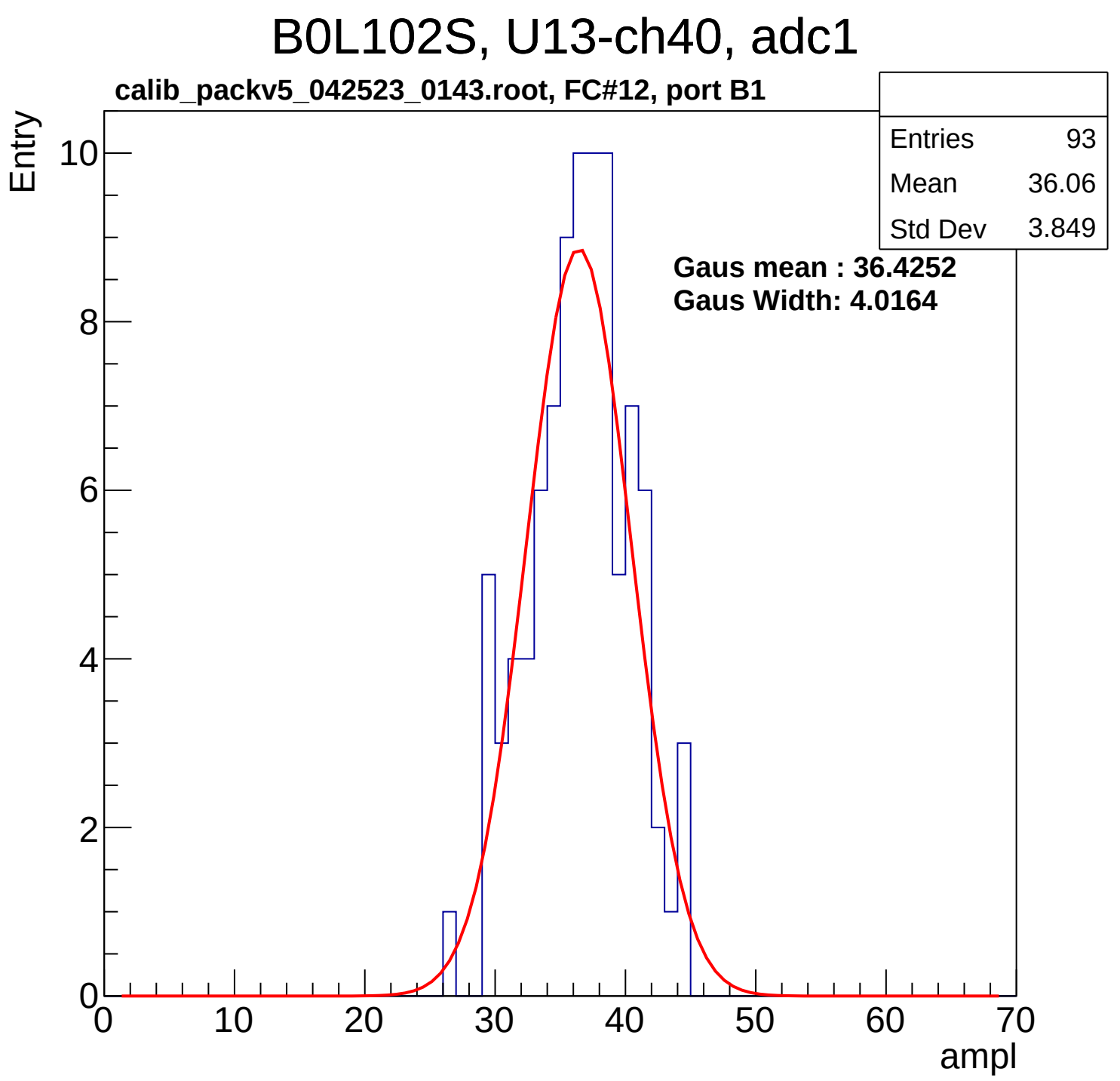
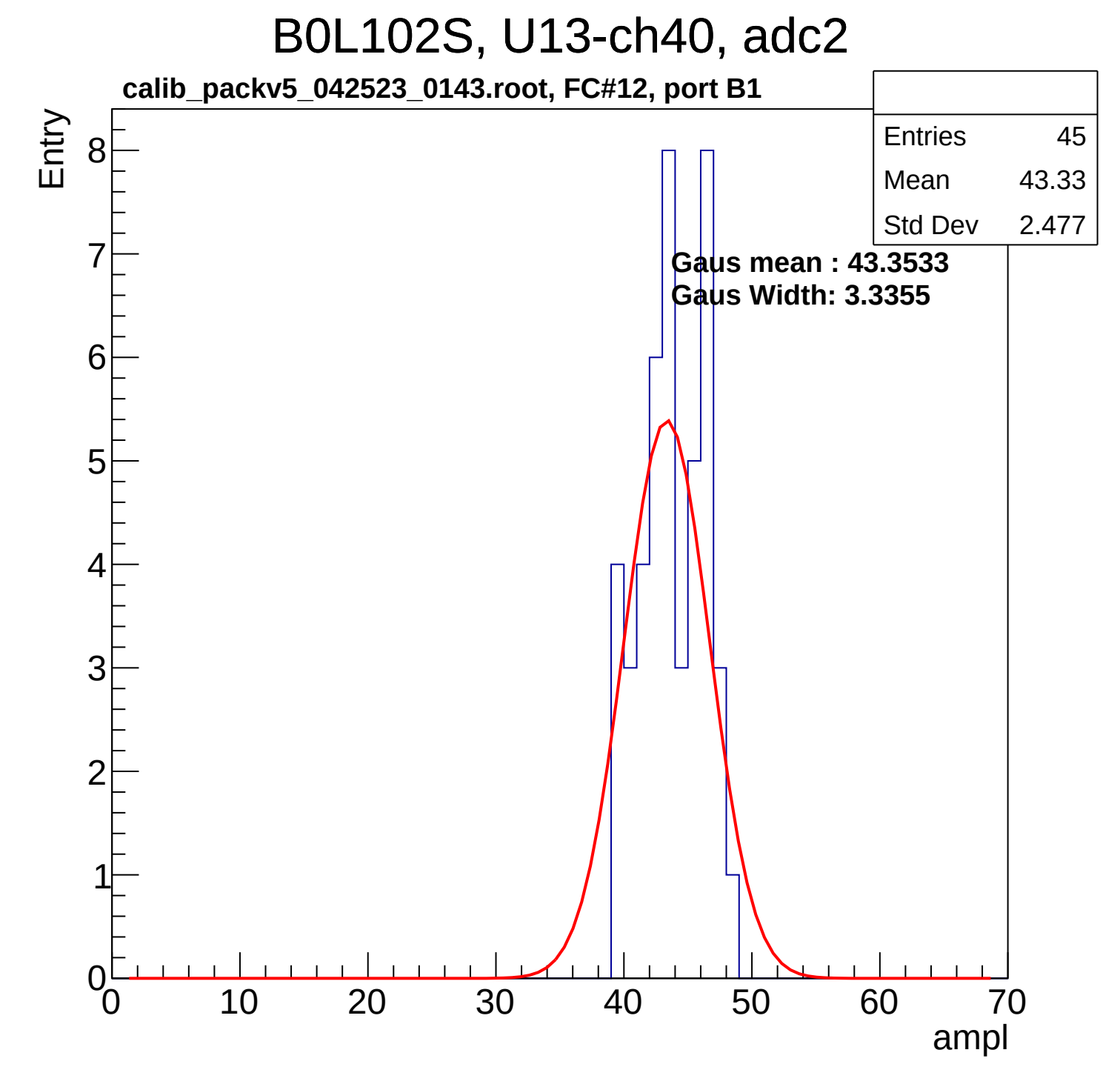
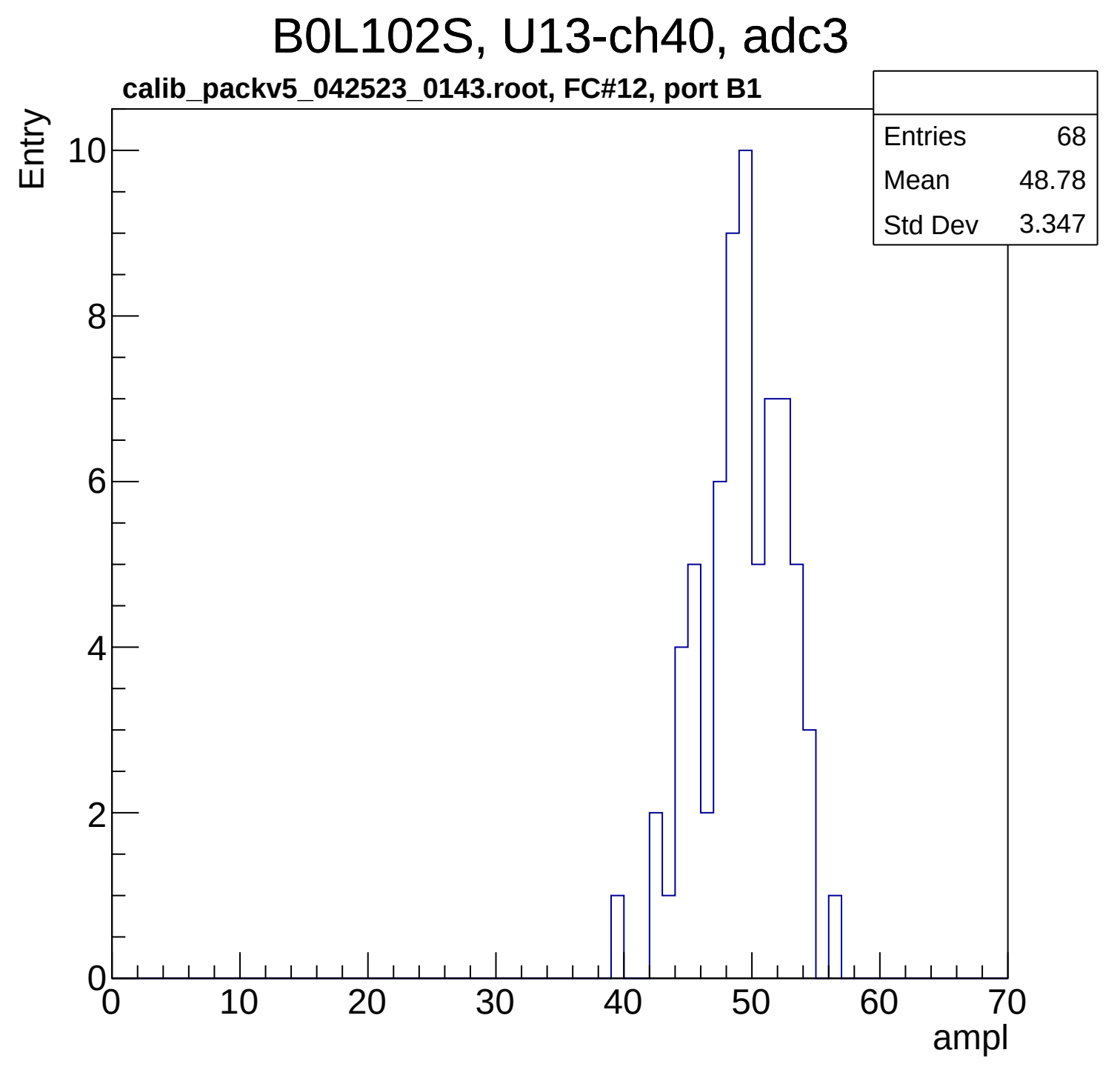
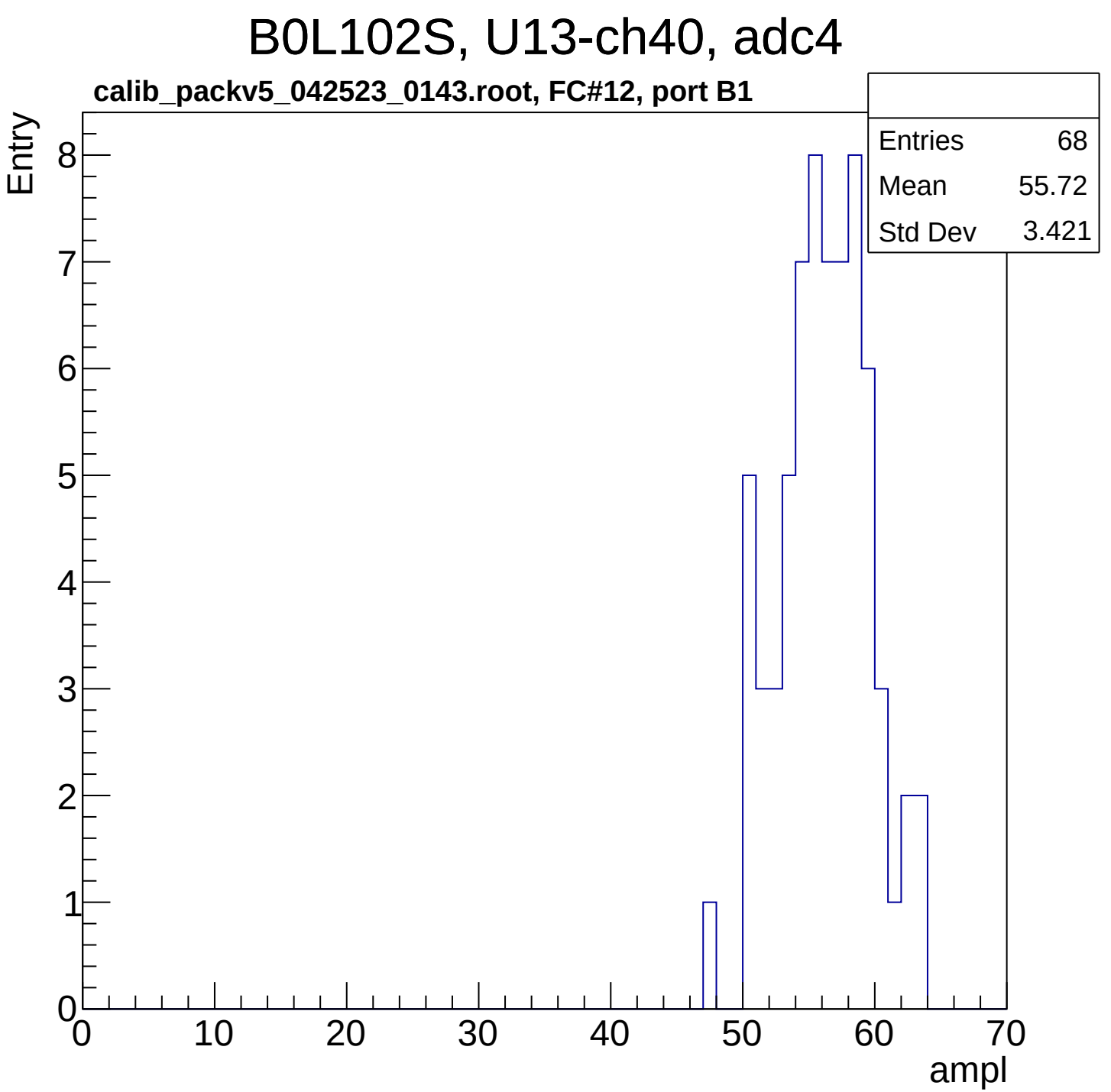
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch40, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 27.76
Std Dev 6.324
Gaus mean : 29.3880
Gaus Width: 4.0427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 93
Mean 36.06
Std Dev 3.849
Gaus mean : 36.4252
Gaus Width: 4.0164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 43.33
Std Dev 2.477
Gaus mean : 43.3533
Gaus Width: 3.3355
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.78
Std Dev 3.347
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55.72
Std Dev 3.421
Entry
ampl
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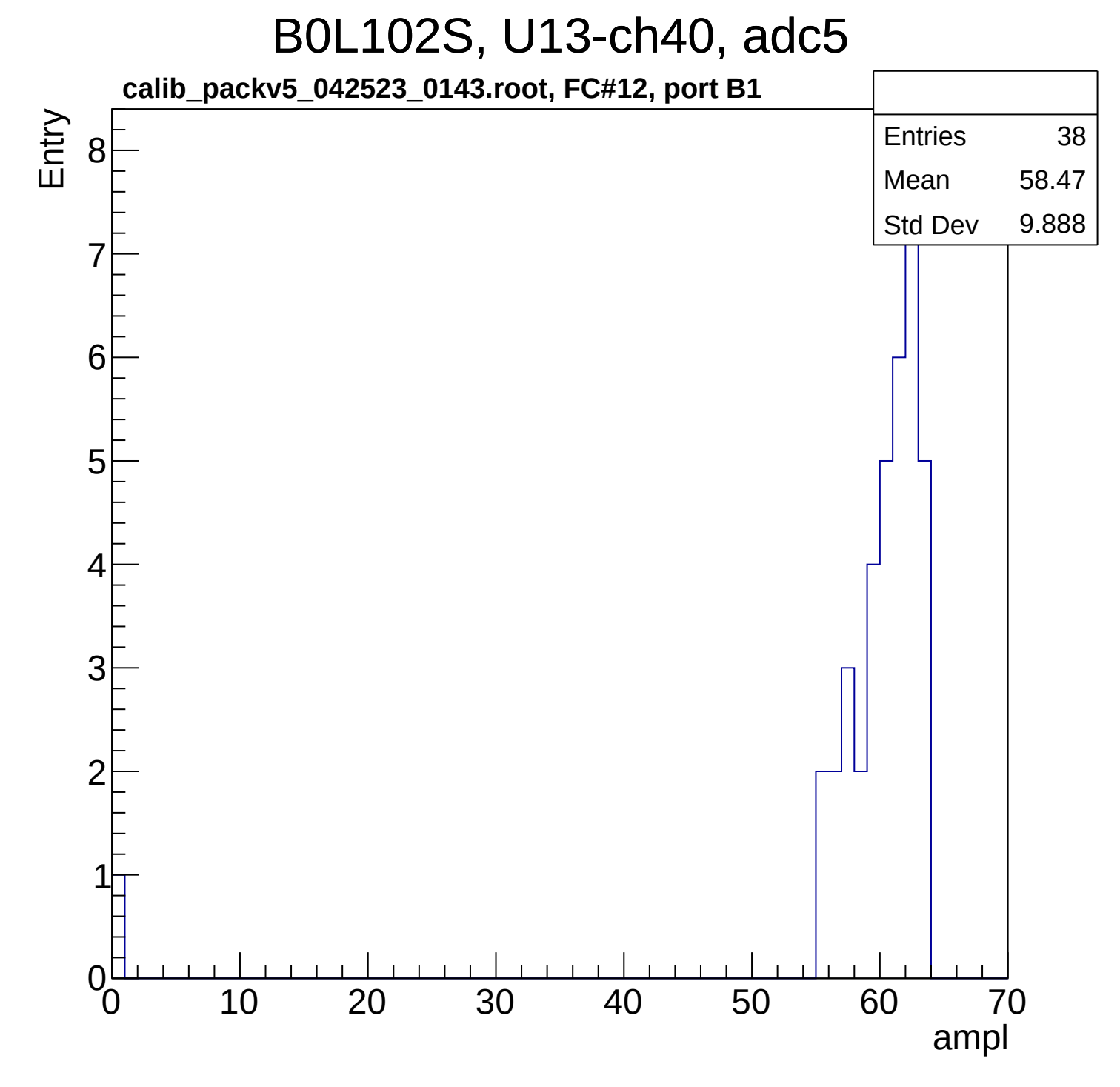
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.47
Std Dev 9.888
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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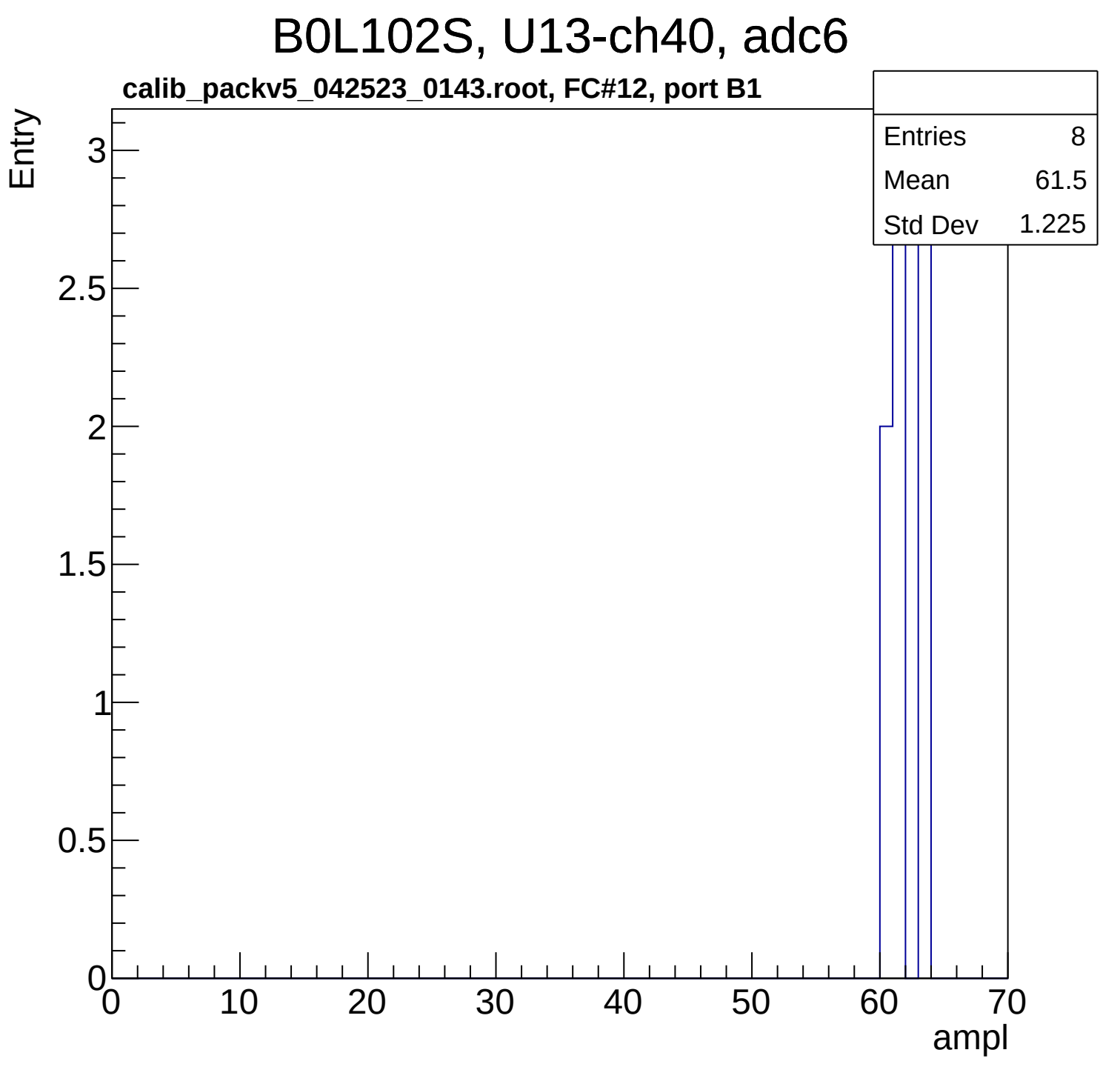
B0L102S, U13-ch40, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.5
Std Dev 1.225
Entry
ampl
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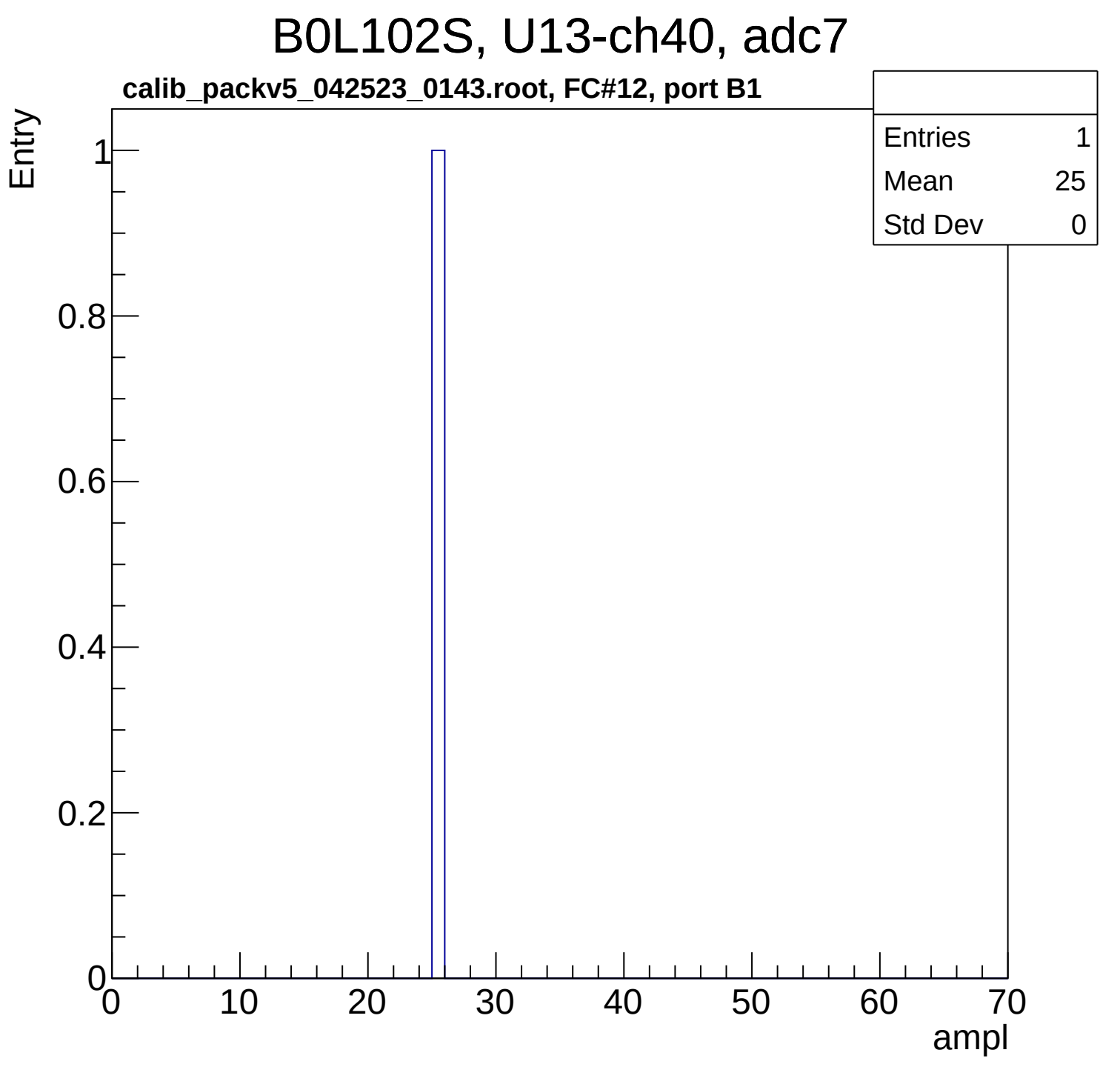
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch40, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 25
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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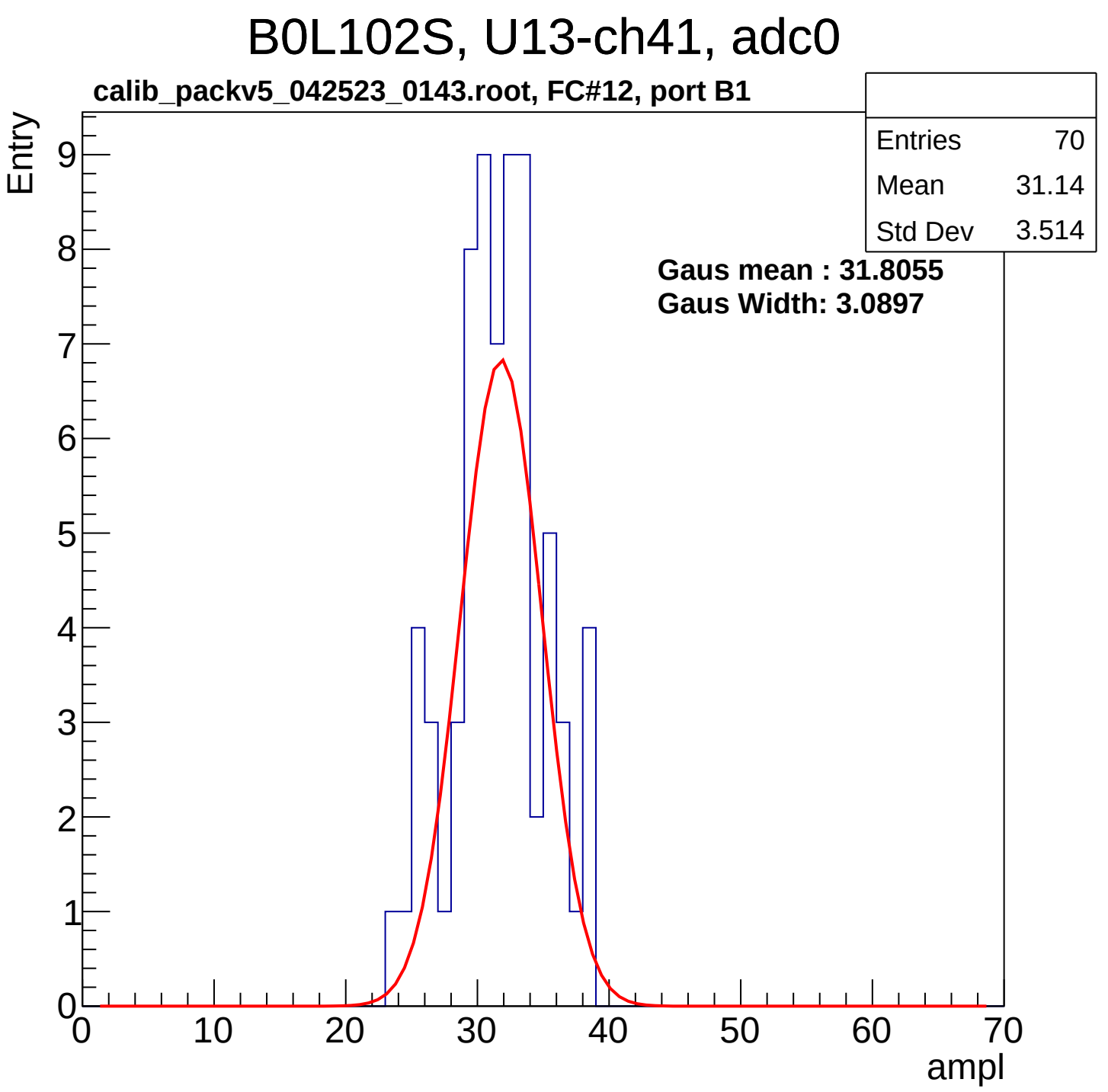
40
50
60
70
ampl


B0L102S, U13-ch41, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 31.14
Std Dev 3.514
Gaus mean : 31.8055
Gaus Width: 3.0897
Entry
ampl
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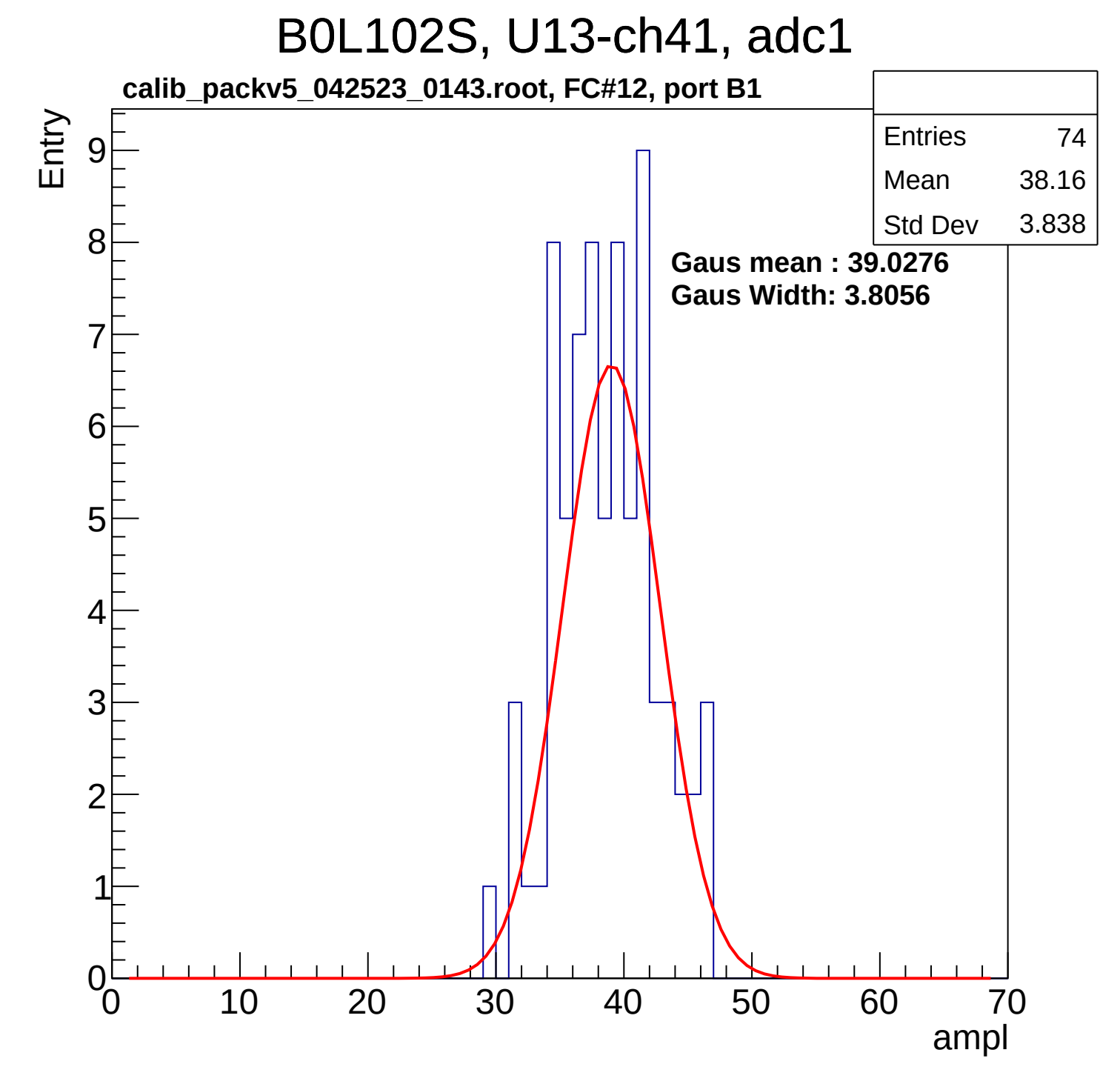
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch41, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 38.16
Std Dev 3.838
Gaus mean : 39.0276
Gaus Width: 3.8056
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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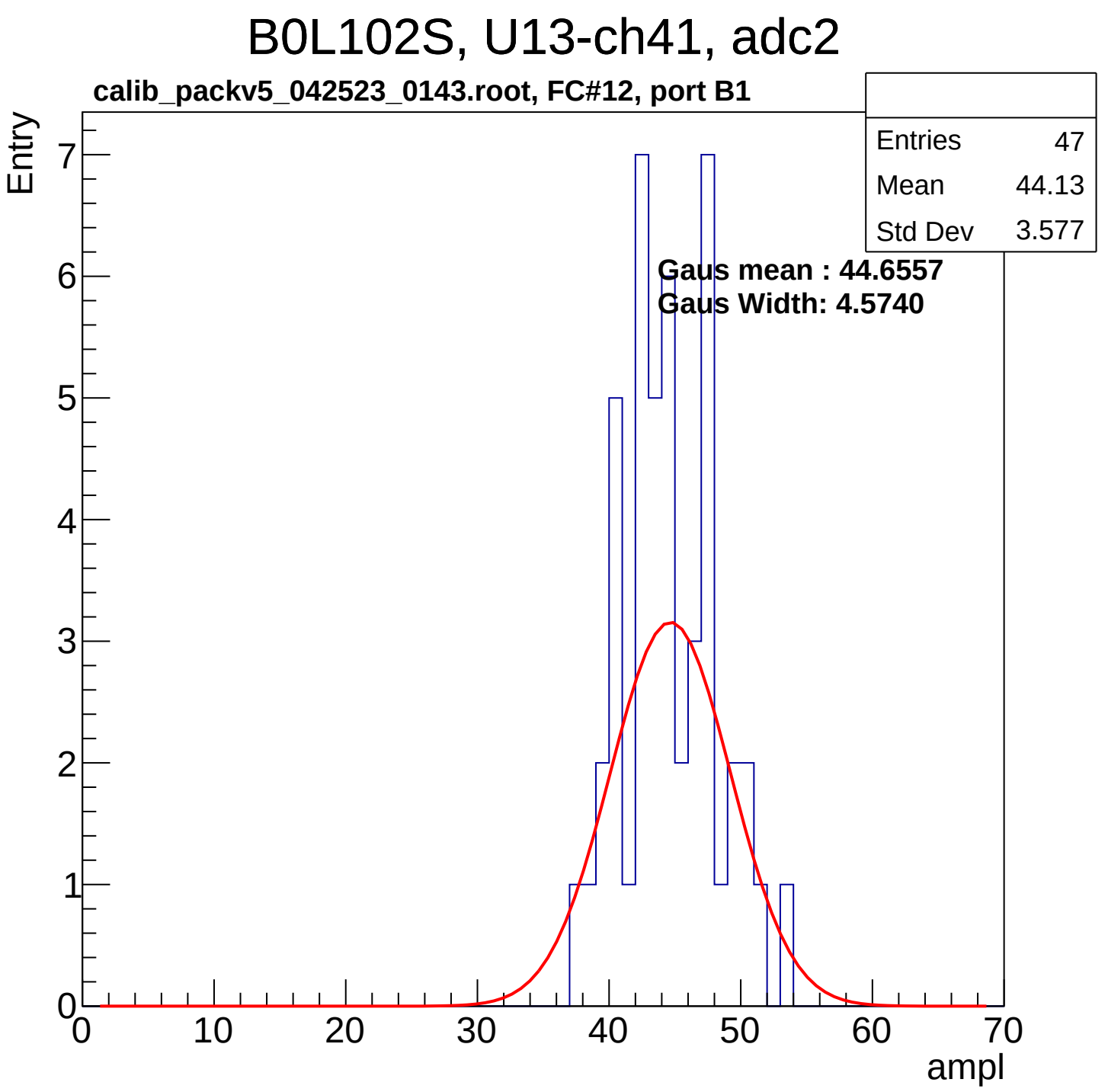
20
30
40
50
60
70


B0L102S, U13-ch41, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 44.13
Std Dev 3.577
Gaus mean : 44.6557
Gaus Width: 4.5740
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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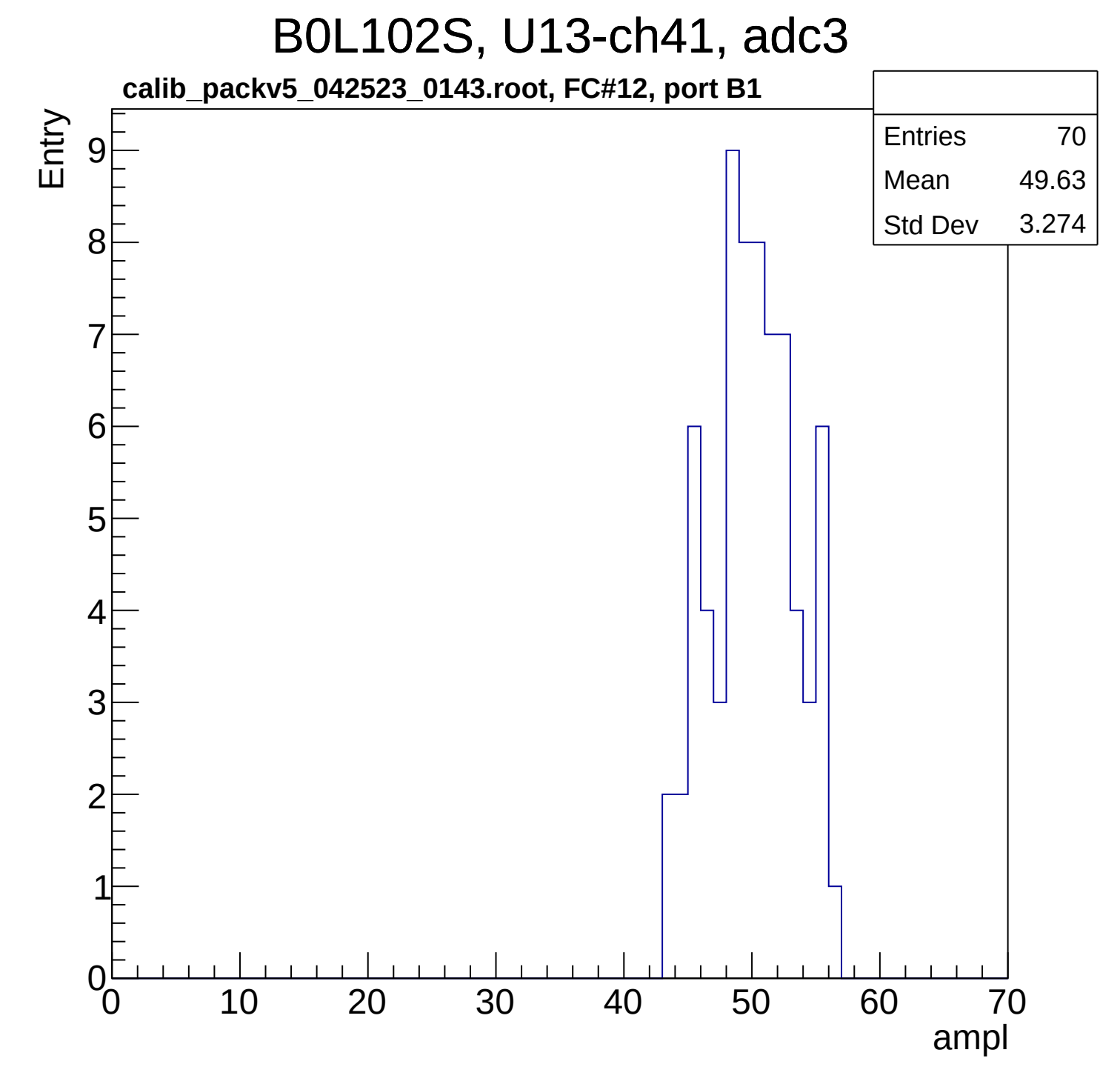
B0L102S, U13-ch41, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 49.63
Std Dev 3.274
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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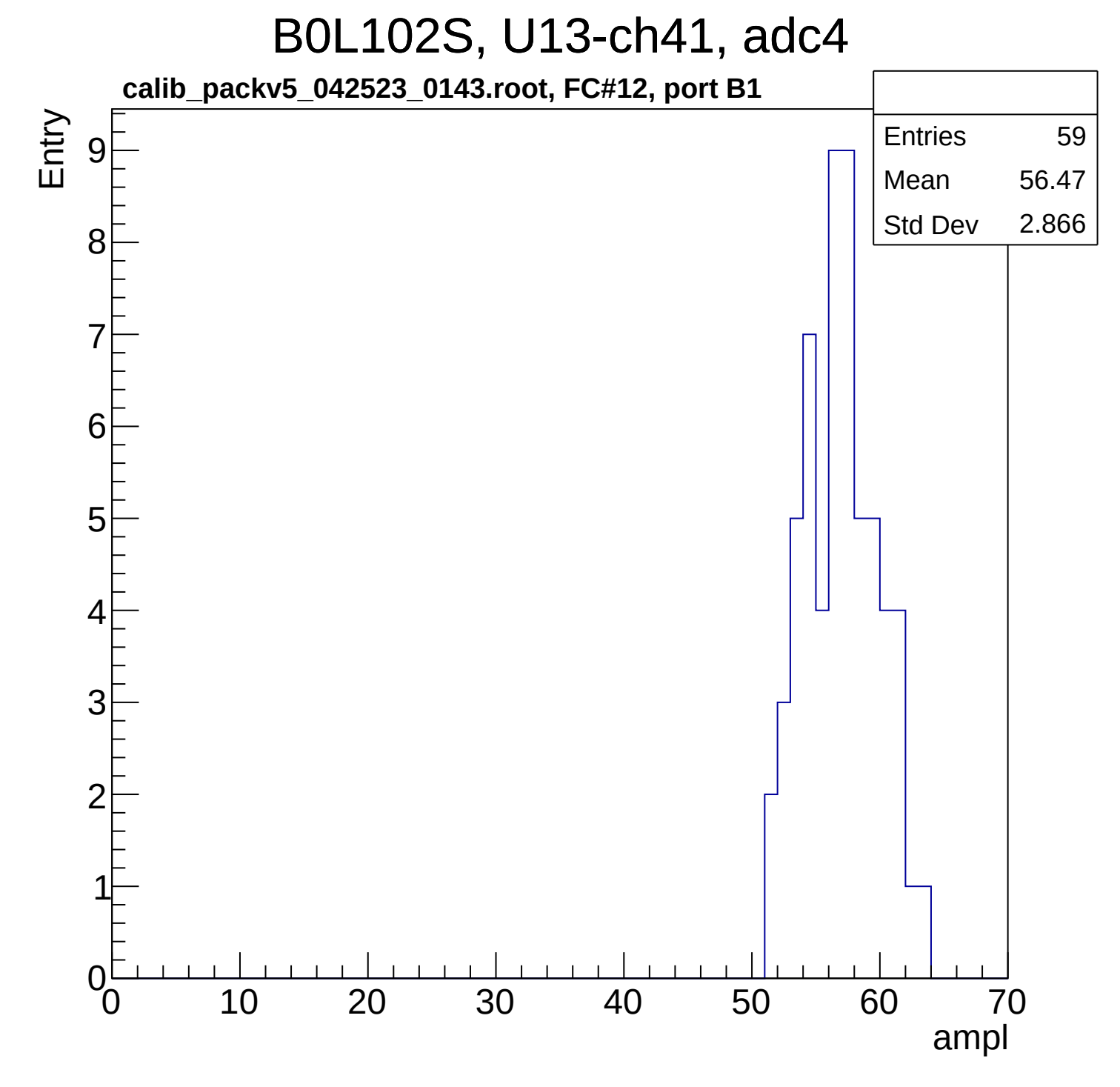
40
50
60
70


B0L102S, U13-ch41, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.47
Std Dev 2.866
Entry
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3
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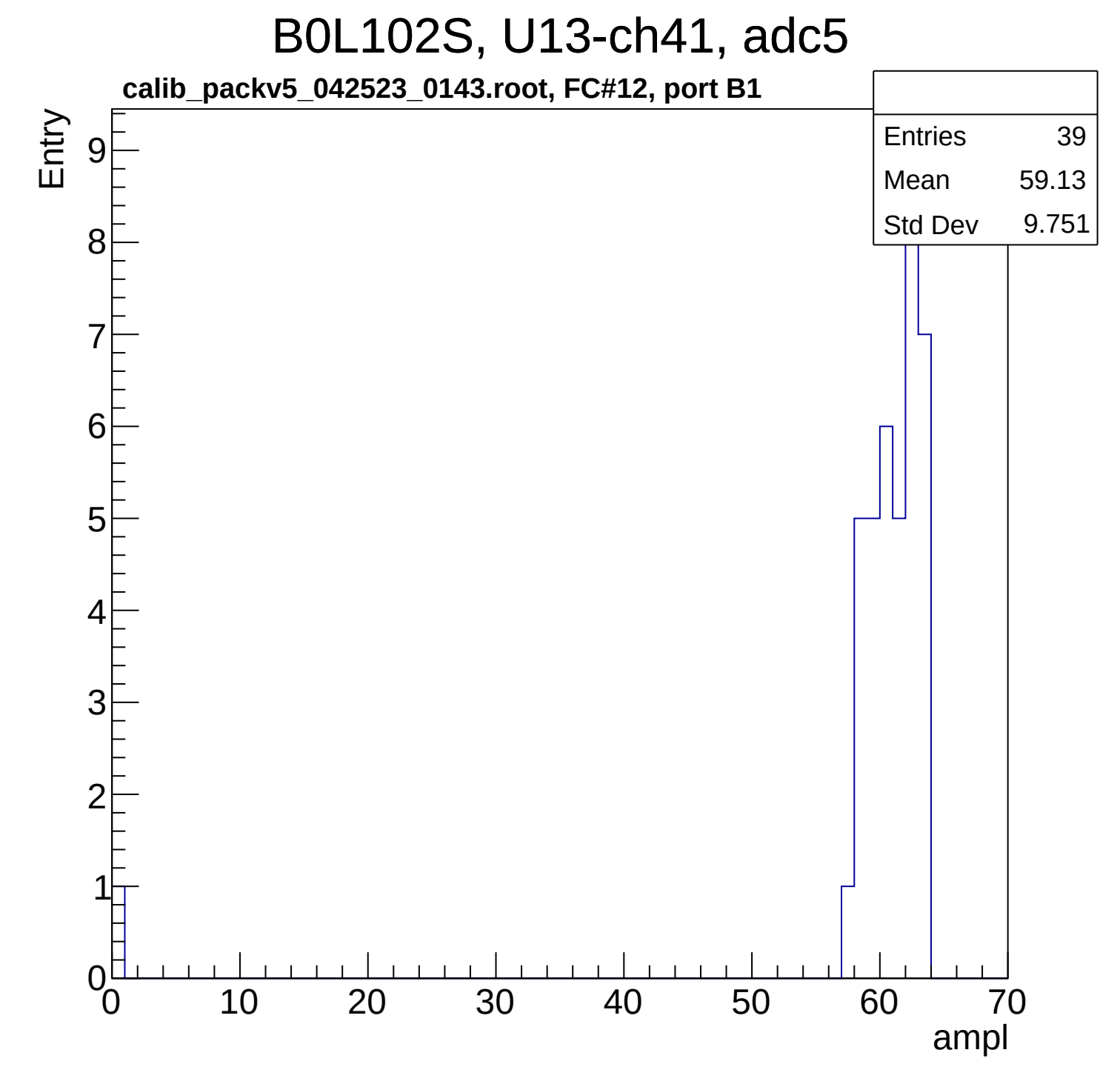
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch41, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 59.13
Std Dev 9.751
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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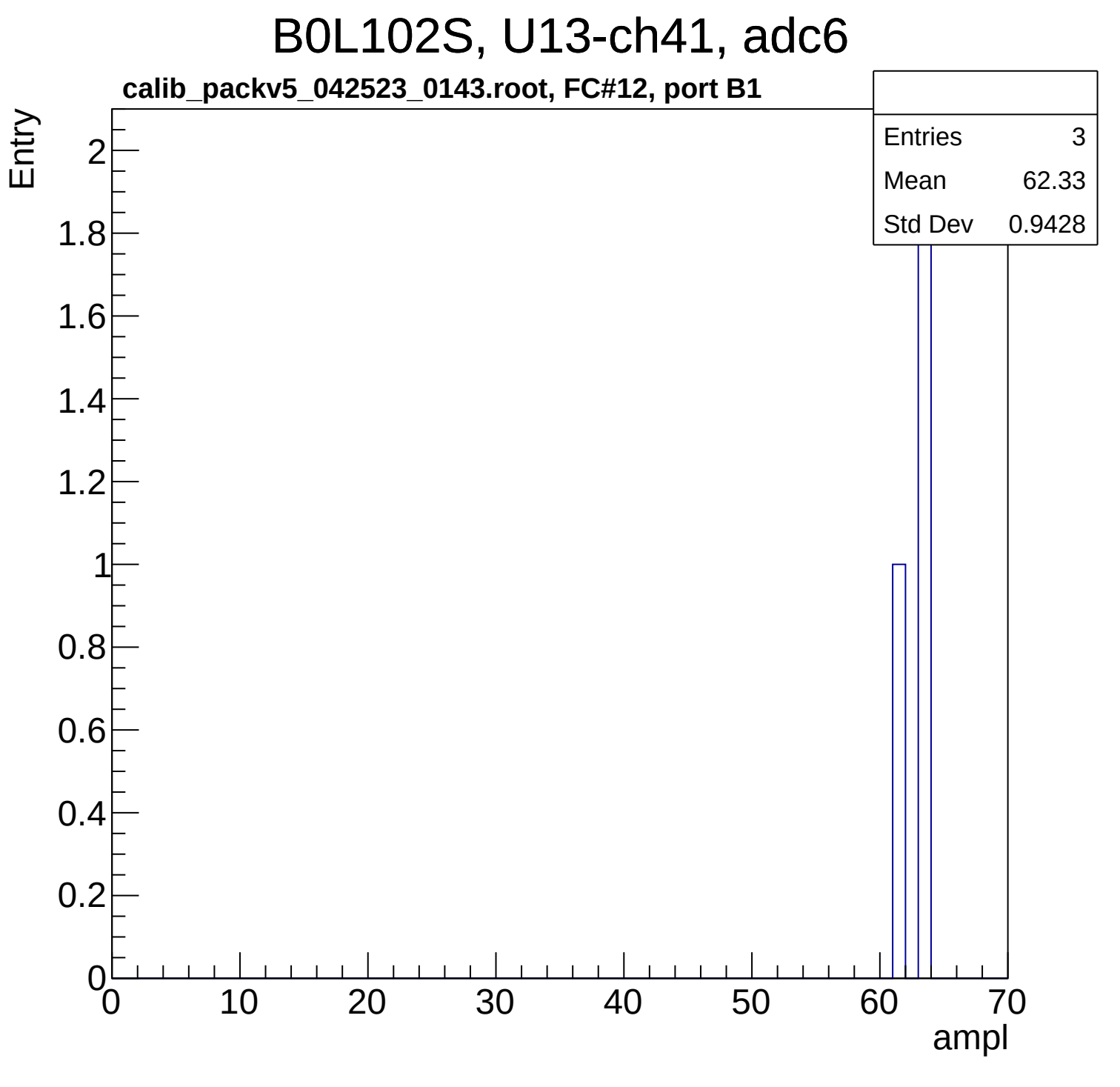
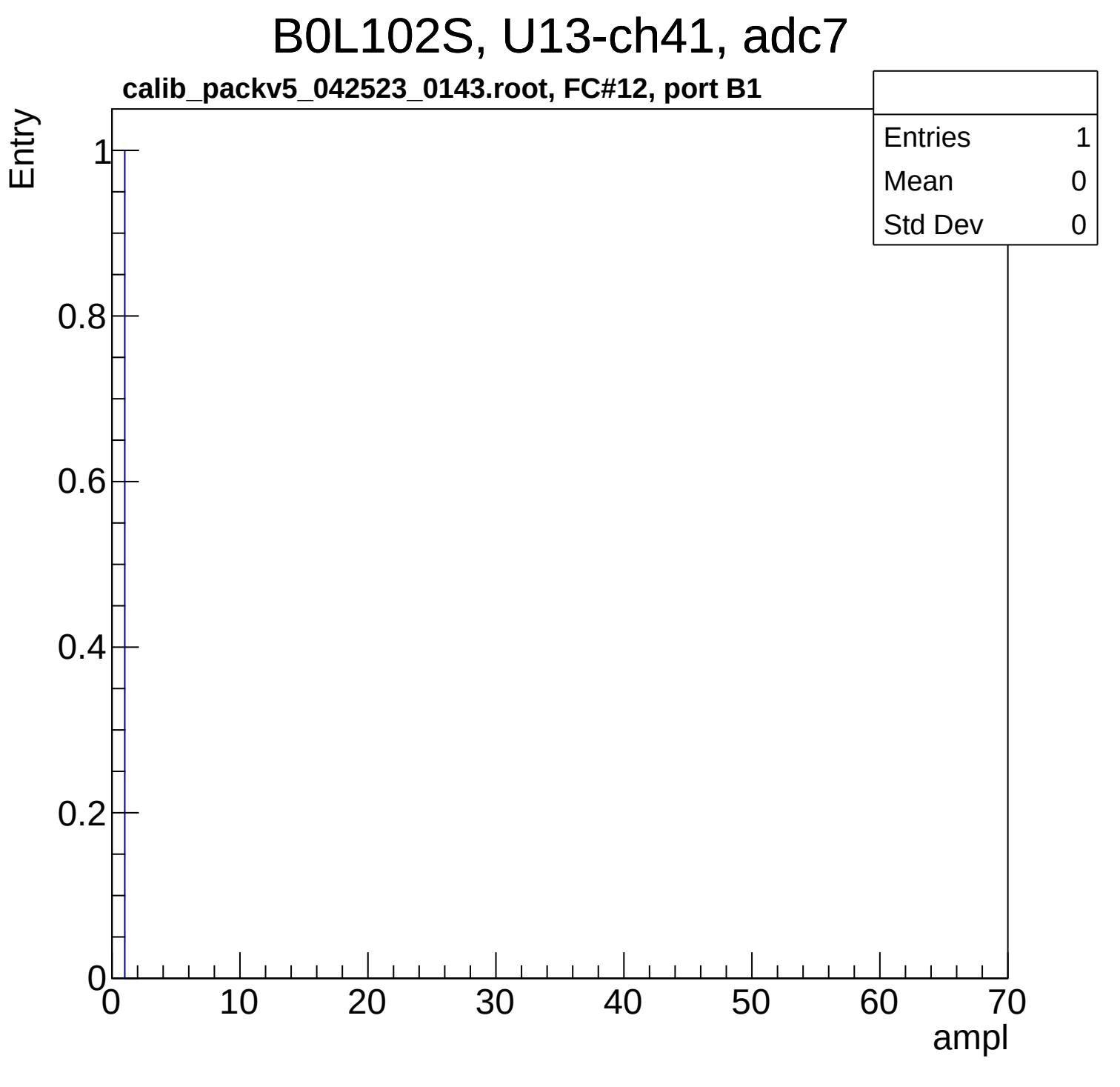
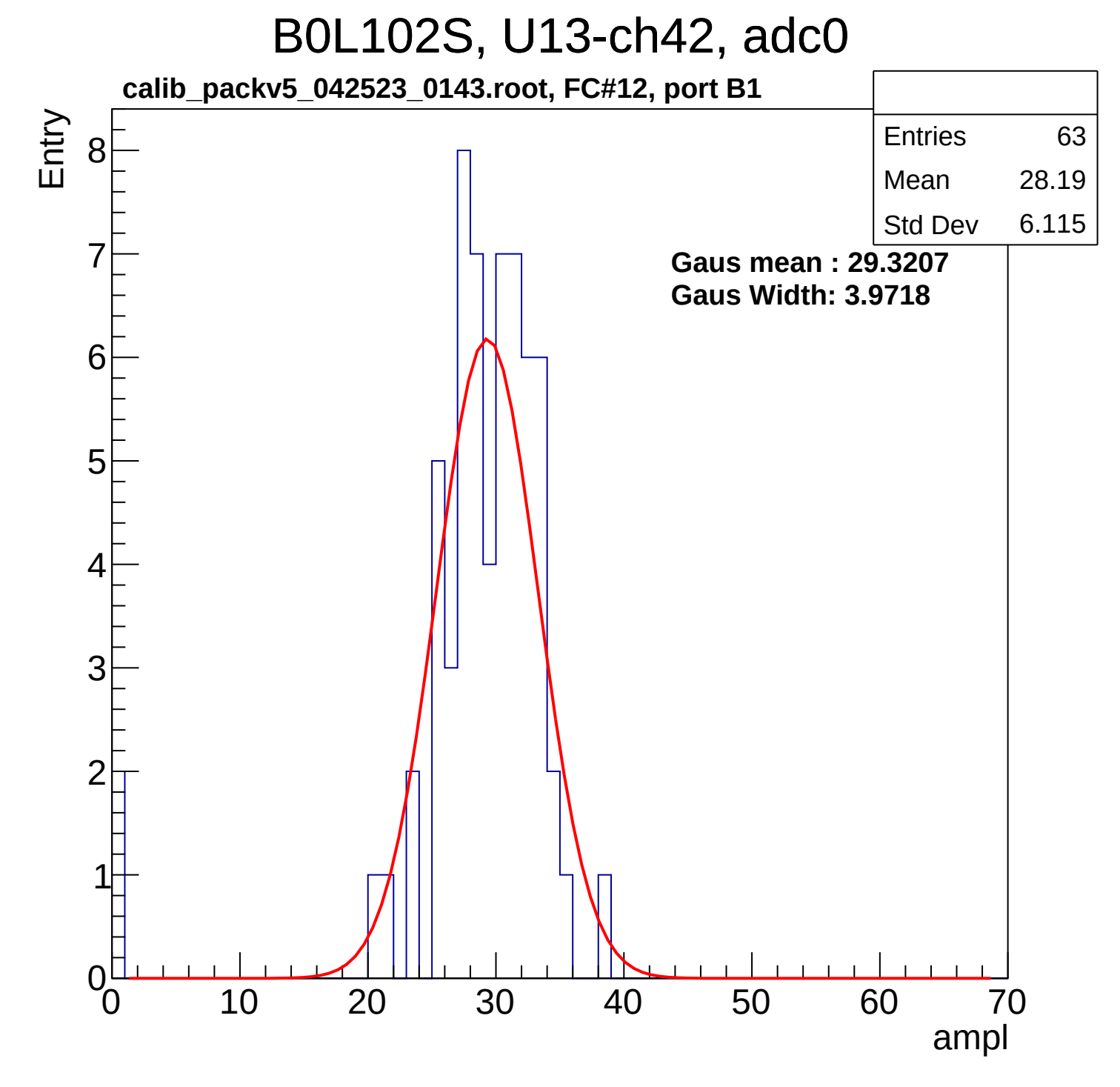
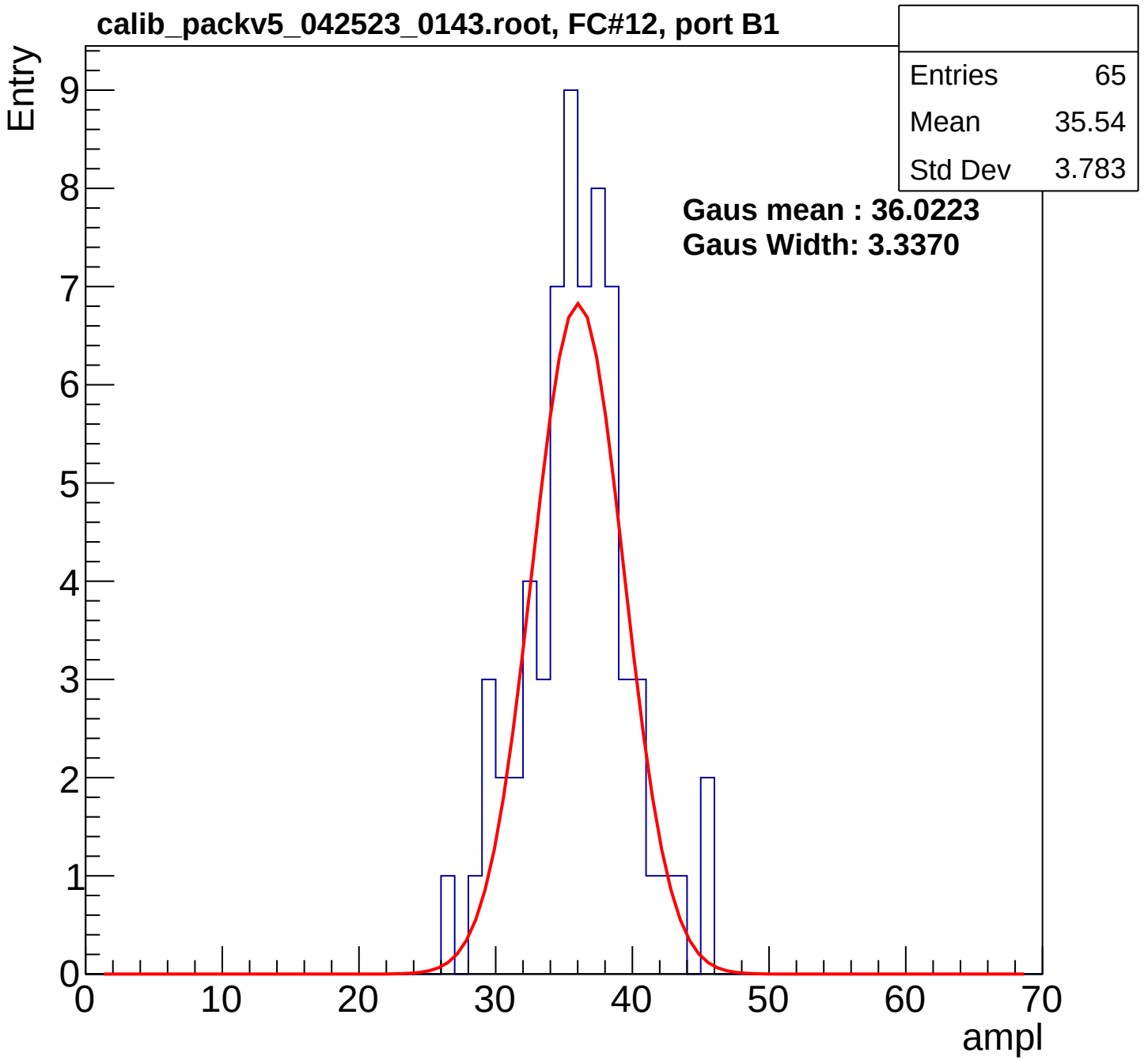
40
50
60
70


B0L102S, U13-ch41, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch41, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch42, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.19
Std Dev 6.115
Gaus mean : 29.3207
Gaus Width: 3.9718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch42, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.54
Std Dev 3.783
Gaus mean : 36.0223
Gaus Width: 3.3370
Entry
ampl
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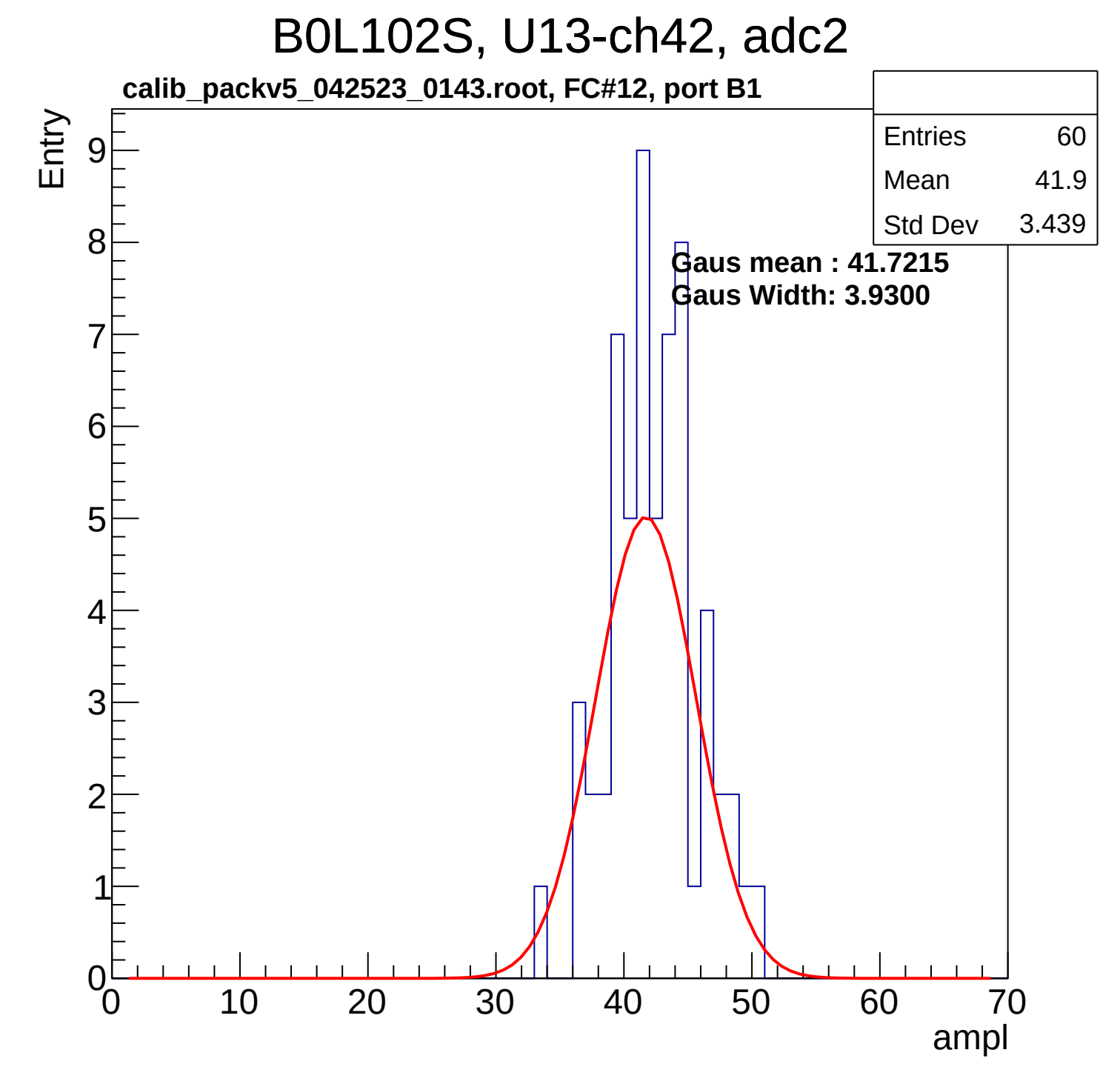
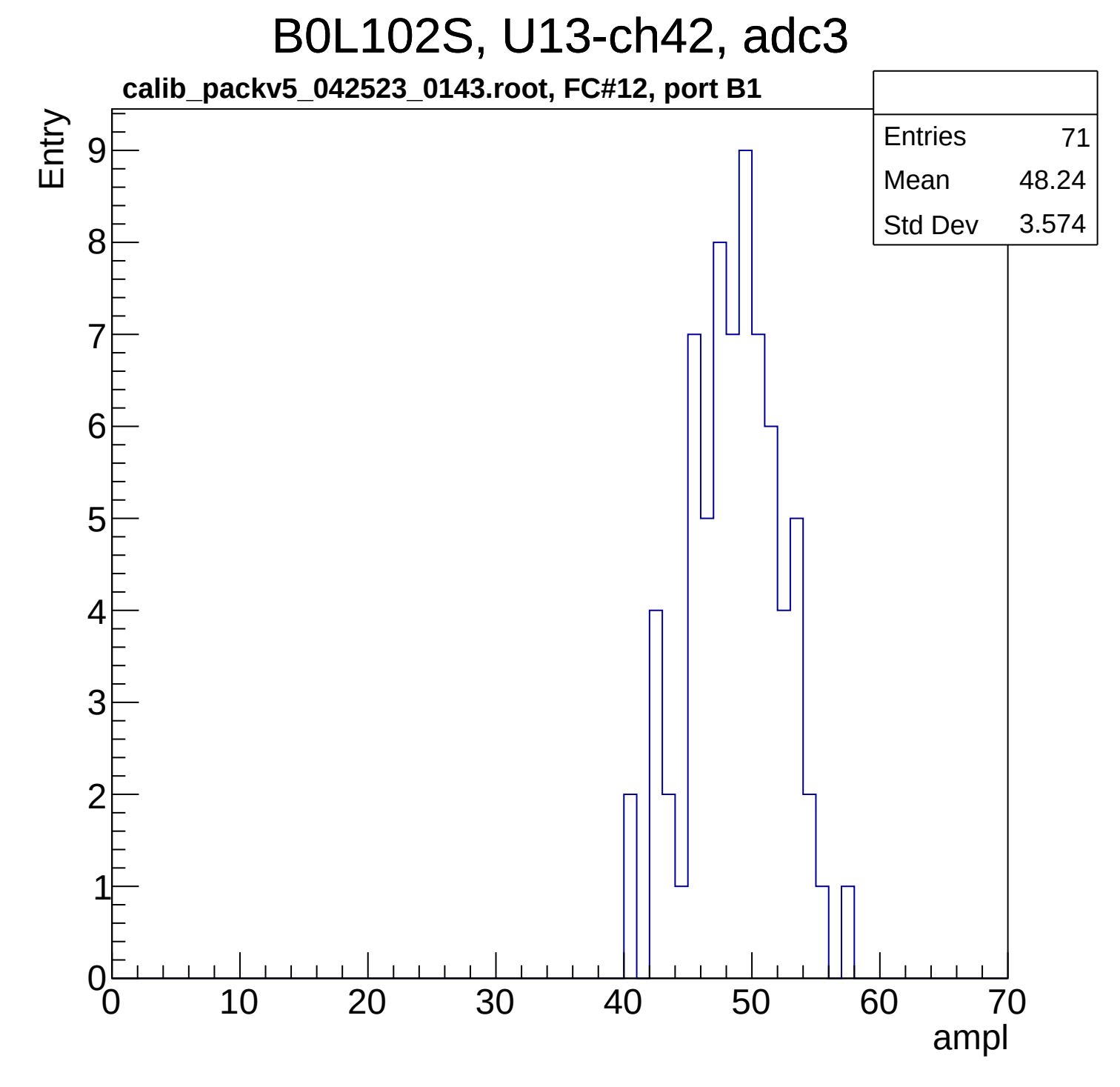
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch42, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 41.9
Std Dev 3.439
Gaus mean : 41.7215
Gaus Width: 3.9300
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch42, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.24
Std Dev 3.574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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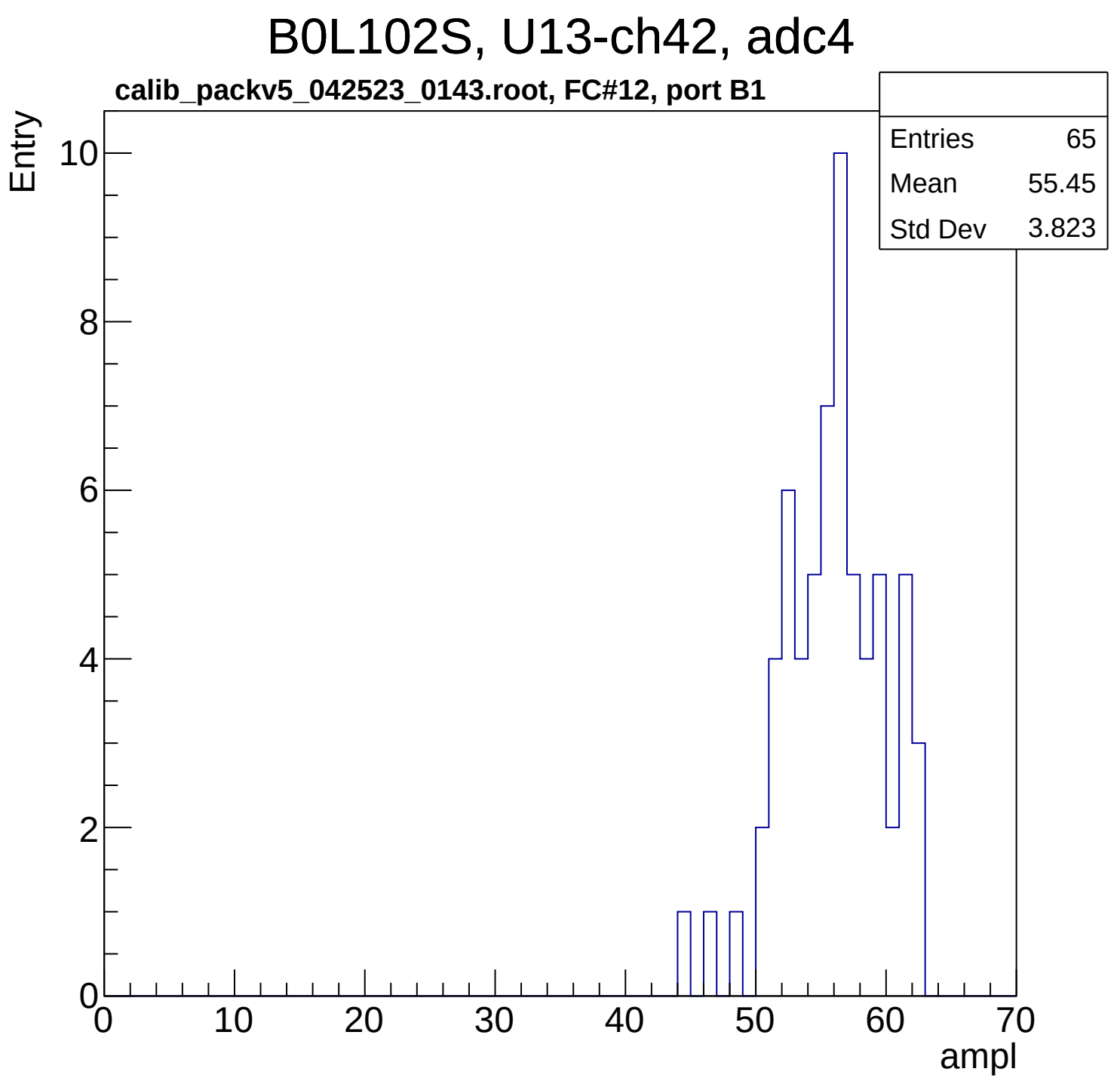
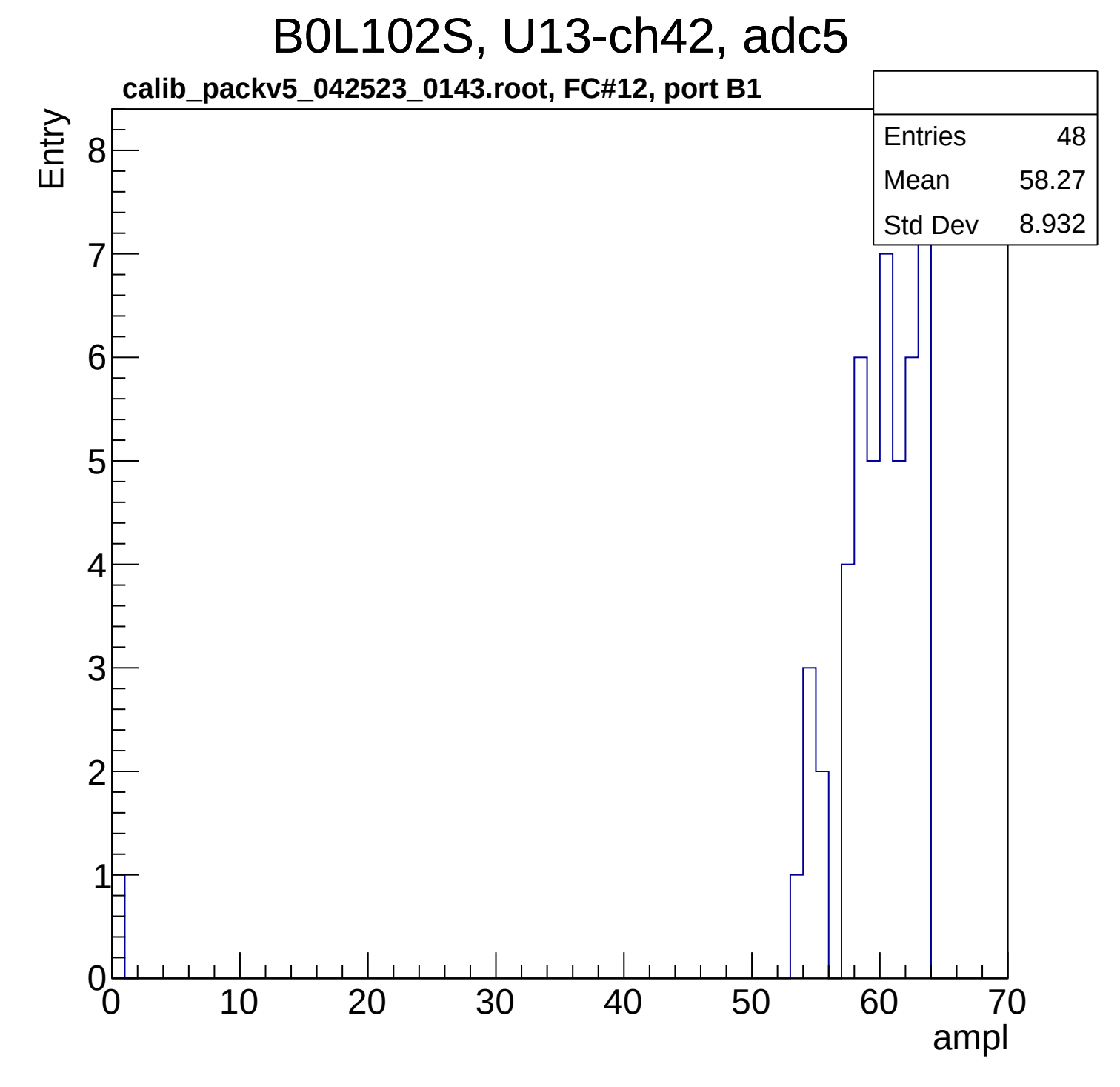

B0L102S, U13-ch42, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 55.45
Std Dev 3.823
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch42, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.27
Std Dev 8.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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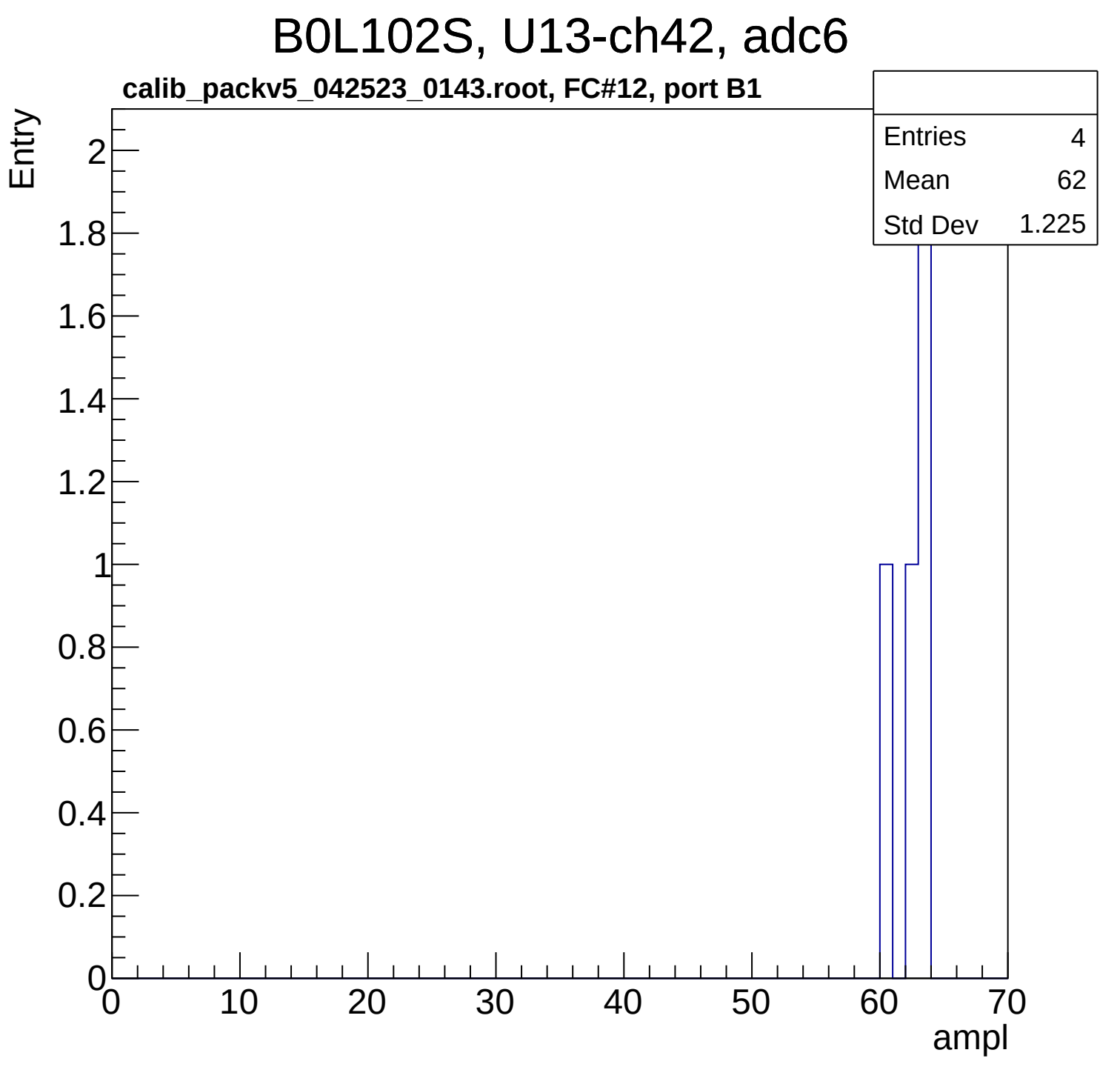
B0L102S, U13-ch42, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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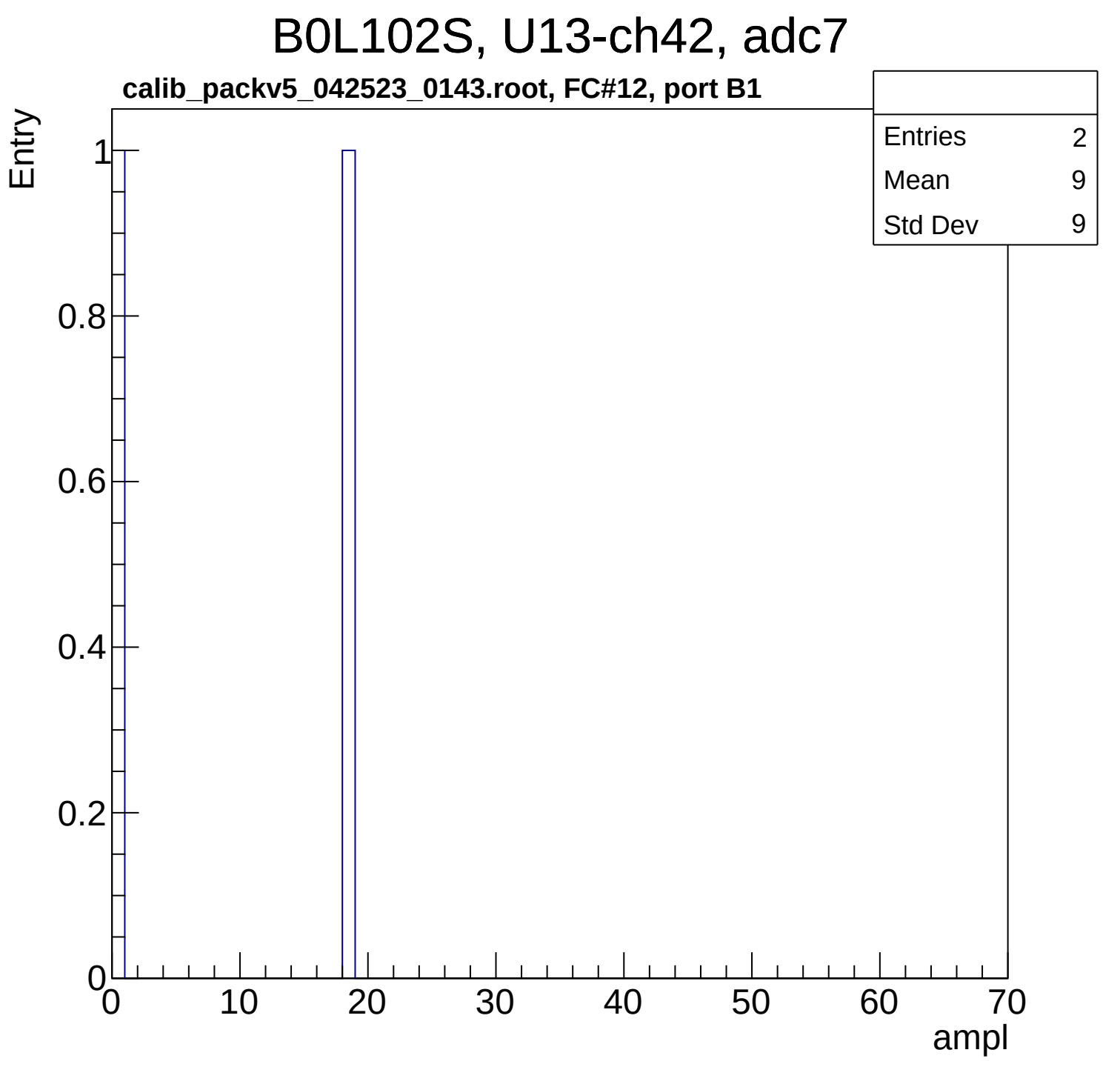
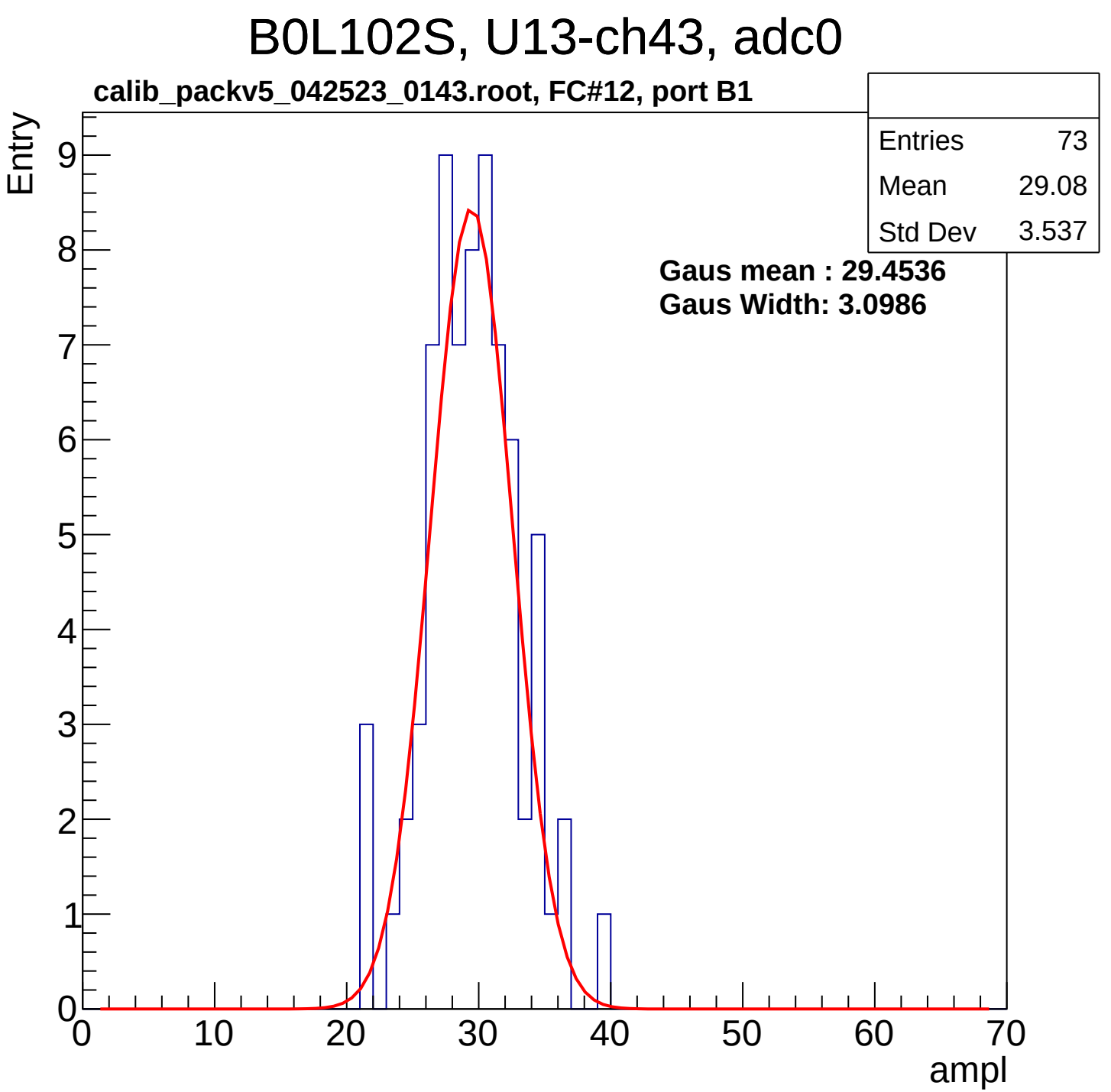

B0L102S, U13-ch42, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 9
Std Dev 9
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch43, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.08
Std Dev 3.537
Gaus mean : 29.4536
Gaus Width: 3.0986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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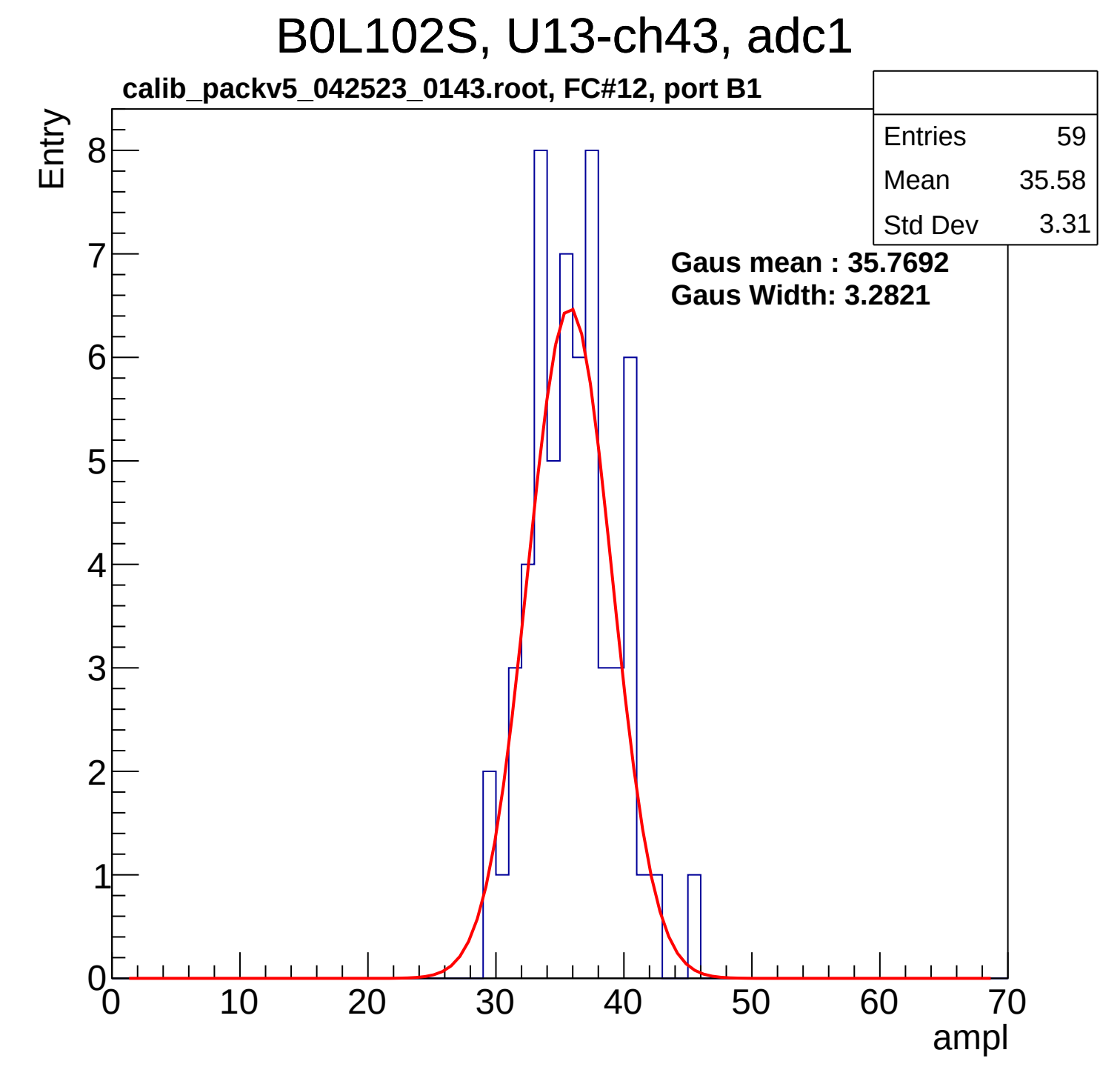
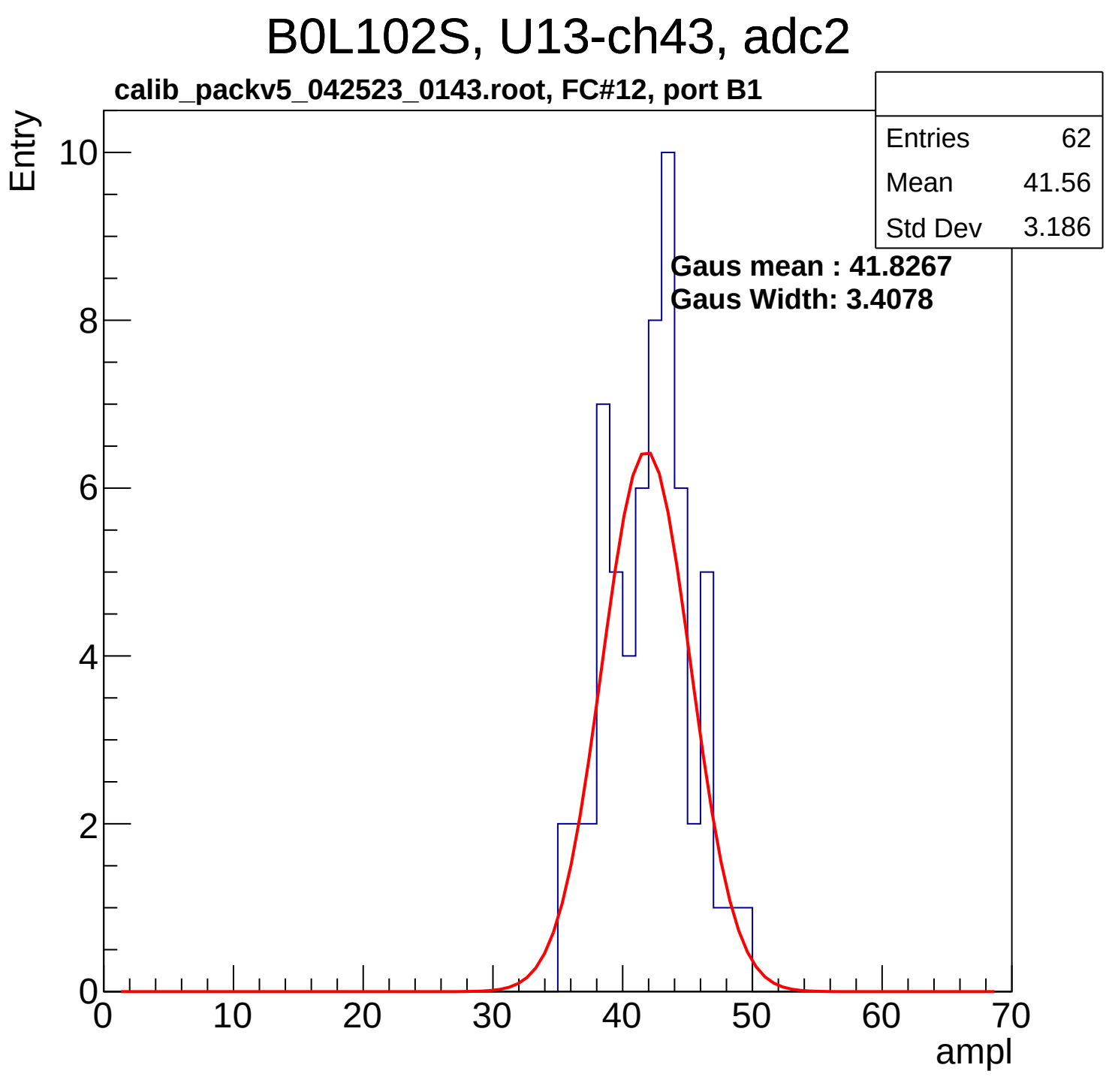

B0L102S, U13-ch43, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 35.58
Std Dev 3.31
Gaus mean : 35.7692
Gaus Width: 3.2821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch43, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.56
Std Dev 3.186
Gaus mean : 41.8267
Gaus Width: 3.4078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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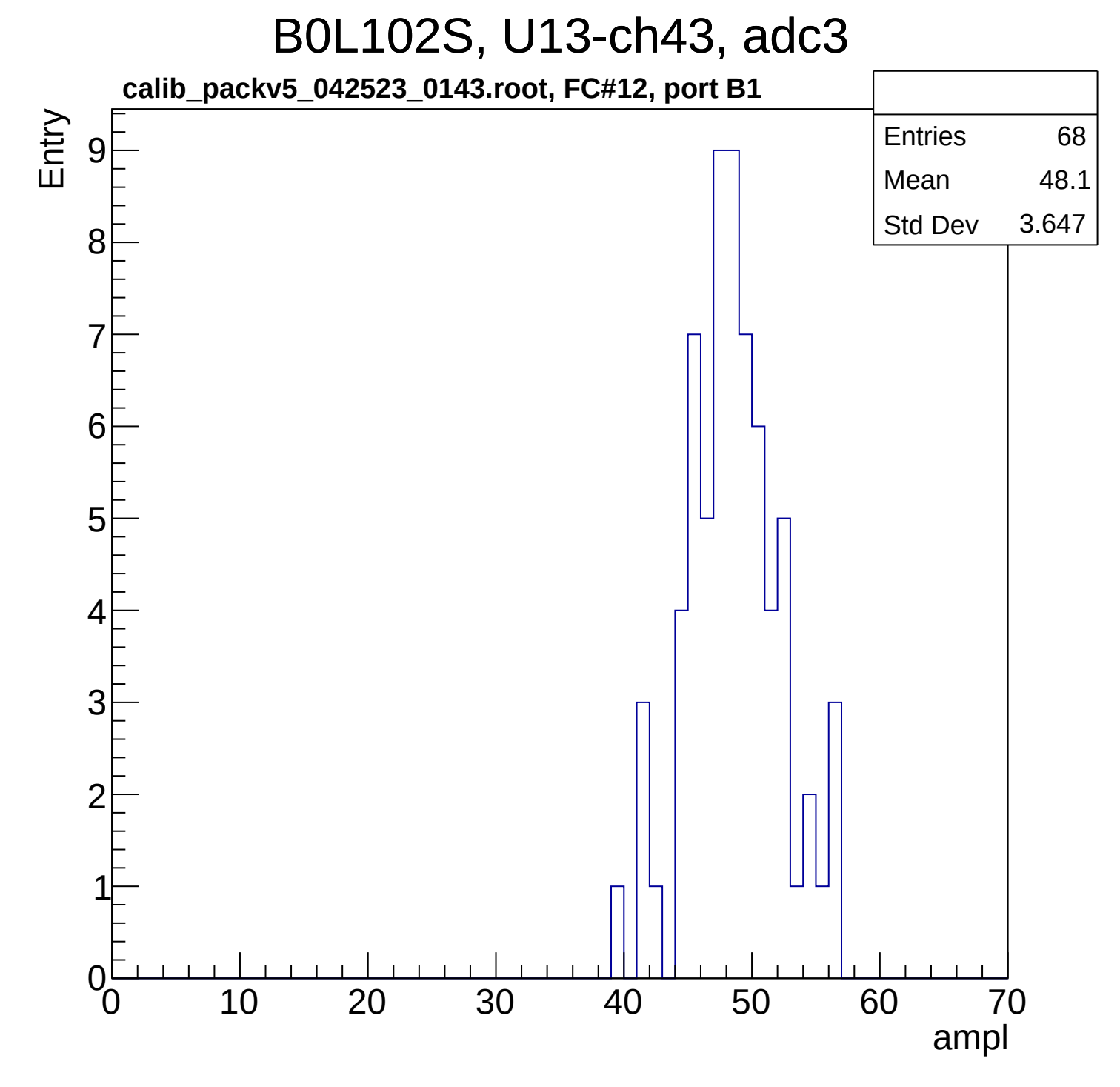
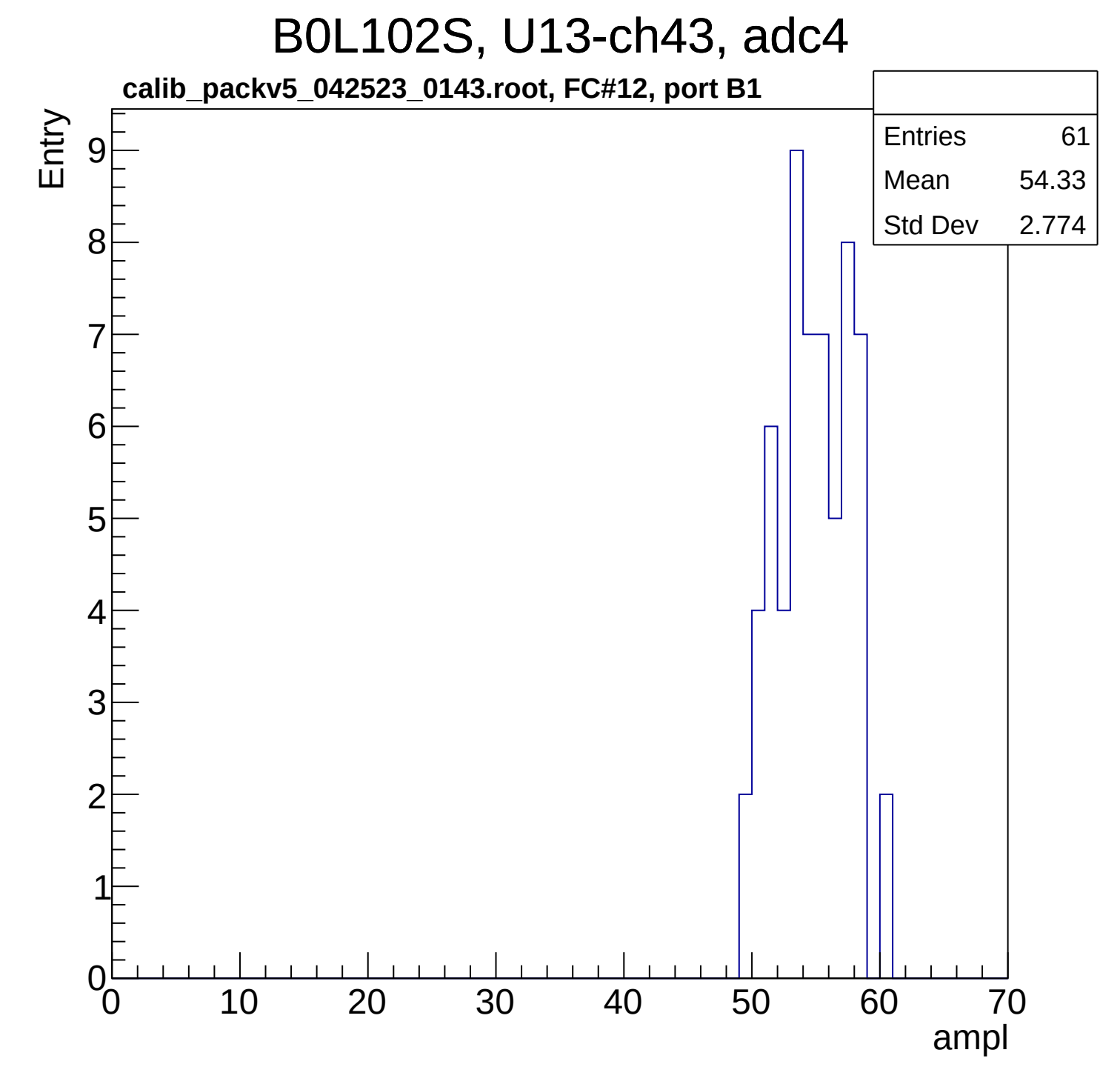
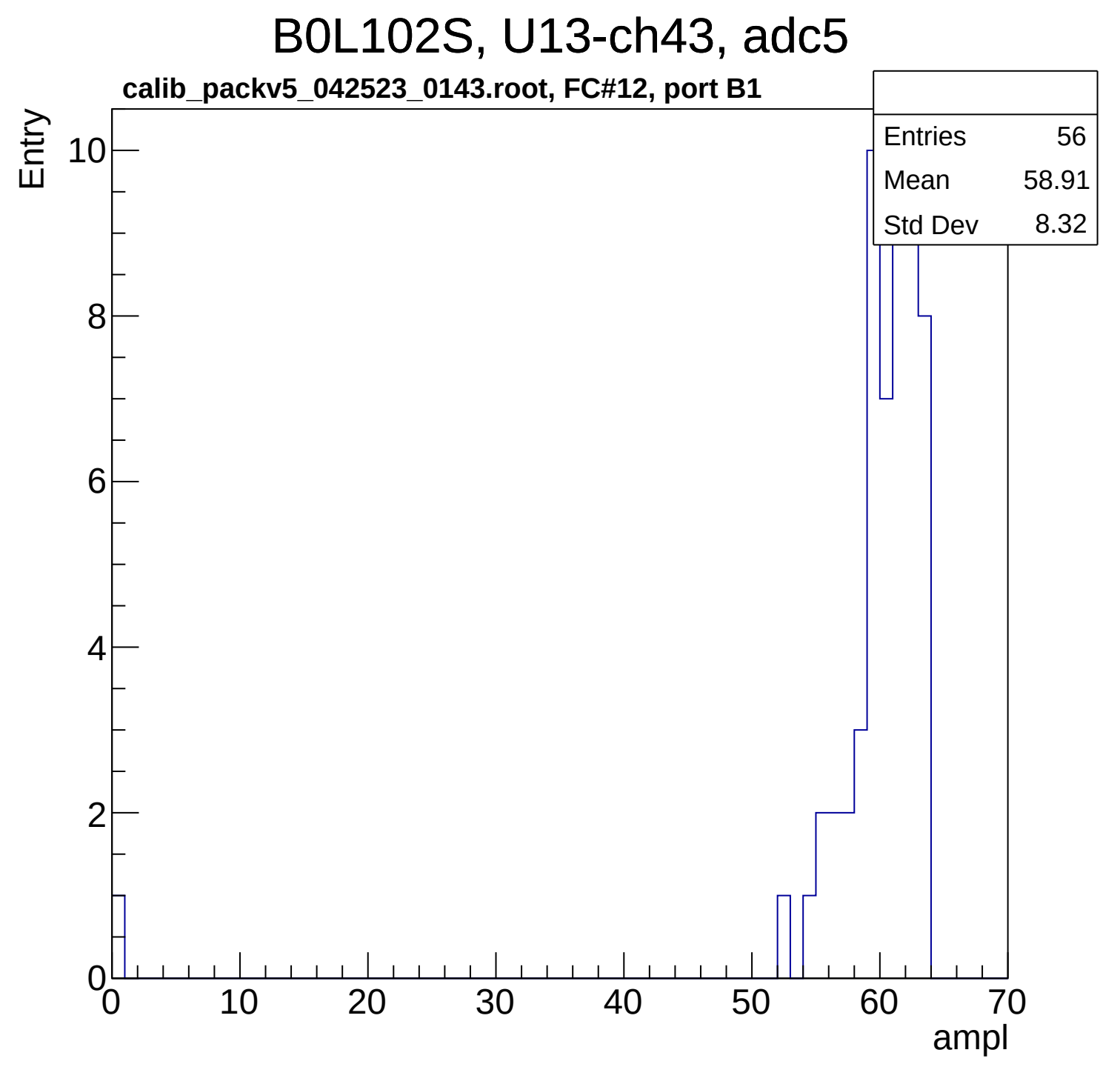
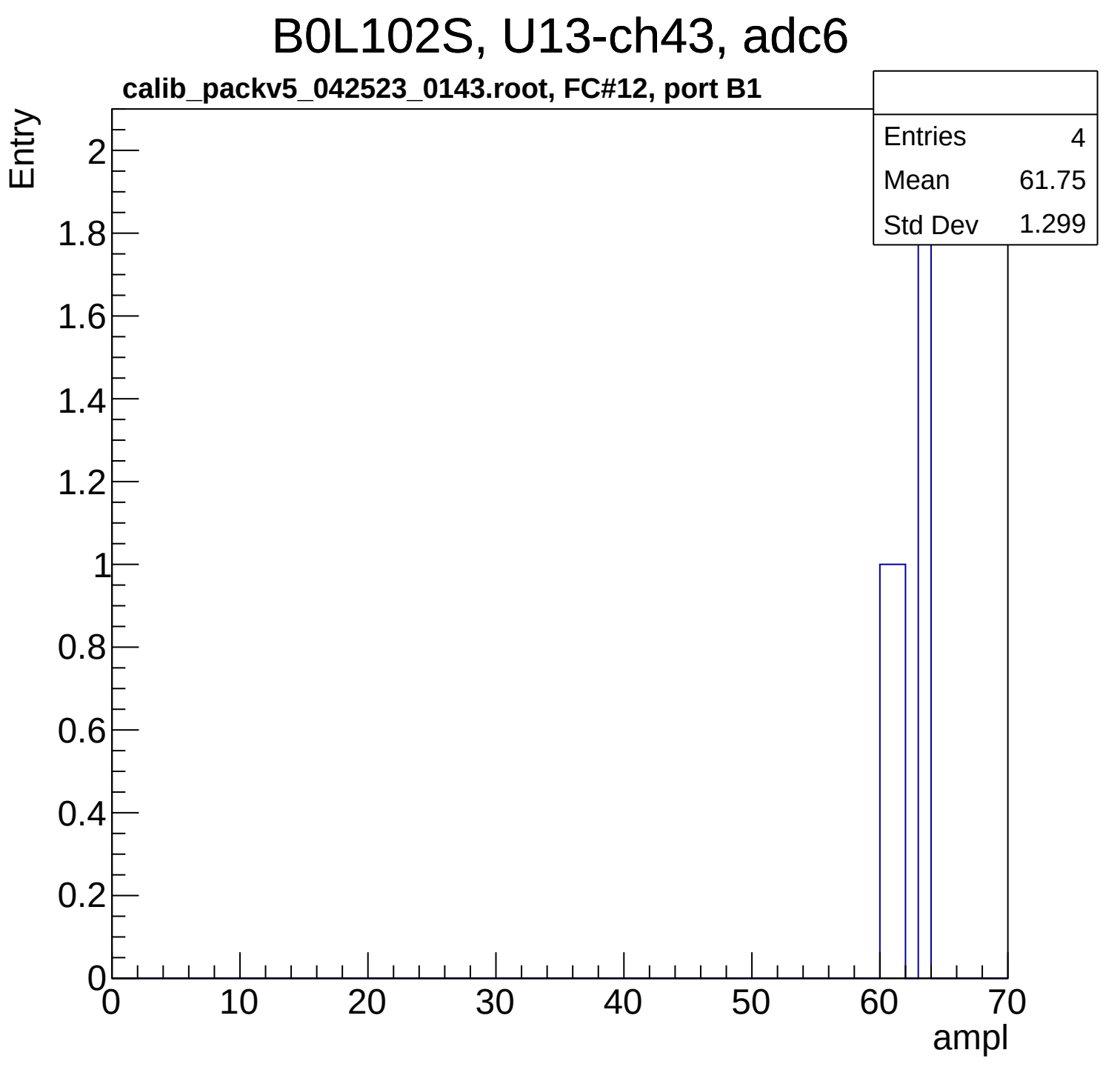

B0L102S, U13-ch43, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.1
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch43, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 54.33
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch43, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 58.91
Std Dev 8.32
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch43, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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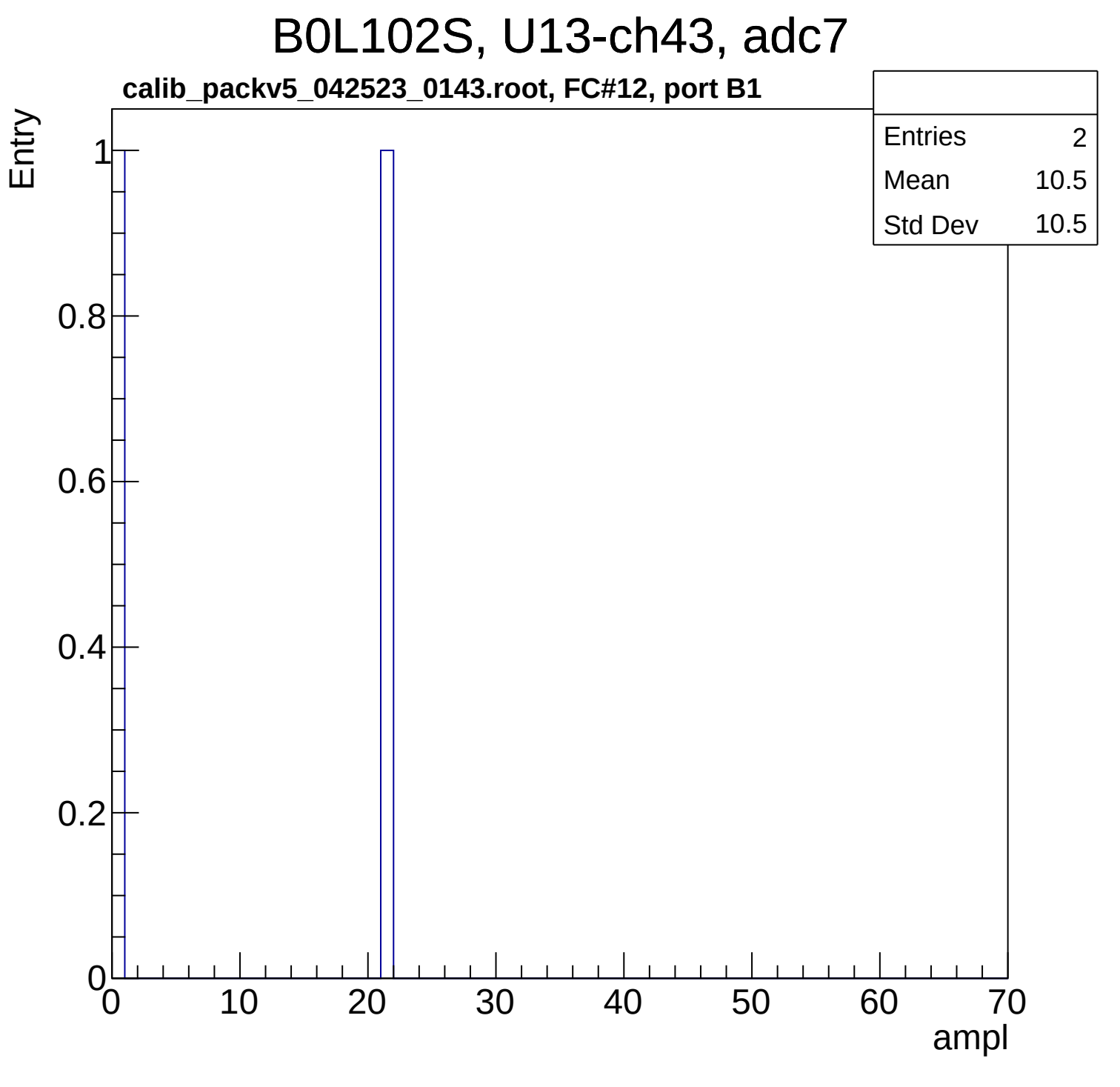
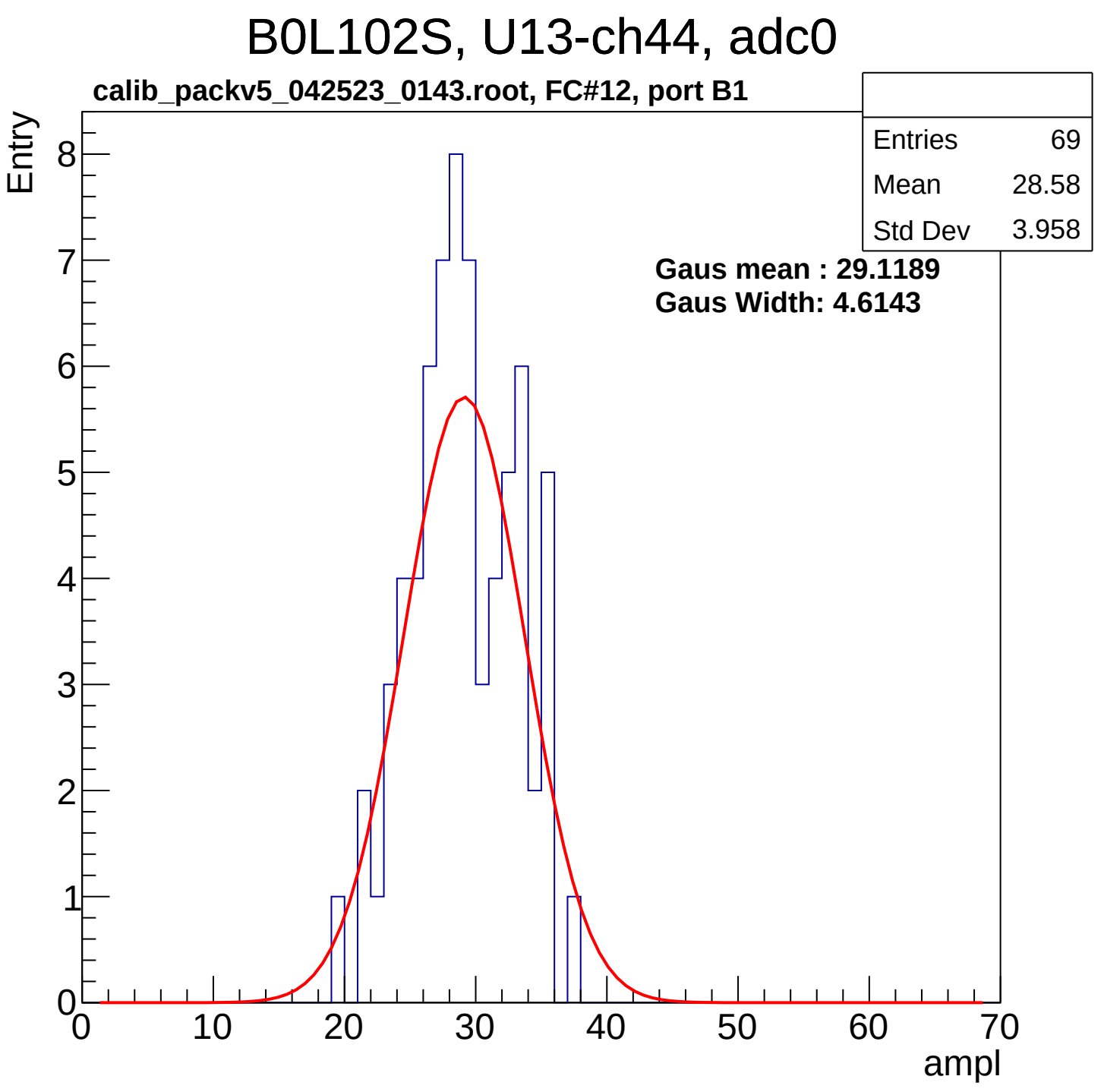
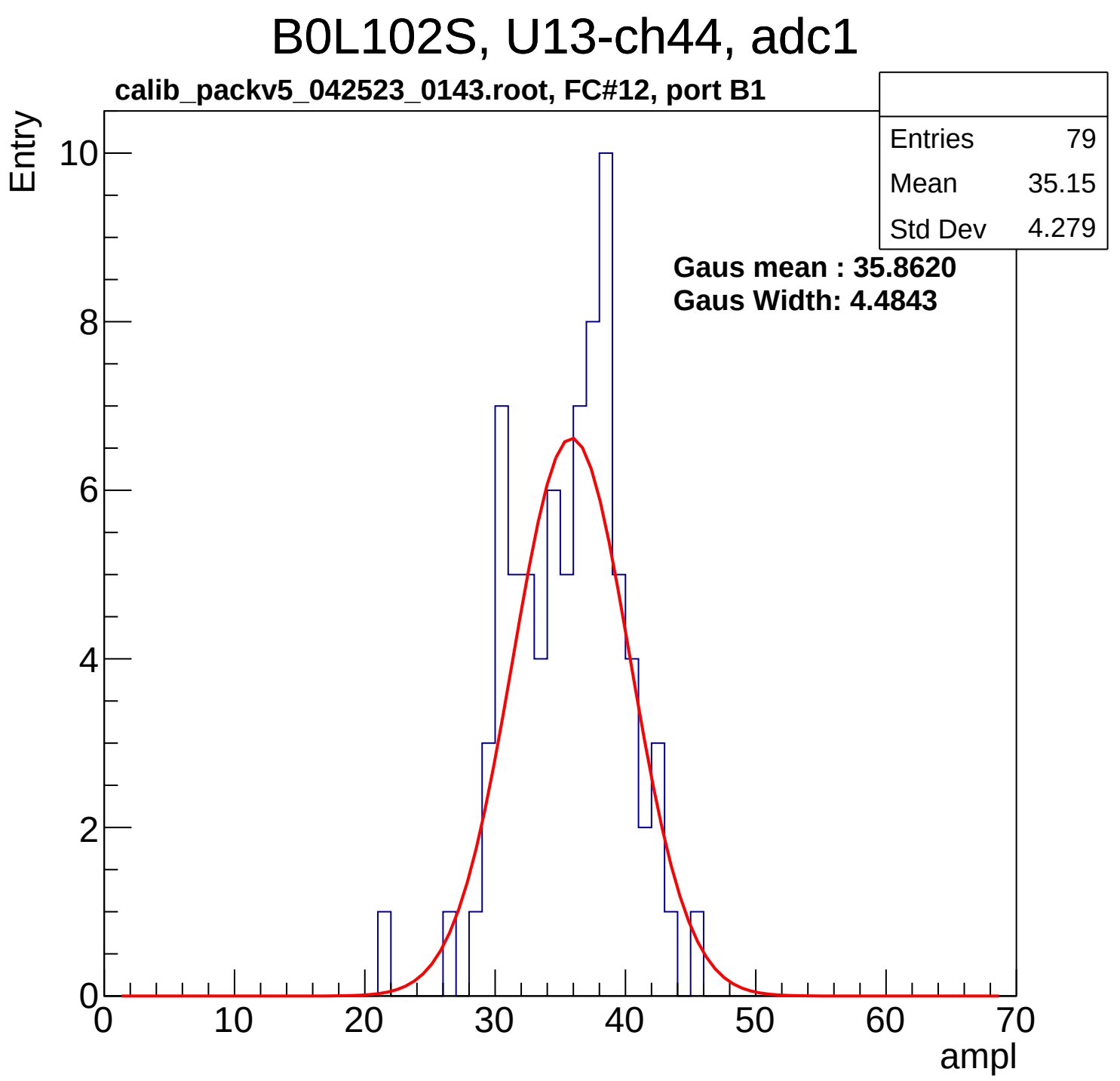

B0L102S, U13-ch43, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch44, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 28.58
Std Dev 3.958
Gaus mean : 29.1189
Gaus Width: 4.6143
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch44, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 35.15
Std Dev 4.279
Gaus mean : 35.8620
Gaus Width: 4.4843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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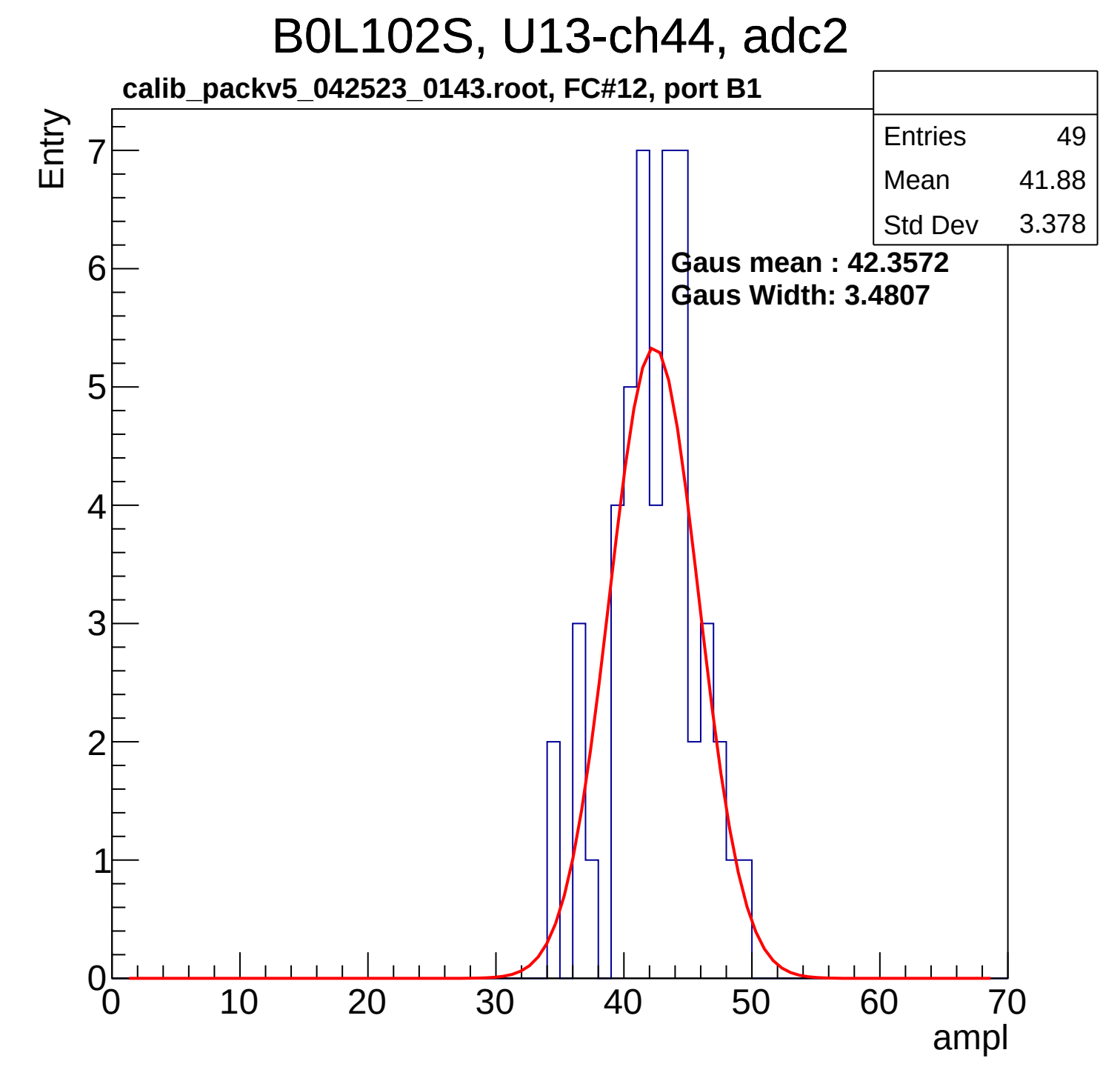
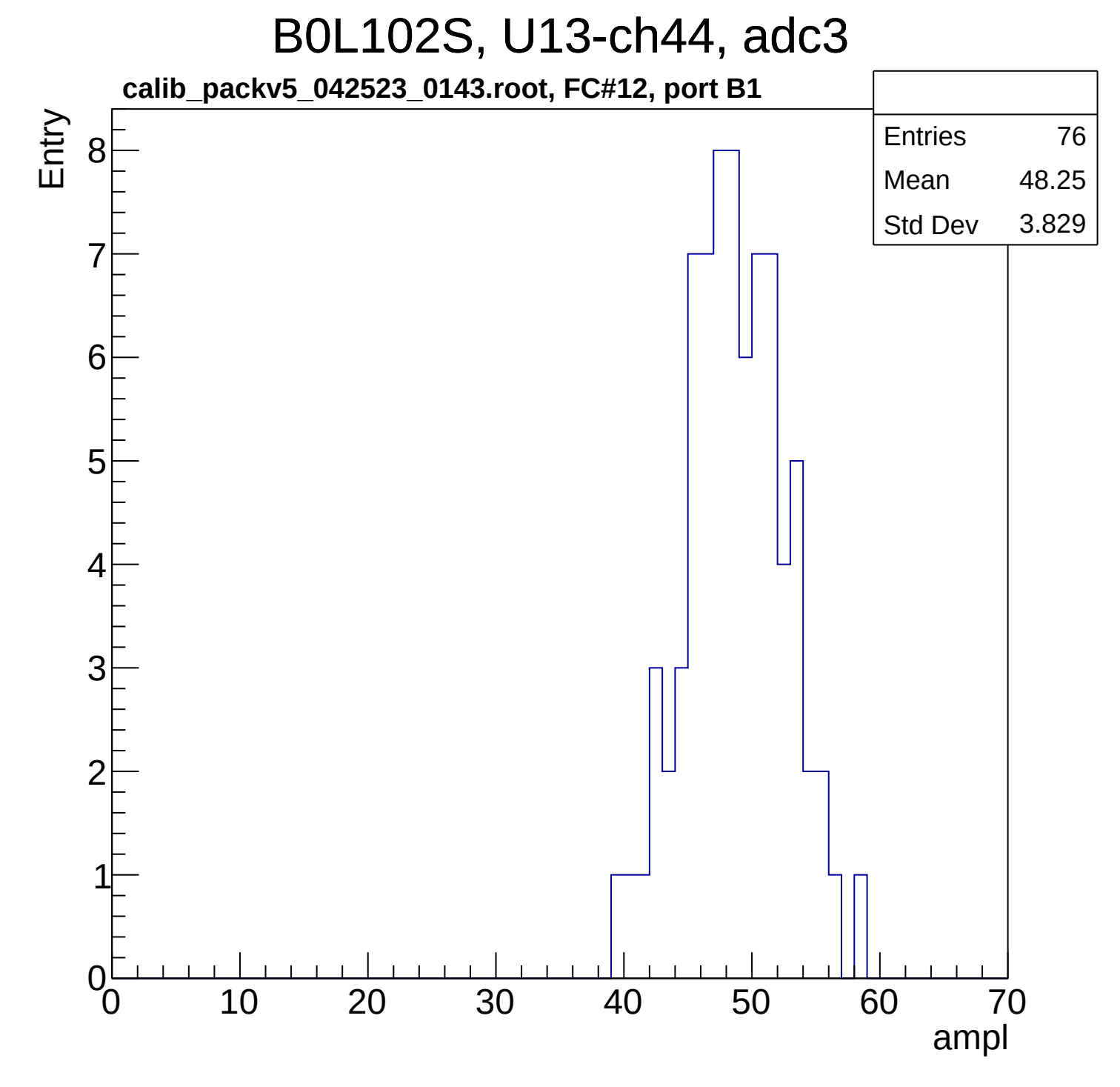

B0L102S, U13-ch44, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 41.88
Std Dev 3.378
Gaus mean : 42.3572
Gaus Width: 3.4807
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch44, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.25
Std Dev 3.829
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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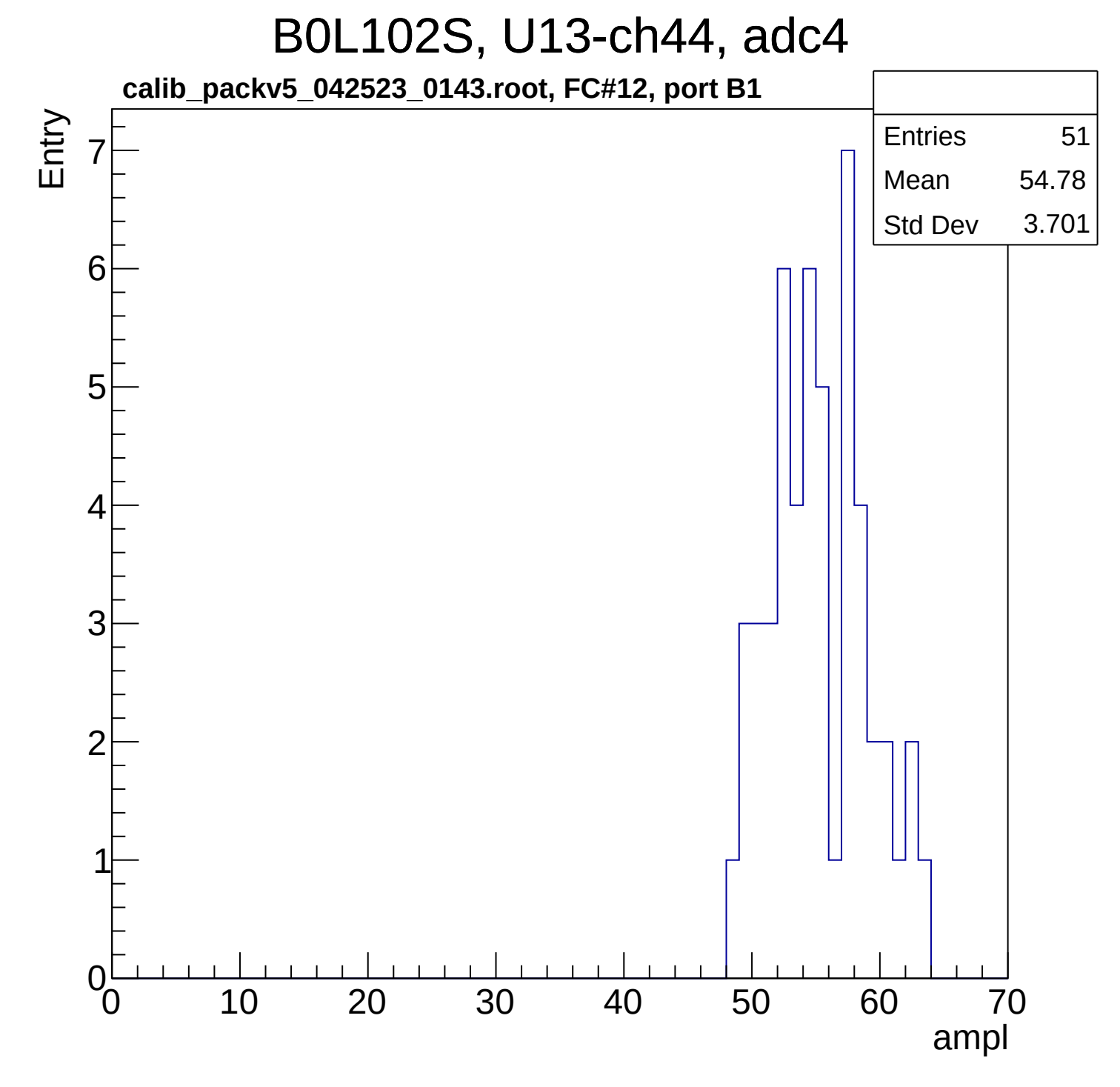
B0L102S, U13-ch44, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 54.78
Std Dev 3.701
Entry
ampl
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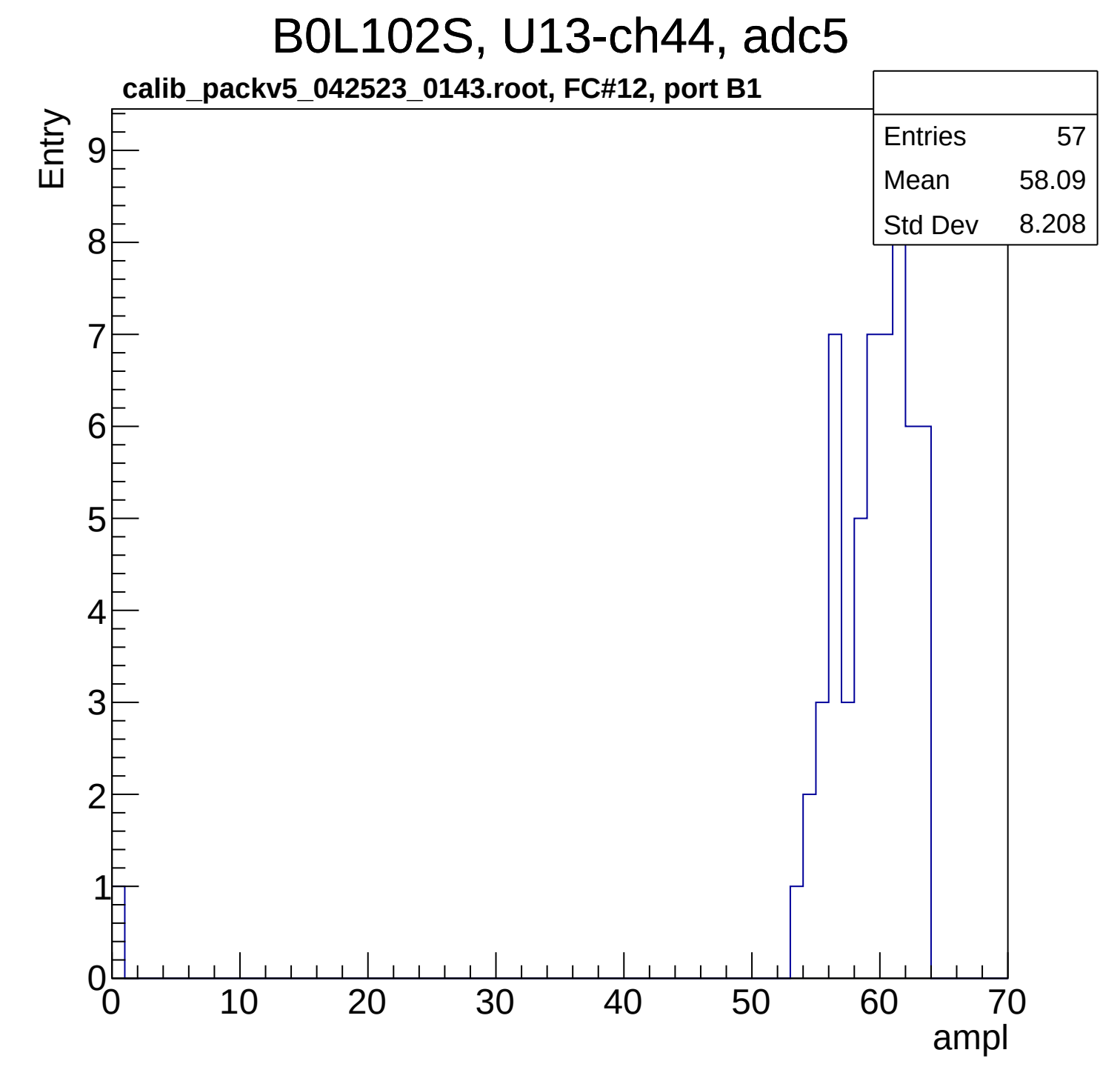
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch44, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 58.09
Std Dev 8.208
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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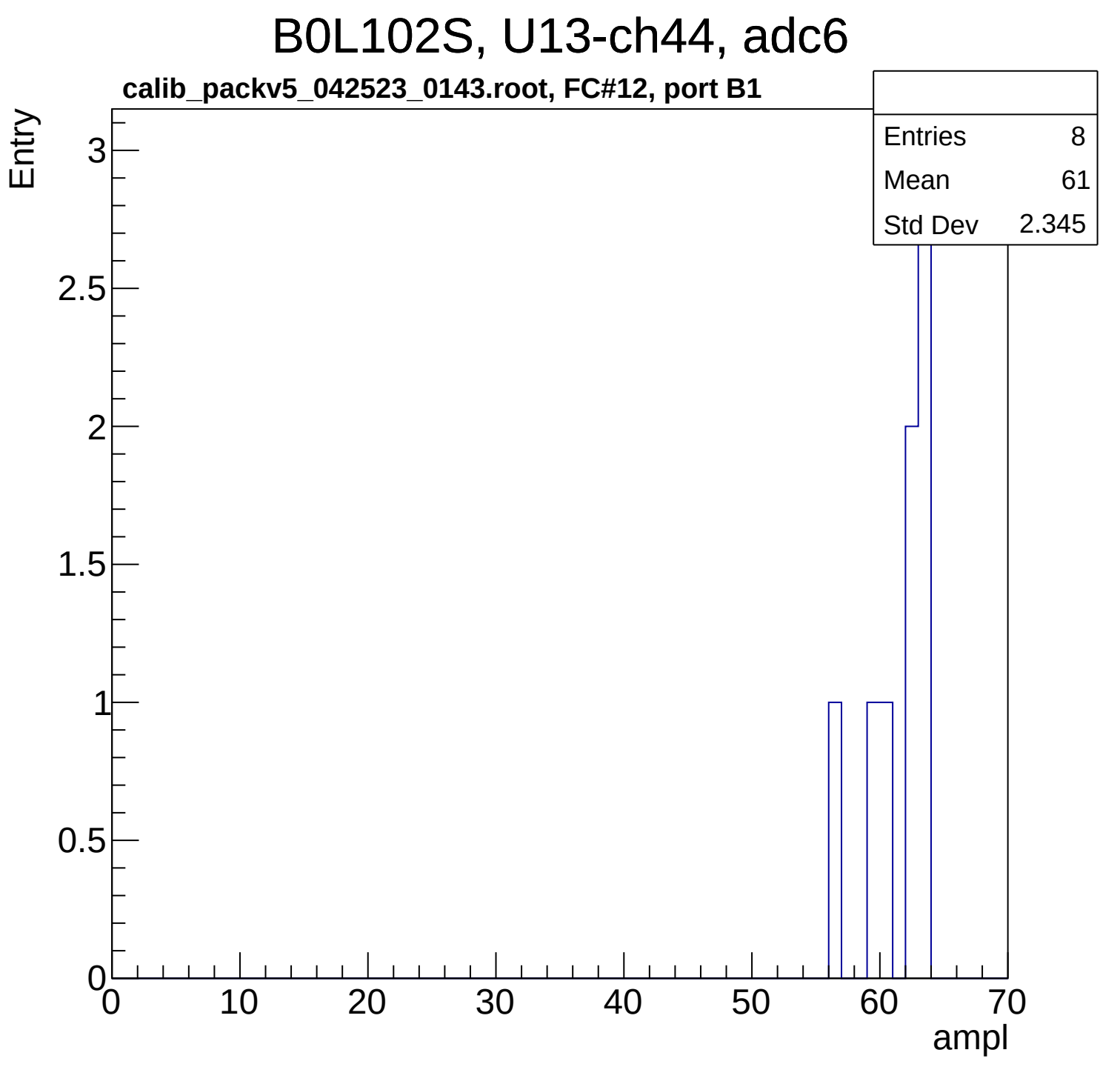
B0L102S, U13-ch44, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61
Std Dev 2.345
Entry
ampl
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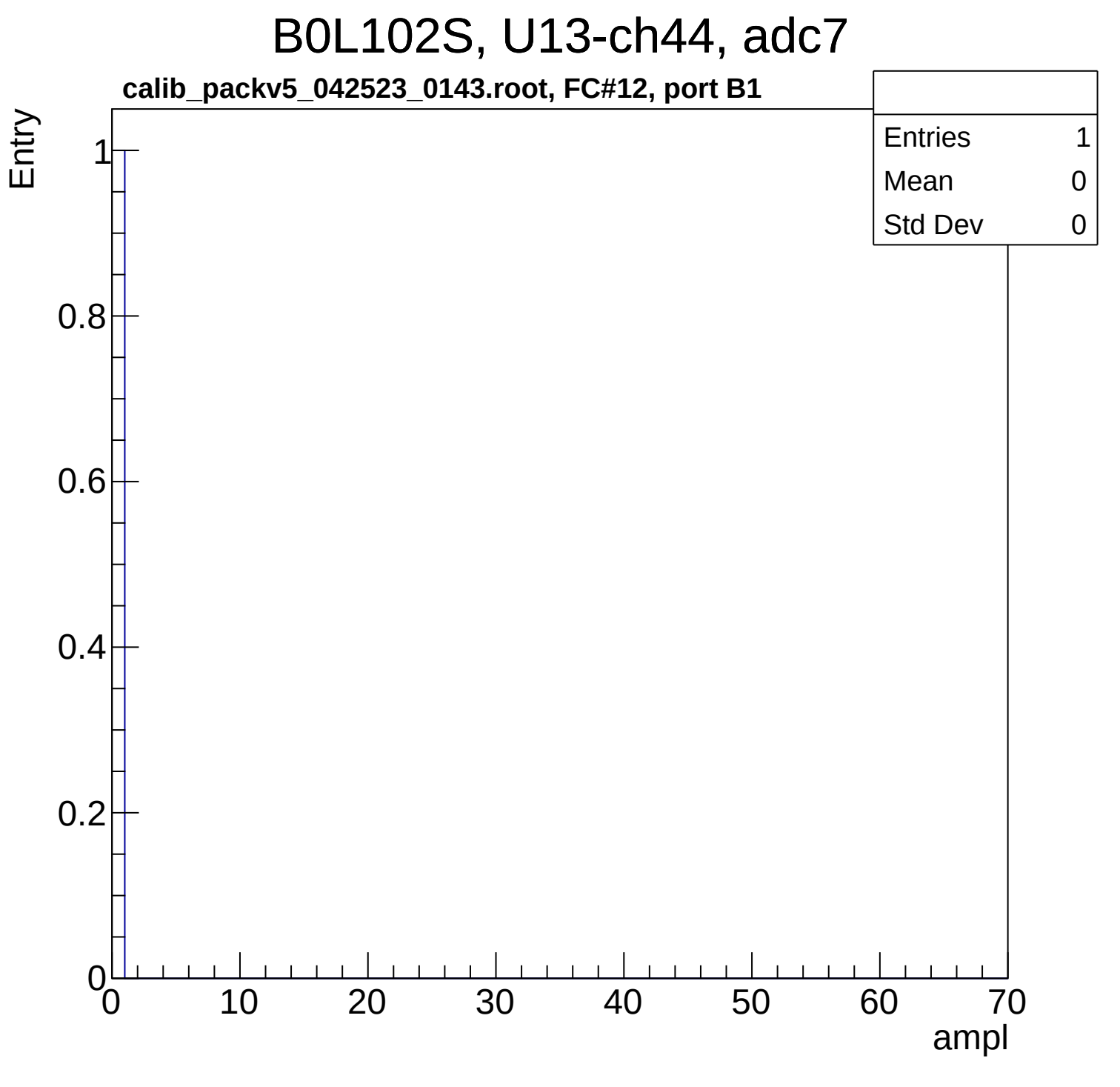
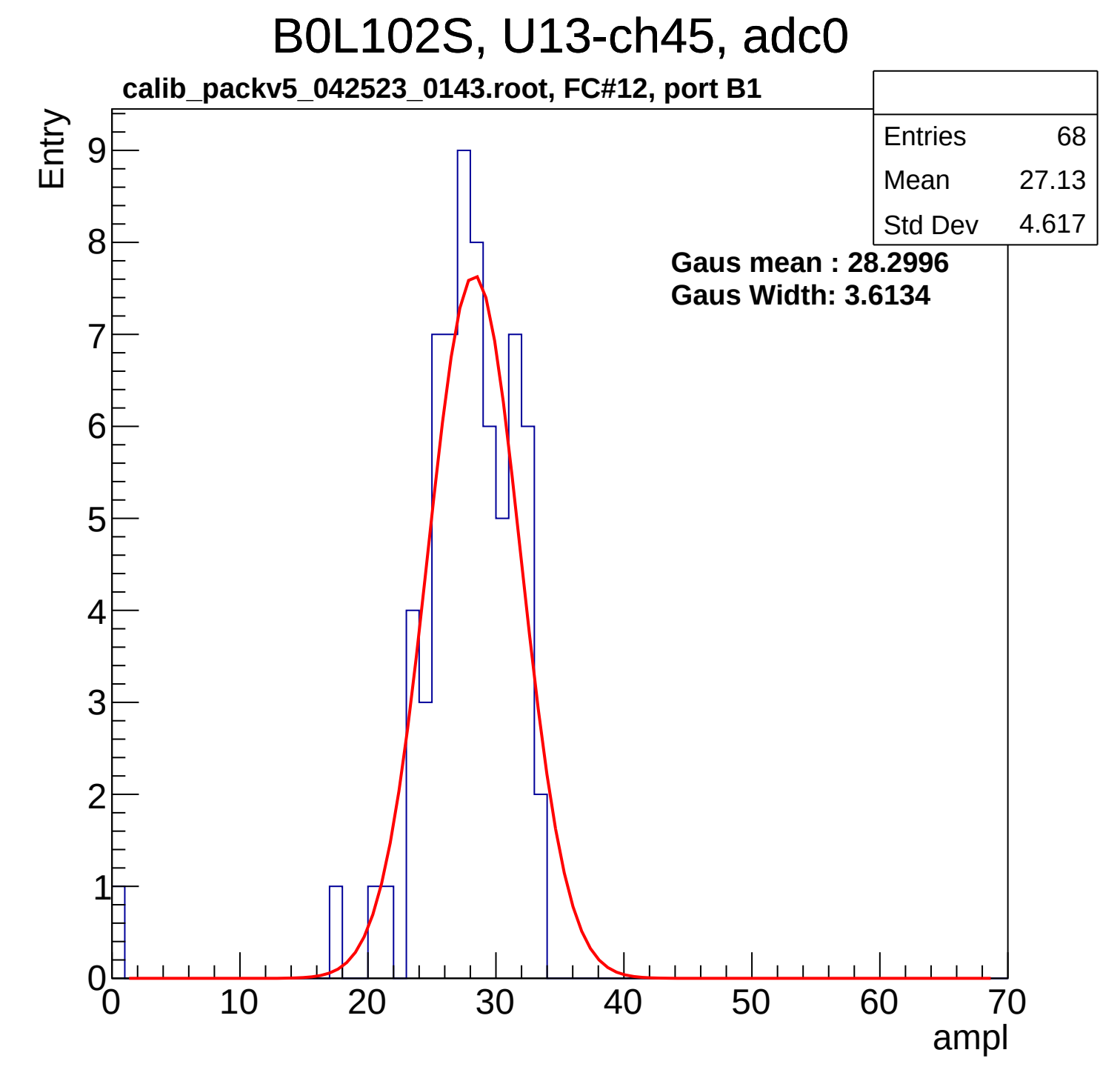
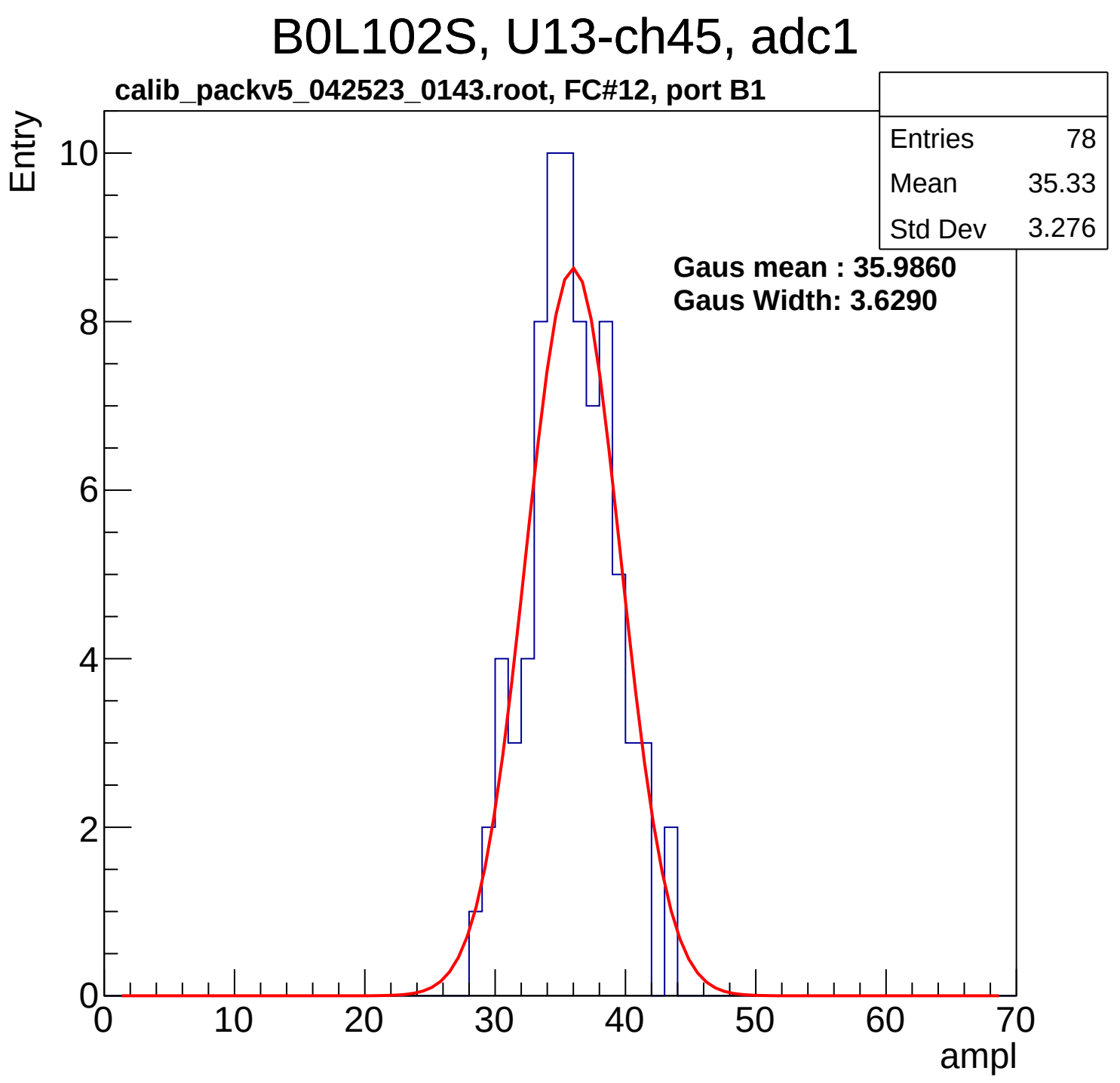
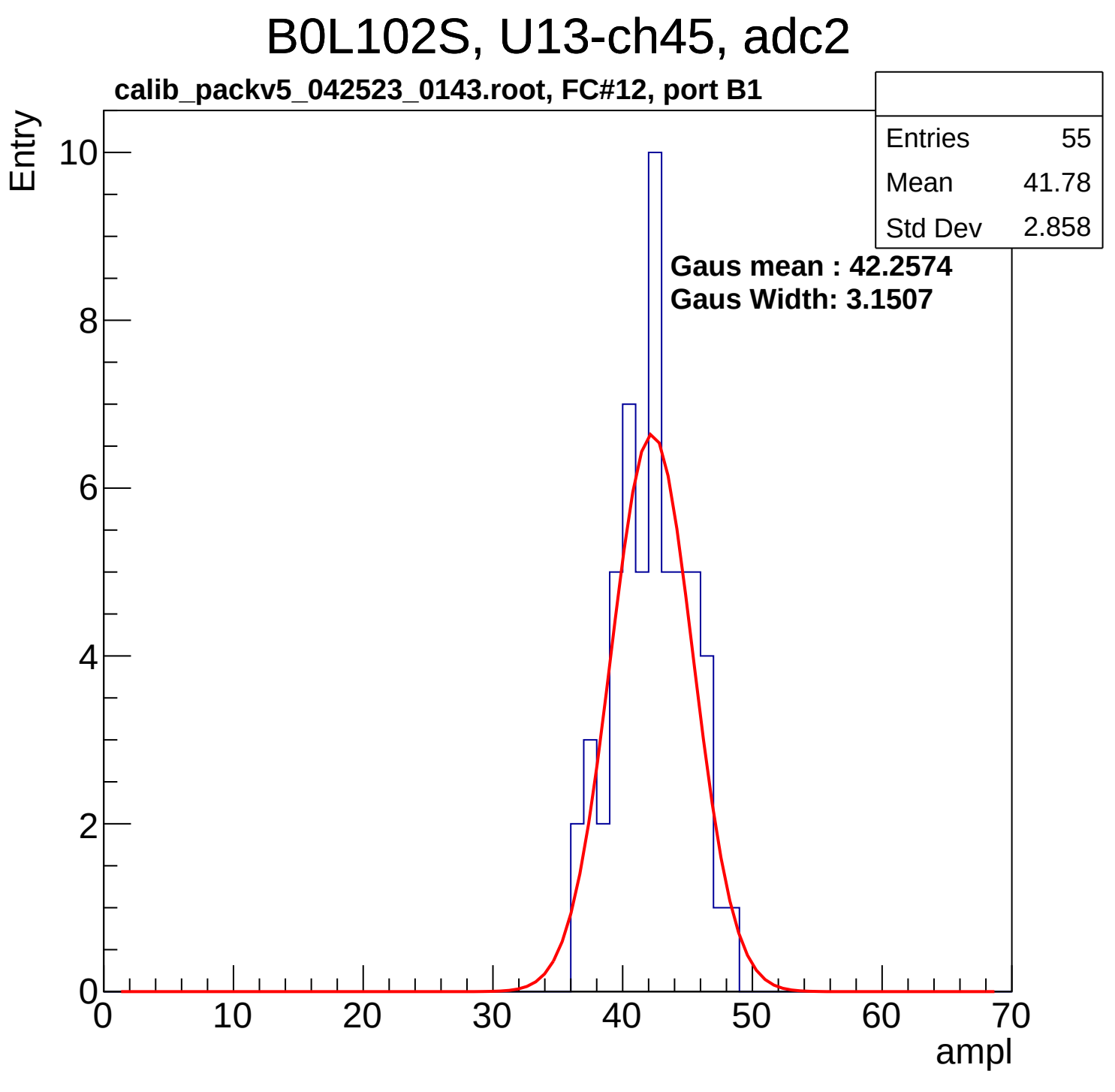
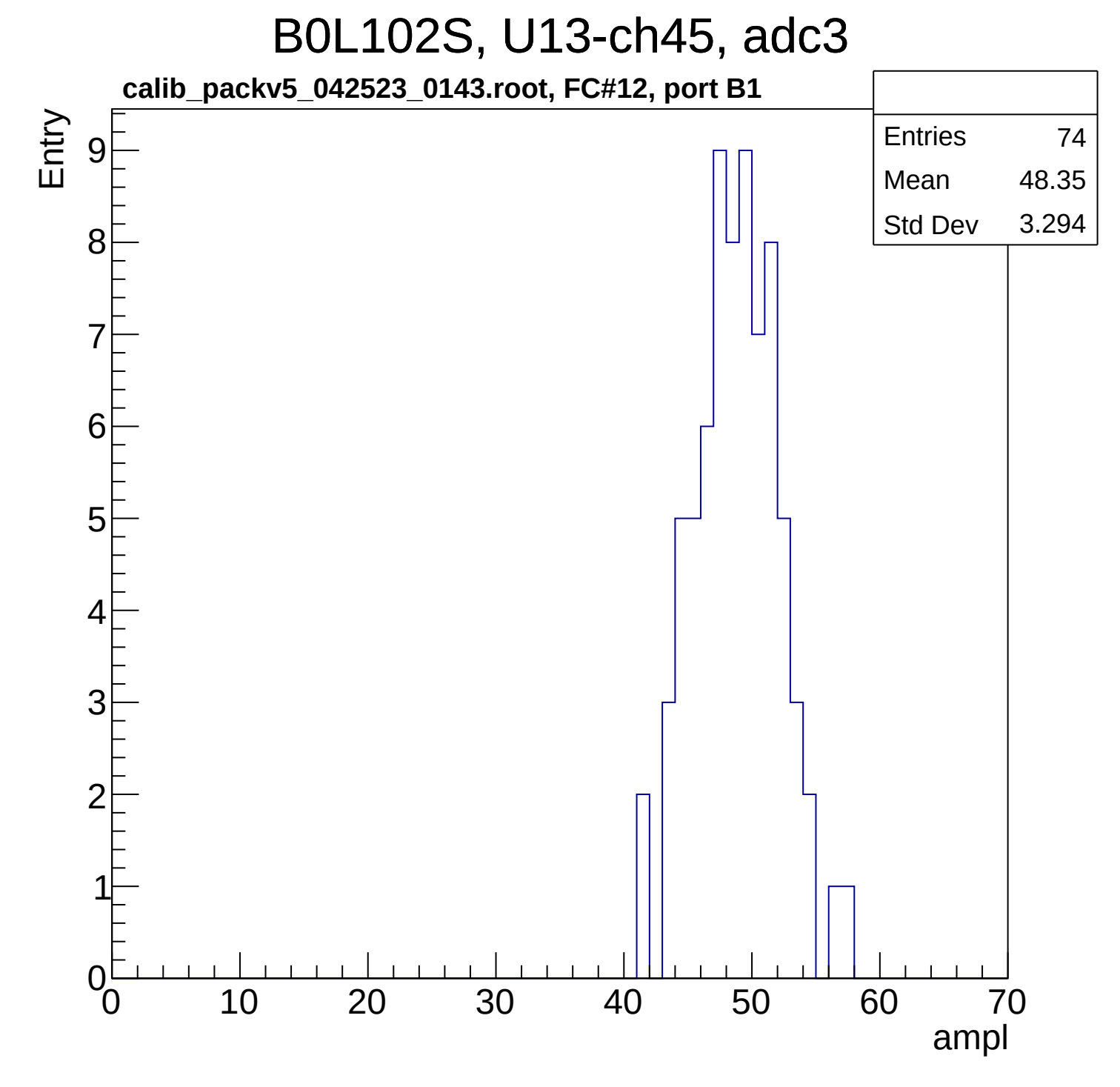
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch44, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch45, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 27.13
Std Dev 4.617
Gaus mean : 28.2996
Gaus Width: 3.6134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch45, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 35.33
Std Dev 3.276
Gaus mean : 35.9860
Gaus Width: 3.6290
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch45, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 41.78
Std Dev 2.858
Gaus mean : 42.2574
Gaus Width: 3.1507
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch45, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 48.35
Std Dev 3.294
Entry
ampl
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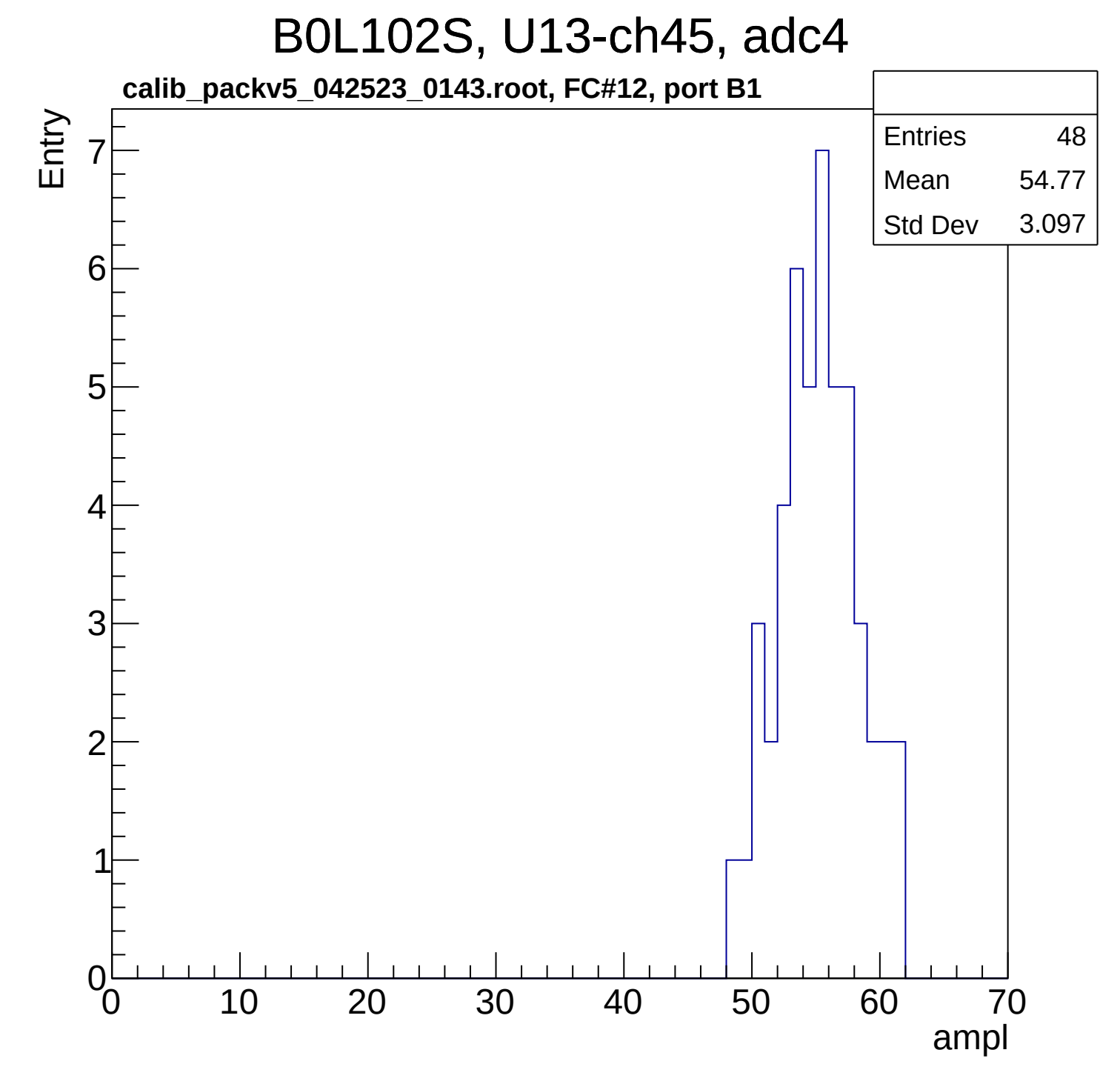
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch45, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 54.77
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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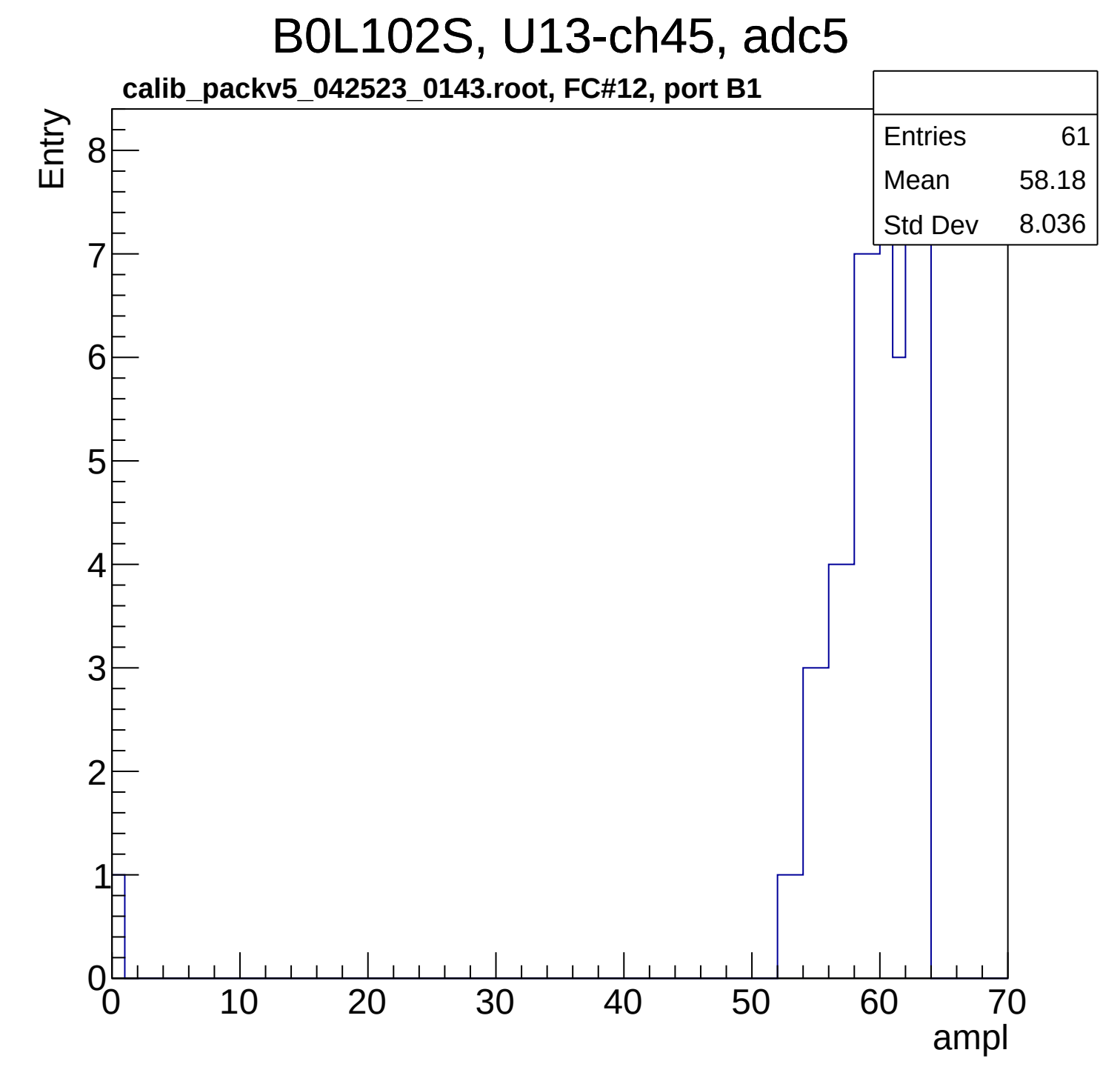
60
70


B0L102S, U13-ch45, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 58.18
Std Dev 8.036
Entry
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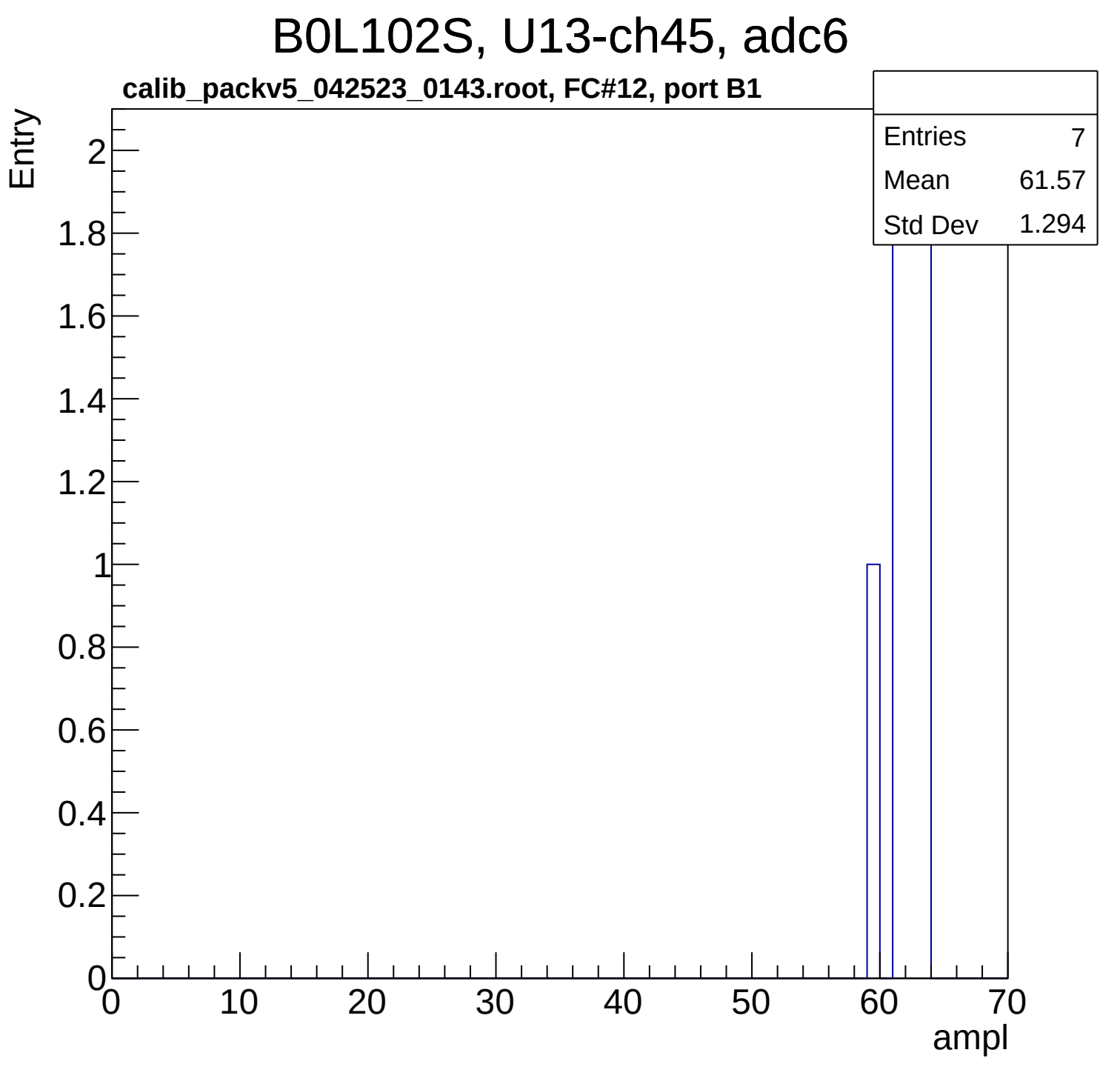
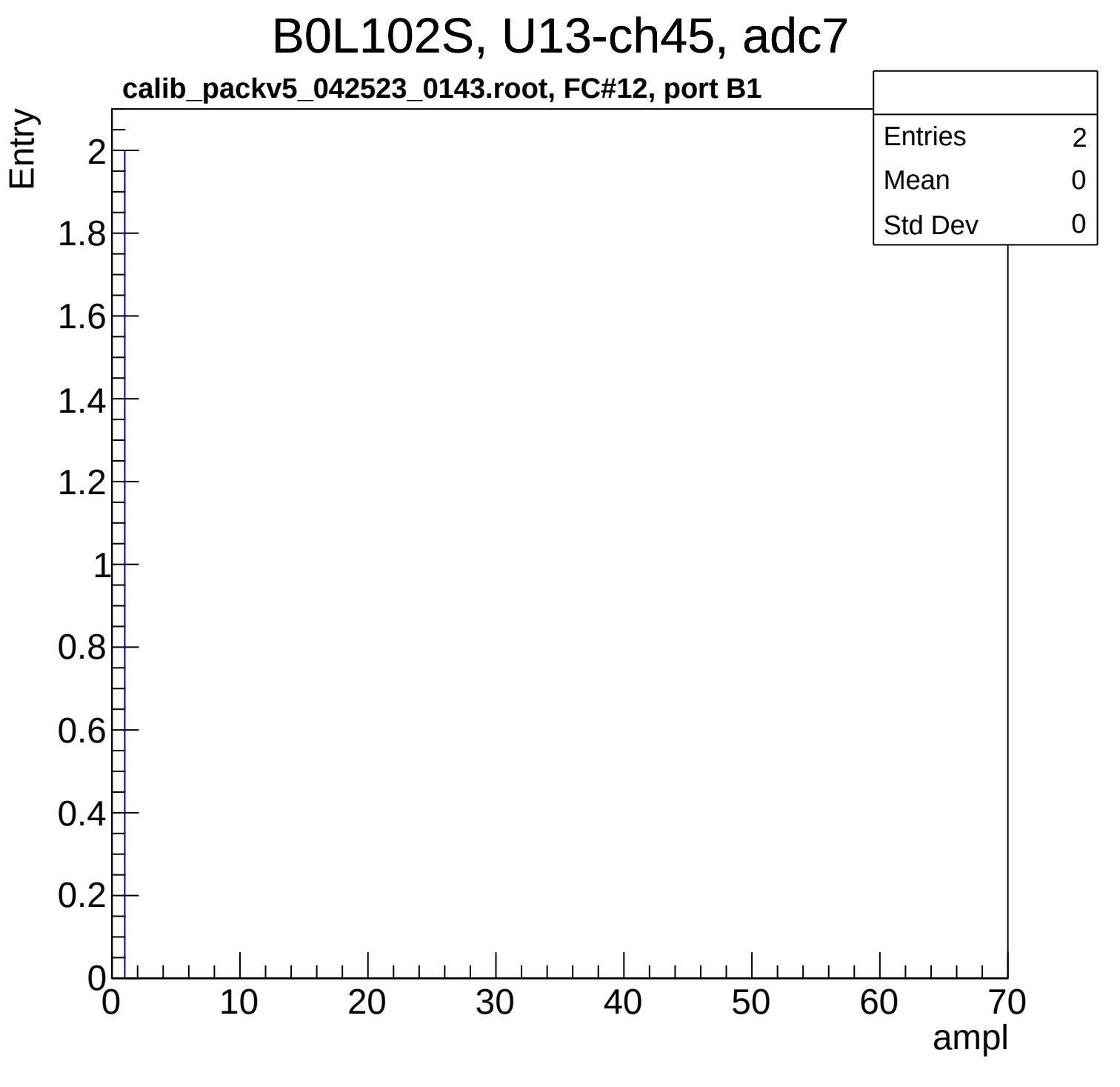
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch45, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.57
Std Dev 1.294
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch45, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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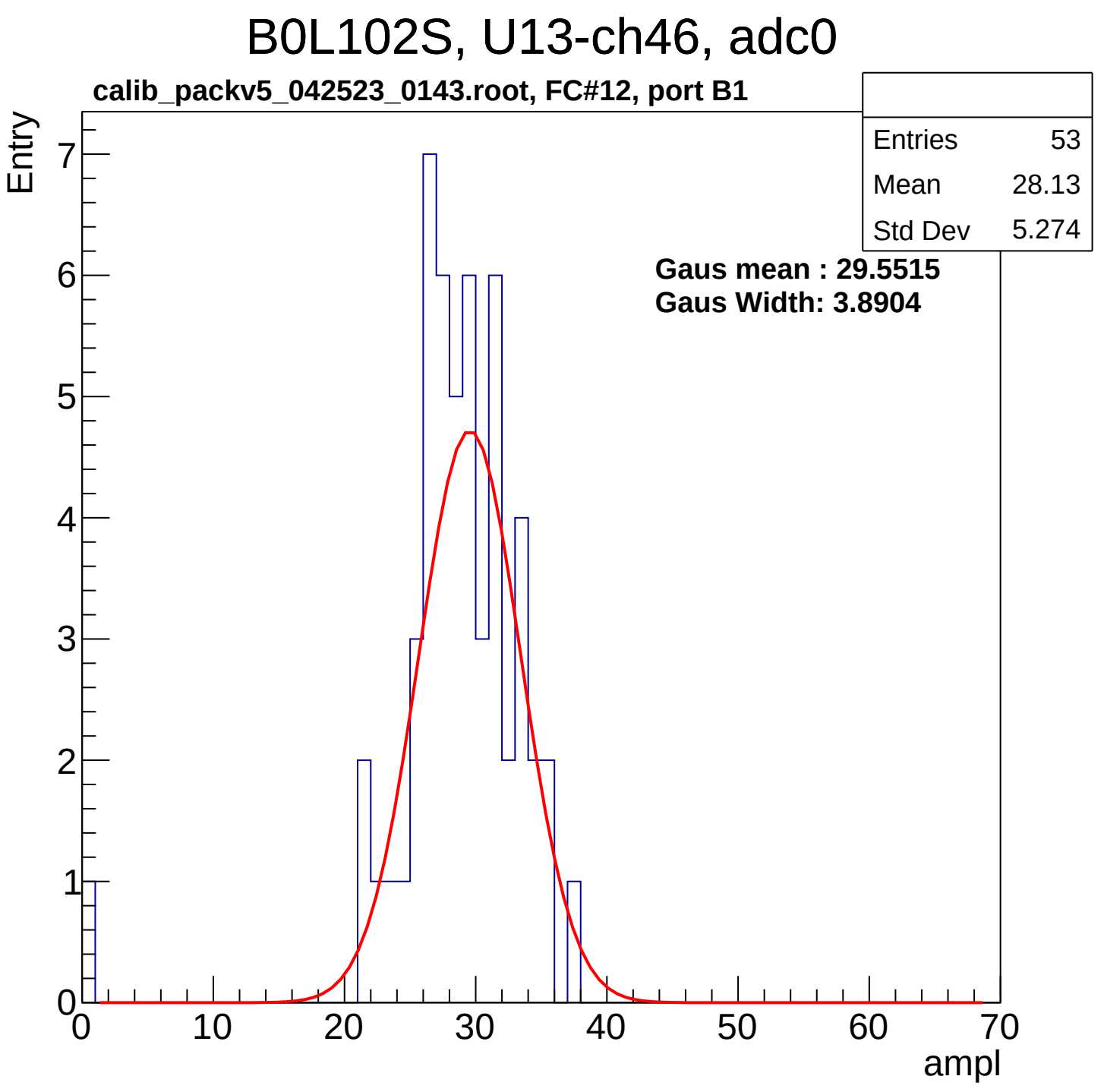
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch46, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 28.13
Std Dev 5.274
Gaus mean : 29.5515
Gaus Width: 3.8904
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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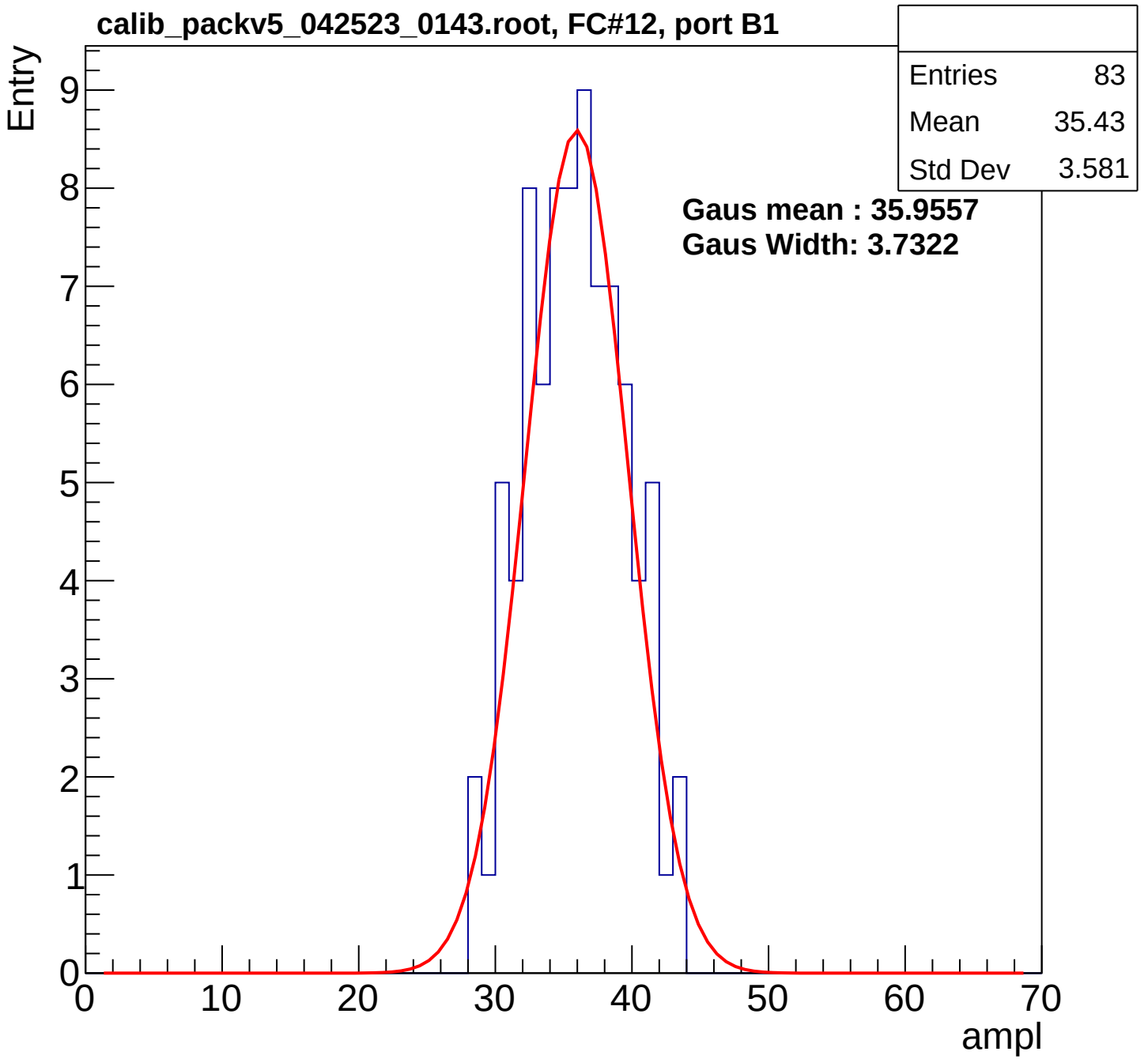
40
50
60
70


B0L102S, U13-ch46, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 35.43
Std Dev 3.581
Gaus mean : 35.9557
Gaus Width: 3.7322
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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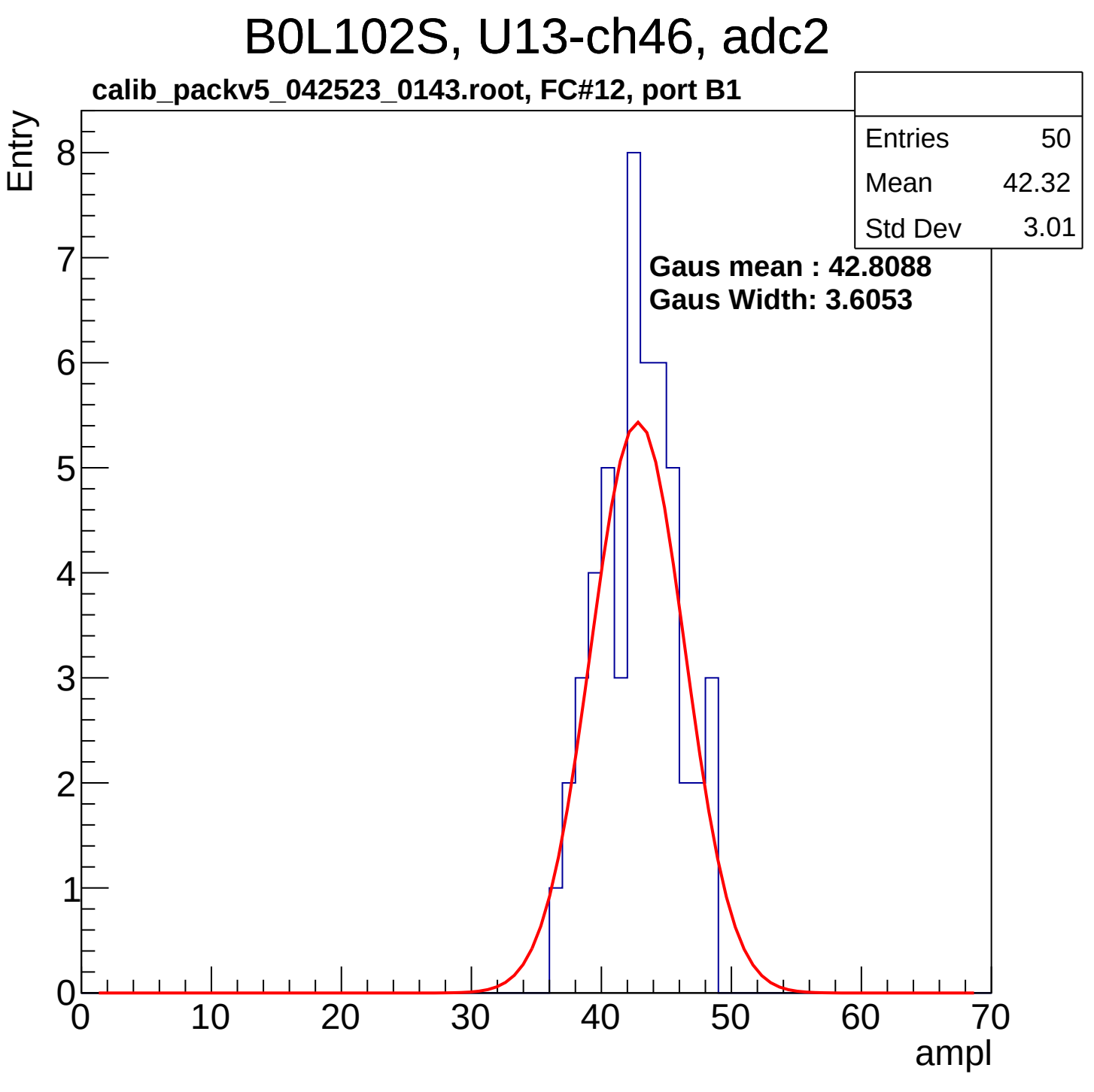
6
7
8
9


B0L102S, U13-ch46, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 42.32
Std Dev 3.01
Gaus mean : 42.8088
Gaus Width: 3.6053
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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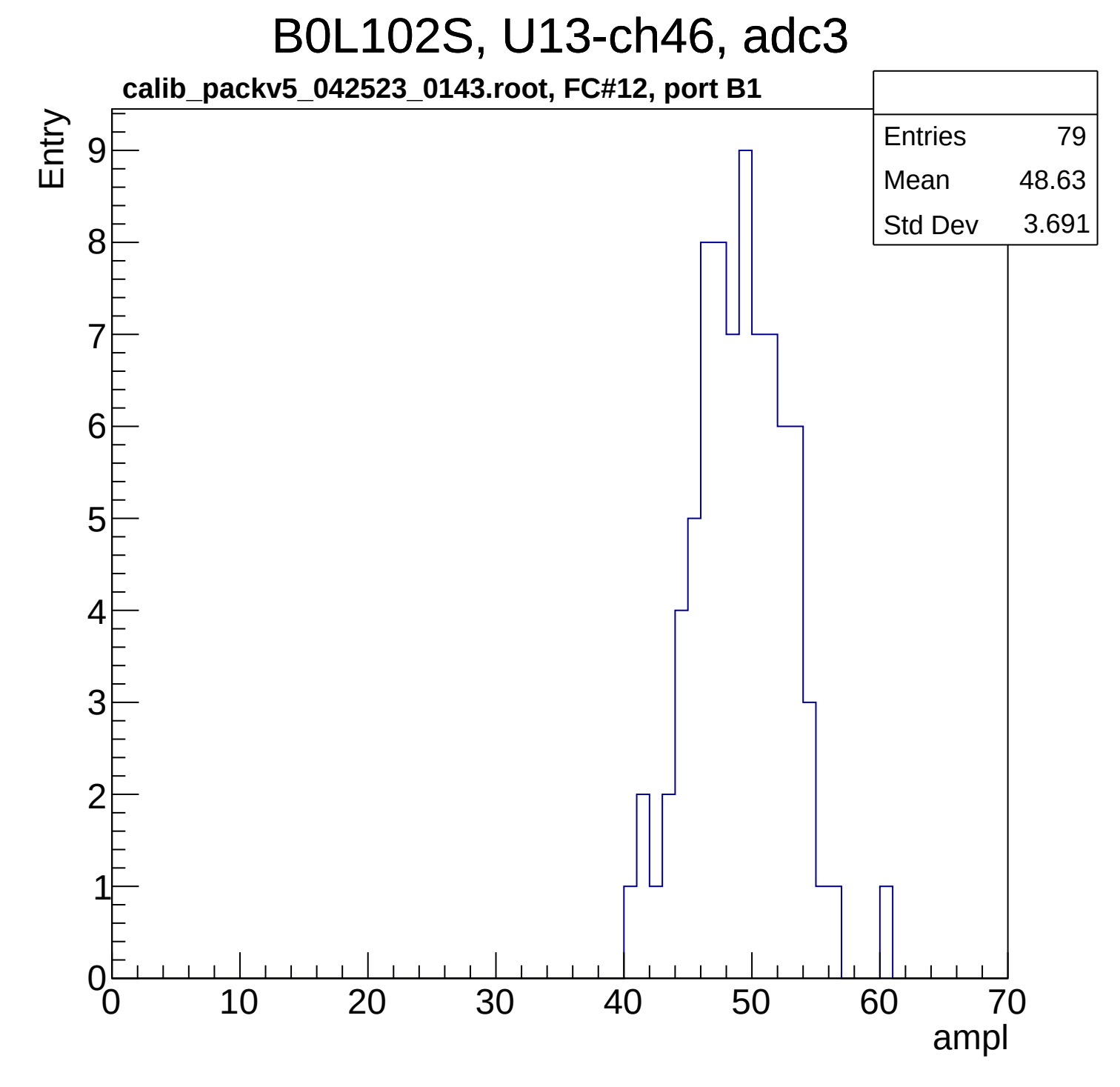
B0L102S, U13-ch46, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 48.63
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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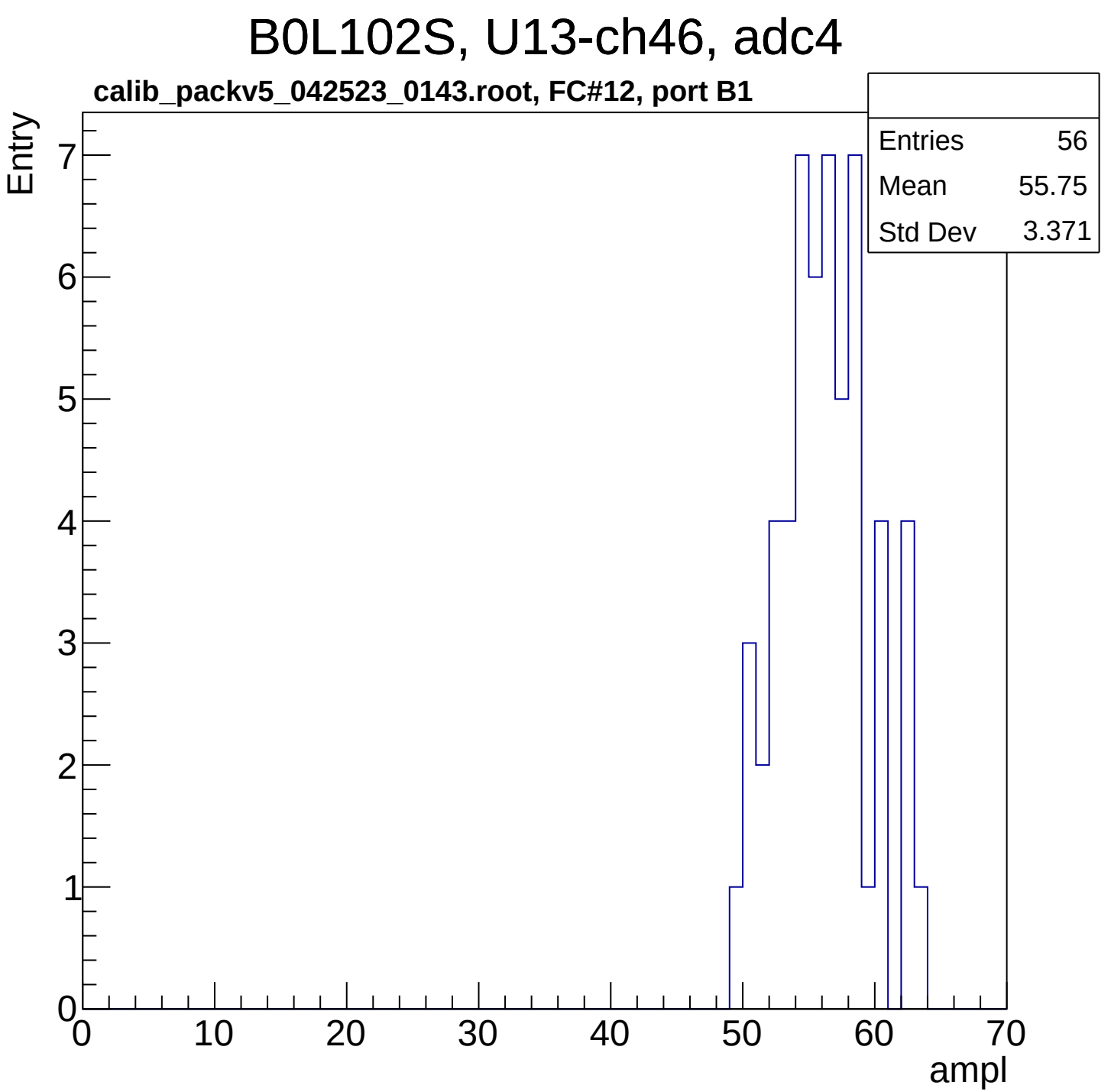
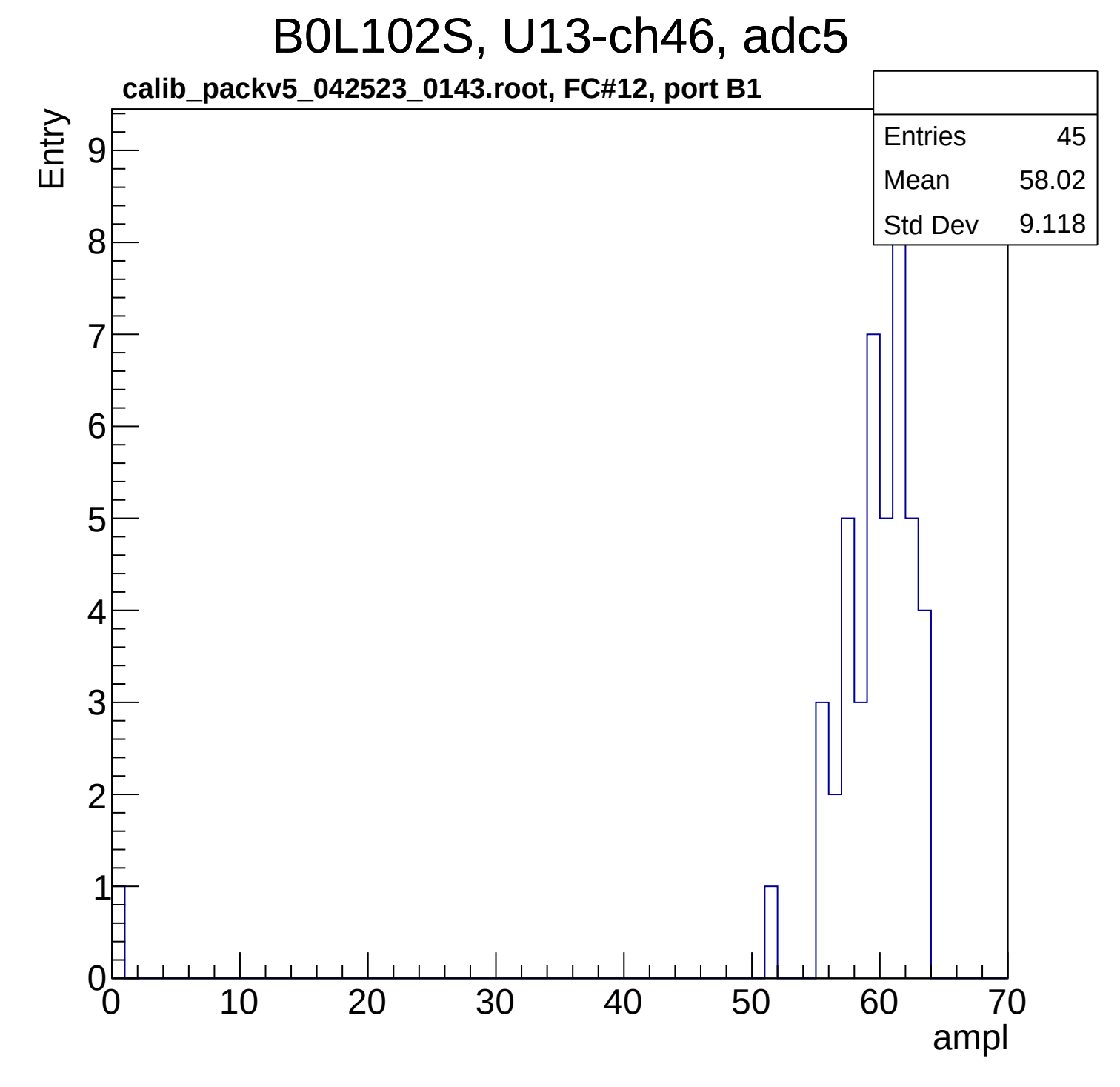

B0L102S, U13-ch46, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.75
Std Dev 3.371
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch46, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.02
Std Dev 9.118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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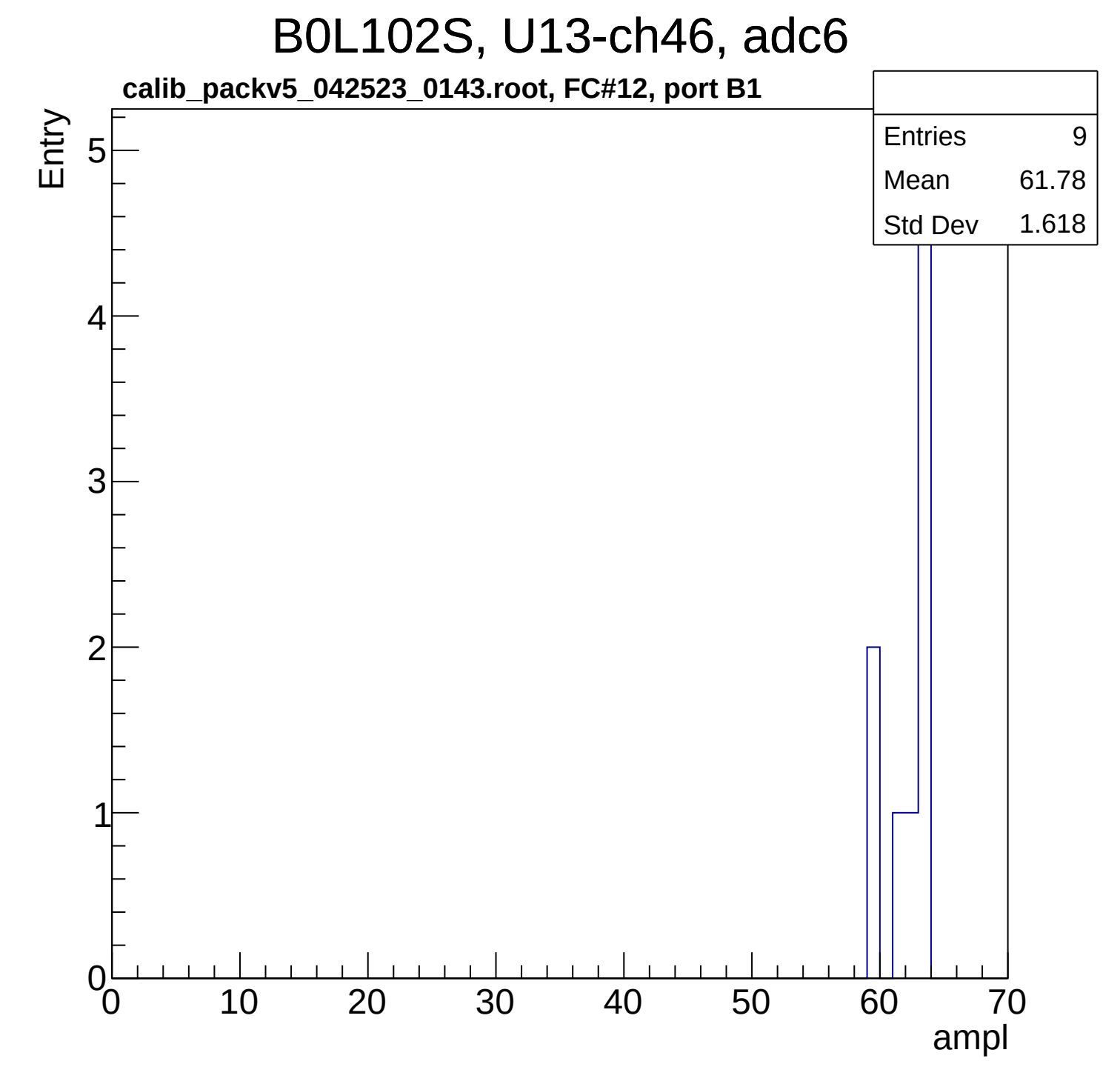
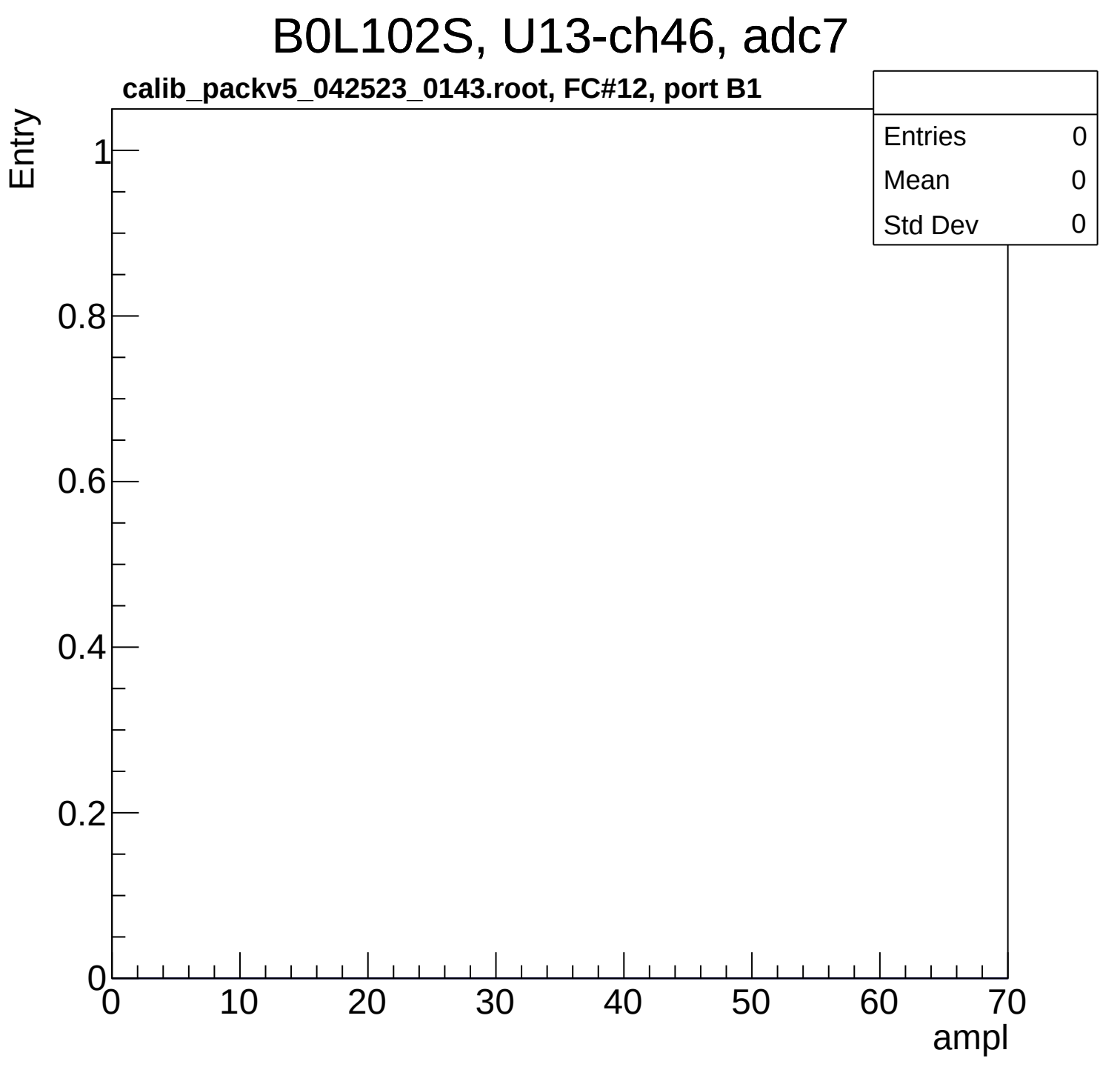
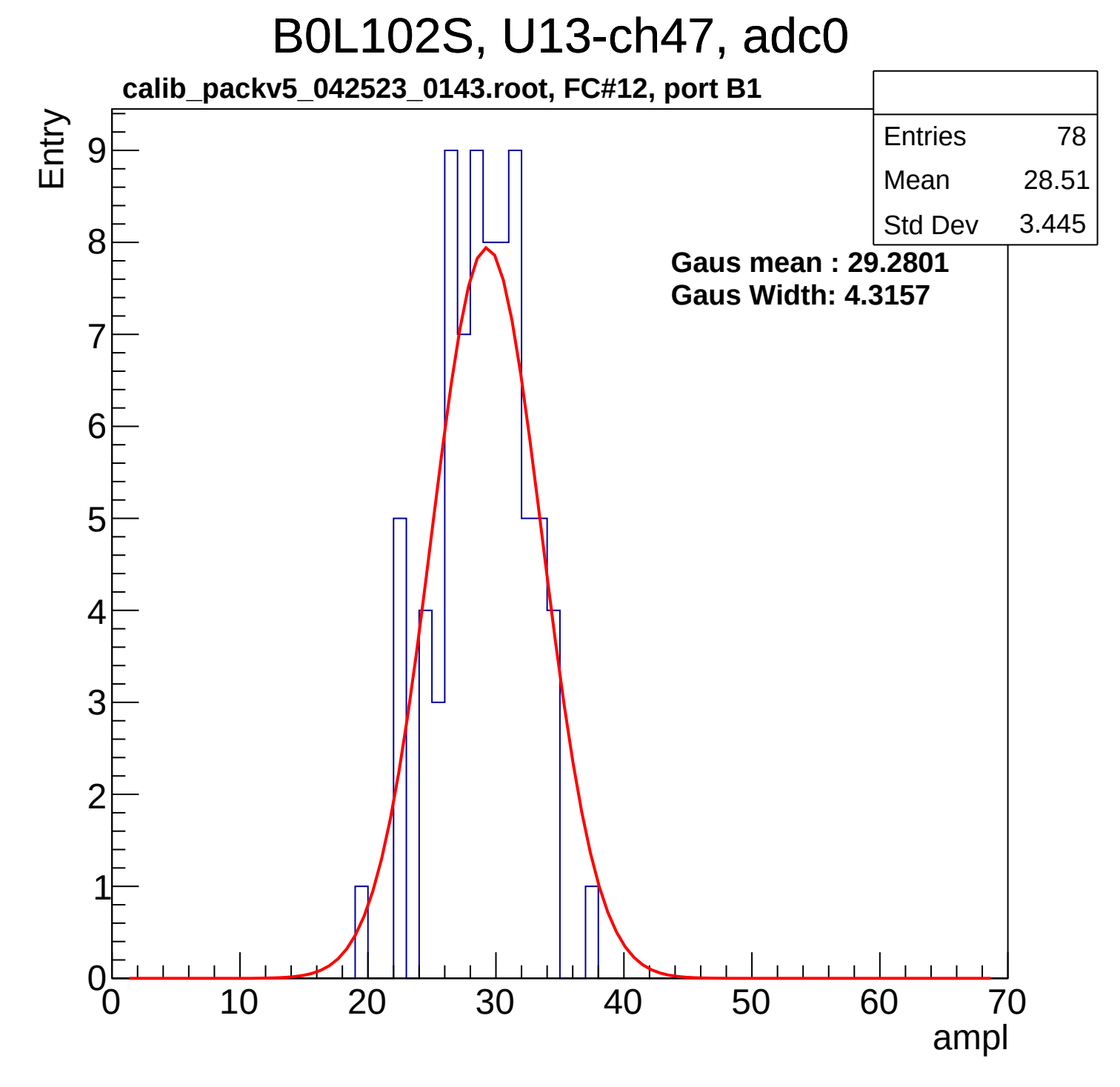

B0L102S, U13-ch46, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.78
Std Dev 1.618
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U13-ch46, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch47, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.51
Std Dev 3.445
Gaus mean : 29.2801
Gaus Width: 4.3157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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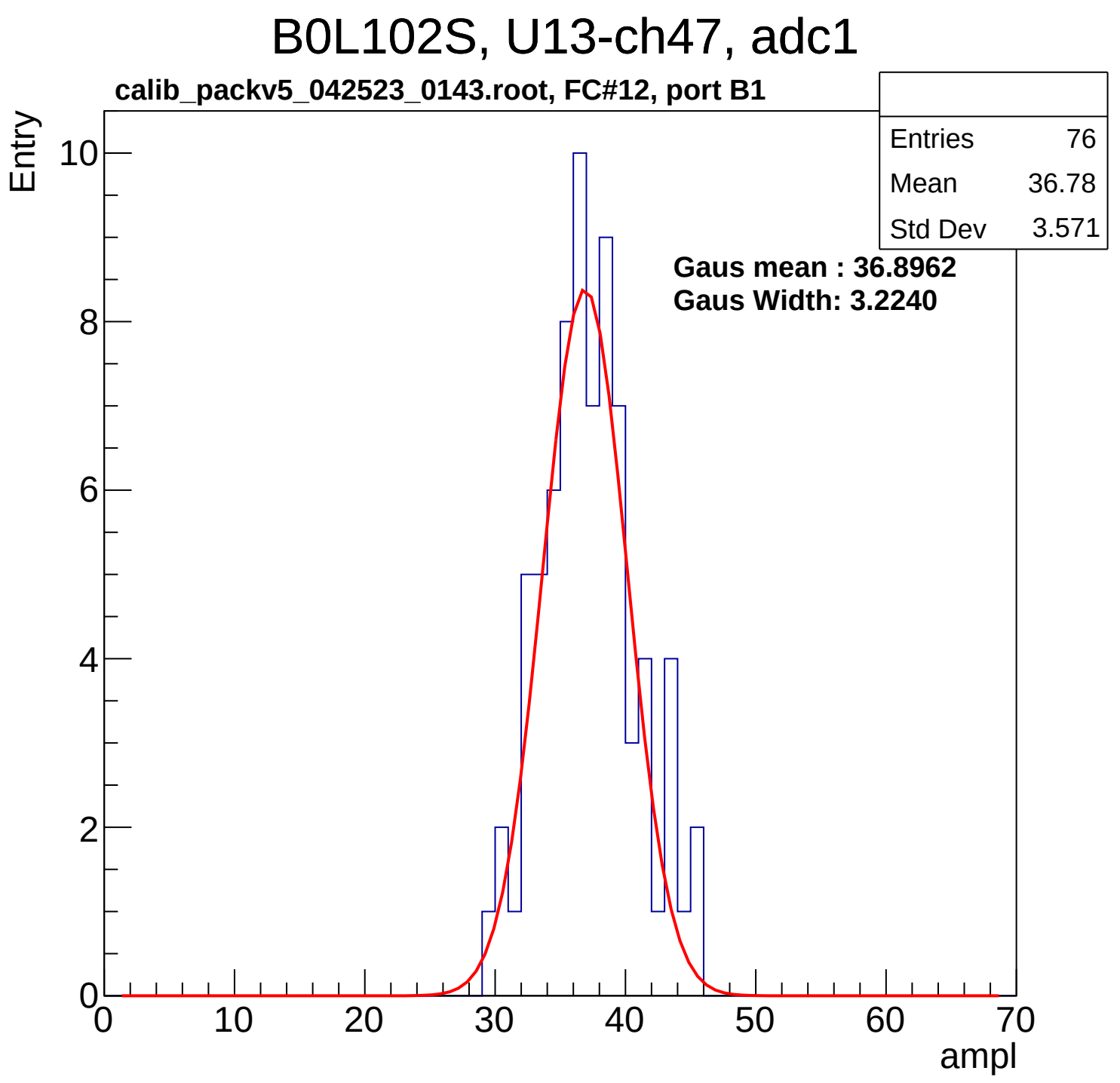
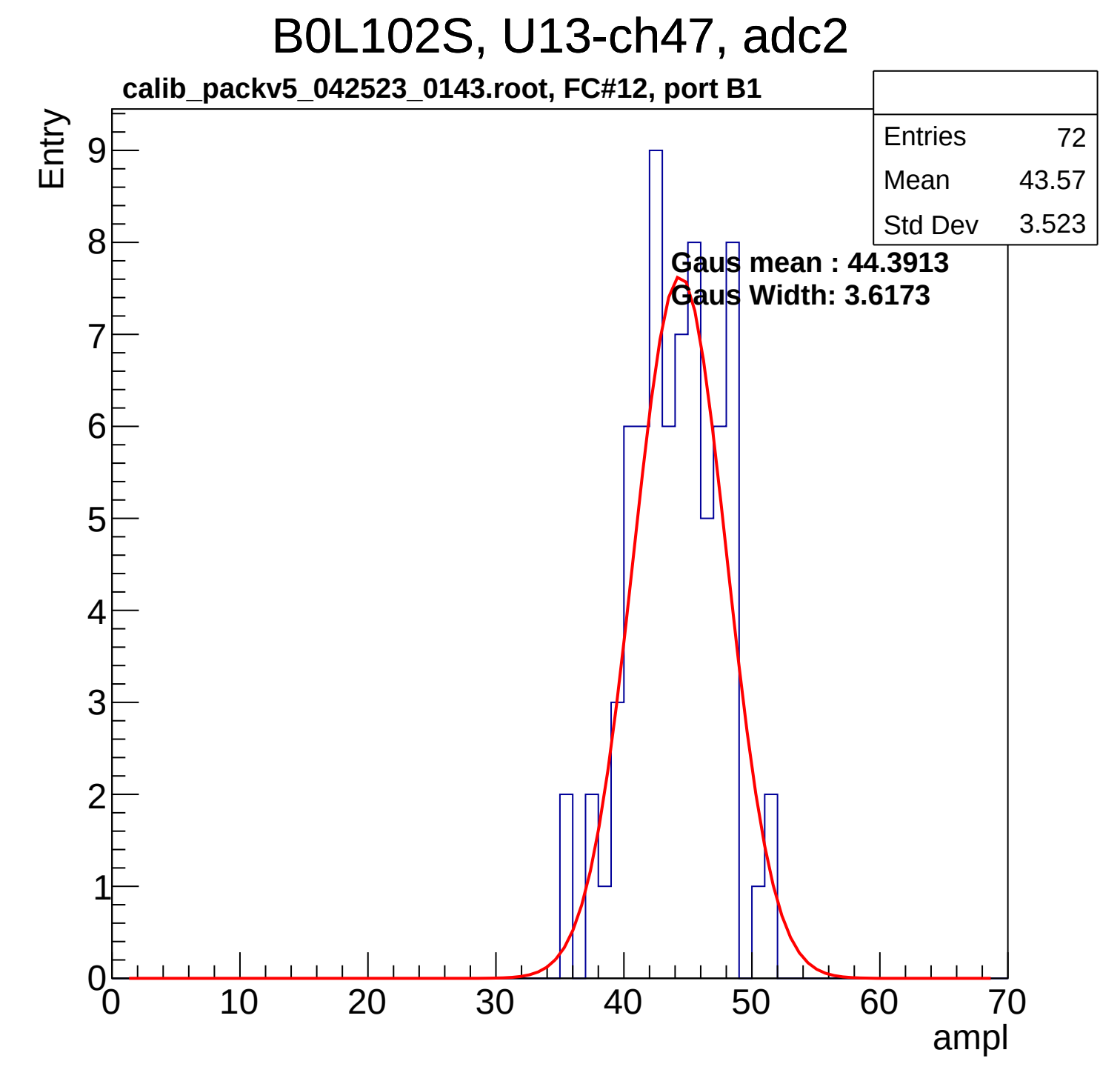
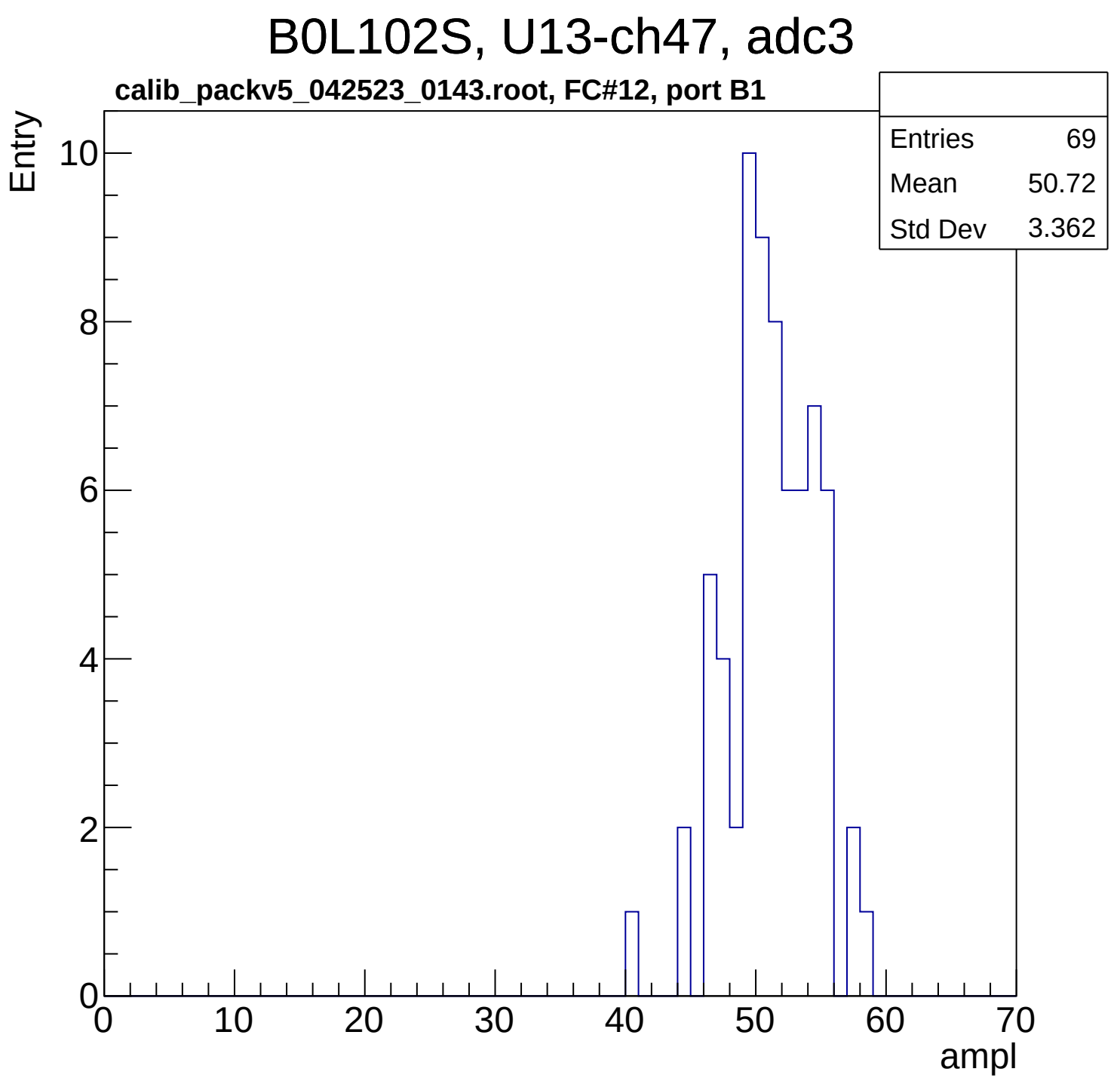
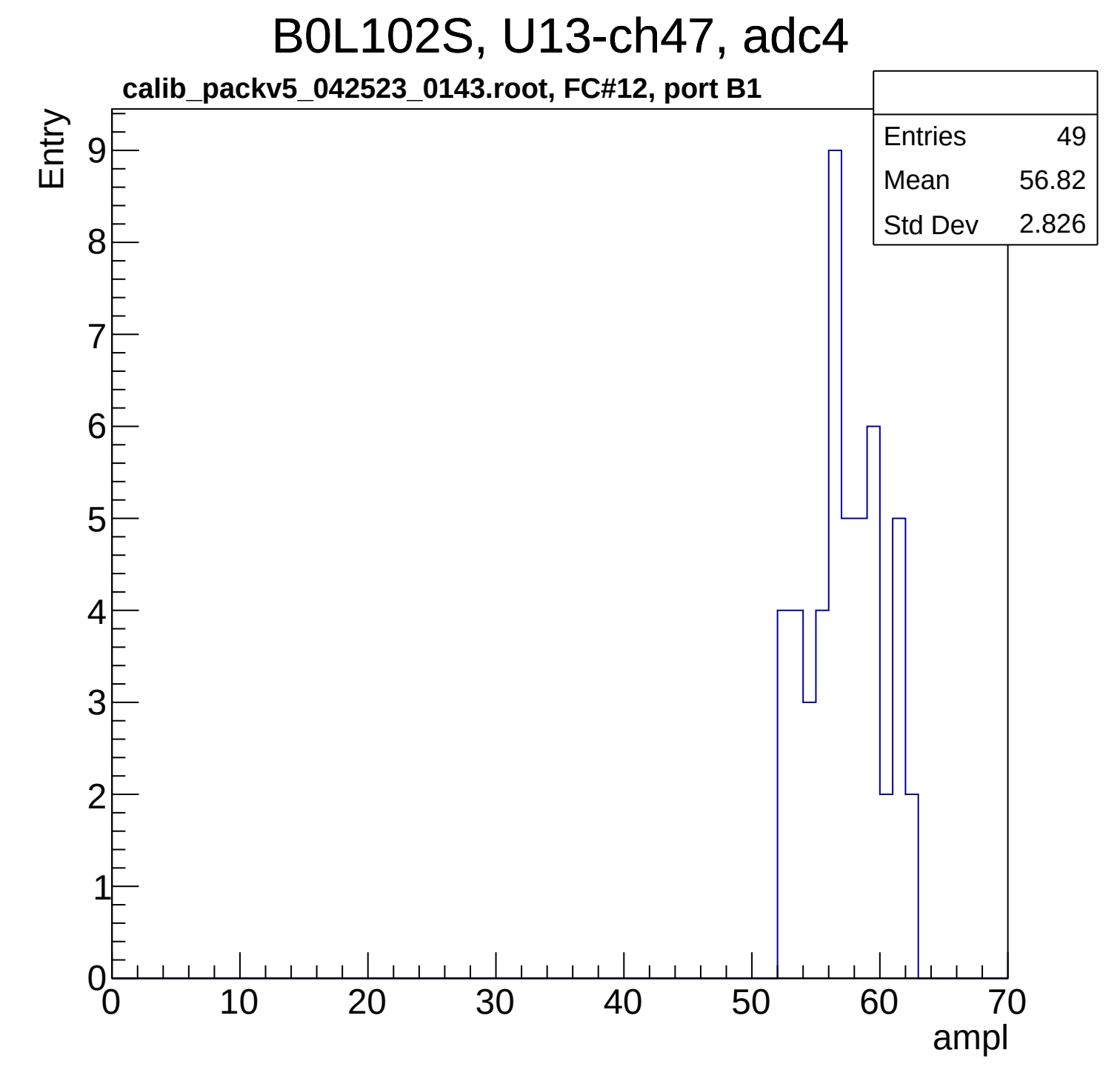
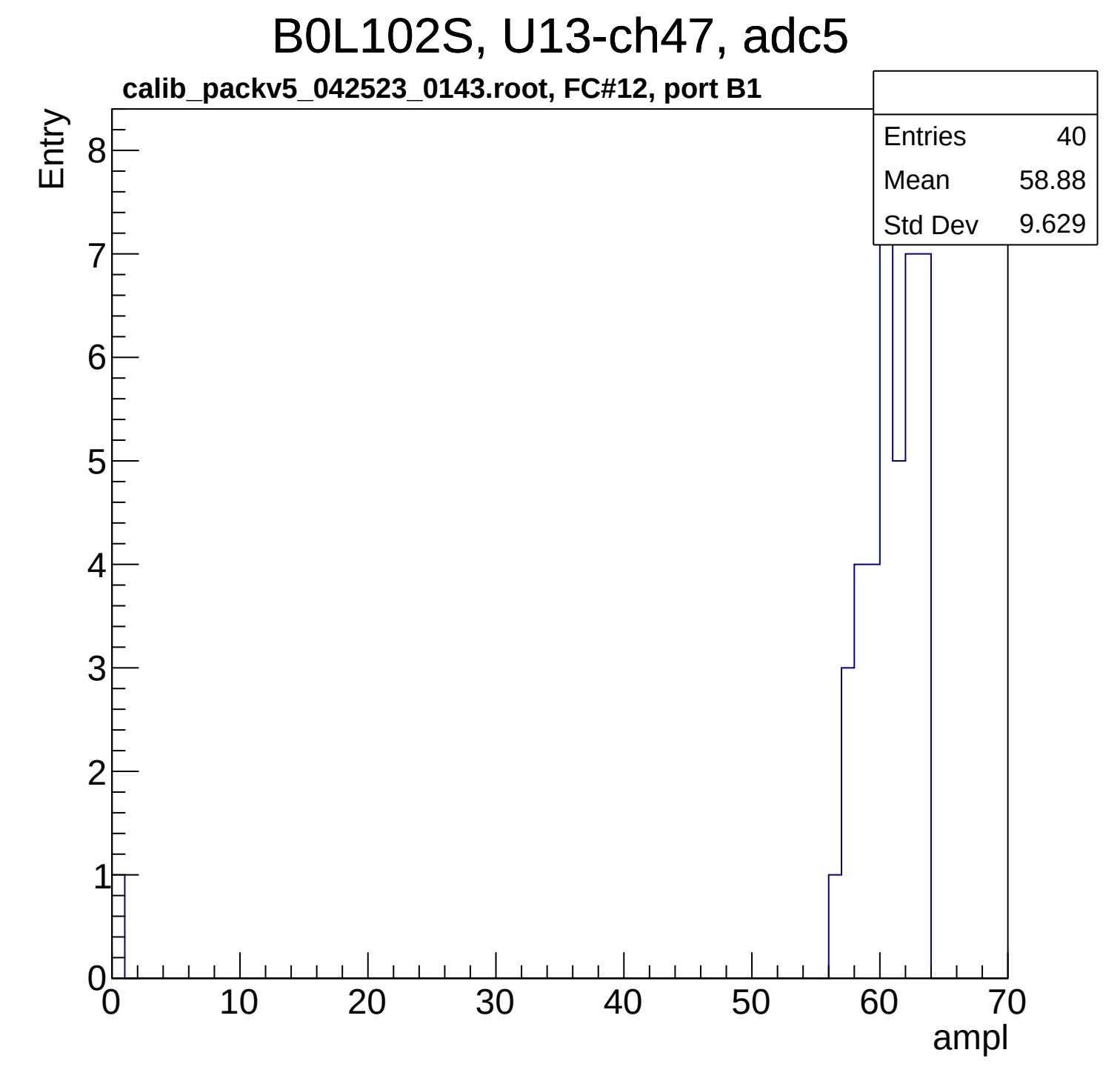

B0L102S, U13-ch47, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 36.78
Std Dev 3.571
Gaus mean : 36.8962
Gaus Width: 3.2240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch47, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 43.57
Std Dev 3.523
Gaus mean : 44.3913
Gaus Width: 3.6173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch47, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 50.72
Std Dev 3.362
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch47, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 56.82
Std Dev 2.826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch47, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.88
Std Dev 9.629
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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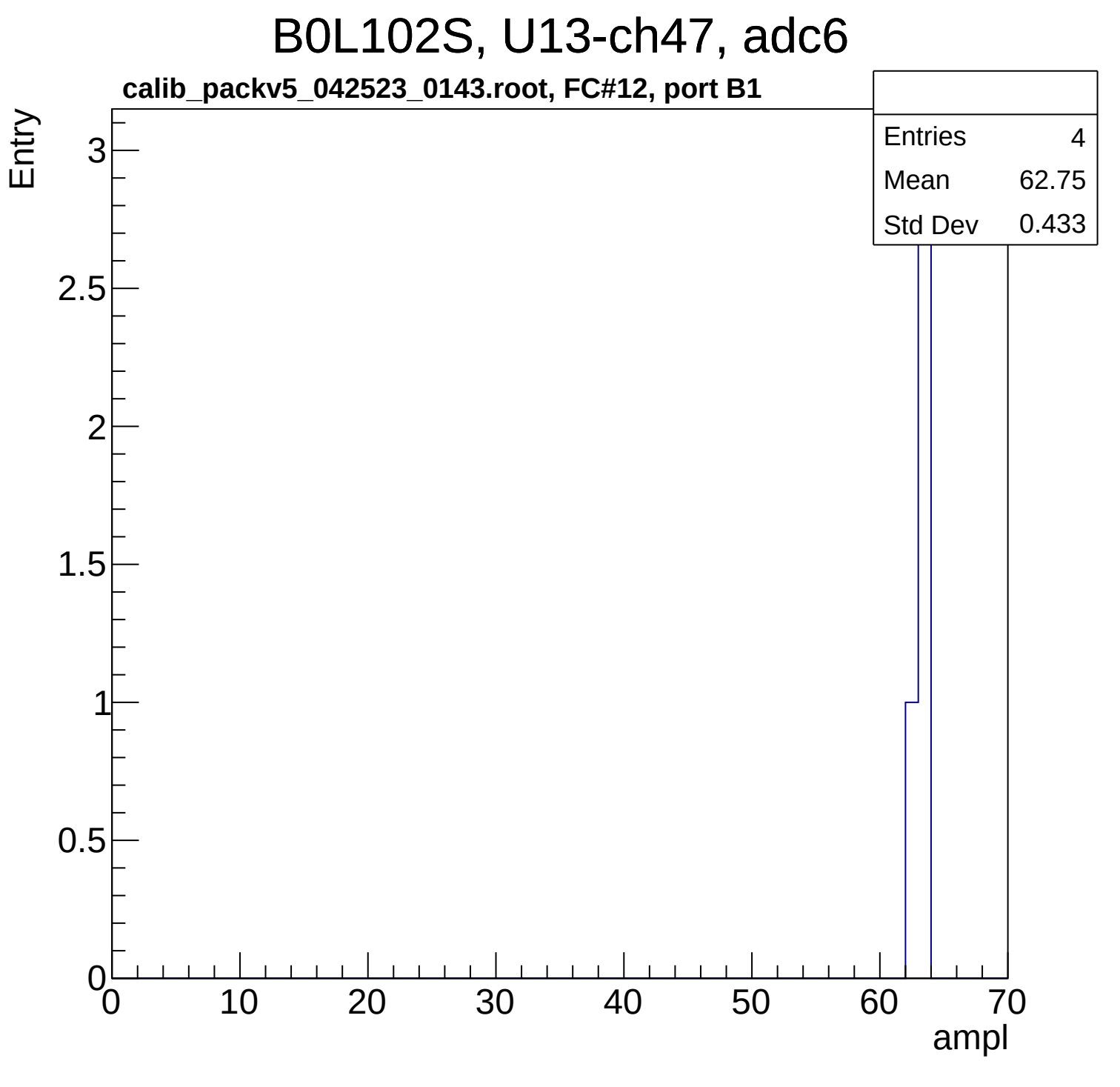
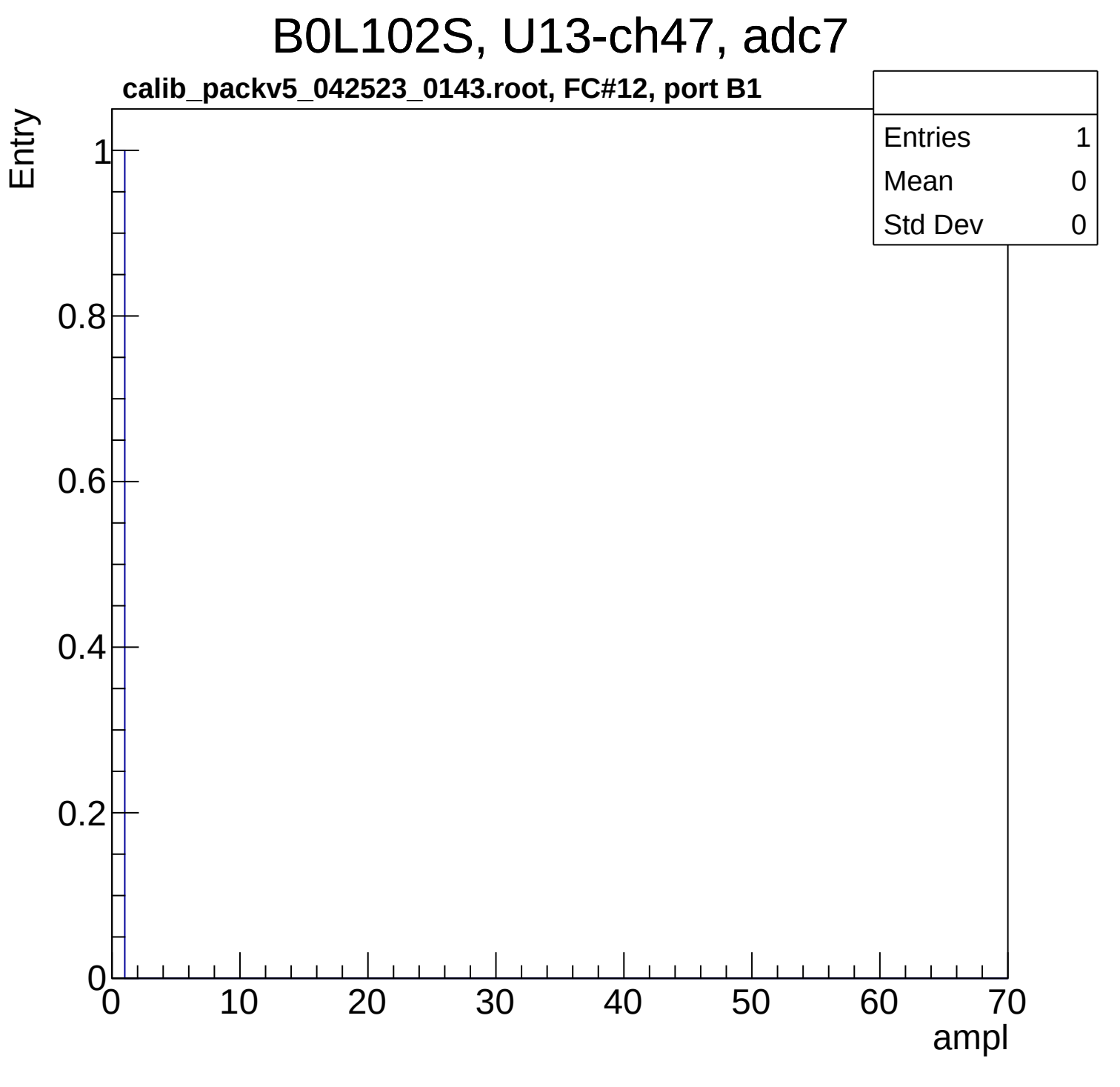
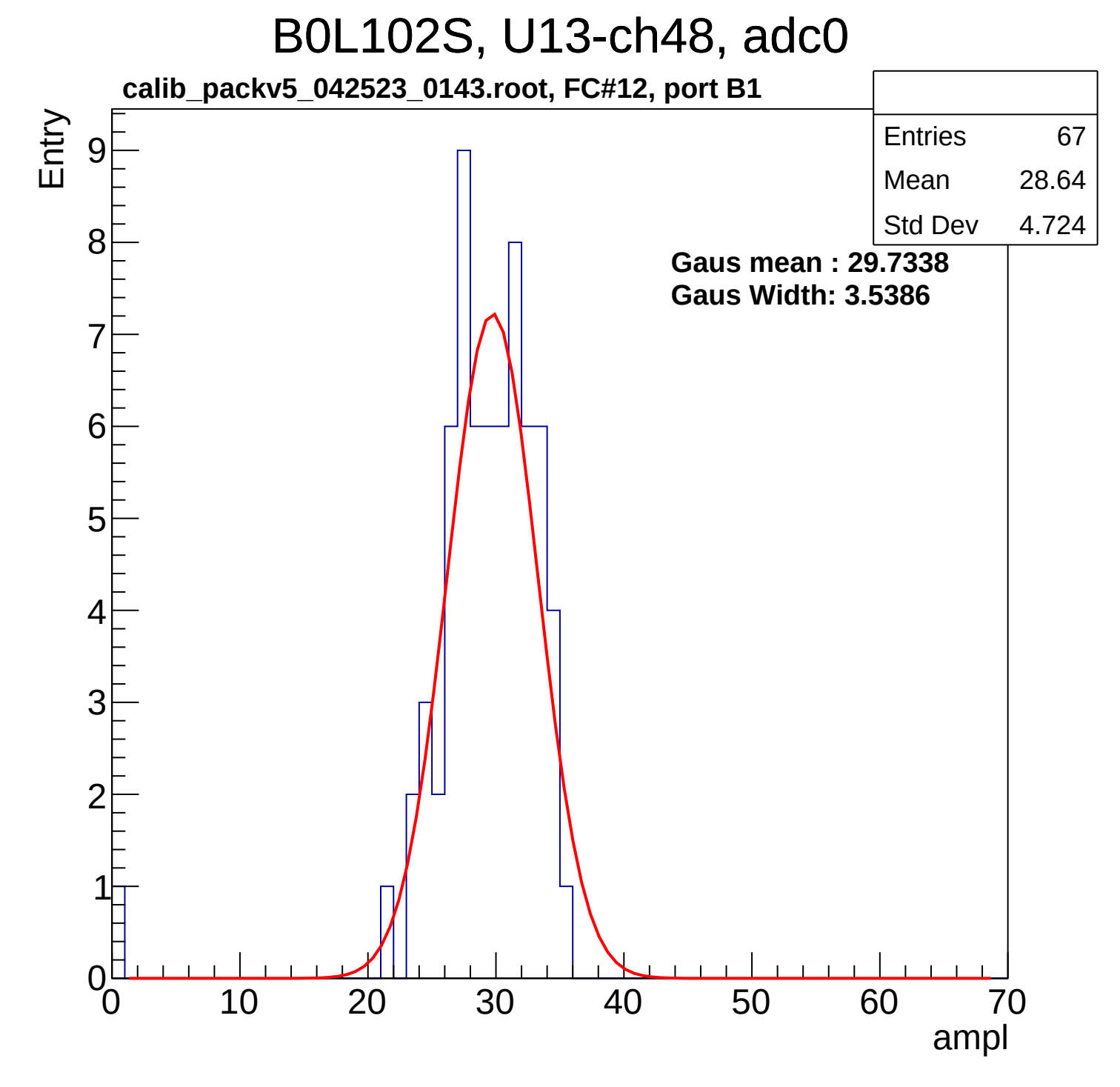
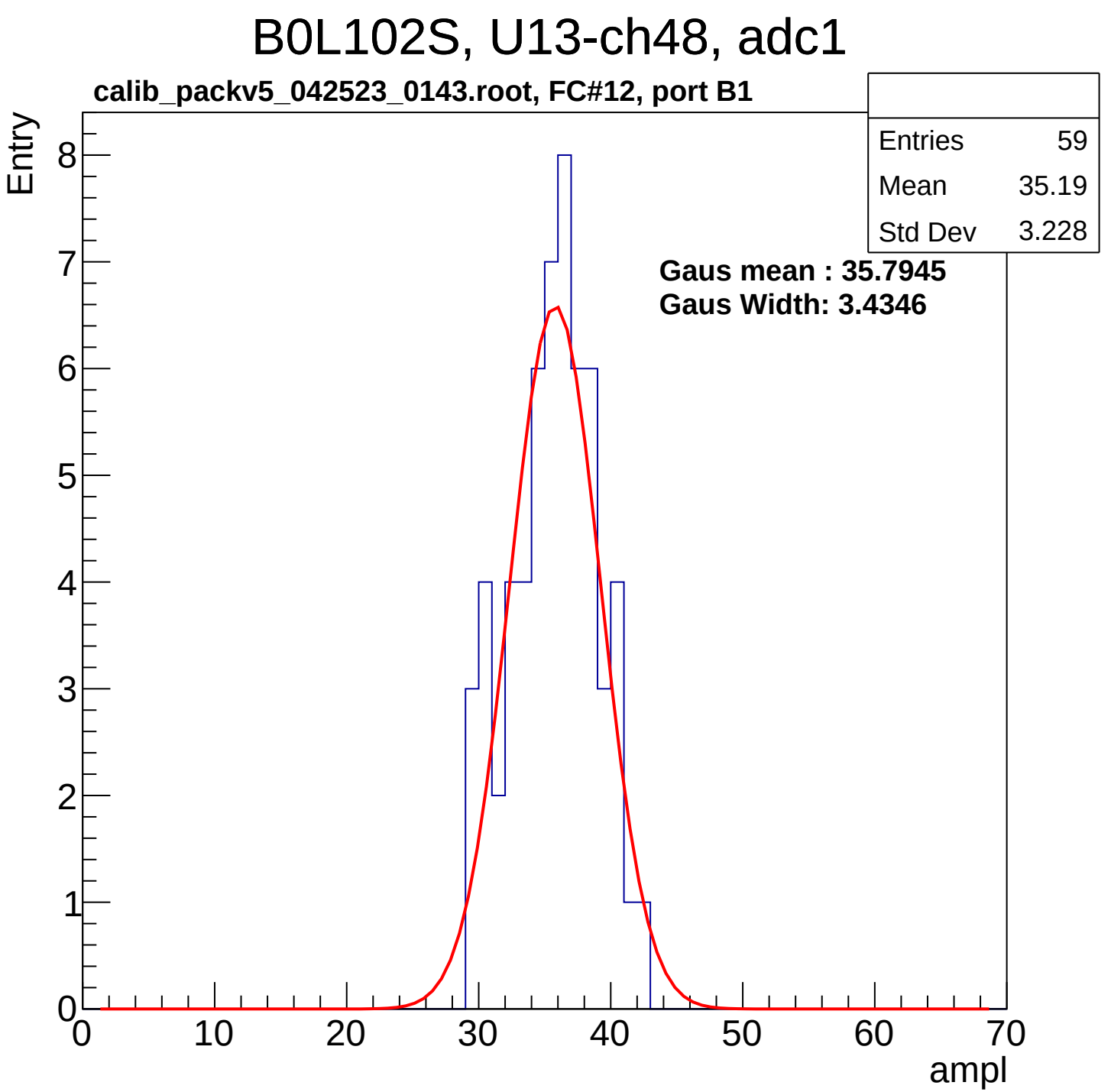

B0L102S, U13-ch47, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch47, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch48, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.64
Std Dev 4.724
Gaus mean : 29.7338
Gaus Width: 3.5386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch48, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 35.19
Std Dev 3.228
Gaus mean : 35.7945
Gaus Width: 3.4346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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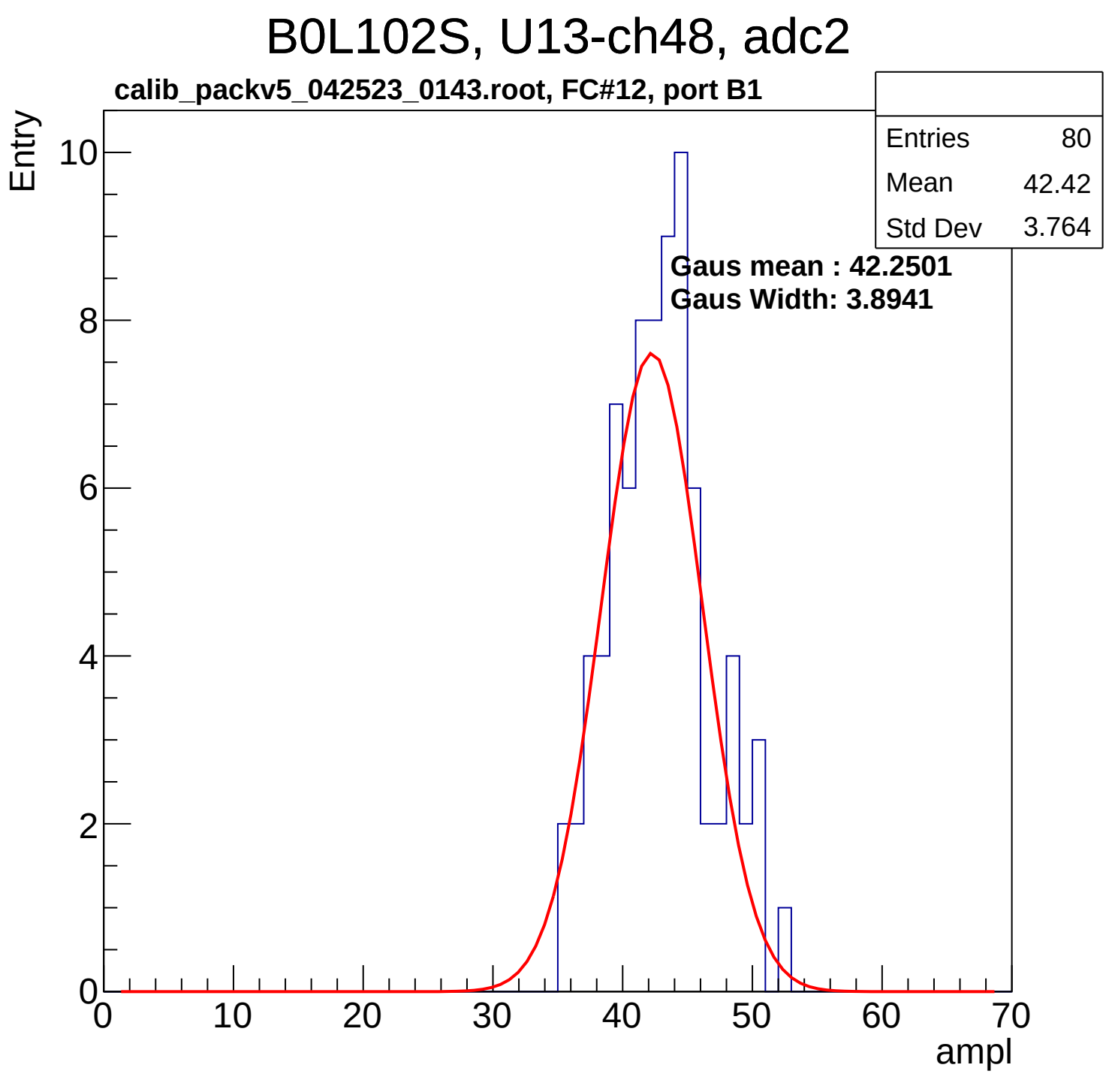
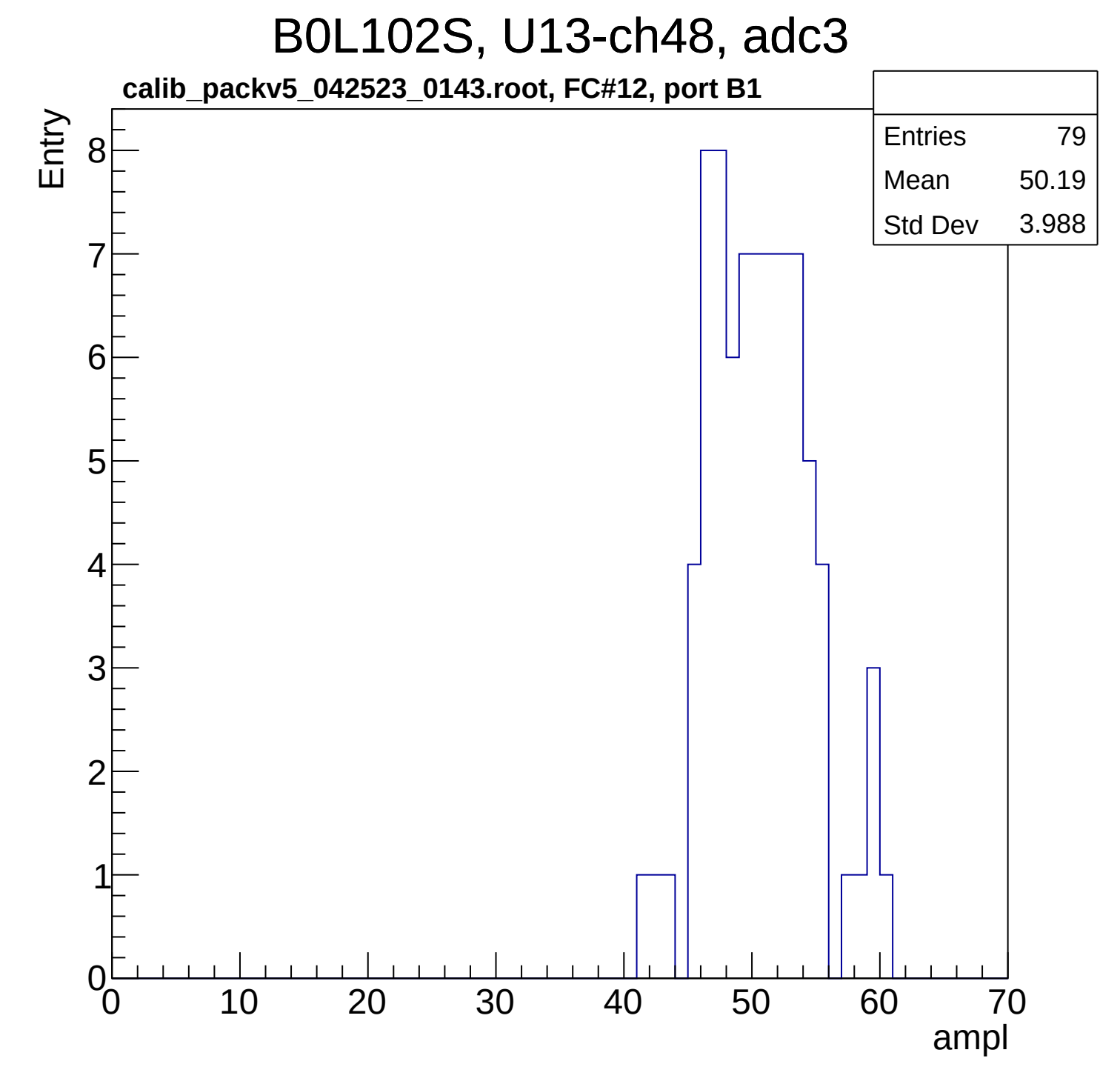
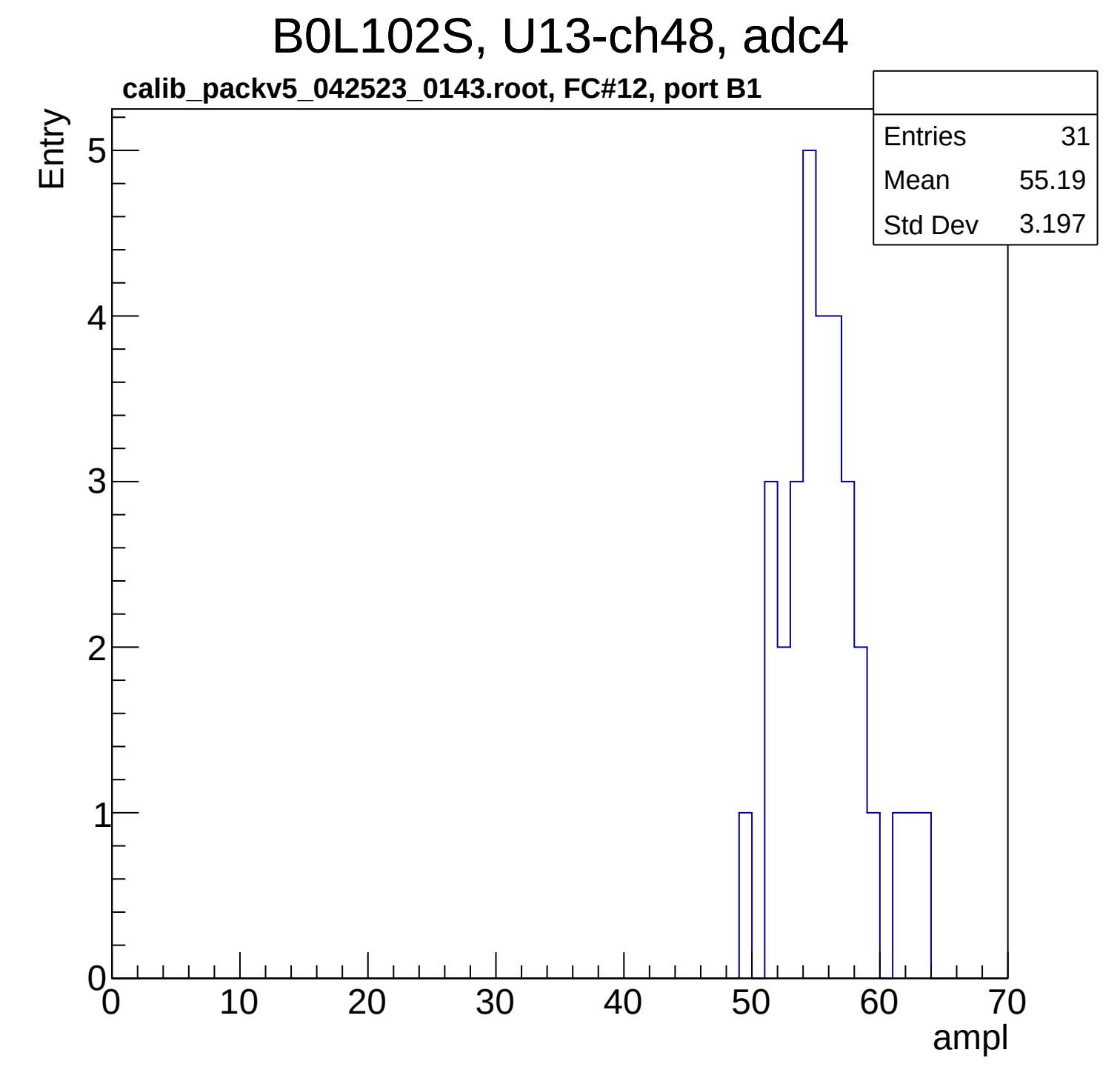

B0L102S, U13-ch48, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 42.42
Std Dev 3.764
Gaus mean : 42.2501
Gaus Width: 3.8941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch48, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 50.19
Std Dev 3.988
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch48, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 55.19
Std Dev 3.197
Entry
ampl
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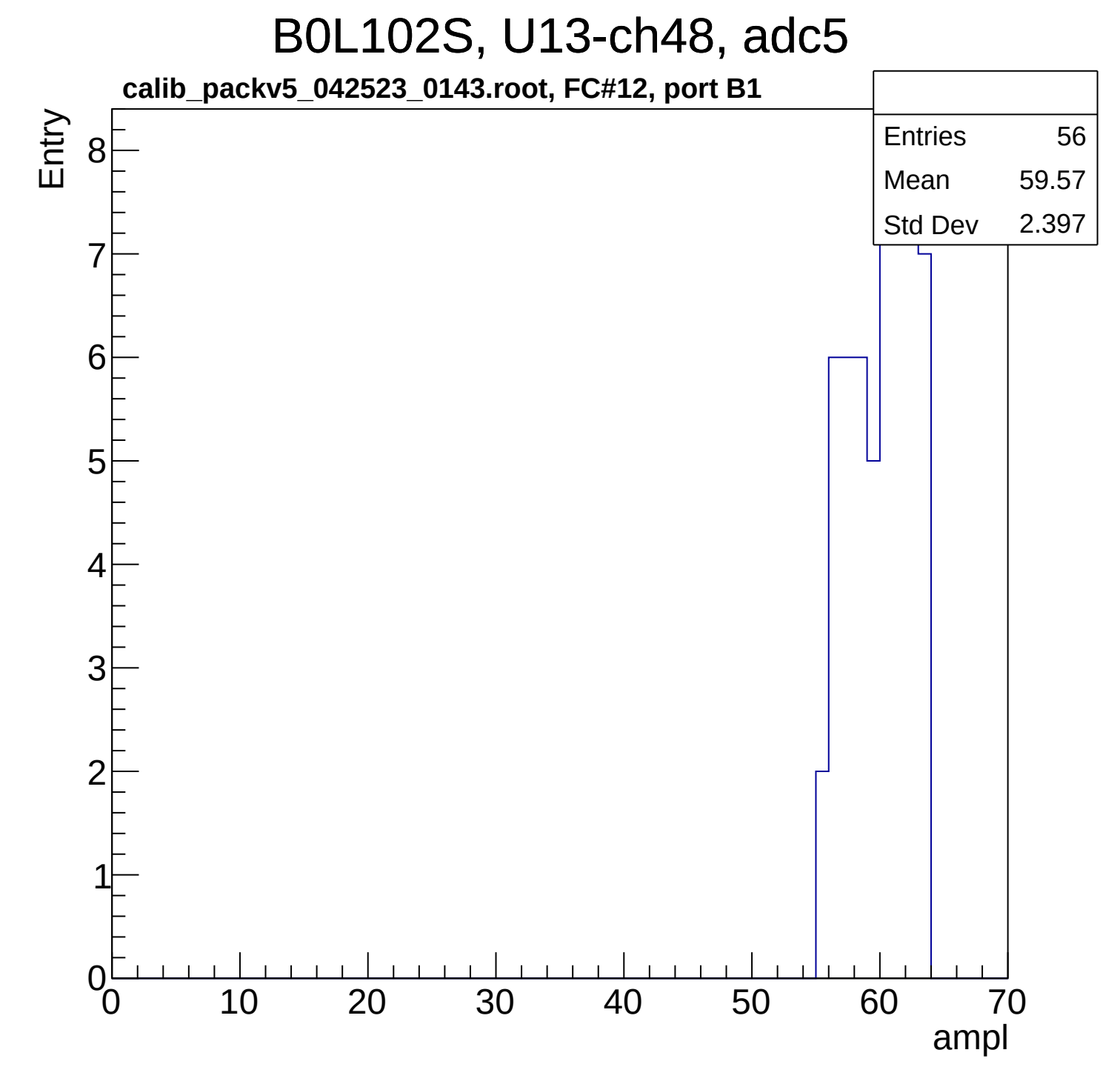
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U13-ch48, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.57
Std Dev 2.397
Entry
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8
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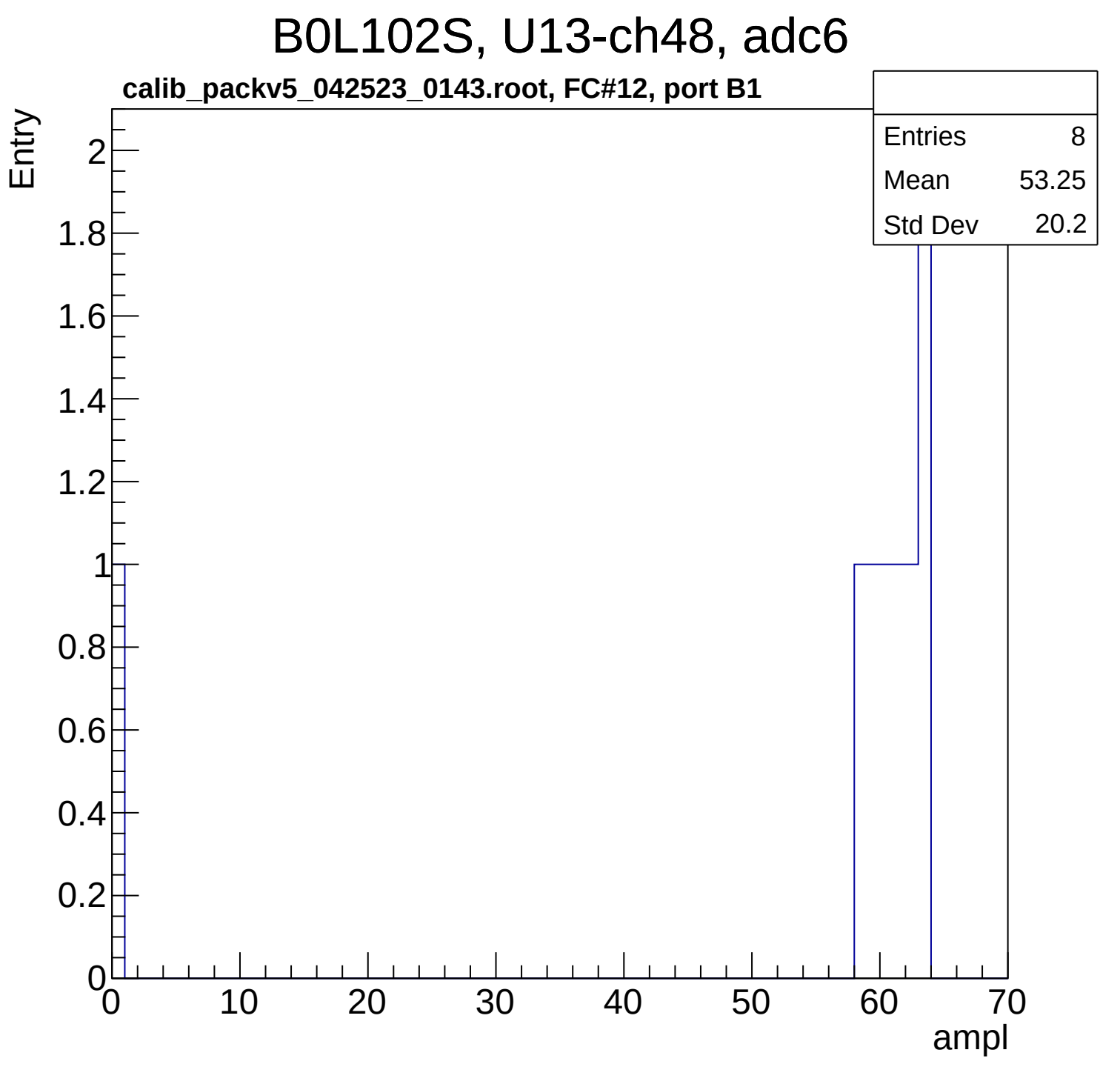
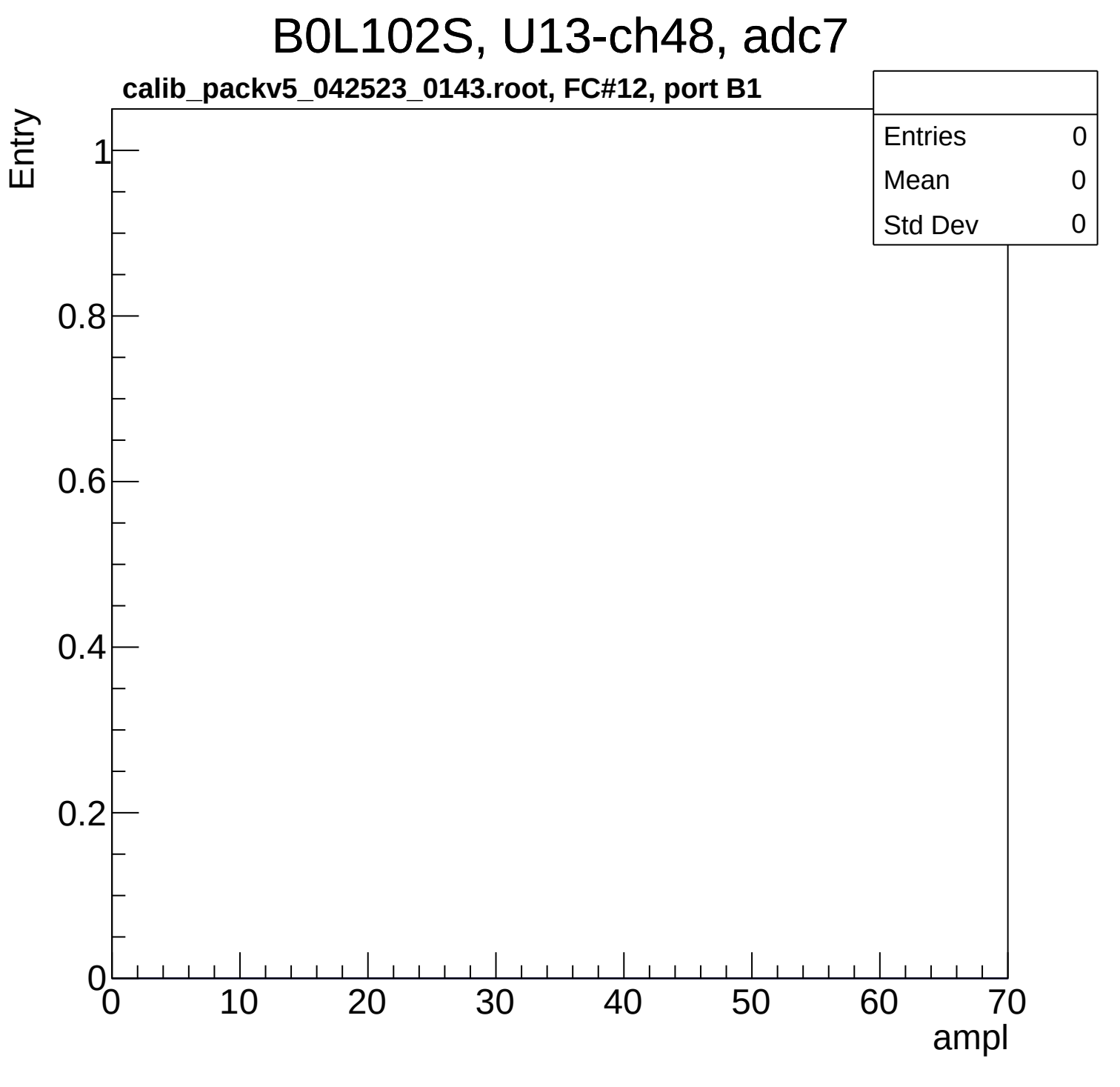
30
40
50
60
70
ampl


B0L102S, U13-ch48, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 53.25
Std Dev 20.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch48, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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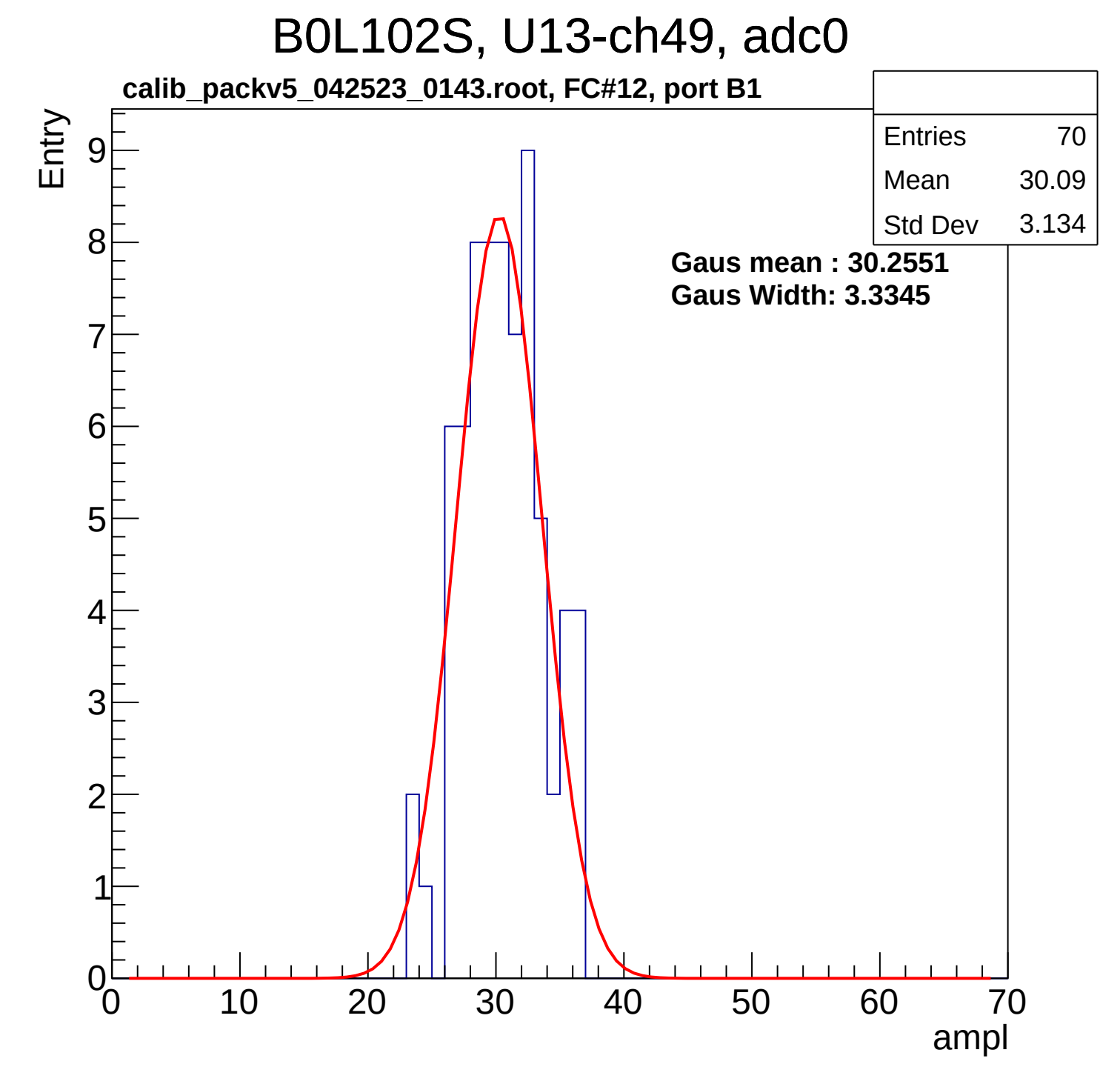
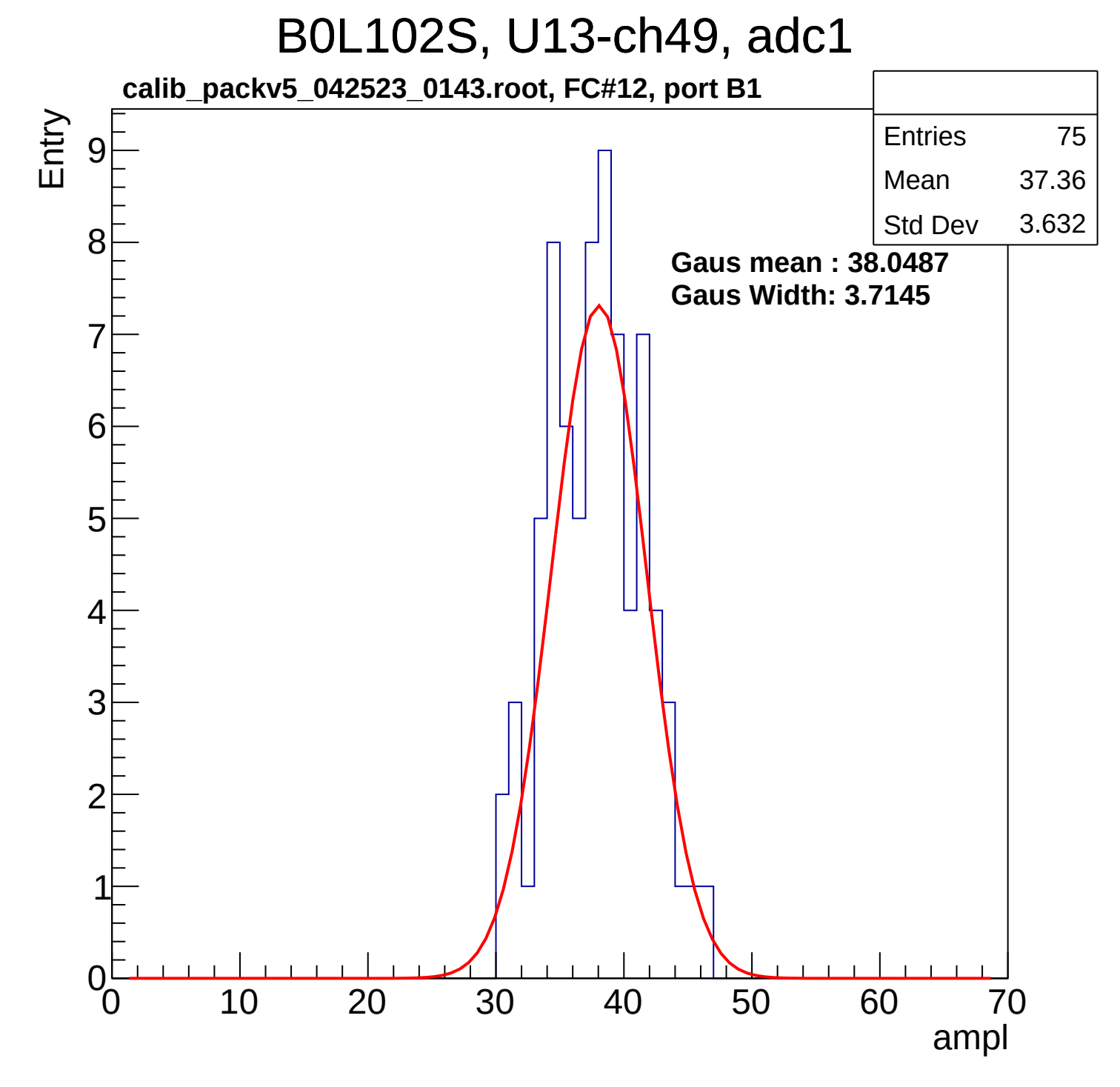
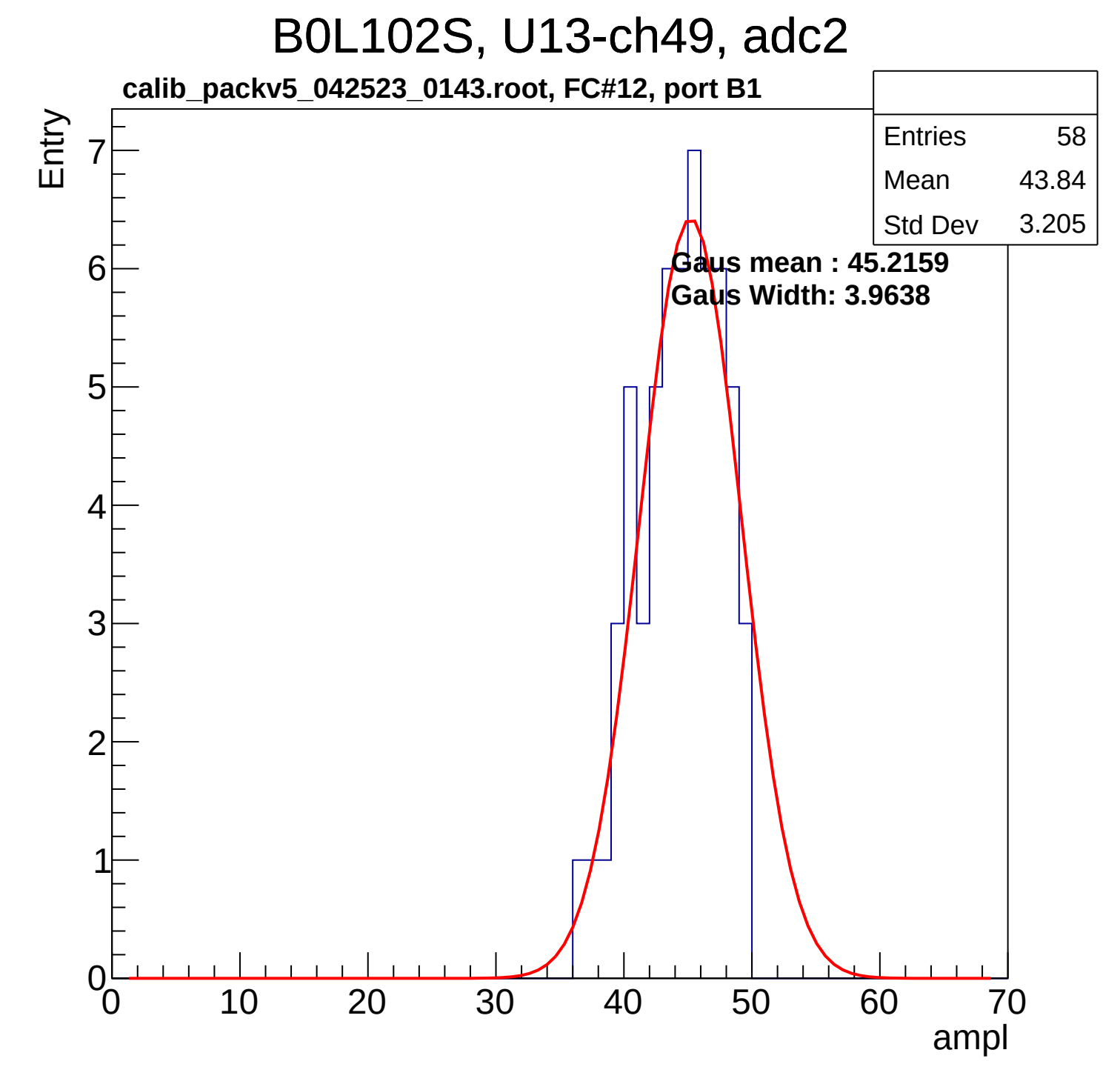
50
60
70
ampl


B0L102S, U13-ch49, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 30.09
Std Dev 3.134
Gaus mean : 30.2551
Gaus Width: 3.3345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch49, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.36
Std Dev 3.632
Gaus mean : 38.0487
Gaus Width: 3.7145
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch49, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 43.84
Std Dev 3.205
Gaus mean : 45.2159
Gaus Width: 3.9638
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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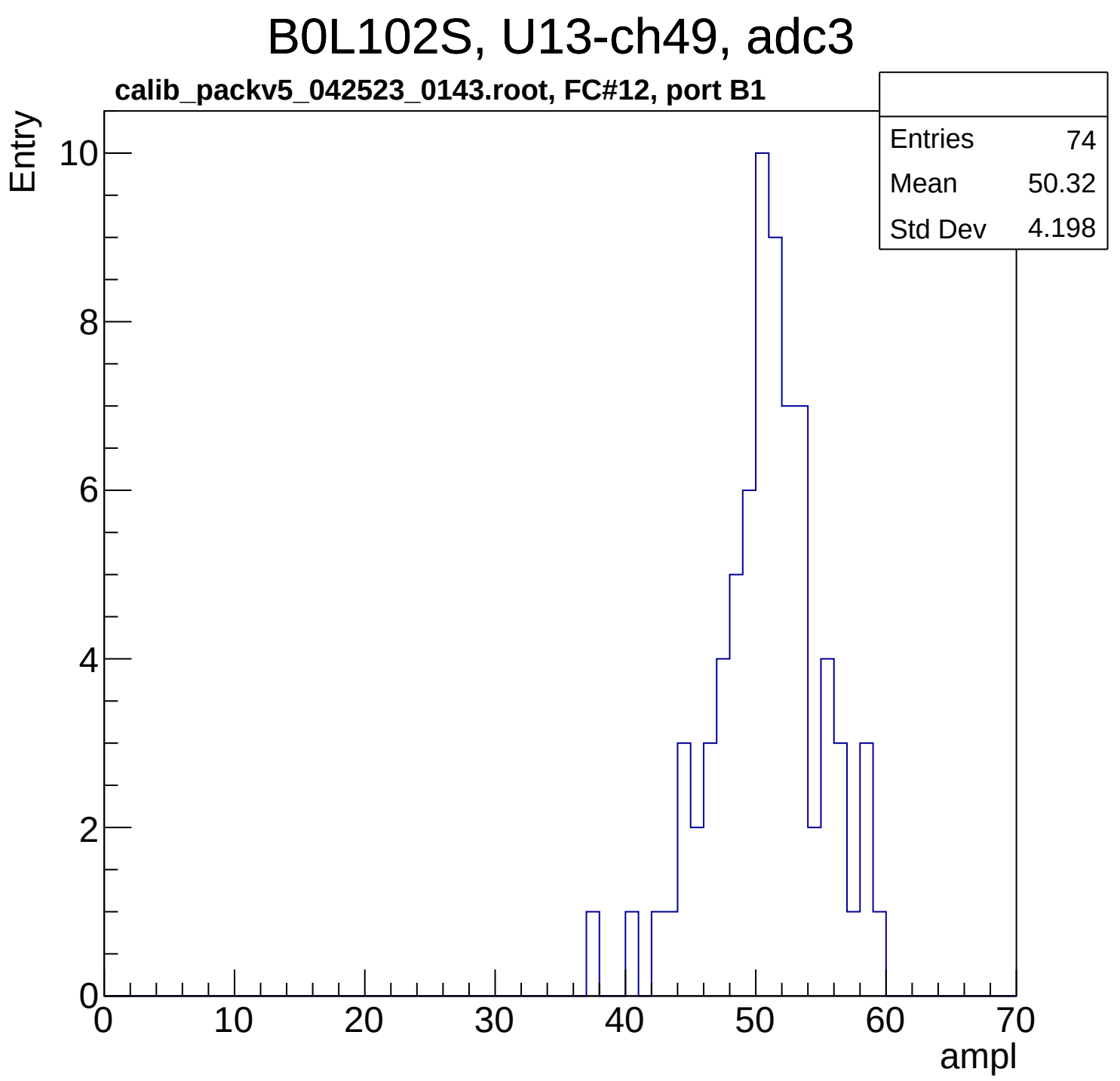
B0L102S, U13-ch49, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 50.32
Std Dev 4.198
Entry
ampl
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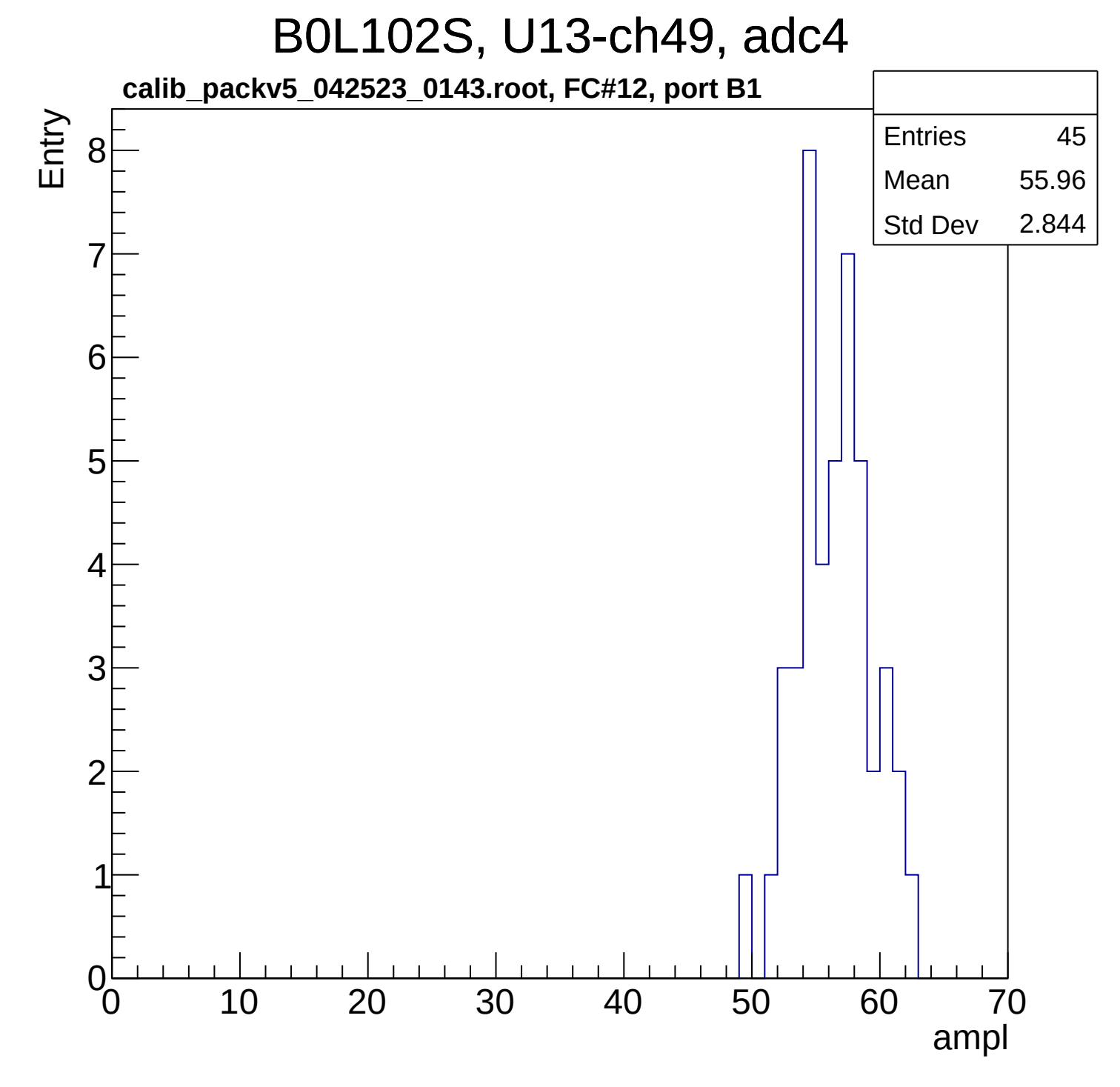
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch49, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 55.96
Std Dev 2.844
Entry
ampl
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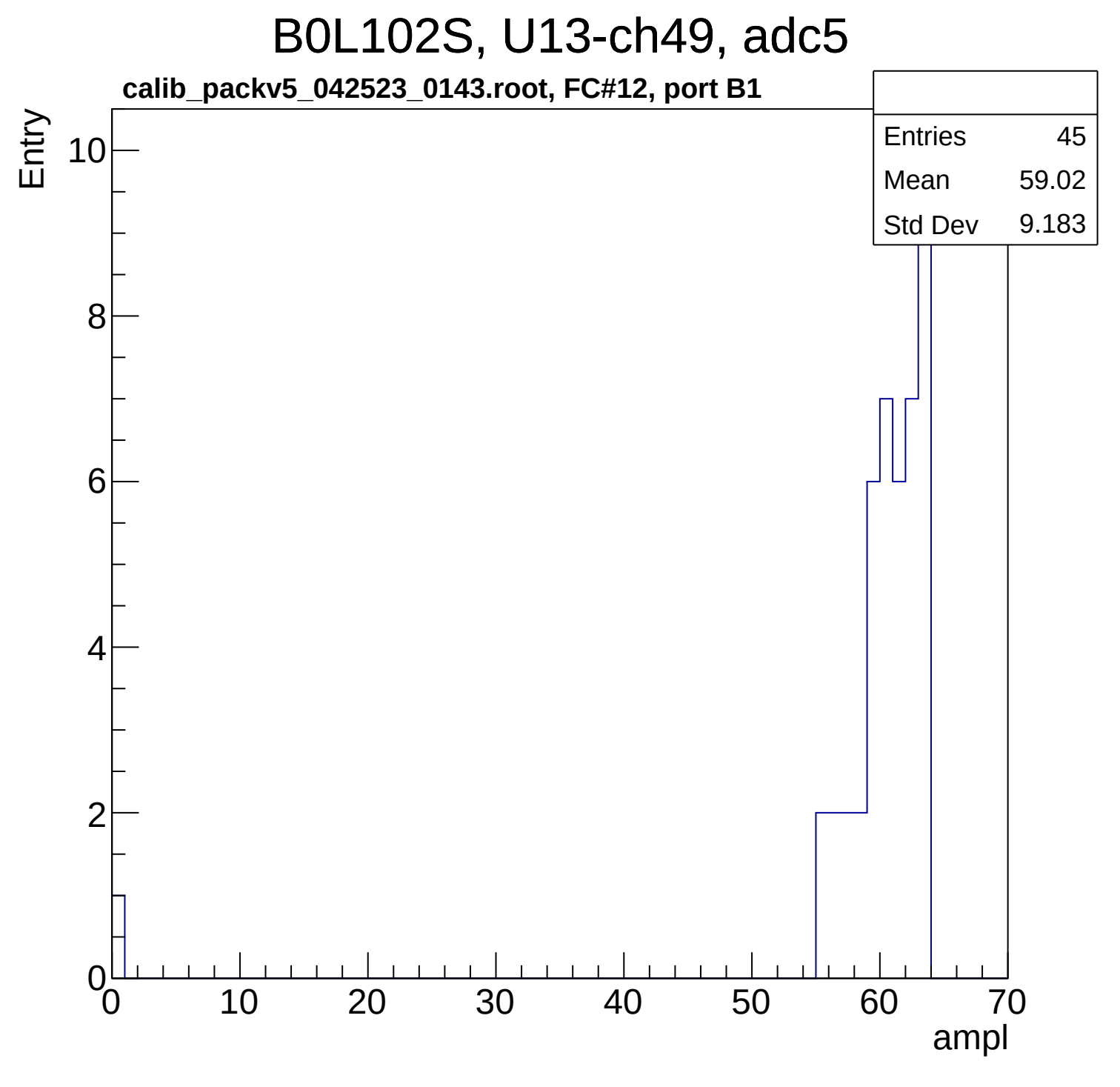
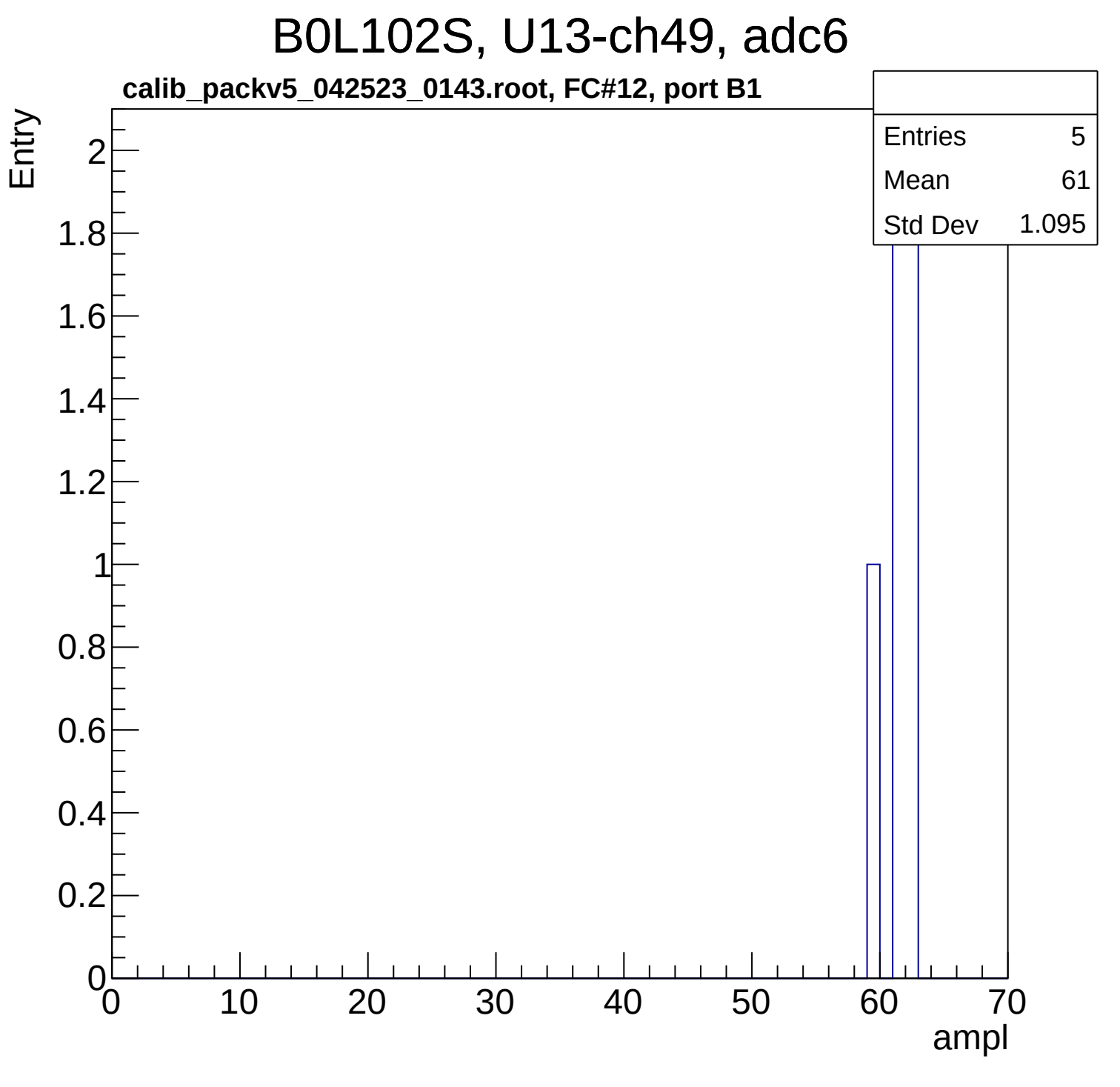
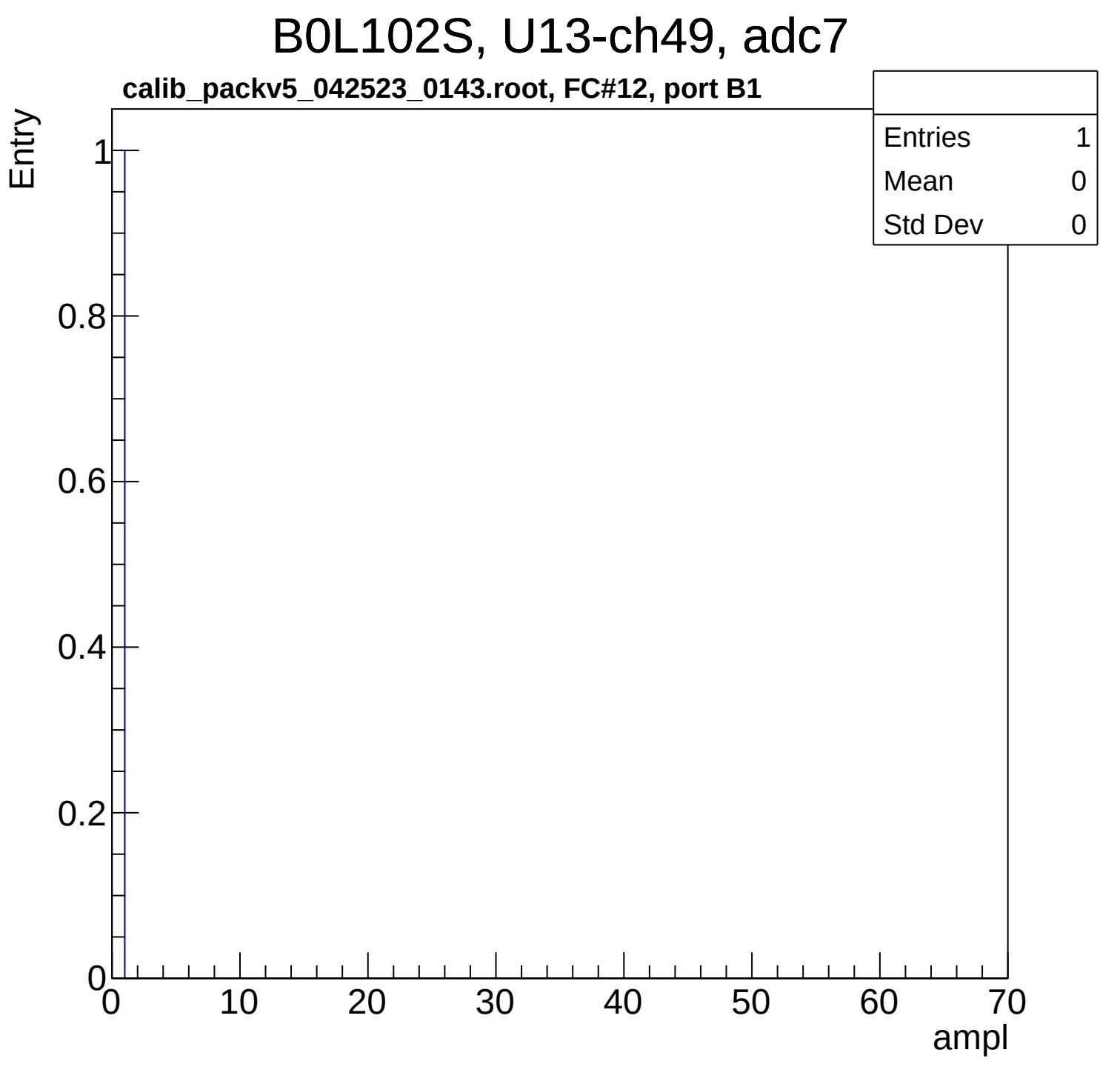
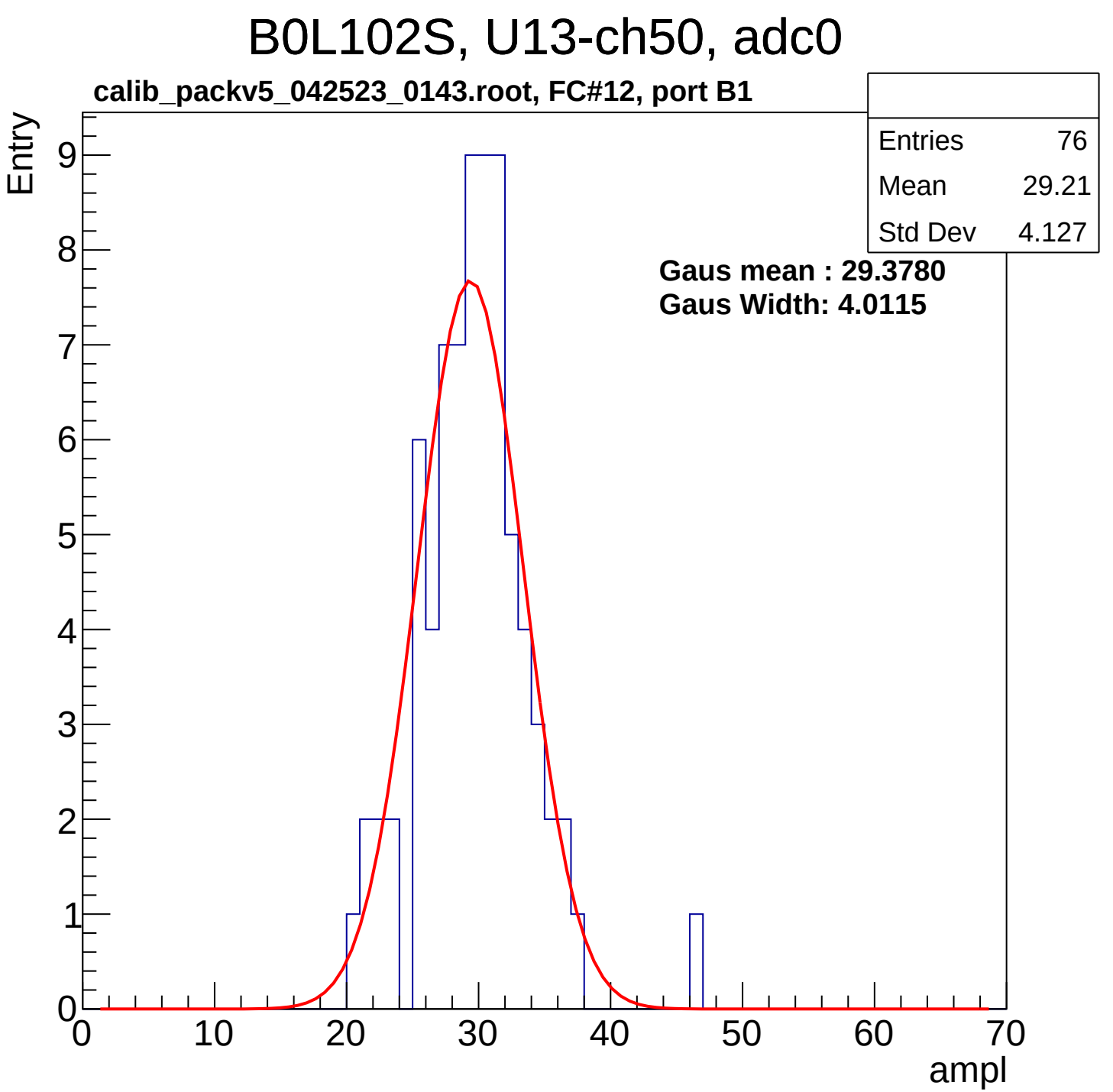
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch49, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 59.02
Std Dev 9.183
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch49, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch49, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch50, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.21
Std Dev 4.127
Gaus mean : 29.3780
Gaus Width: 4.0115
Entry
ampl
0
1
2
3
4
5
6
7
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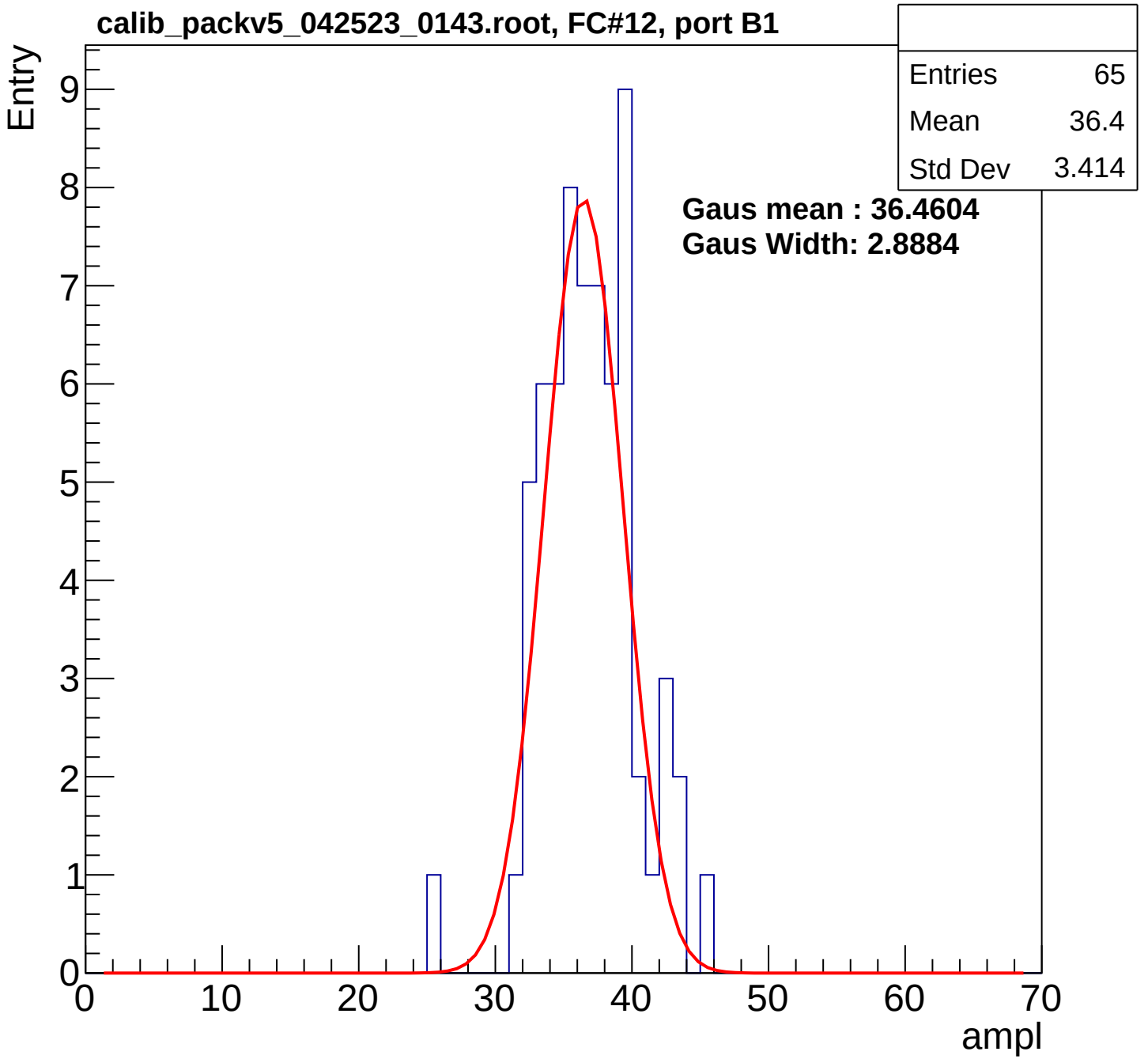
B0L102S, U13-ch50, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 36.4
Std Dev 3.414
Gaus mean : 36.4604
Gaus Width: 2.8884
Entry
ampl
0
1
2
3
4
5
6
7
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0
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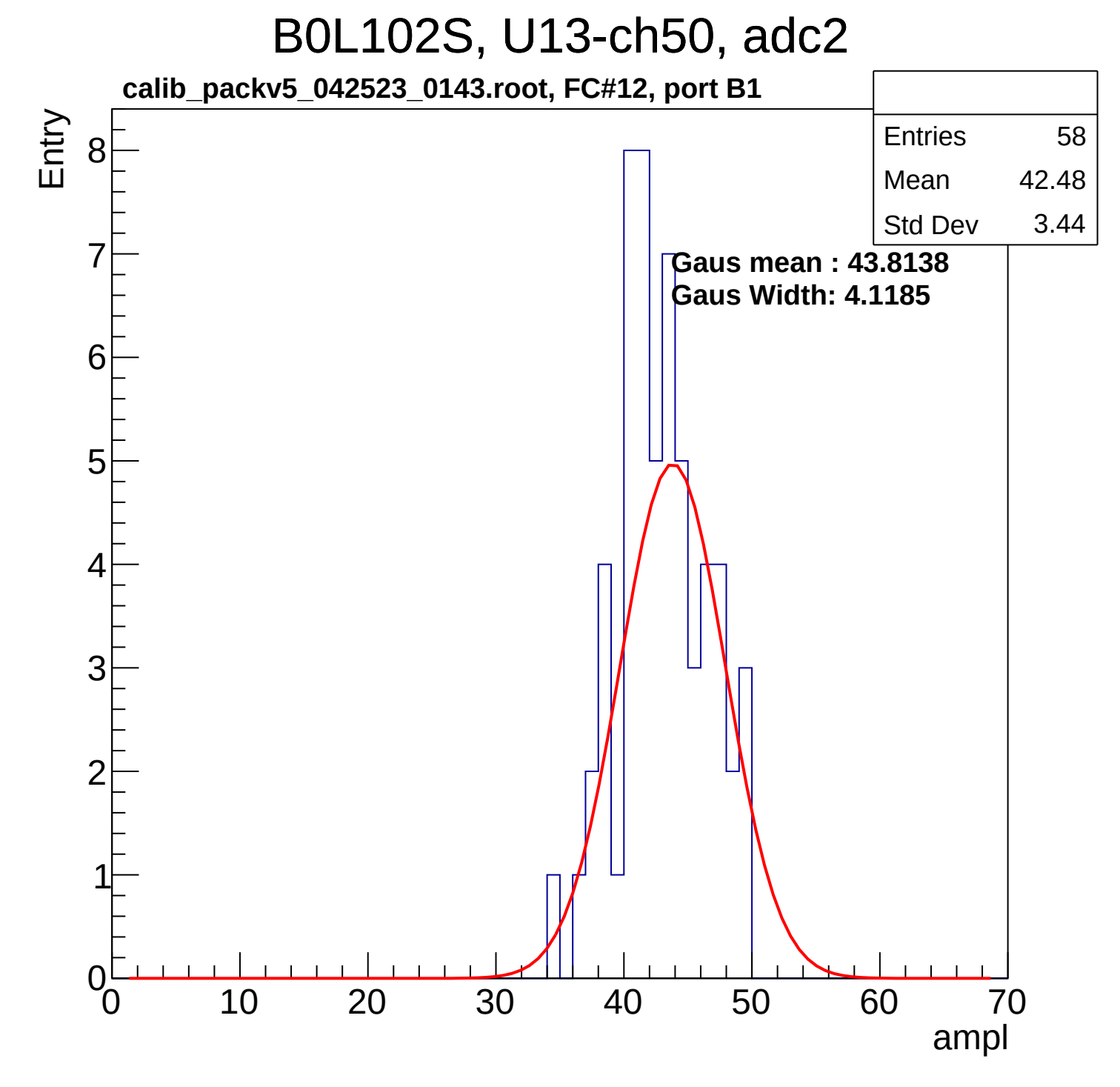
B0L102S, U13-ch50, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.48
Std Dev 3.44
Gaus mean : 43.8138
Gaus Width: 4.1185
Entry
ampl
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1
2
3
4
5
6
7
8
0
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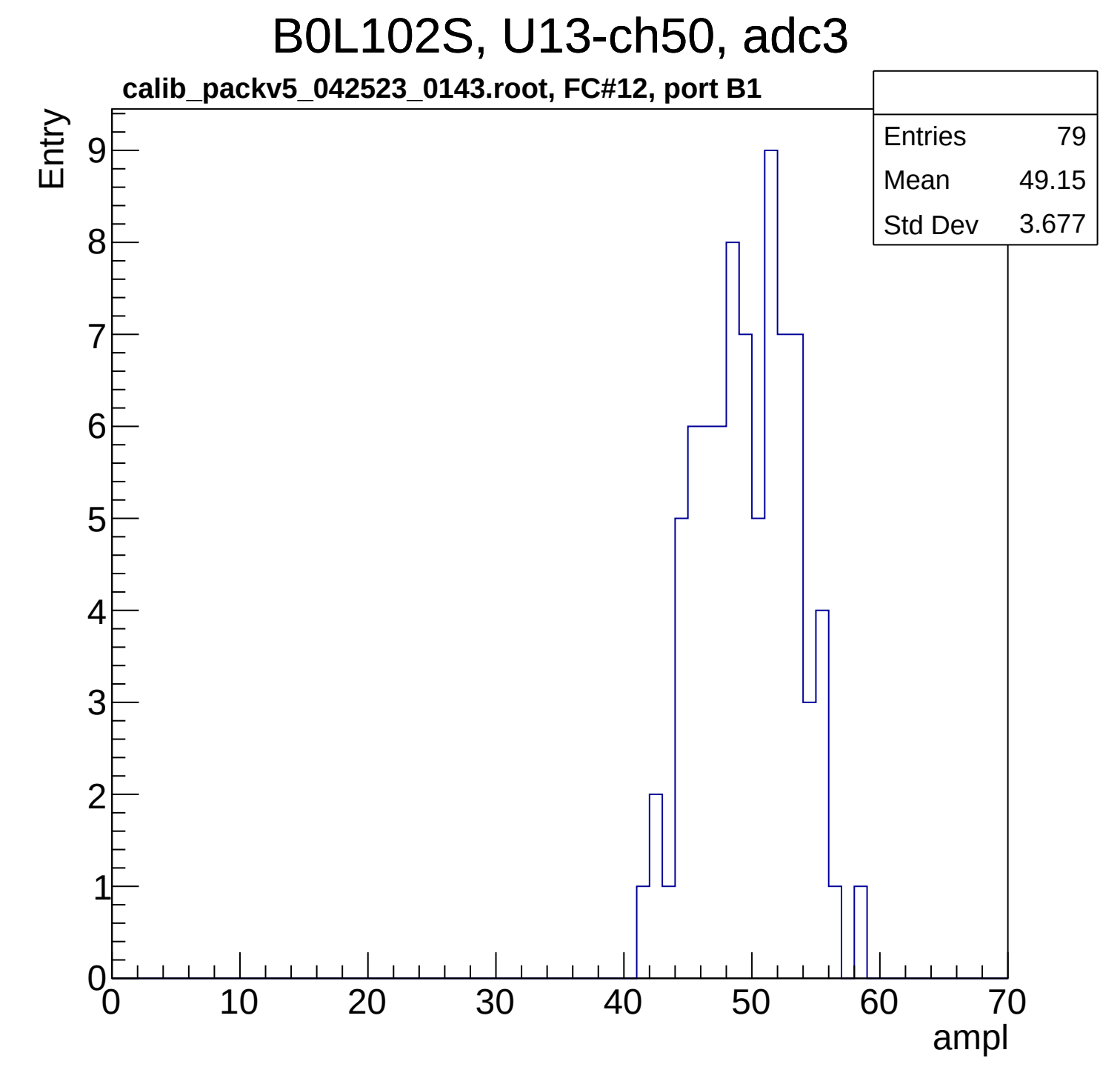
B0L102S, U13-ch50, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 49.15
Std Dev 3.677
Entry
ampl
0
1
2
3
4
5
6
7
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0
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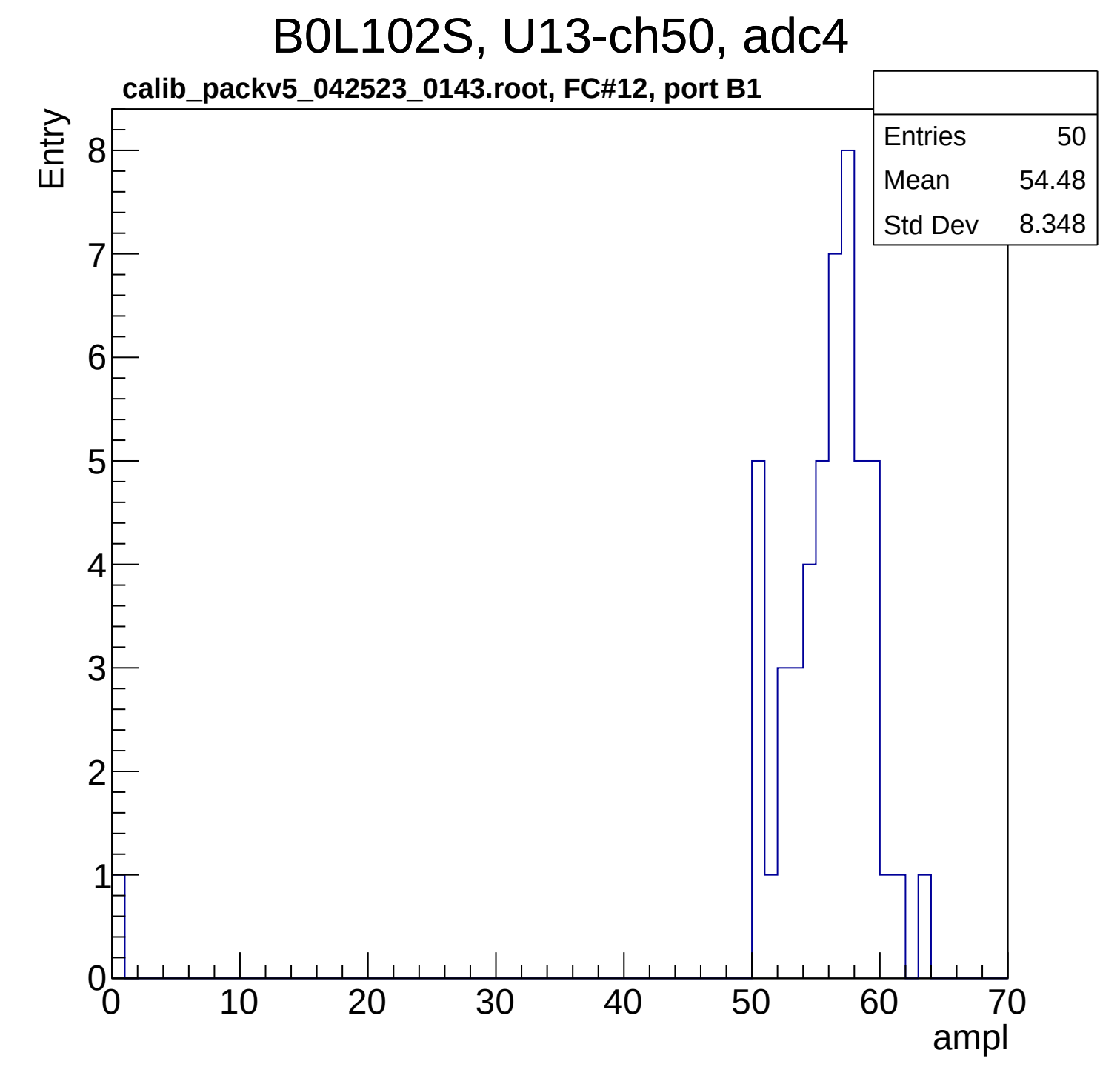
B0L102S, U13-ch50, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 54.48
Std Dev 8.348
Entry
ampl
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1
2
3
4
5
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0
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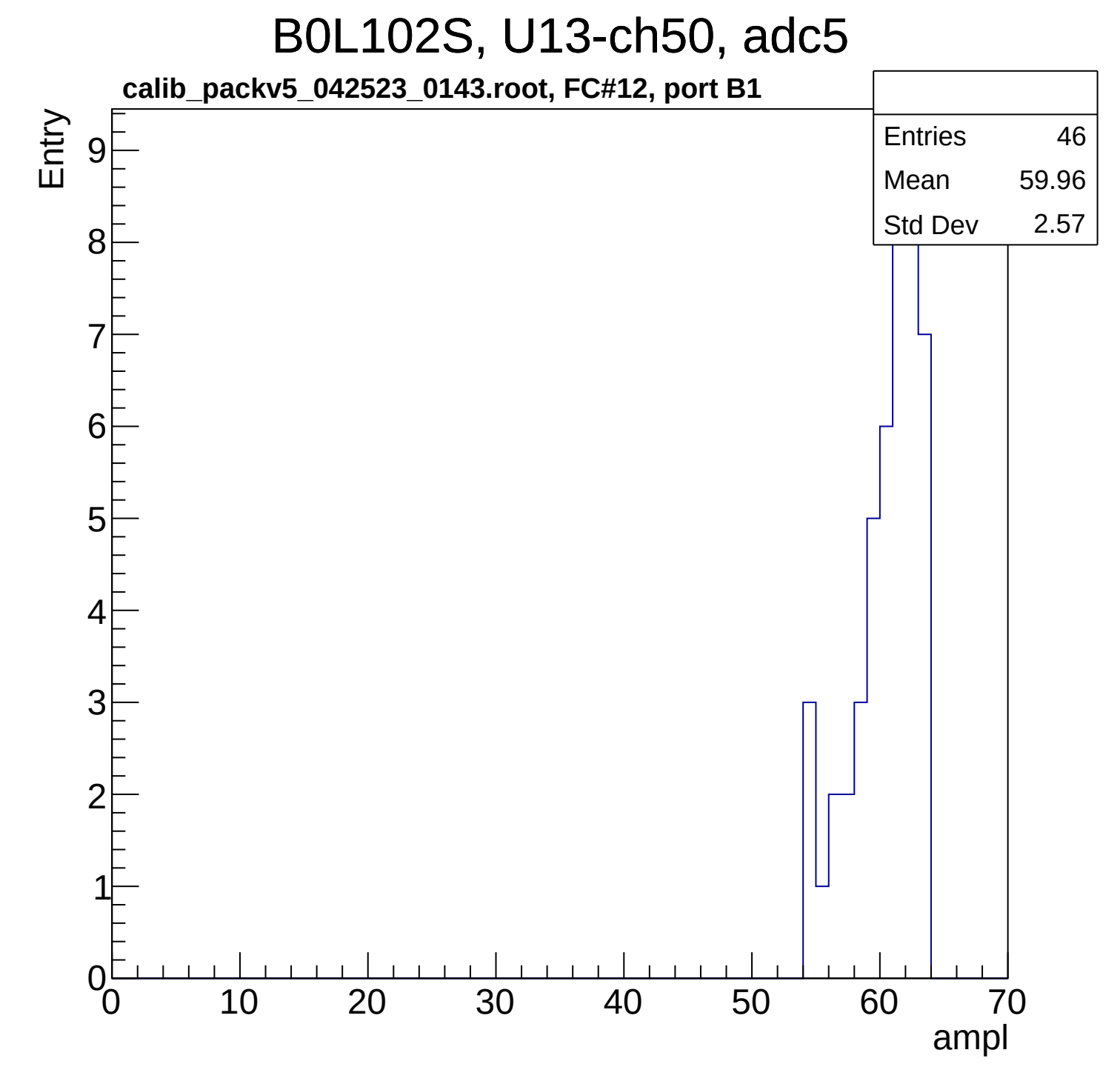
B0L102S, U13-ch50, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.96
Std Dev 2.57
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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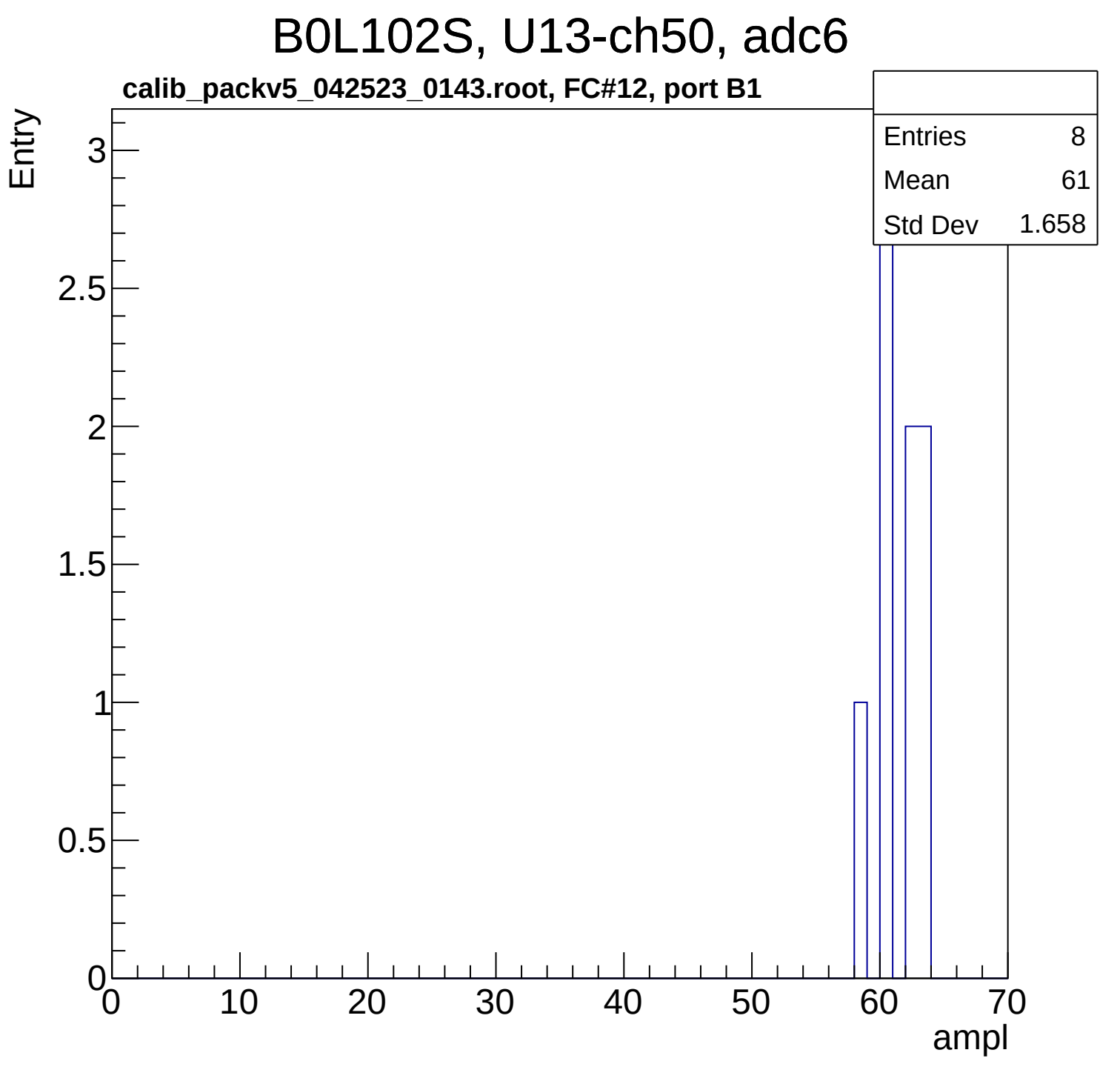
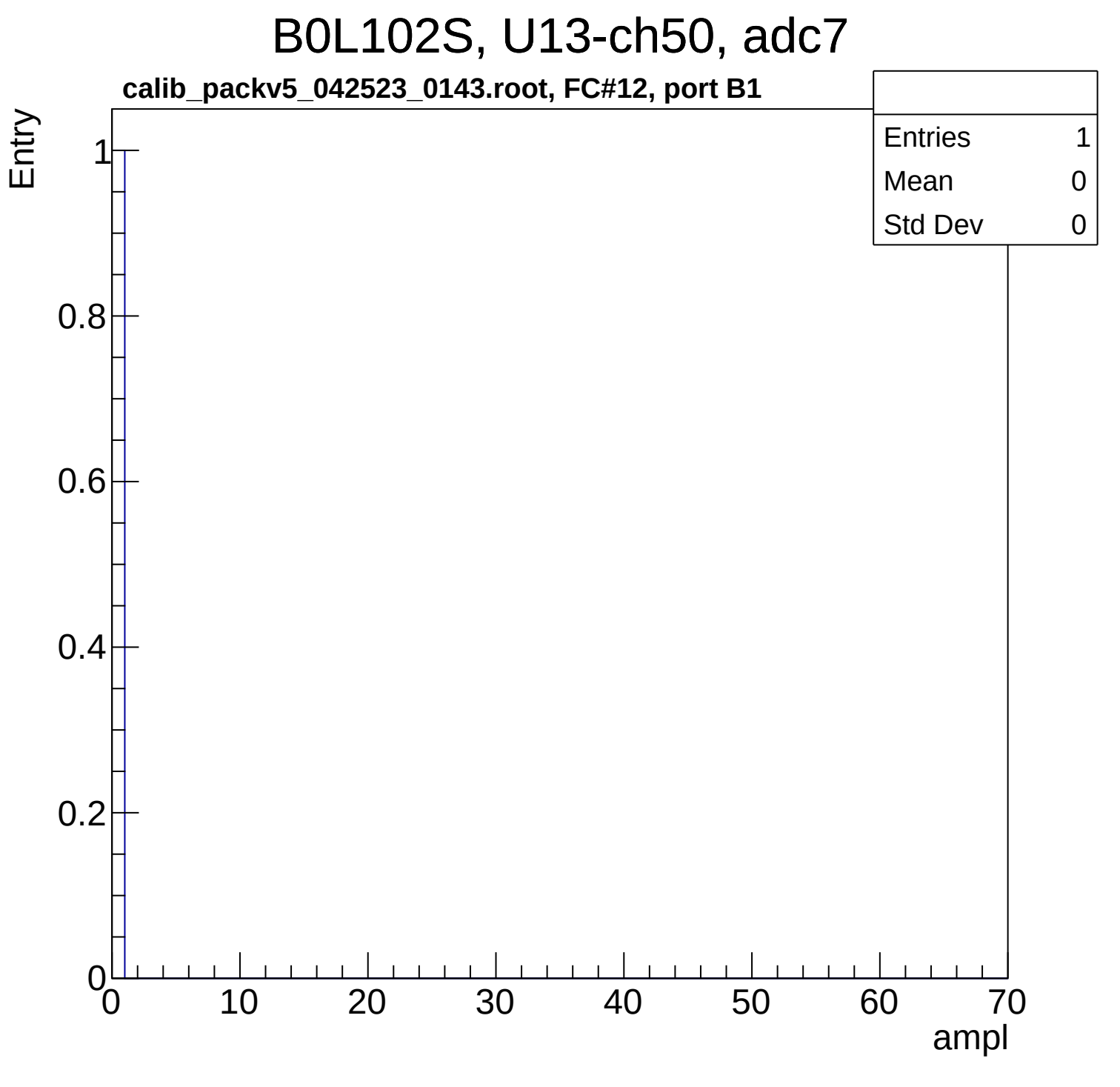
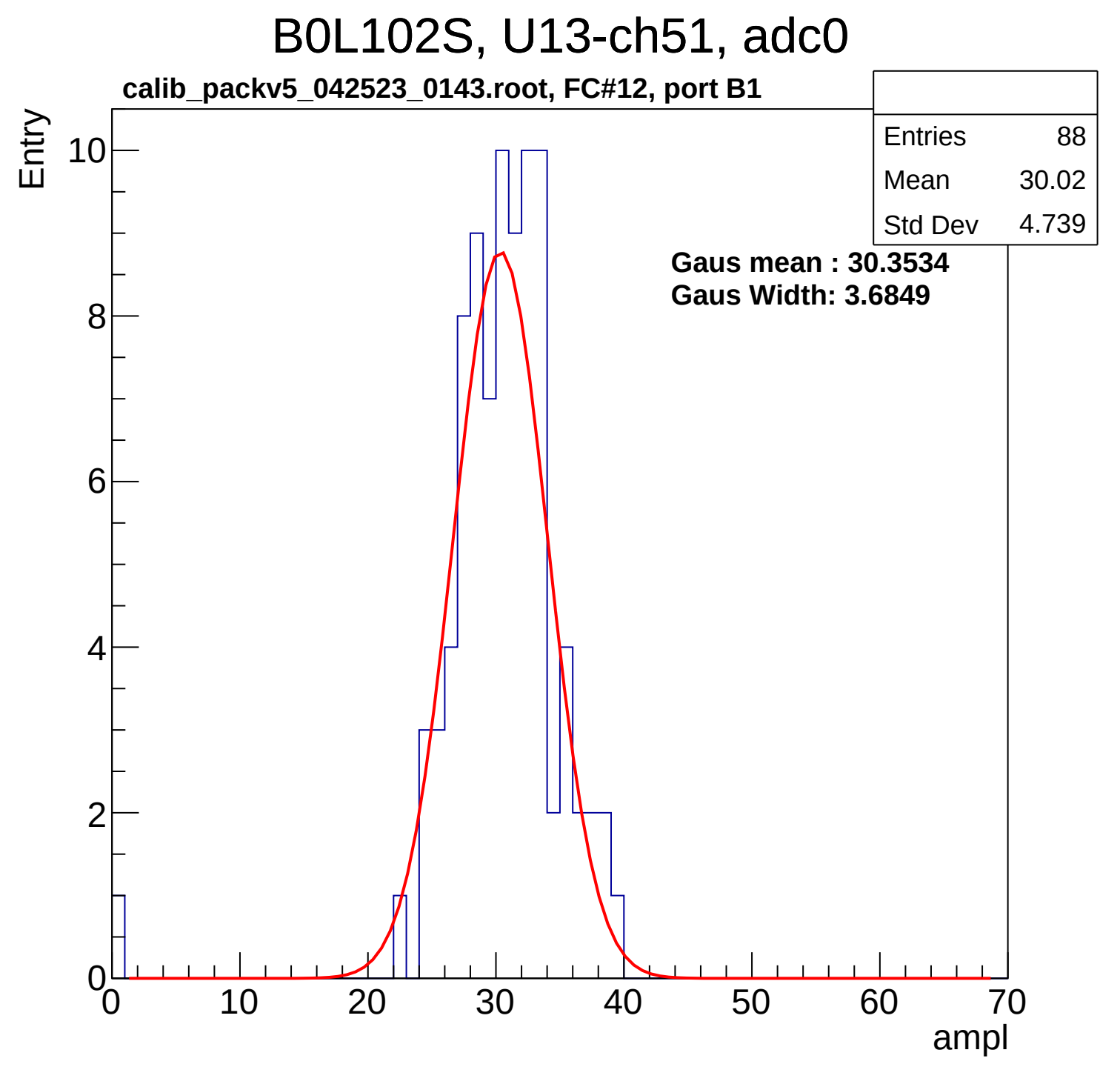

B0L102S, U13-ch50, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61
Std Dev 1.658
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch50, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch51, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 30.02
Std Dev 4.739
Gaus mean : 30.3534
Gaus Width: 3.6849
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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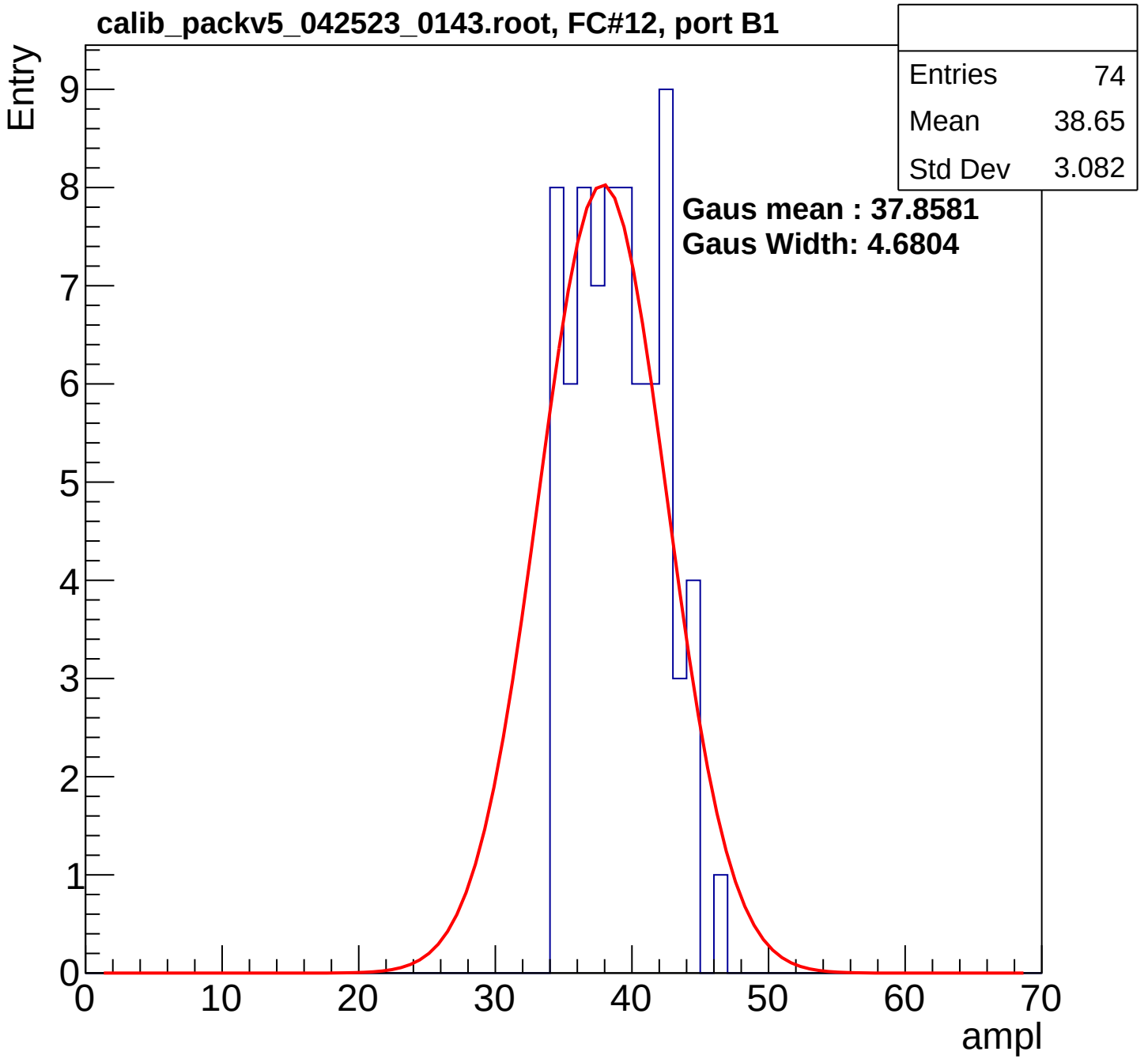
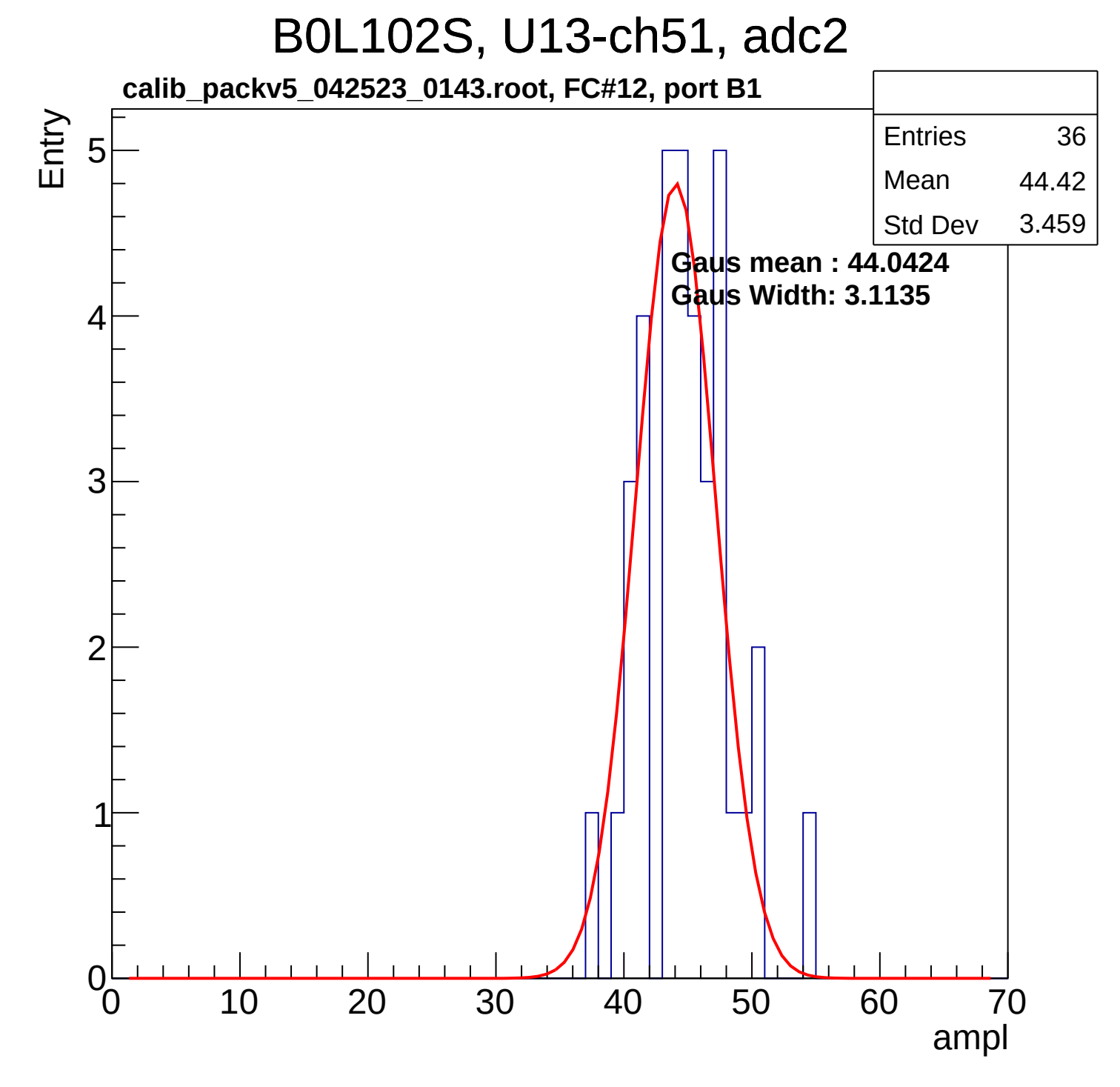
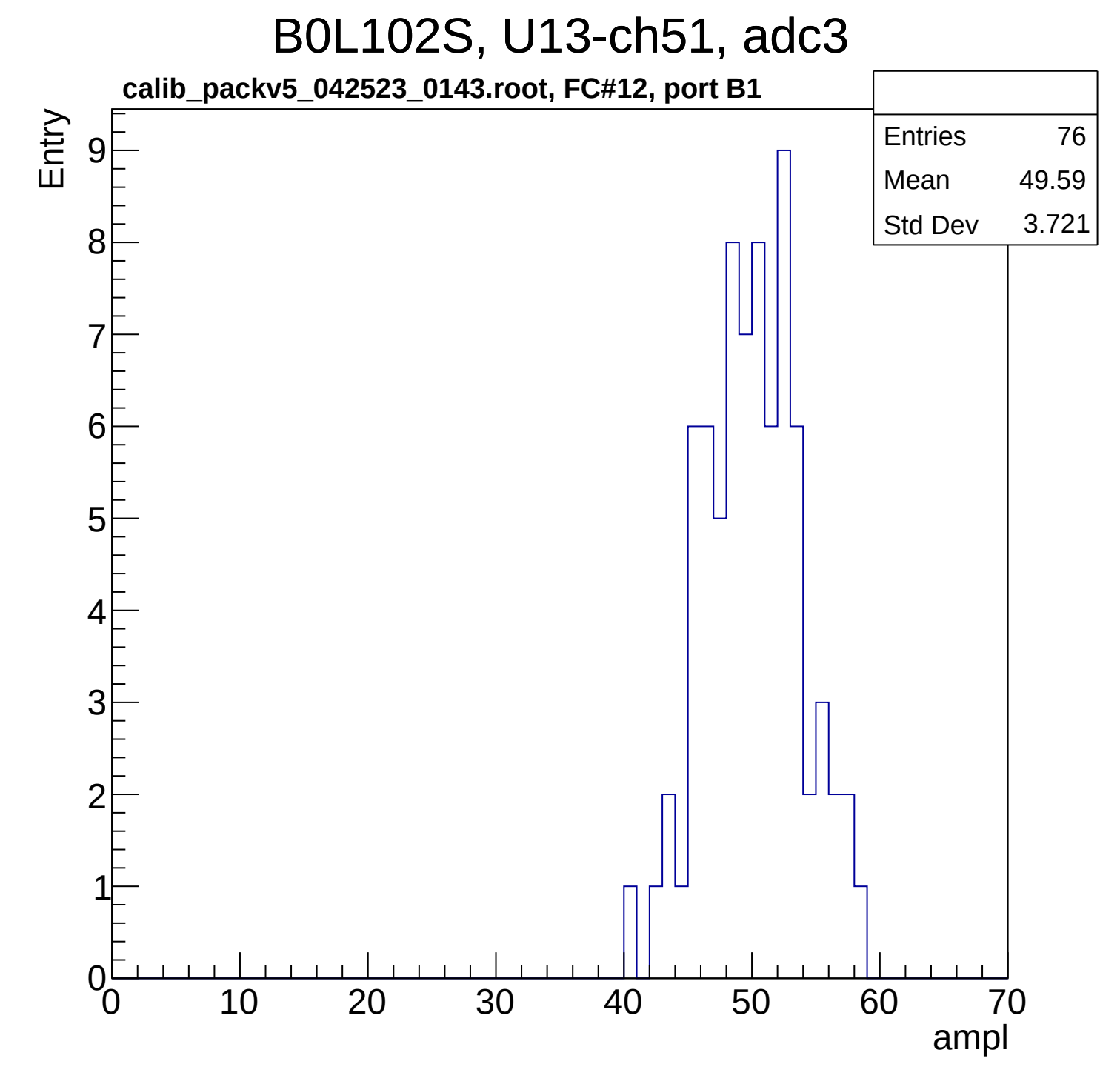

B0L102S, U13-ch51, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 38.65
Std Dev 3.082
Gaus mean : 37.8581
Gaus Width: 4.6804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch51, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 44.42
Std Dev 3.459
Gaus mean : 44.0424
Gaus Width: 3.1135
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U13-ch51, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.59
Std Dev 3.721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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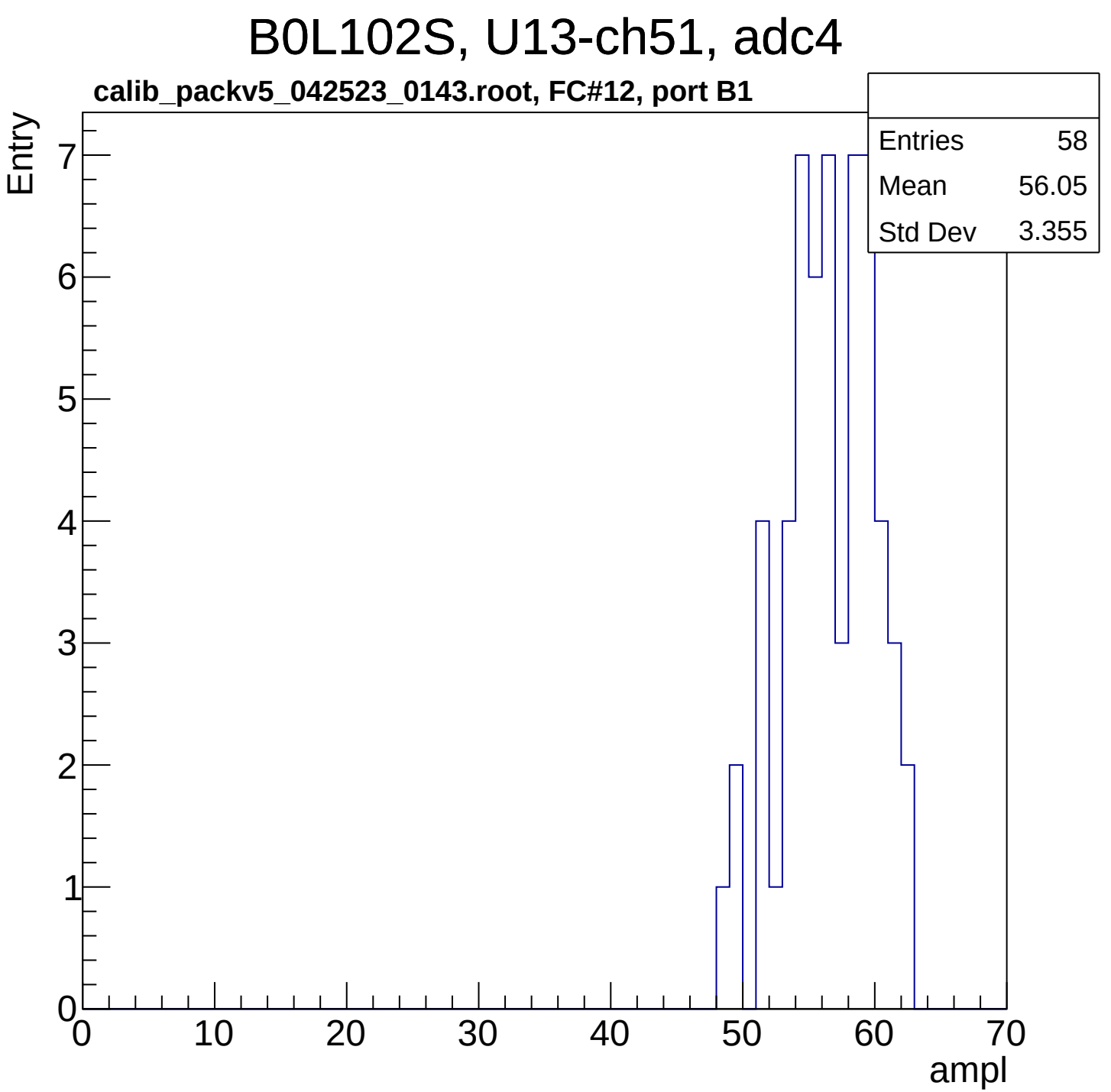
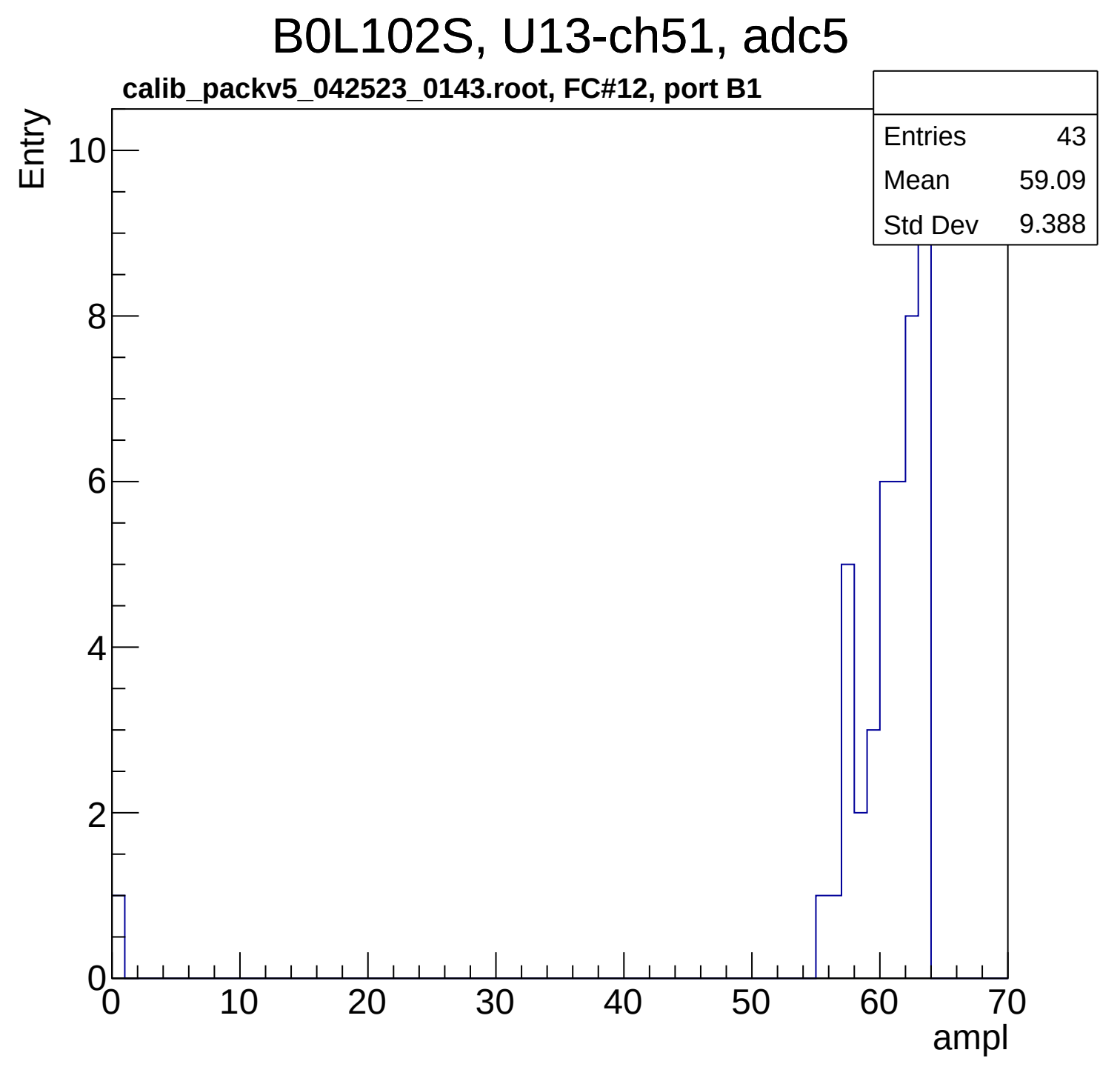
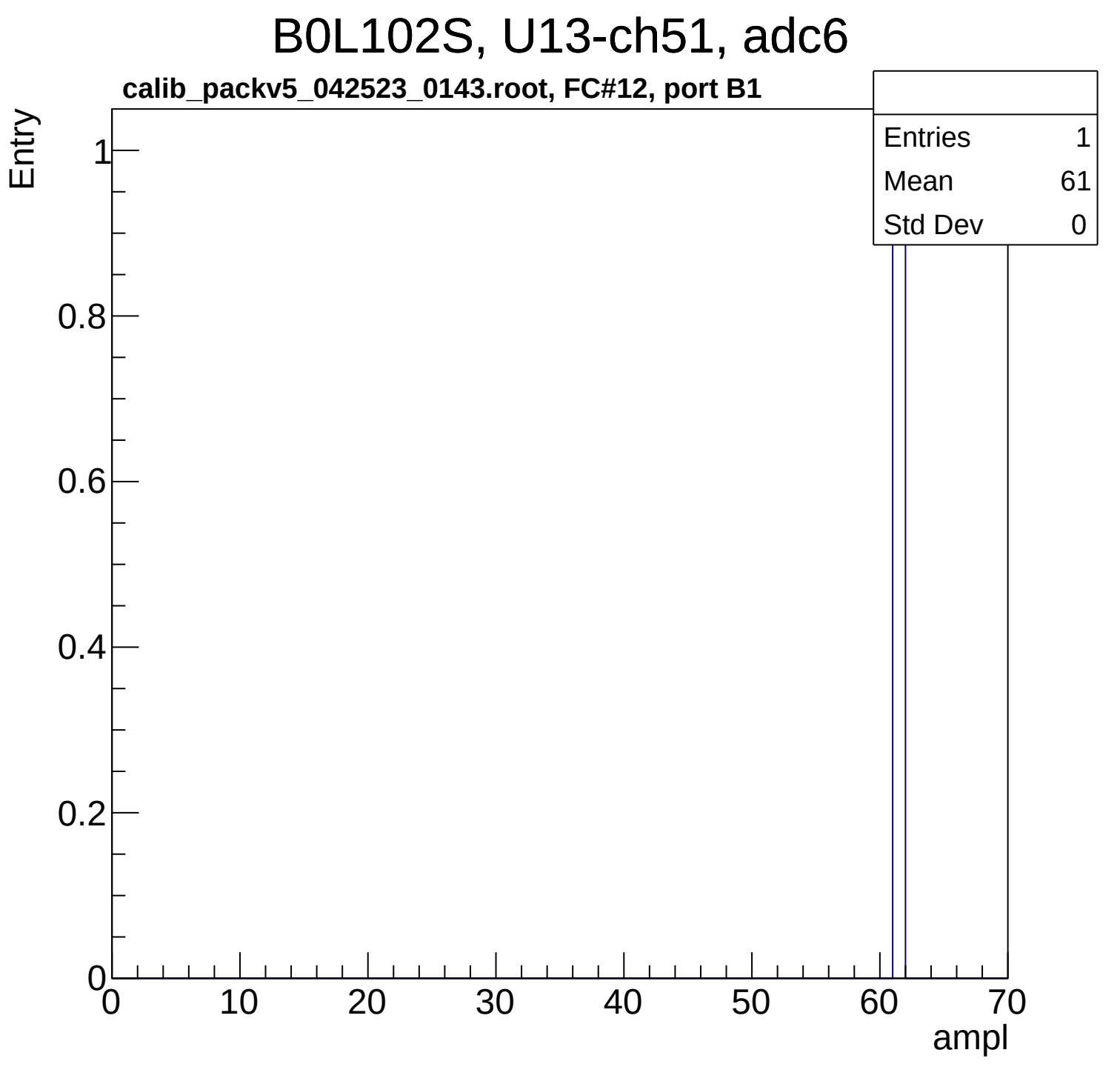

B0L102S, U13-ch51, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 56.05
Std Dev 3.355
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch51, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.09
Std Dev 9.388
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch51, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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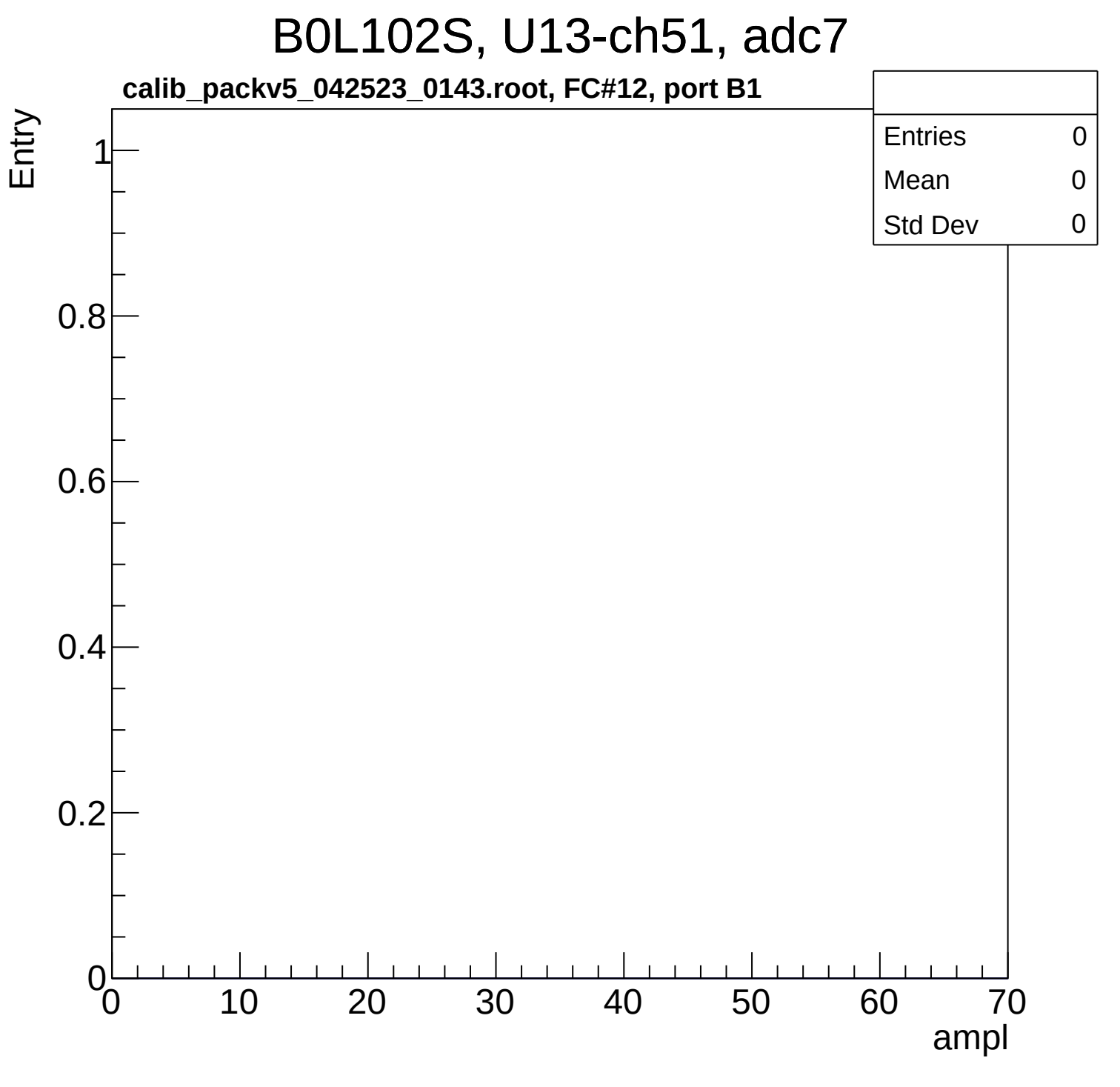
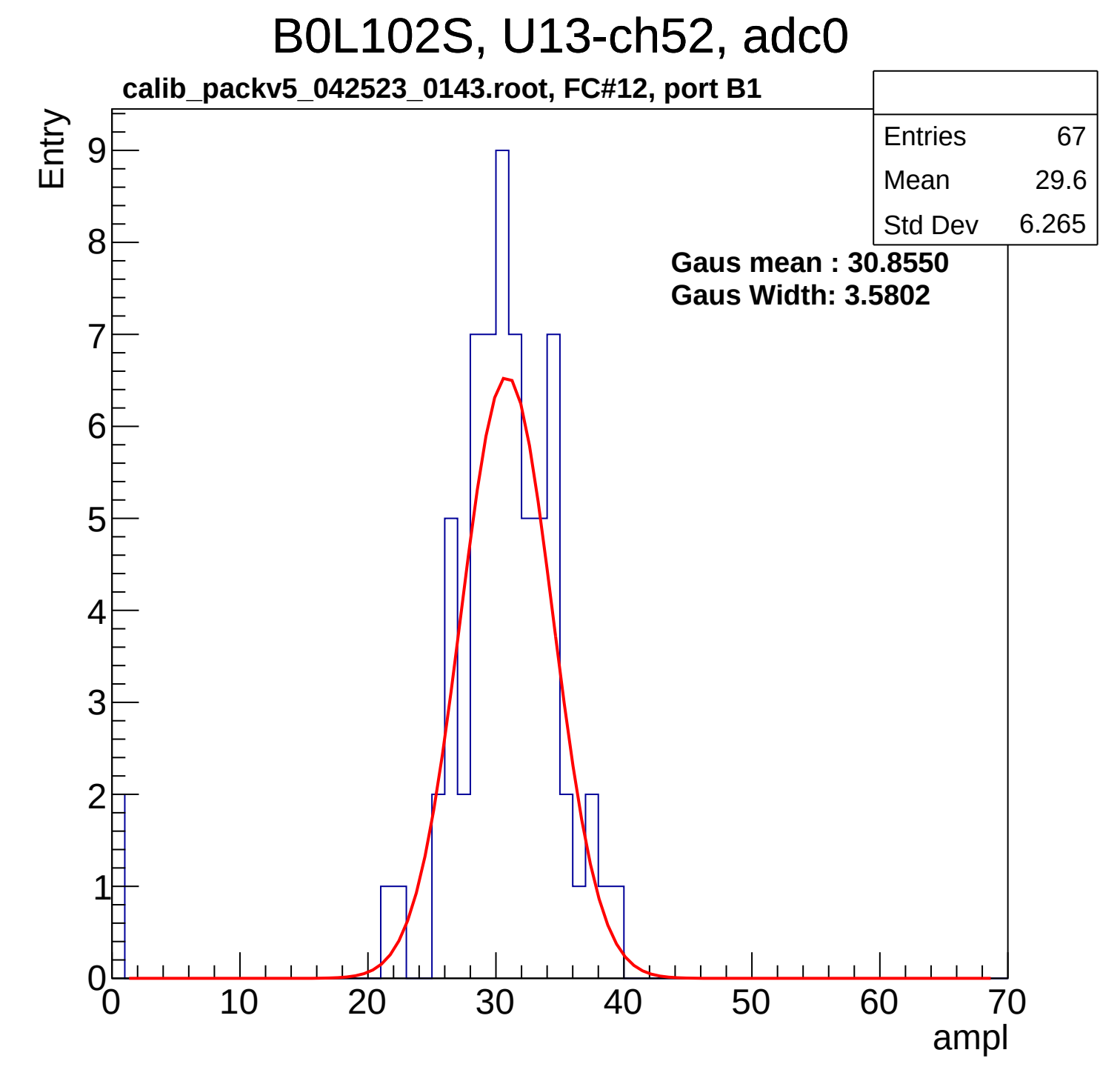
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch51, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch52, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.6
Std Dev 6.265
Gaus mean : 30.8550
Gaus Width: 3.5802
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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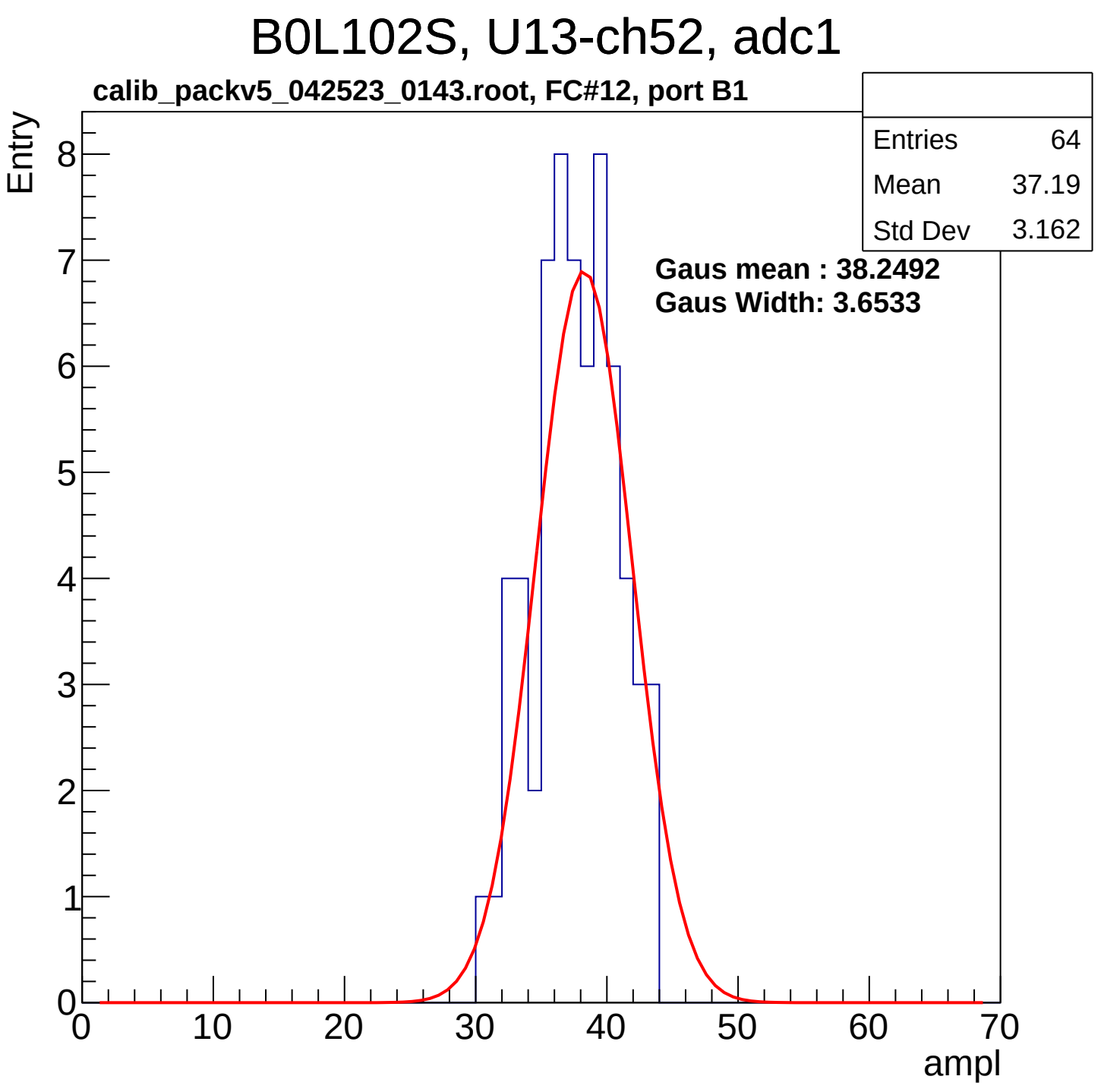
B0L102S, U13-ch52, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 37.19
Std Dev 3.162
Gaus mean : 38.2492
Gaus Width: 3.6533
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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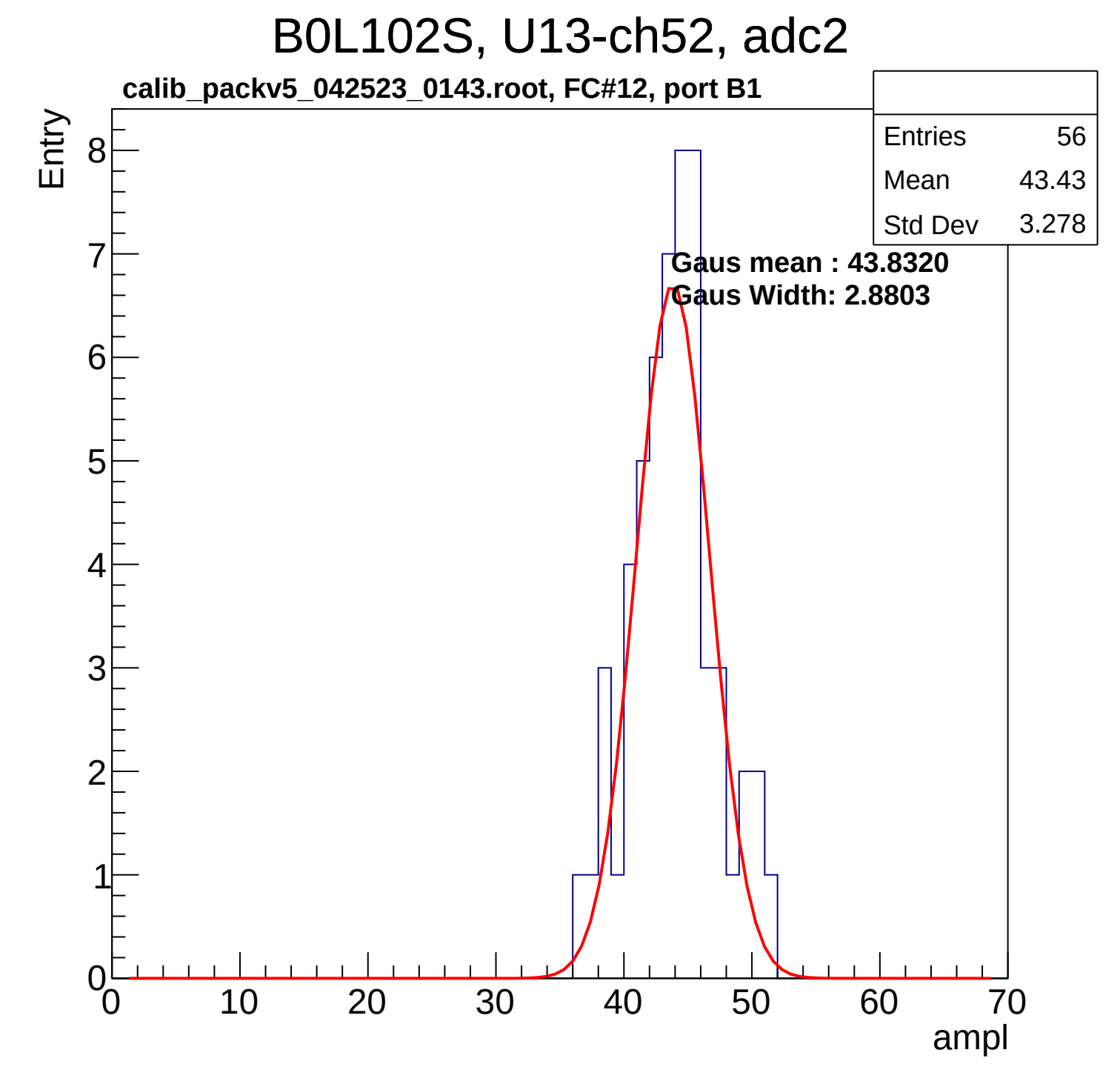
B0L102S, U13-ch52, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 43.43
Std Dev 3.278
Gaus mean : 43.8320
Gaus Width: 2.8803
Entry
ampl
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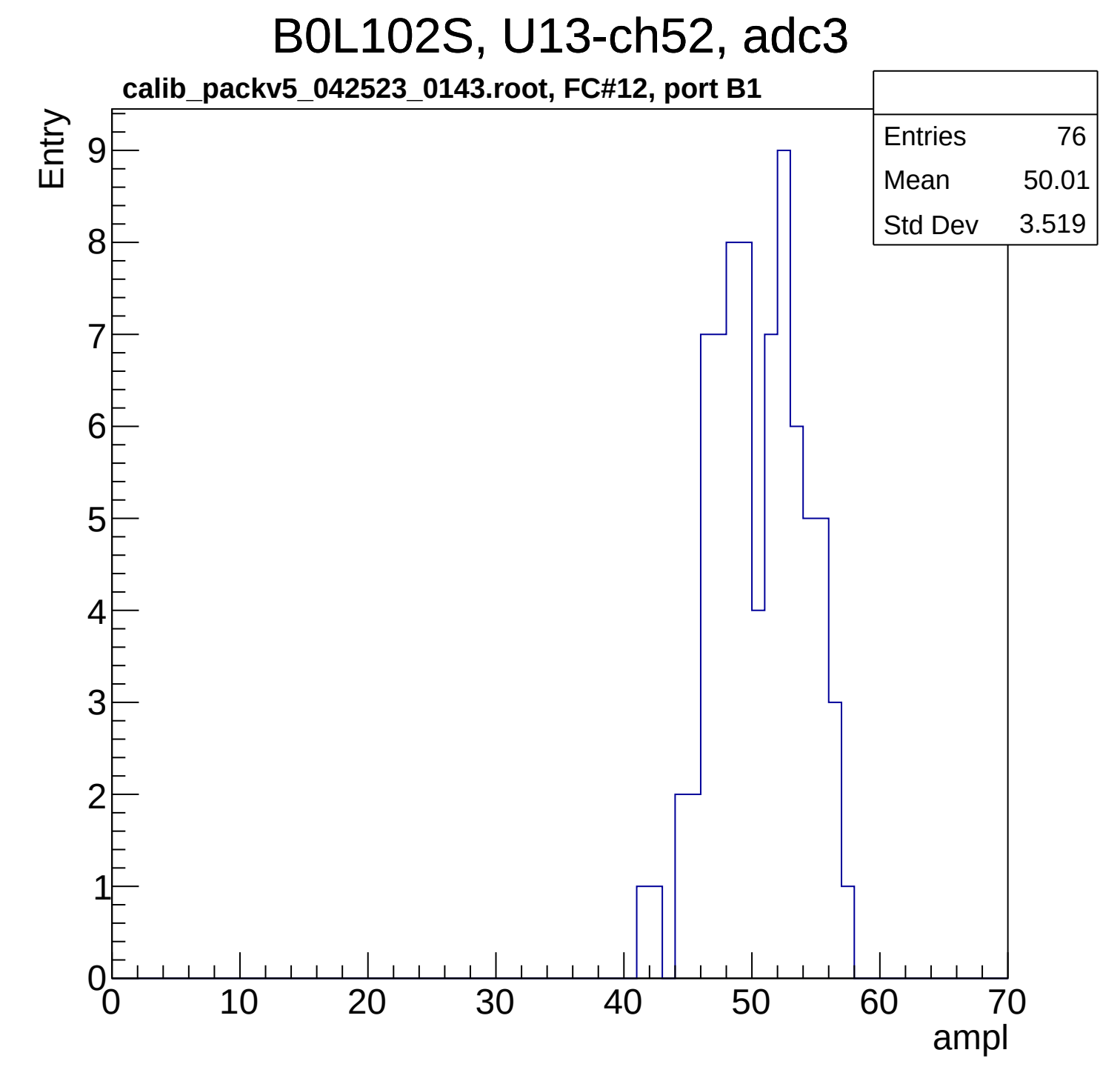
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch52, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 50.01
Std Dev 3.519
Entry
ampl
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2
3
4
5
6
7
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0
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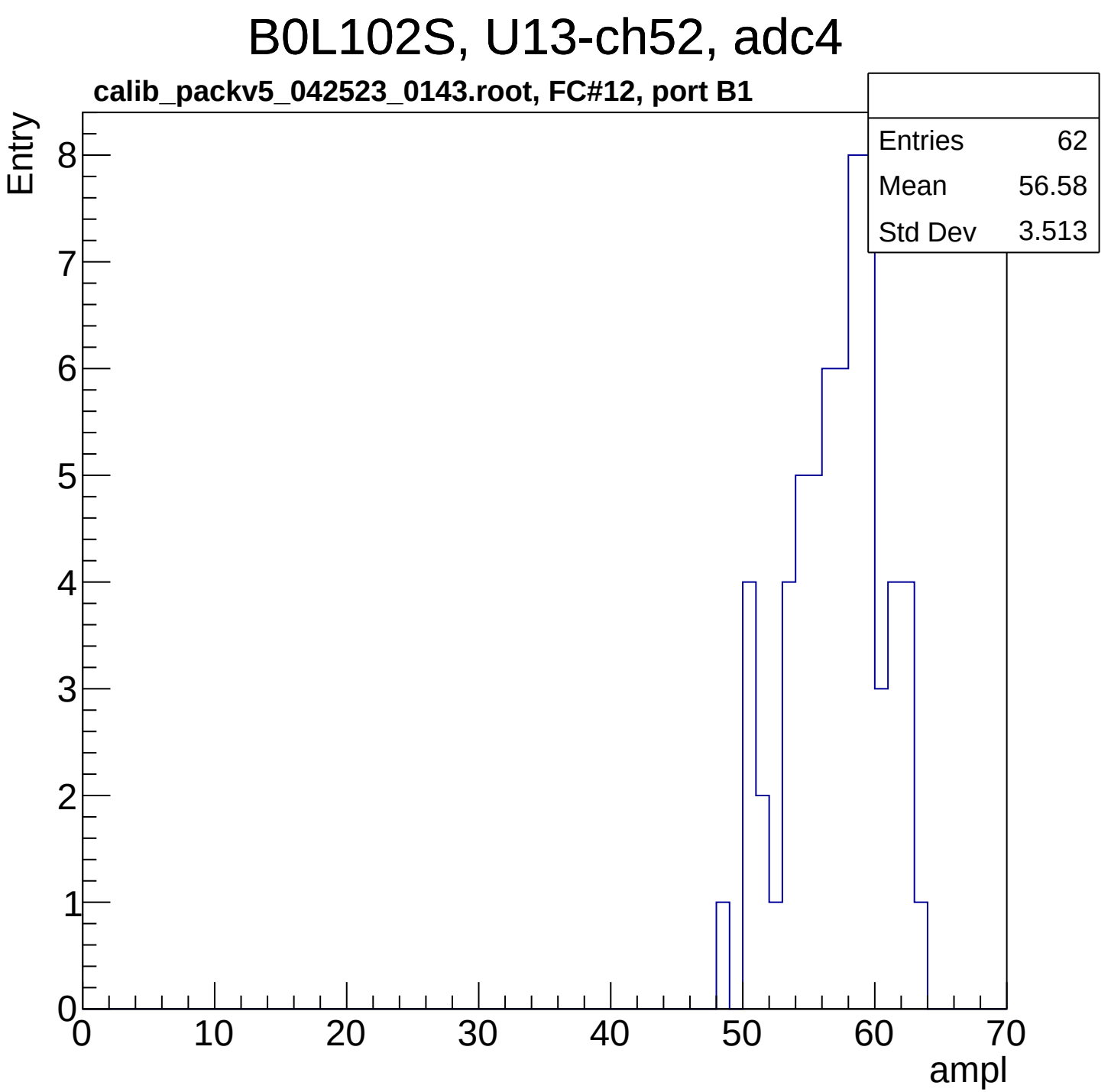
B0L102S, U13-ch52, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 56.58
Std Dev 3.513
Entry
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4
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6
7
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10
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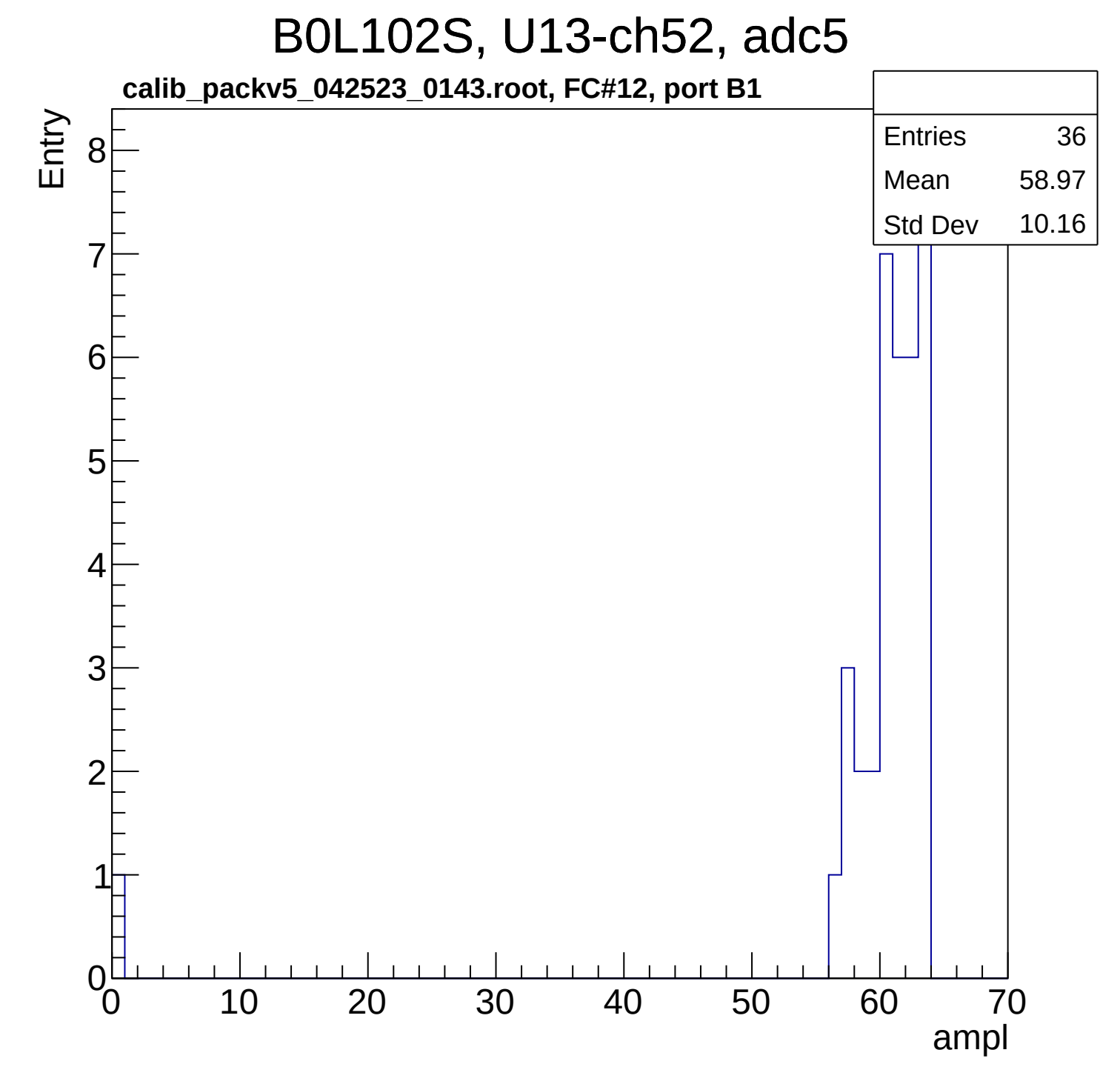
50
60
70
ampl


B0L102S, U13-ch52, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.97
Std Dev 10.16
Entry
ampl
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1
2
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4
5
6
7
8
0
10
20
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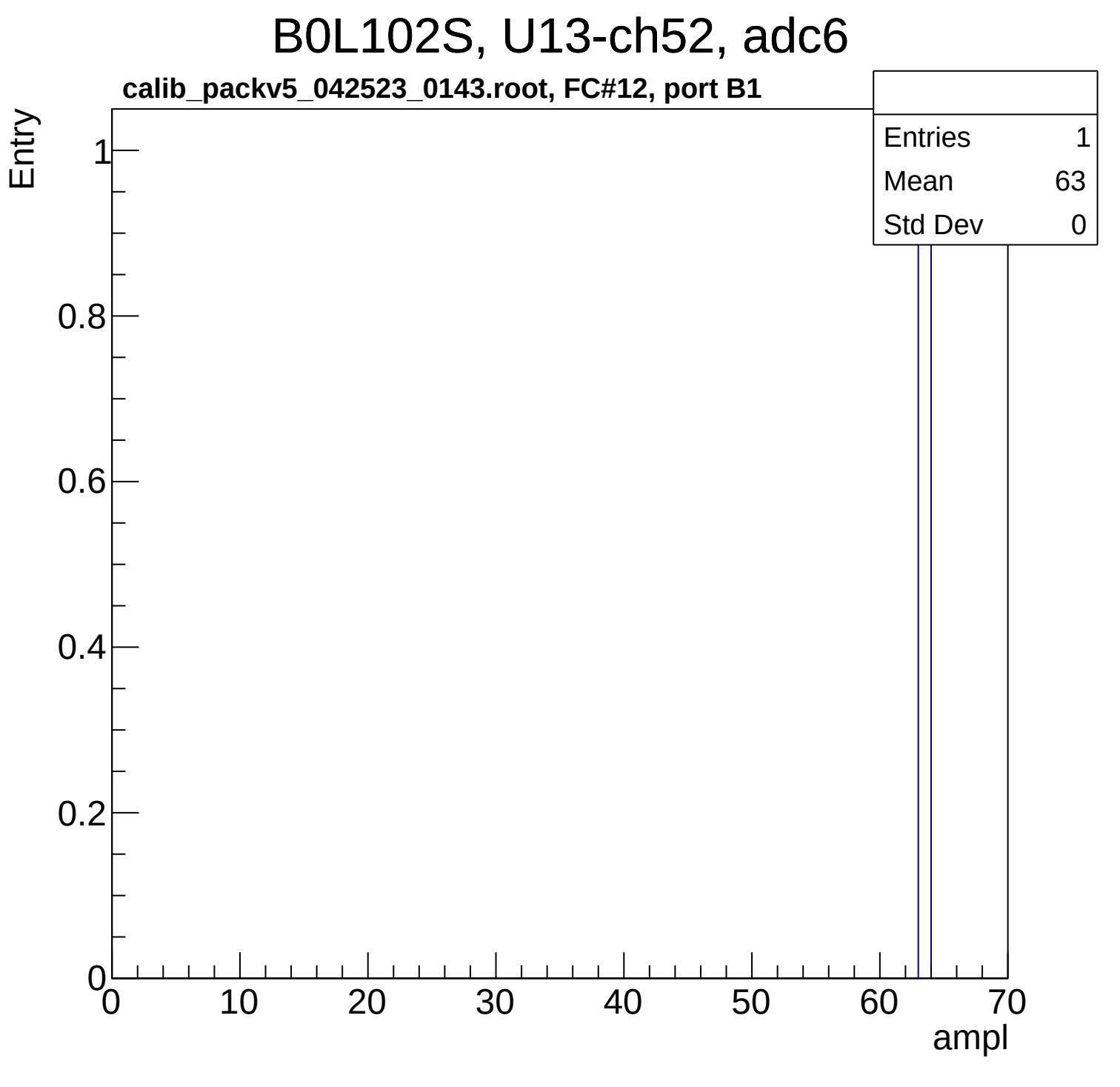
50
60
70


B0L102S, U13-ch52, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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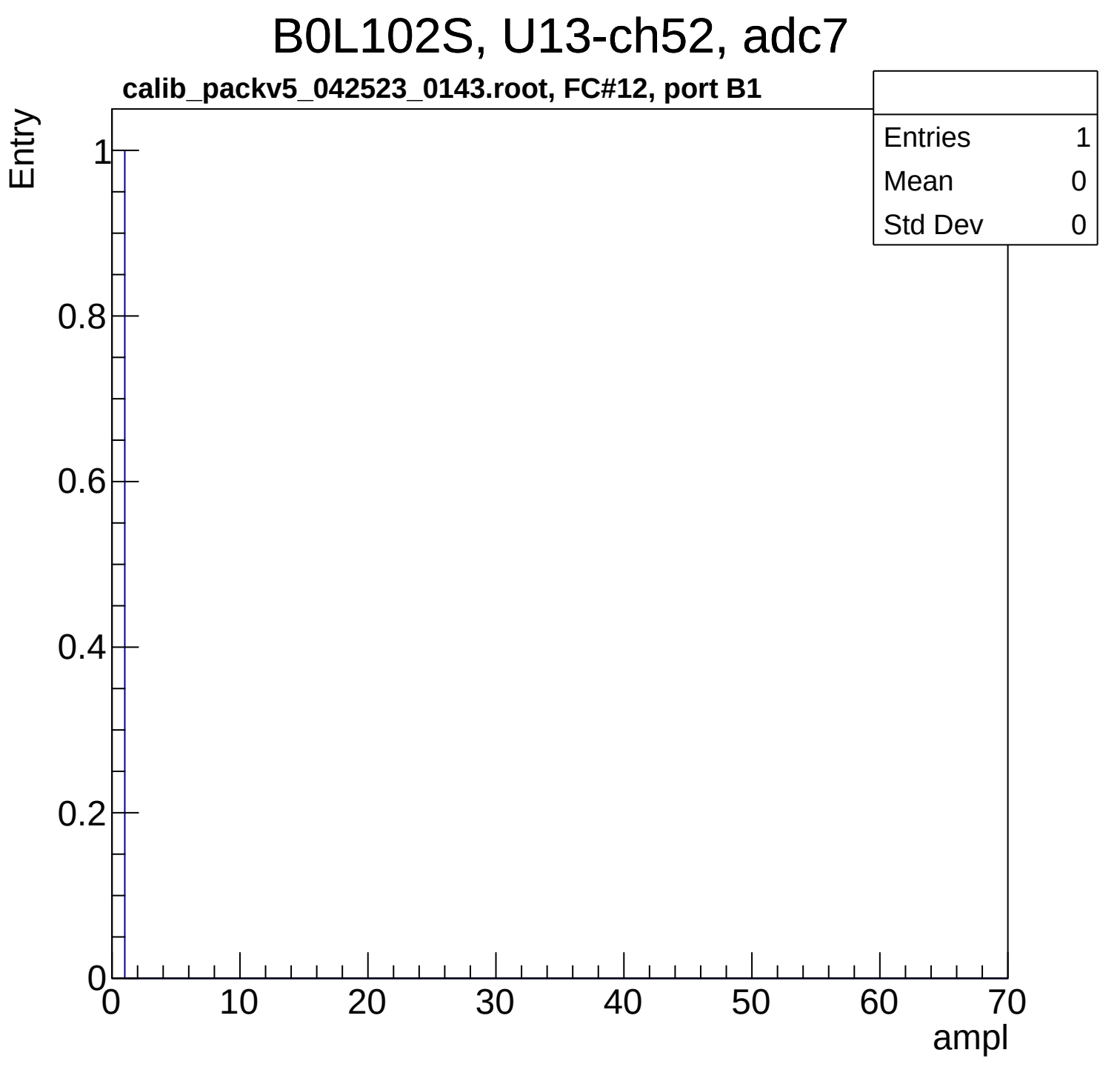
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch52, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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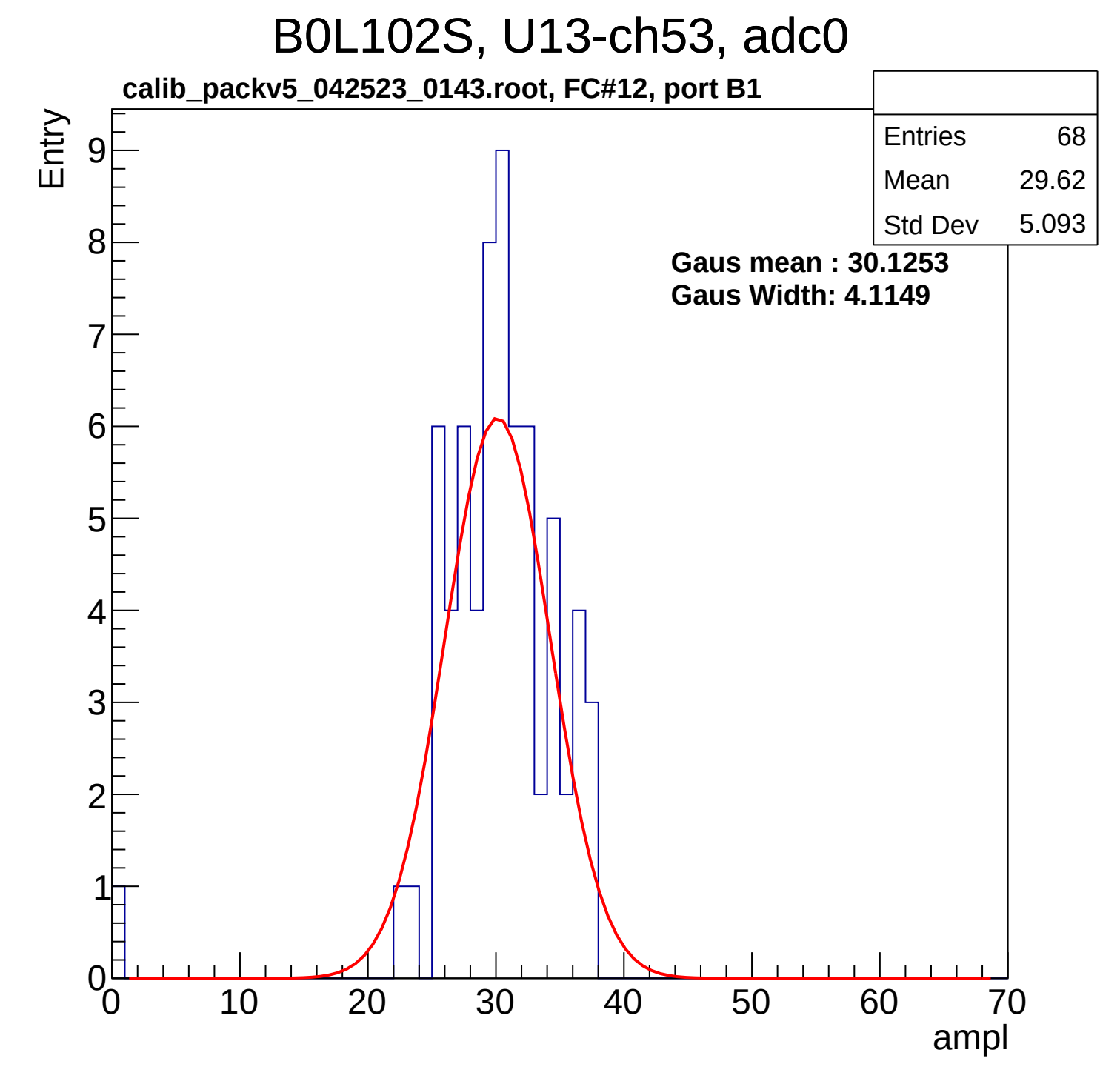
50
60
70
ampl


B0L102S, U13-ch53, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.62
Std Dev 5.093
Gaus mean : 30.1253
Gaus Width: 4.1149
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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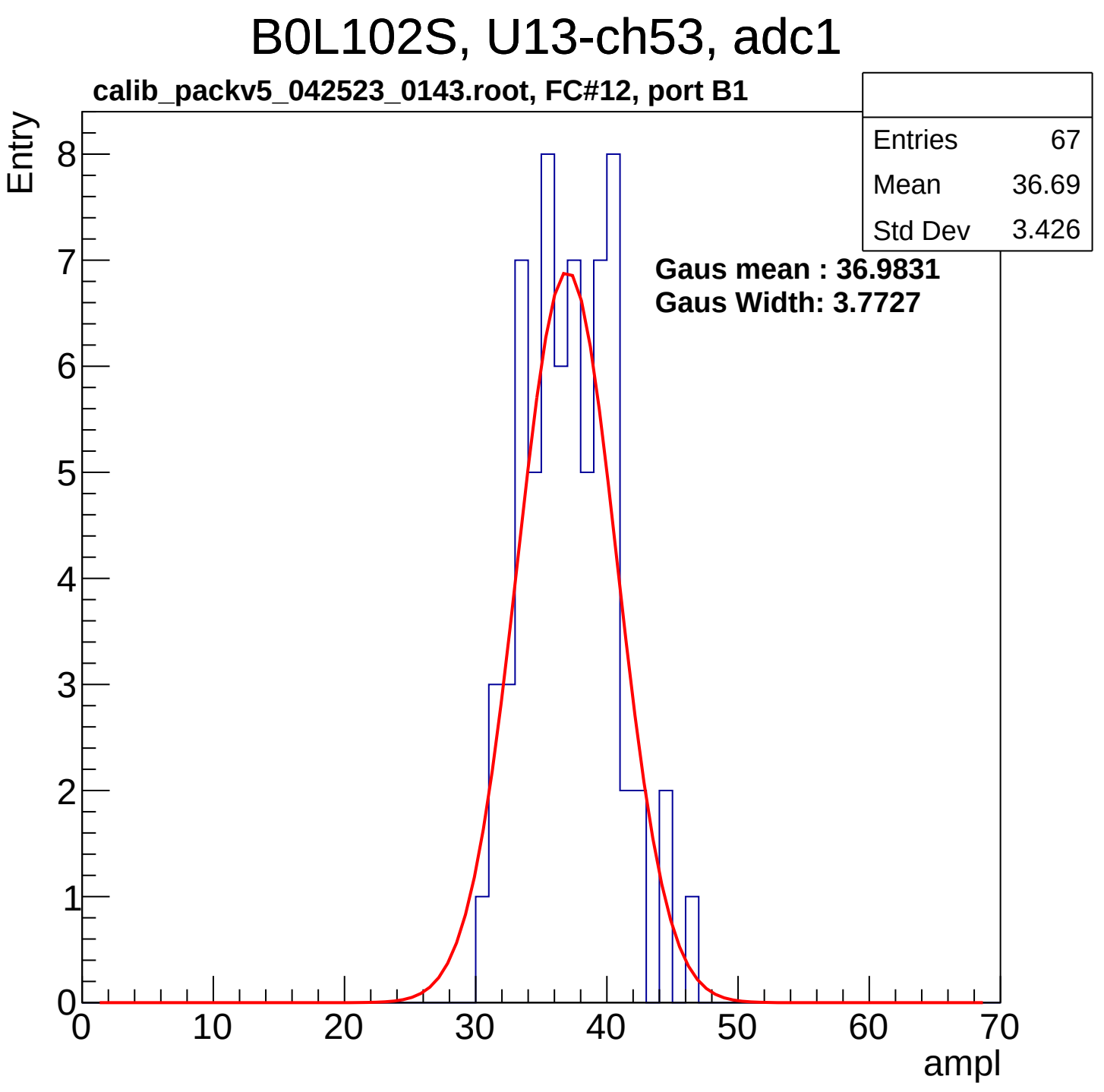
B0L102S, U13-ch53, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 36.69
Std Dev 3.426
Gaus mean : 36.9831
Gaus Width: 3.7727
Entry
ampl
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2
3
4
5
6
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0
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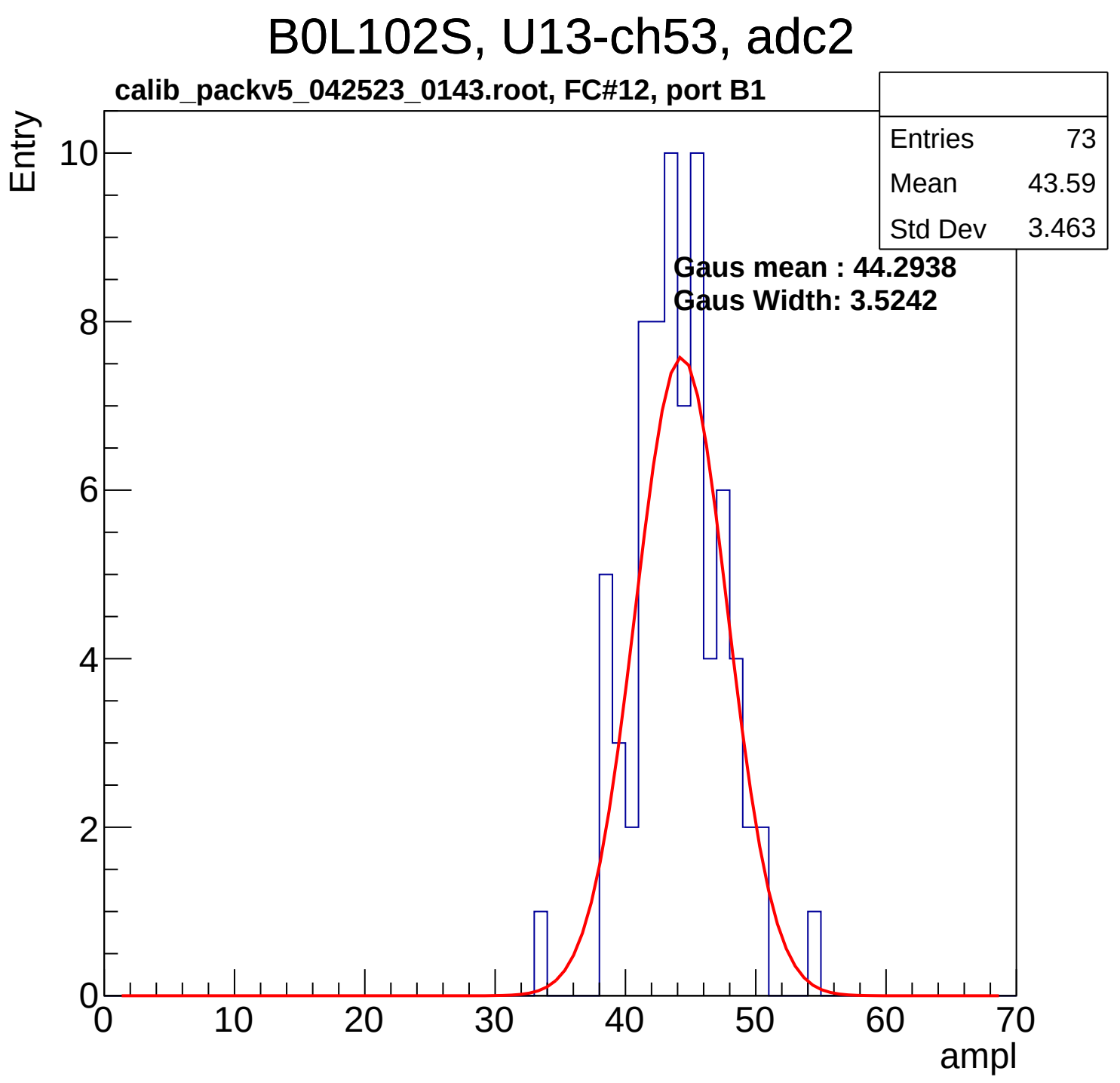
B0L102S, U13-ch53, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 43.59
Std Dev 3.463
Gaus mean : 44.2938
Gaus Width: 3.5242
Entry
ampl
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2
4
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0
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60
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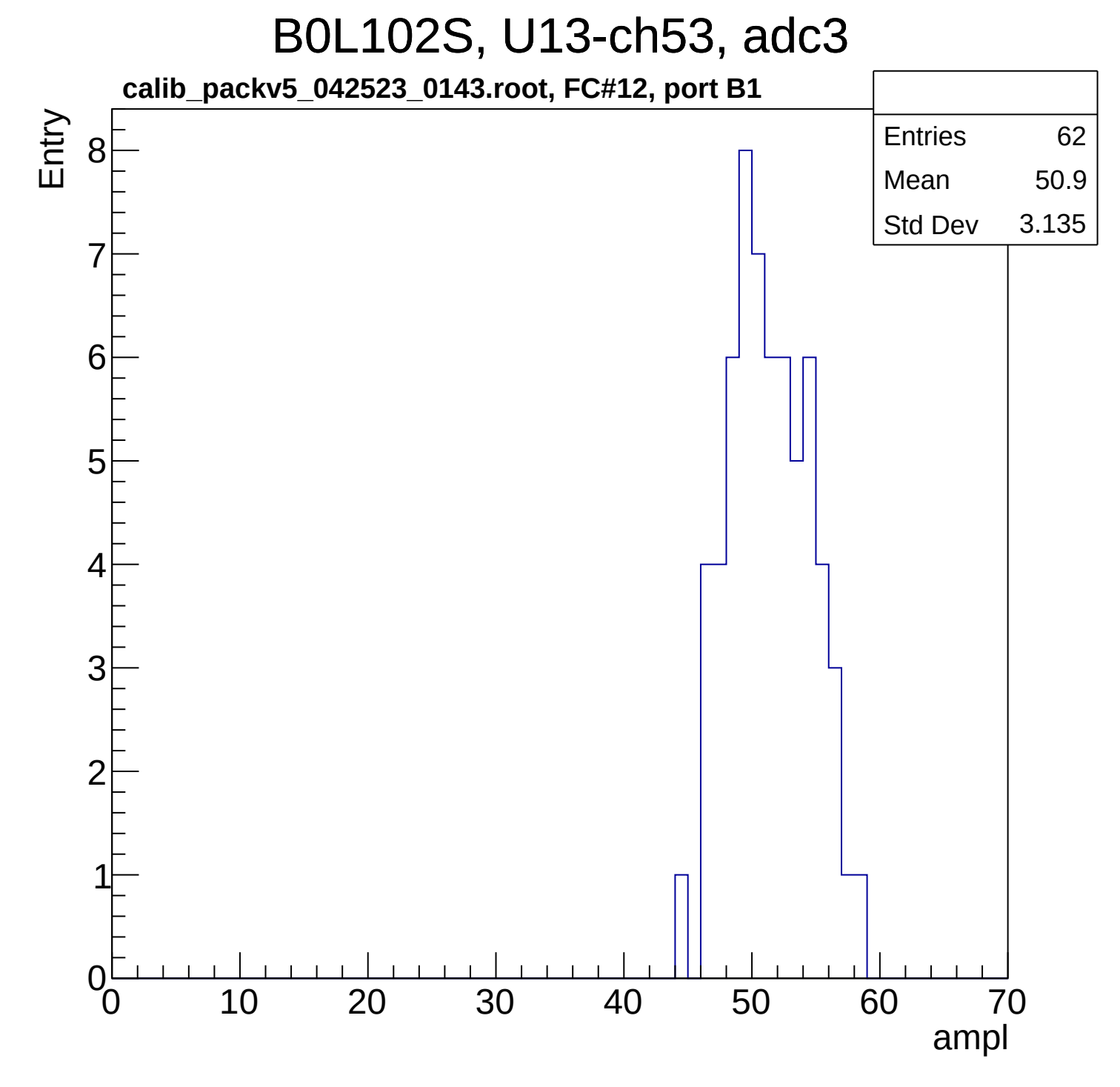
B0L102S, U13-ch53, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 50.9
Std Dev 3.135
Entry
ampl
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2
3
4
5
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7
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0
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60
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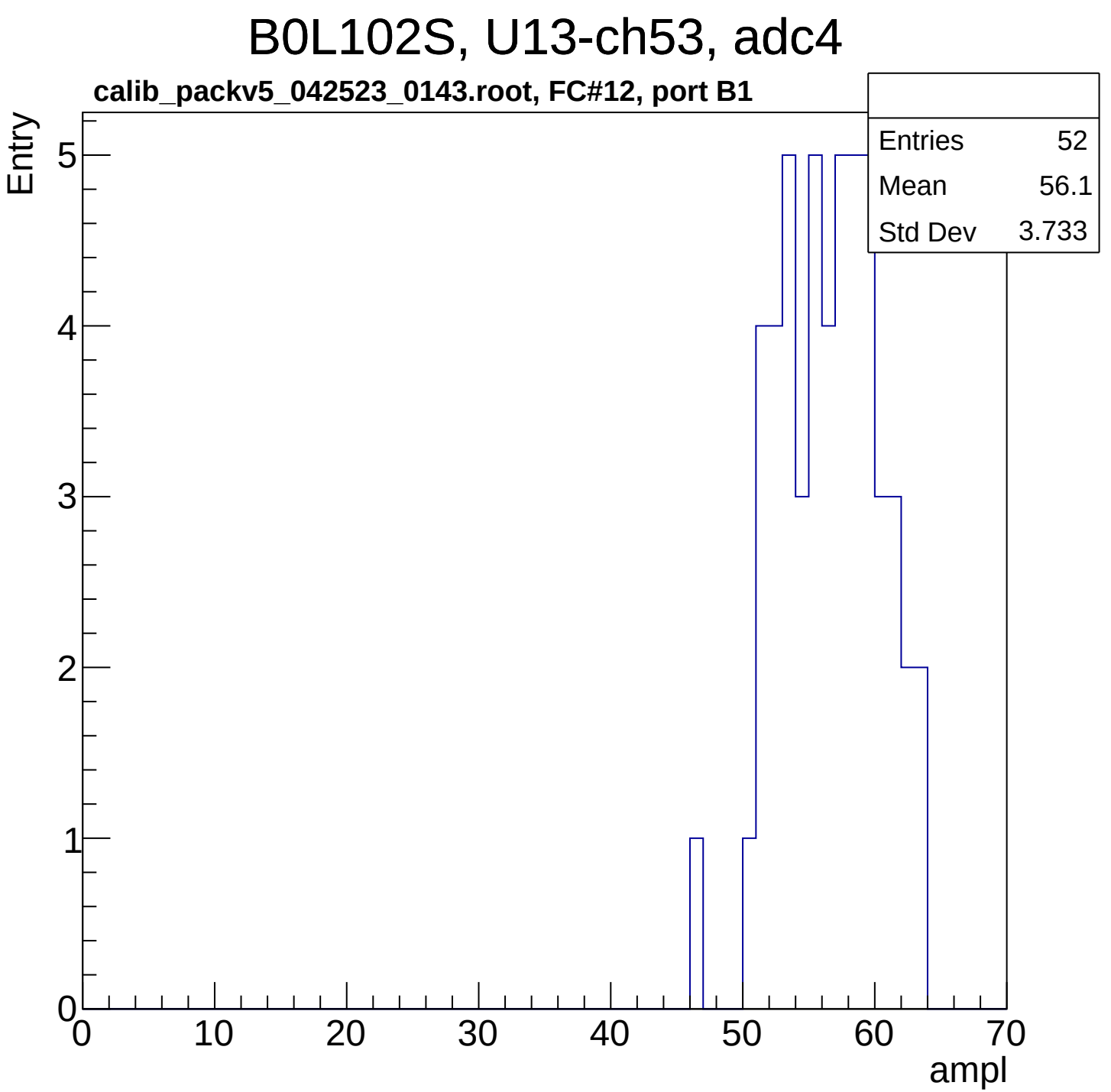
B0L102S, U13-ch53, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 56.1
Std Dev 3.733
Entry
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1
2
3
4
5
0
10
20
30
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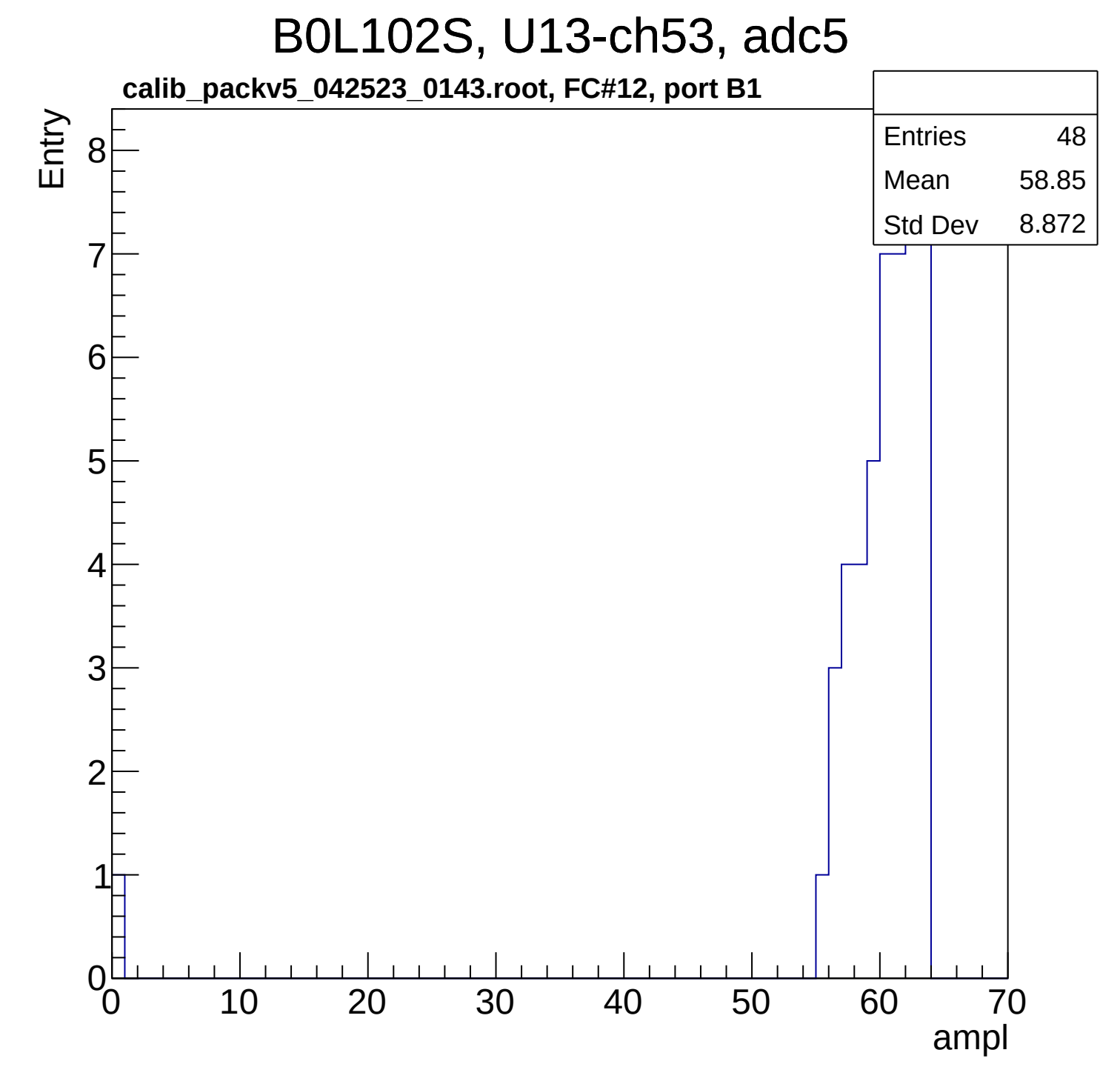
50
60
70
ampl


B0L102S, U13-ch53, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.85
Std Dev 8.872
Entry
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2
3
4
5
6
7
8
0
10
20
30
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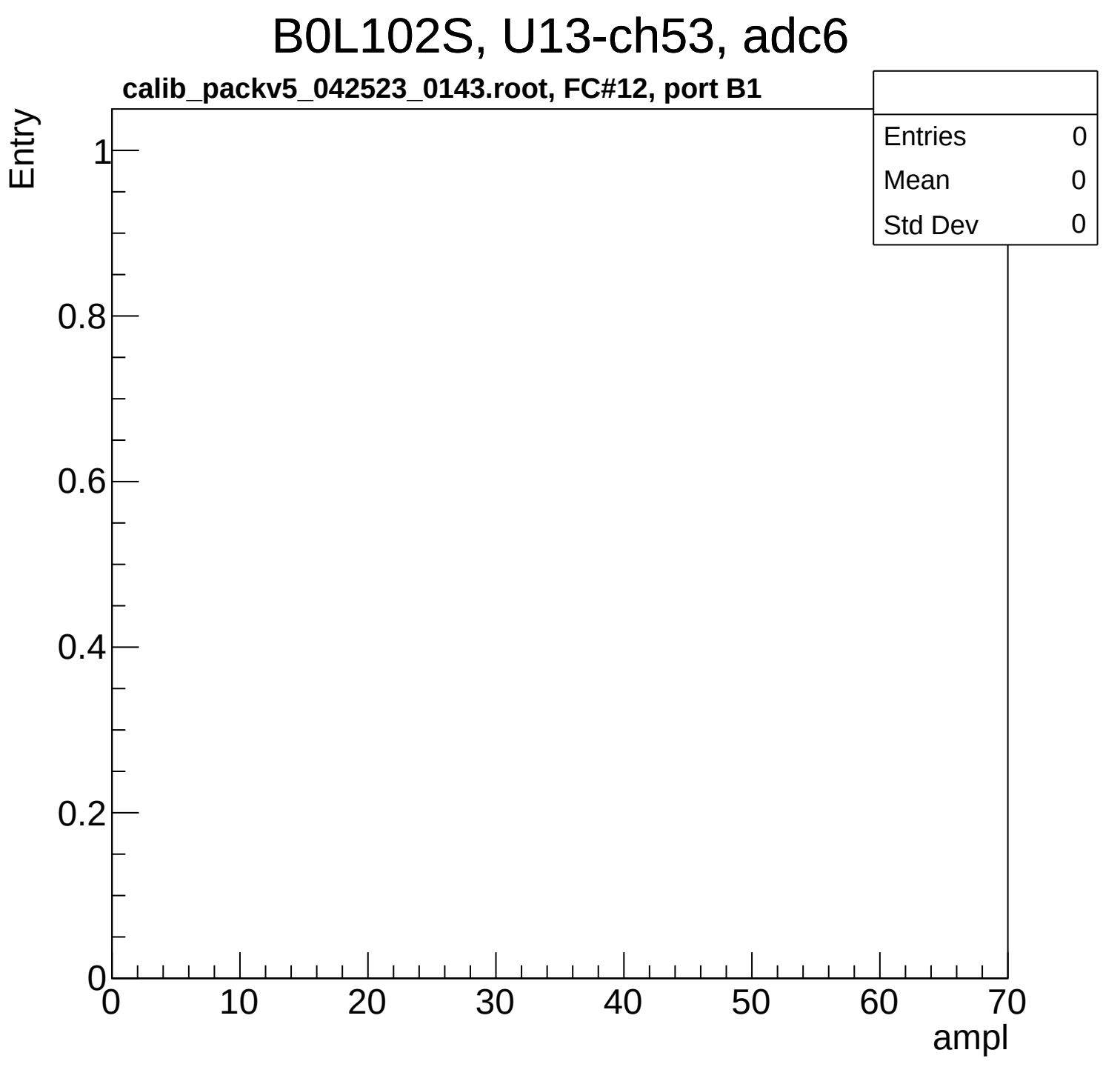
50
60
70
ampl


B0L102S, U13-ch53, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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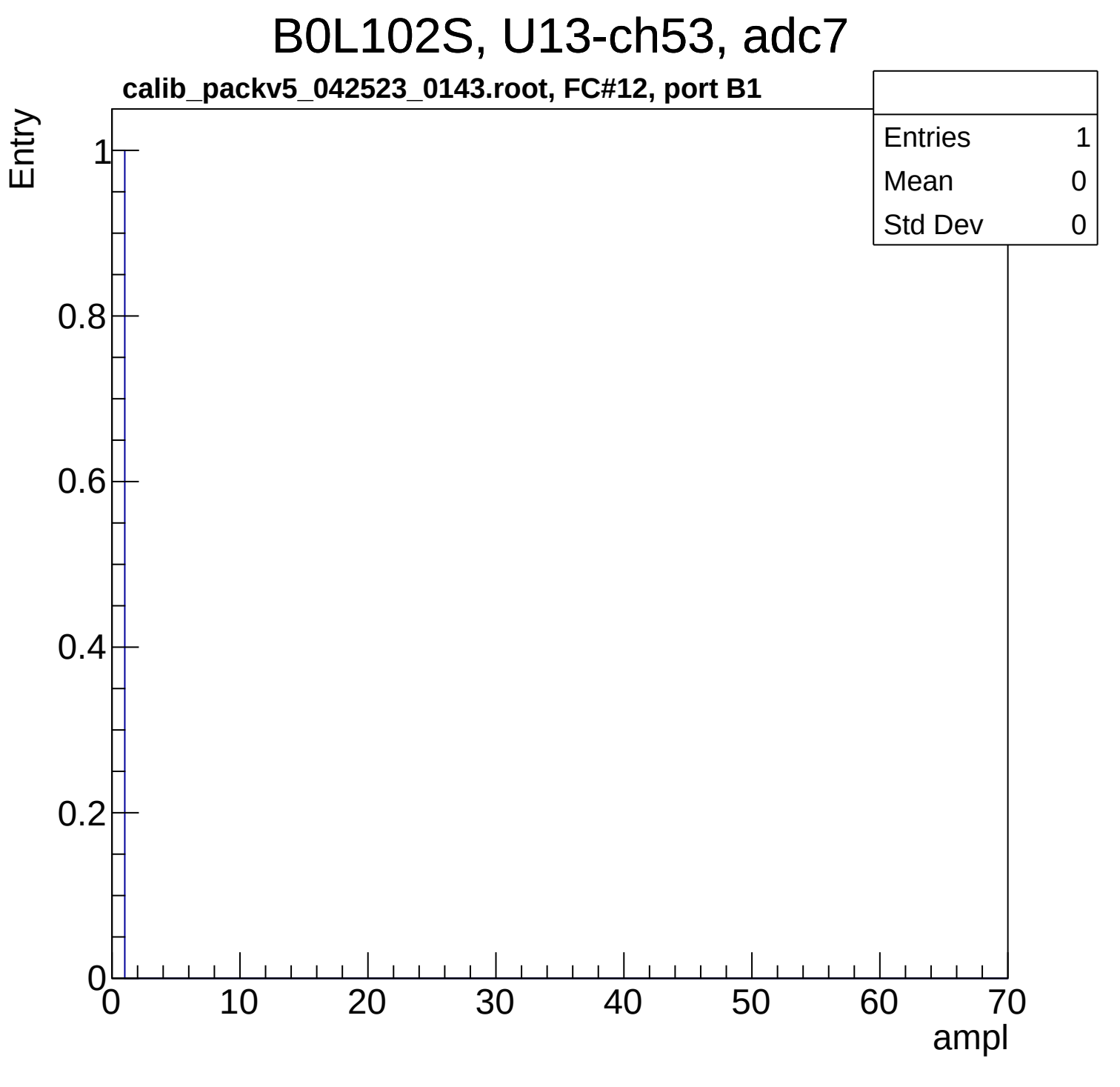
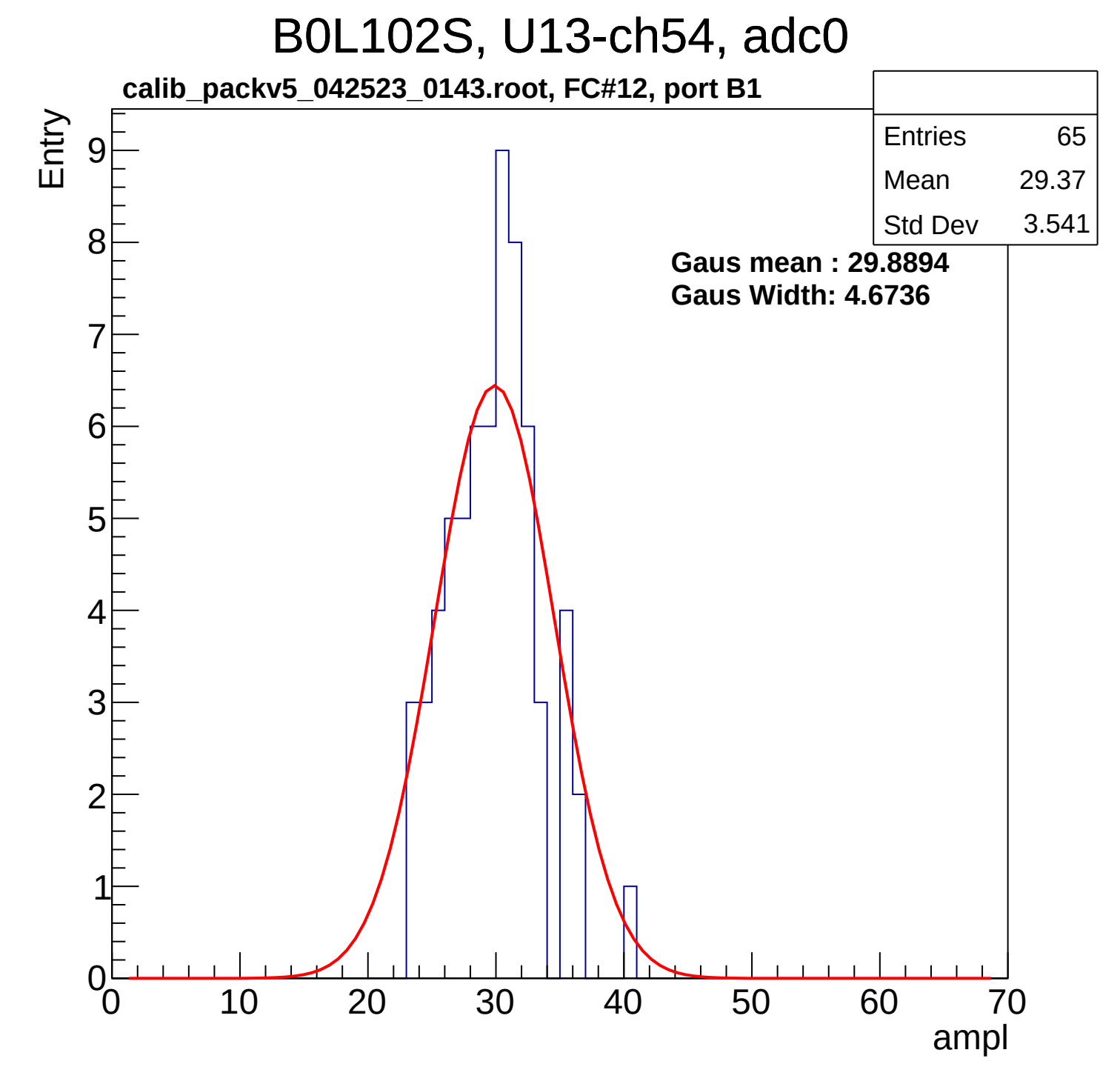
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch53, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch54, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 29.37
Std Dev 3.541
Gaus mean : 29.8894
Gaus Width: 4.6736
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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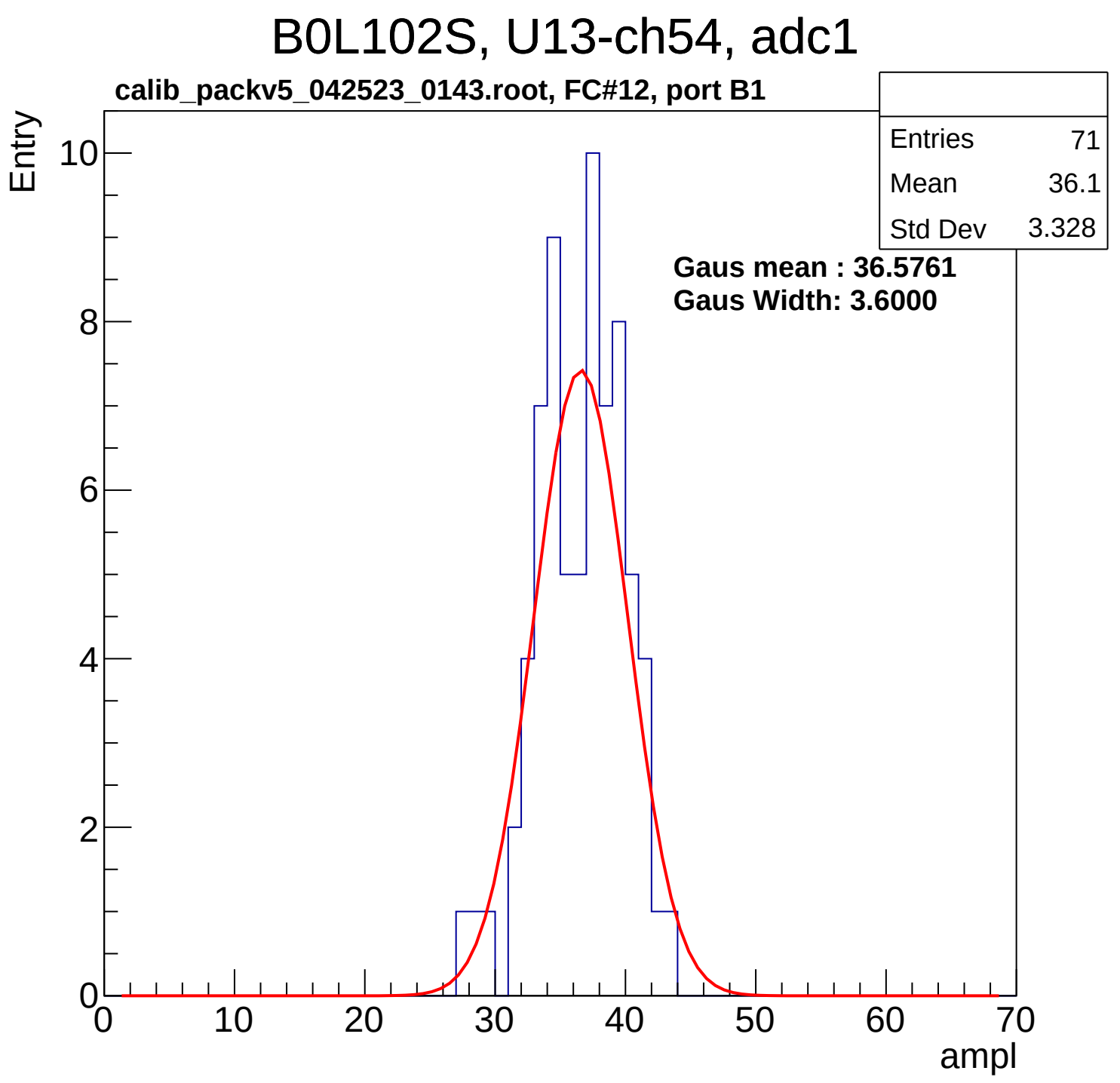
B0L102S, U13-ch54, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 36.1
Std Dev 3.328
Gaus mean : 36.5761
Gaus Width: 3.6000
Entry
ampl
0
2
4
6
8
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0
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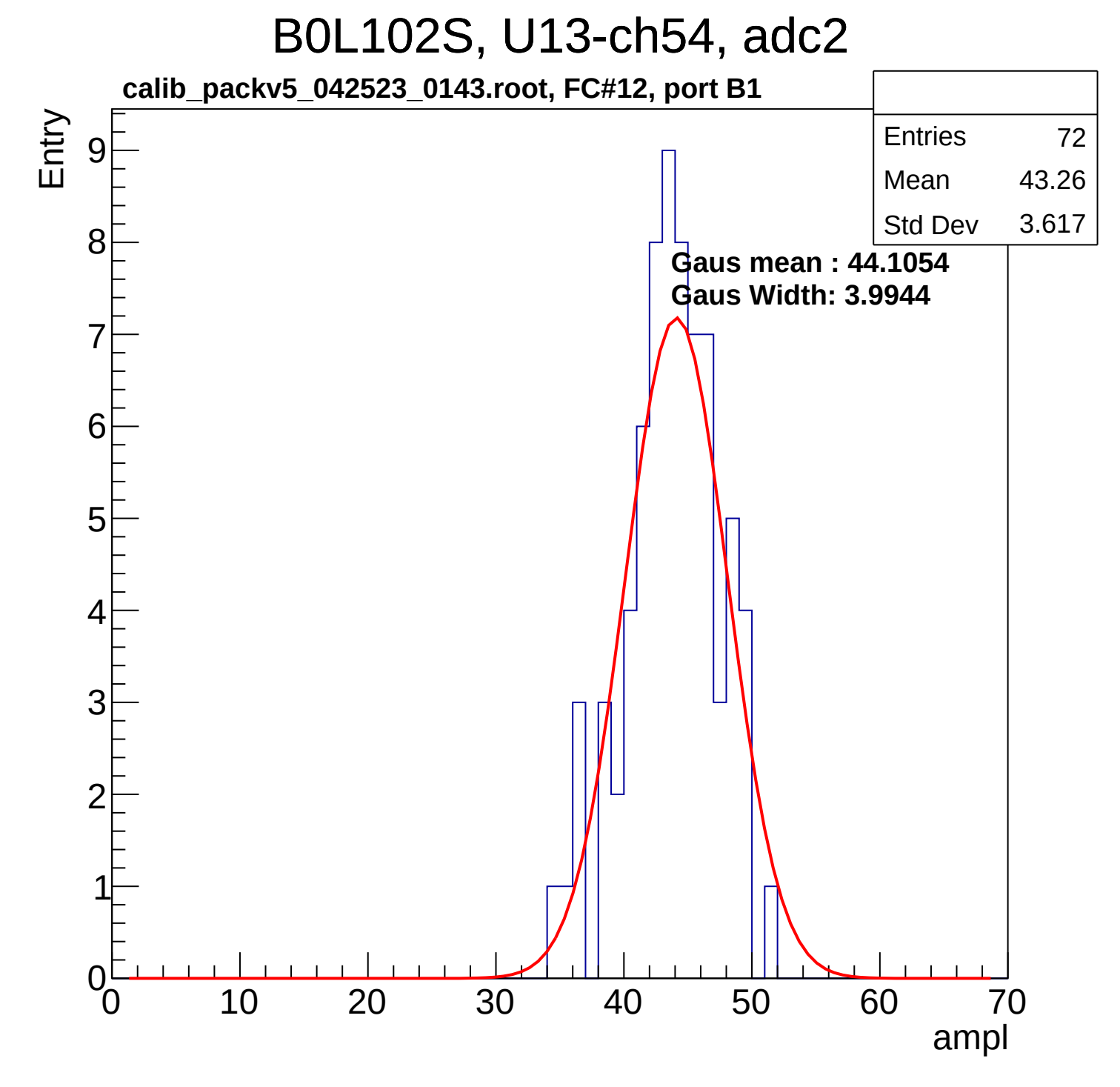
B0L102S, U13-ch54, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 43.26
Std Dev 3.617
Gaus mean : 44.1054
Gaus Width: 3.9944
Entry
ampl
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0
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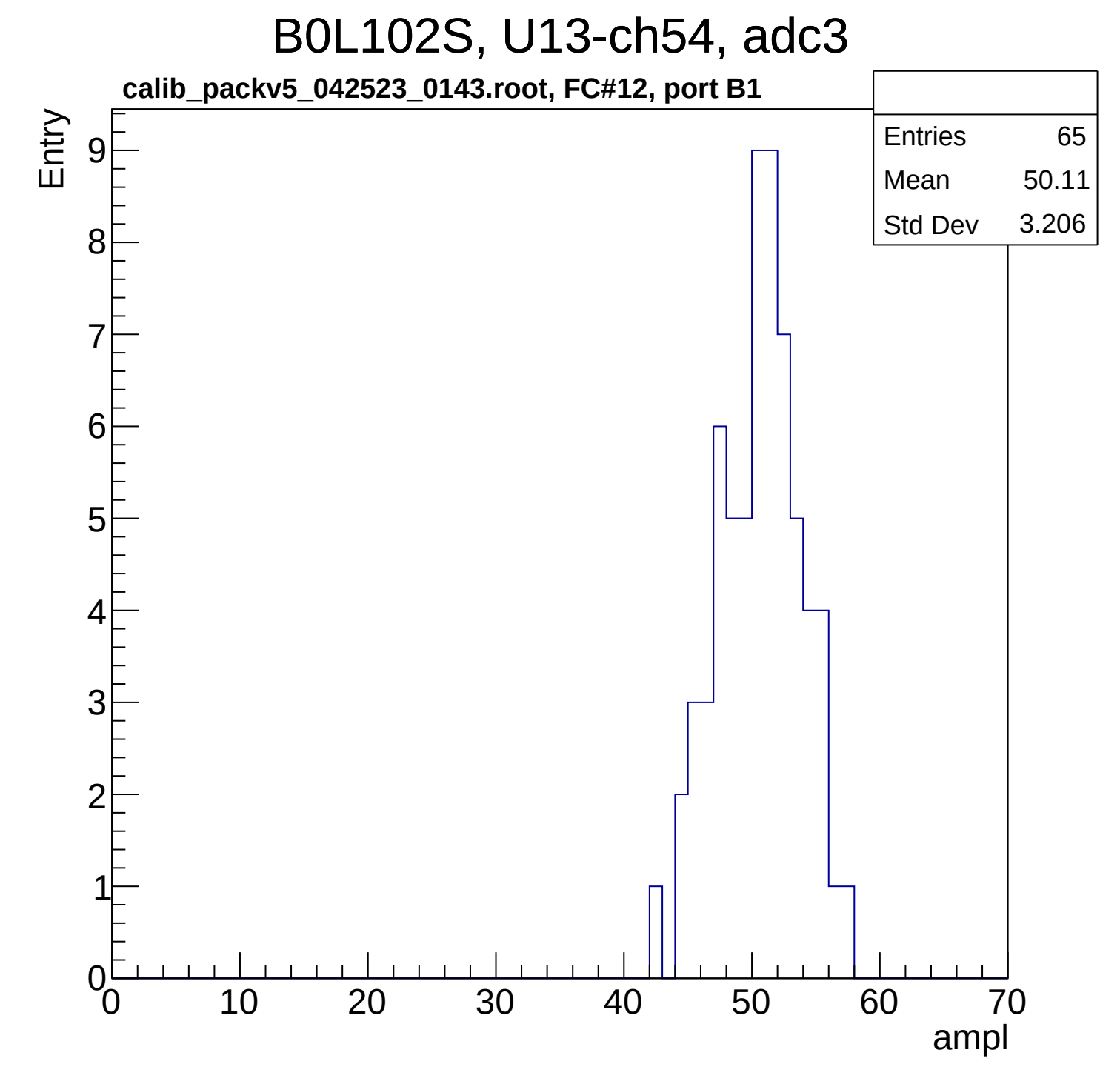
B0L102S, U13-ch54, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.11
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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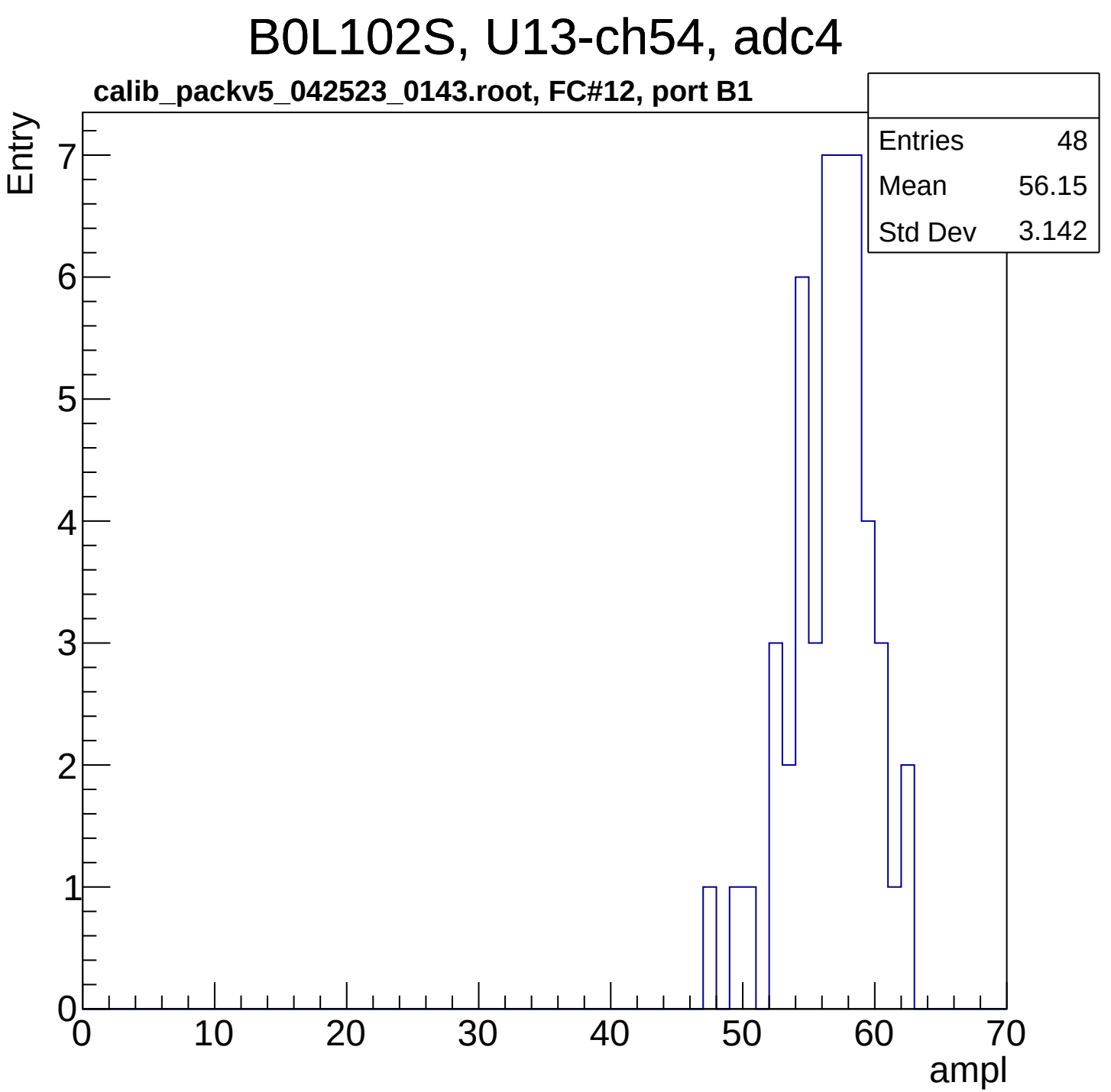
B0L102S, U13-ch54, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 56.15
Std Dev 3.142
Entry
ampl
0
1
2
3
4
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6
7
0
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60
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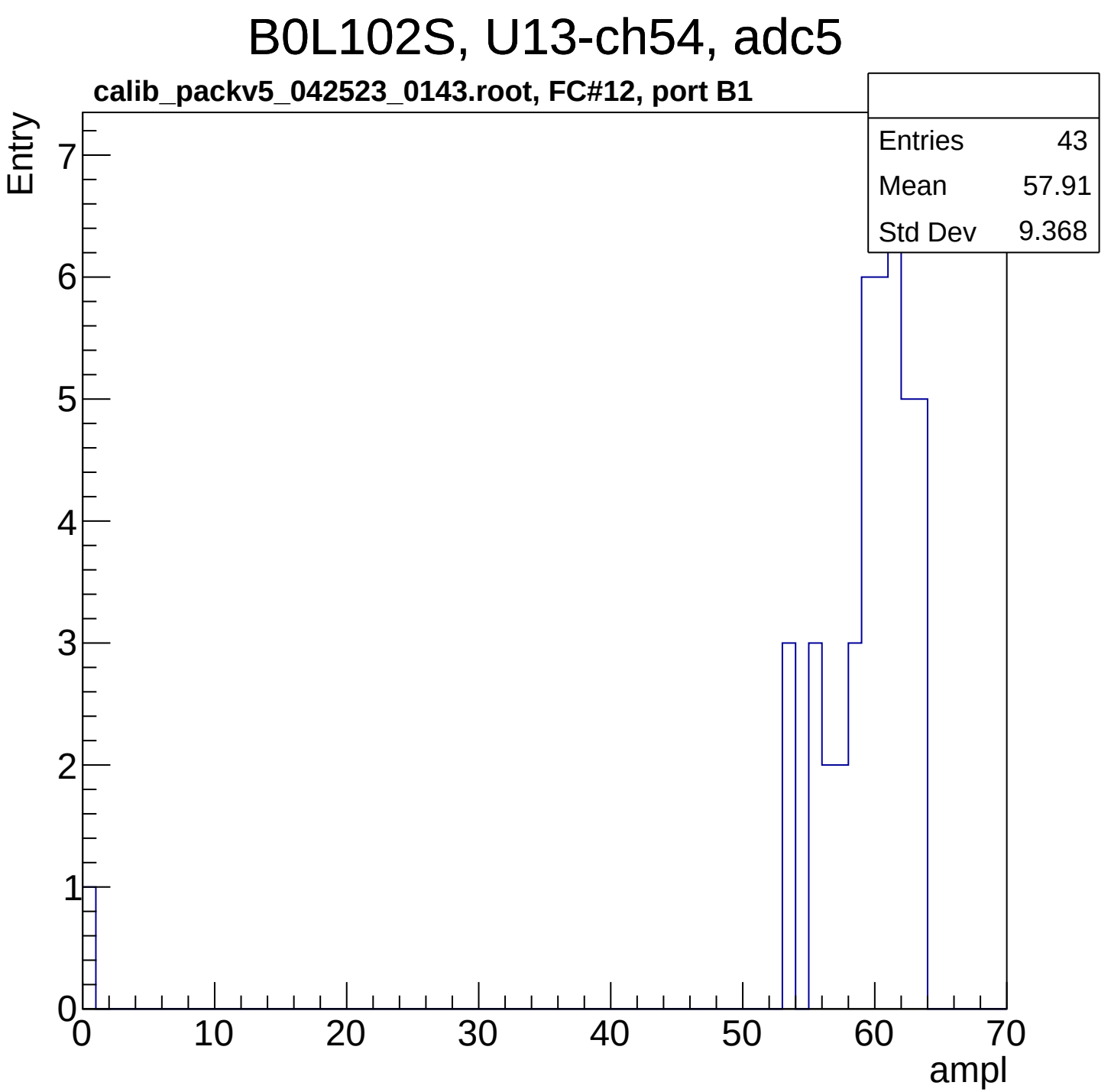
B0L102S, U13-ch54, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 57.91
Std Dev 9.368
Entry
ampl
0
1
2
3
4
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0
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60
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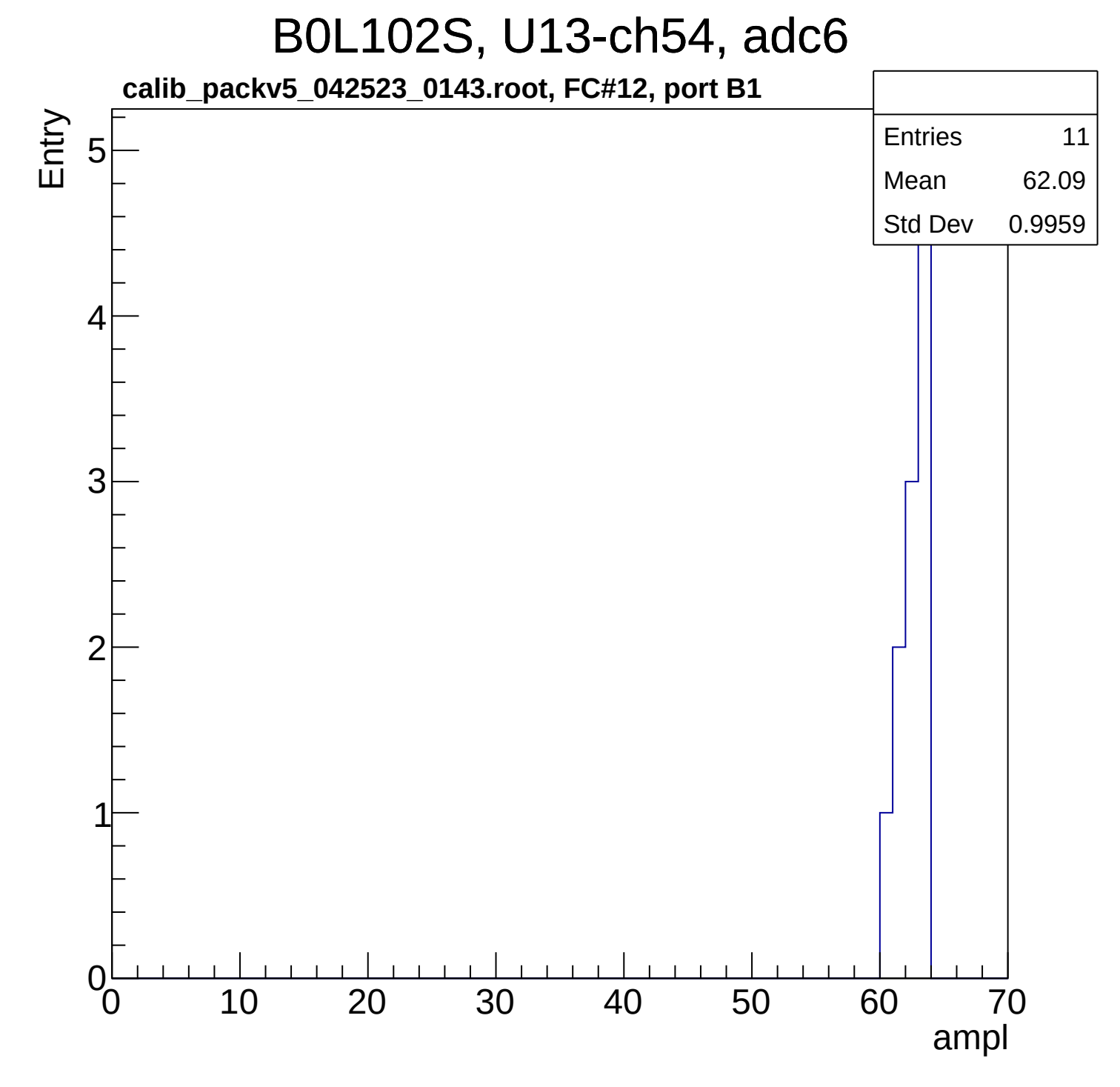
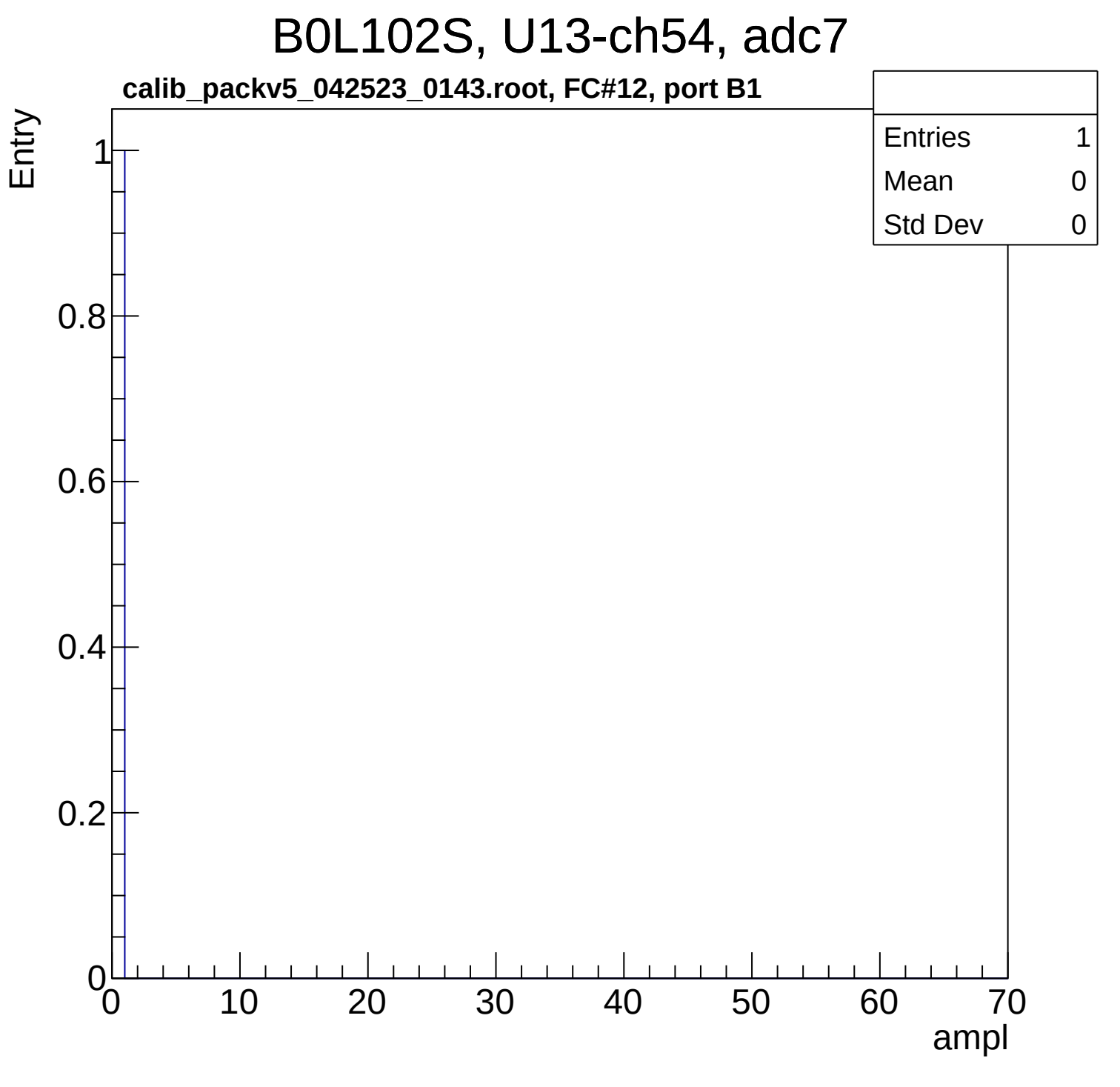

B0L102S, U13-ch54, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 62.09
Std Dev 0.9959
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch54, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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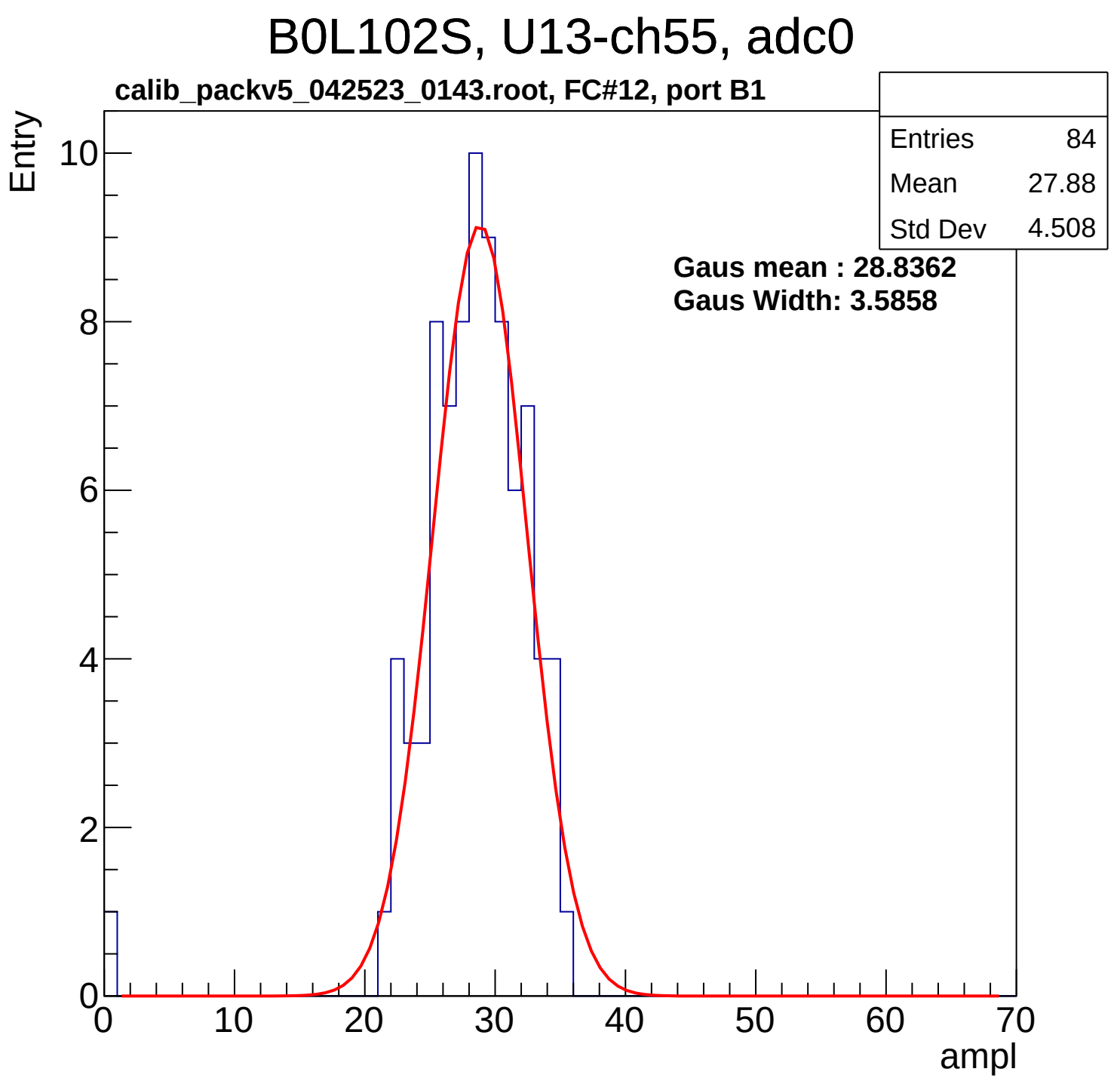
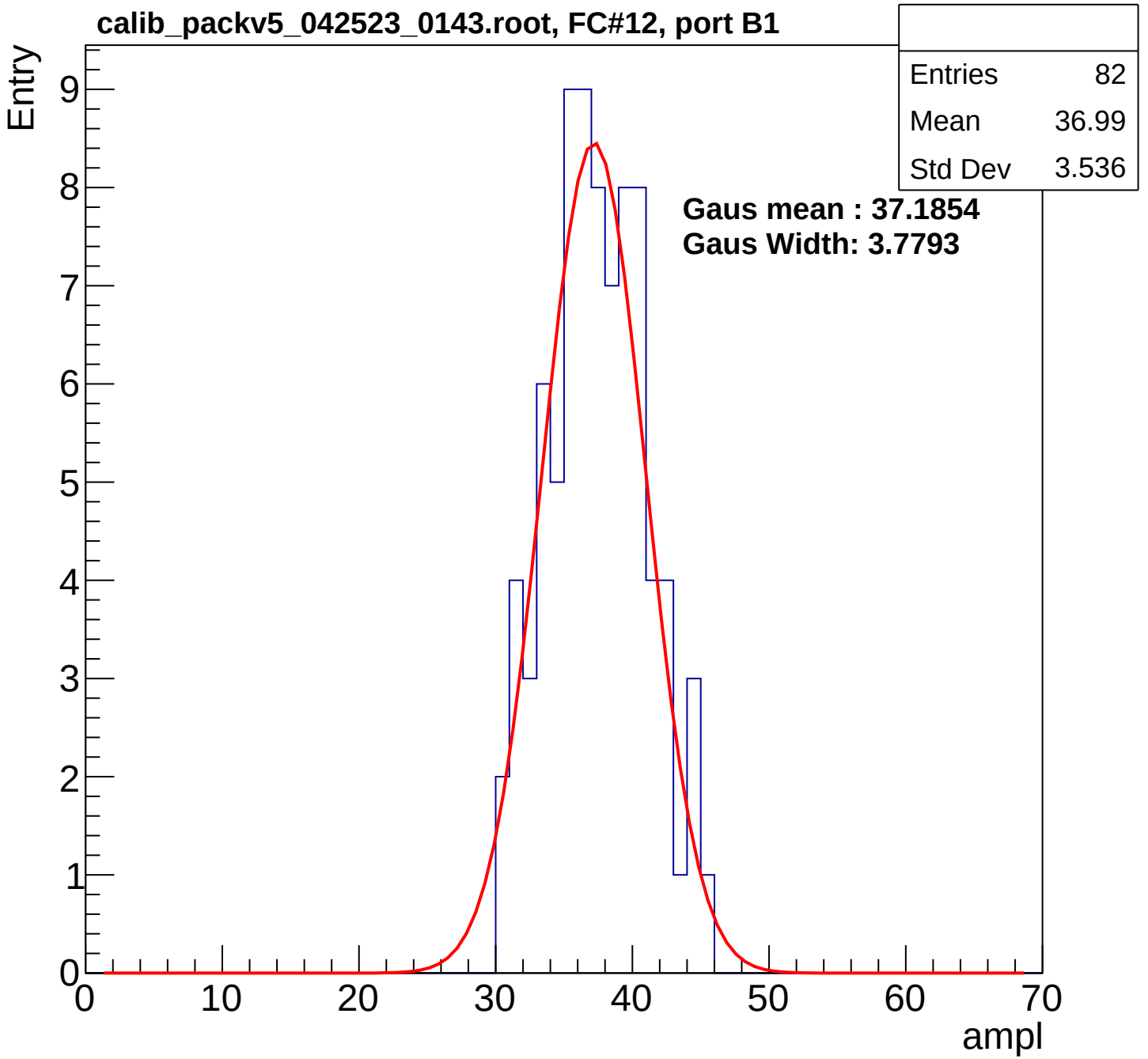
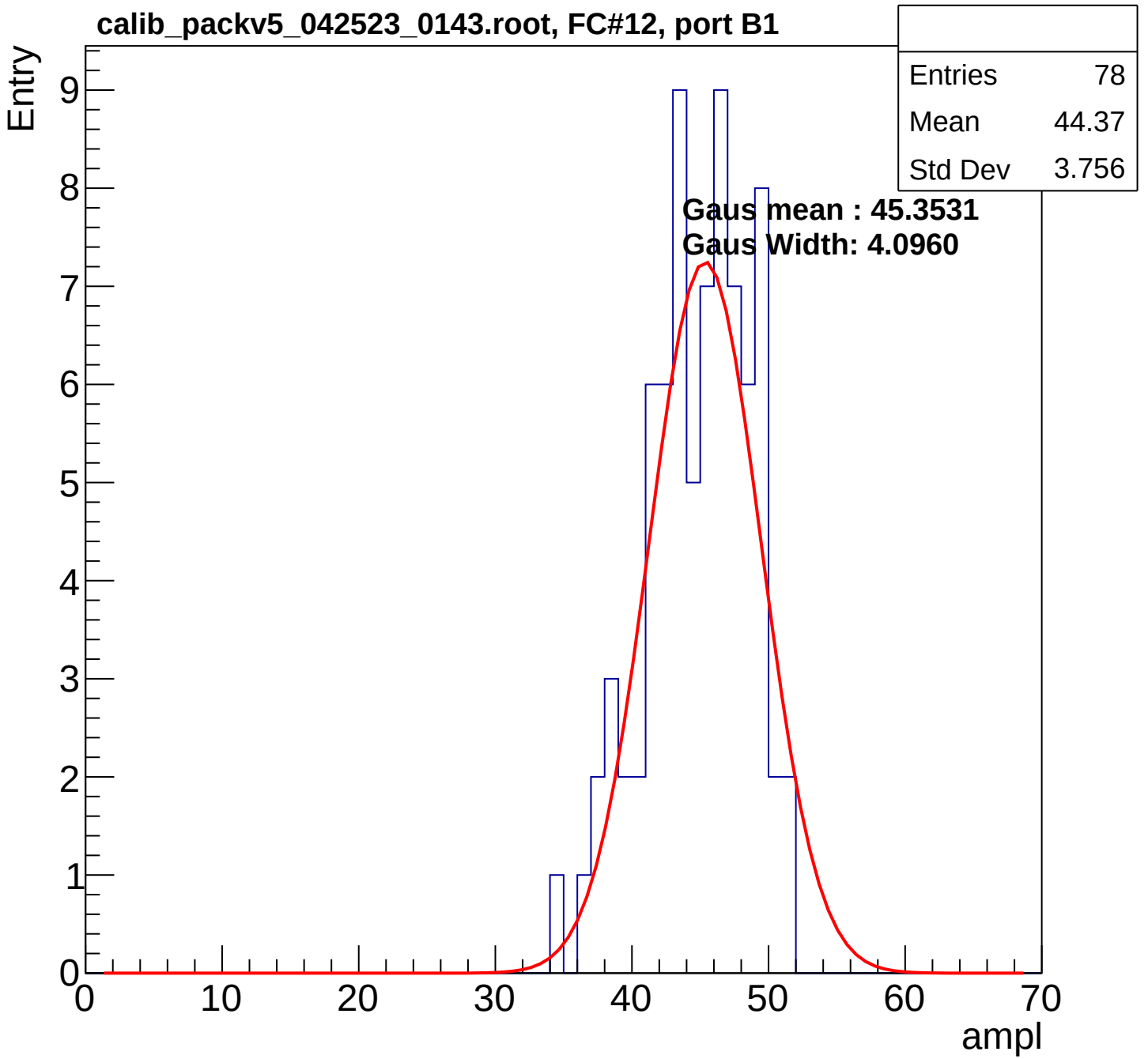
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch55, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 27.88
Std Dev 4.508
Gaus mean : 28.8362
Gaus Width: 3.5858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch55, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 36.99
Std Dev 3.536
Gaus mean : 37.1854
Gaus Width: 3.7793
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch55, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 44.37
Std Dev 3.756
Gaus mean : 45.3531
Gaus Width: 4.0960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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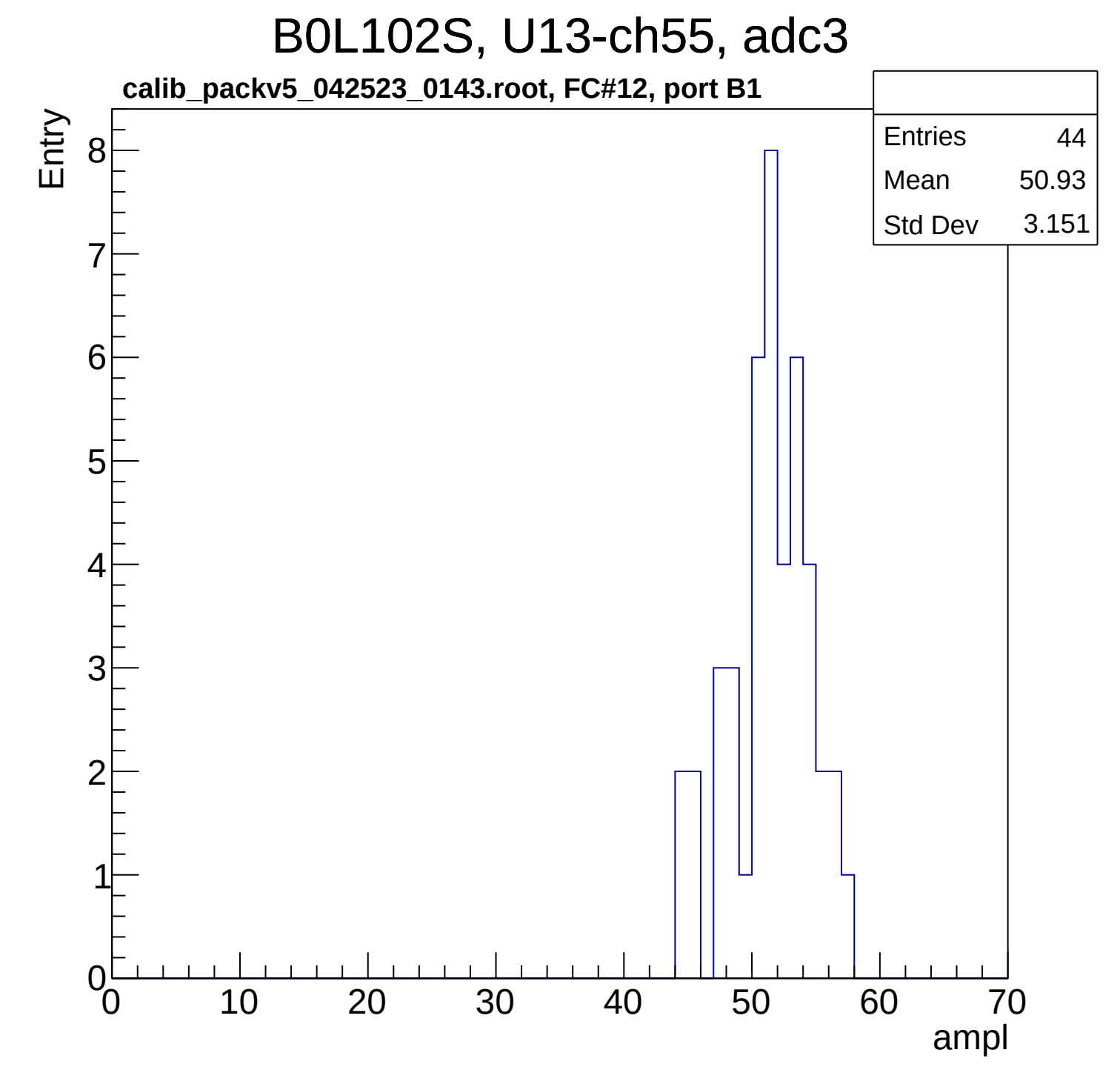
B0L102S, U13-ch55, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 50.93
Std Dev 3.151
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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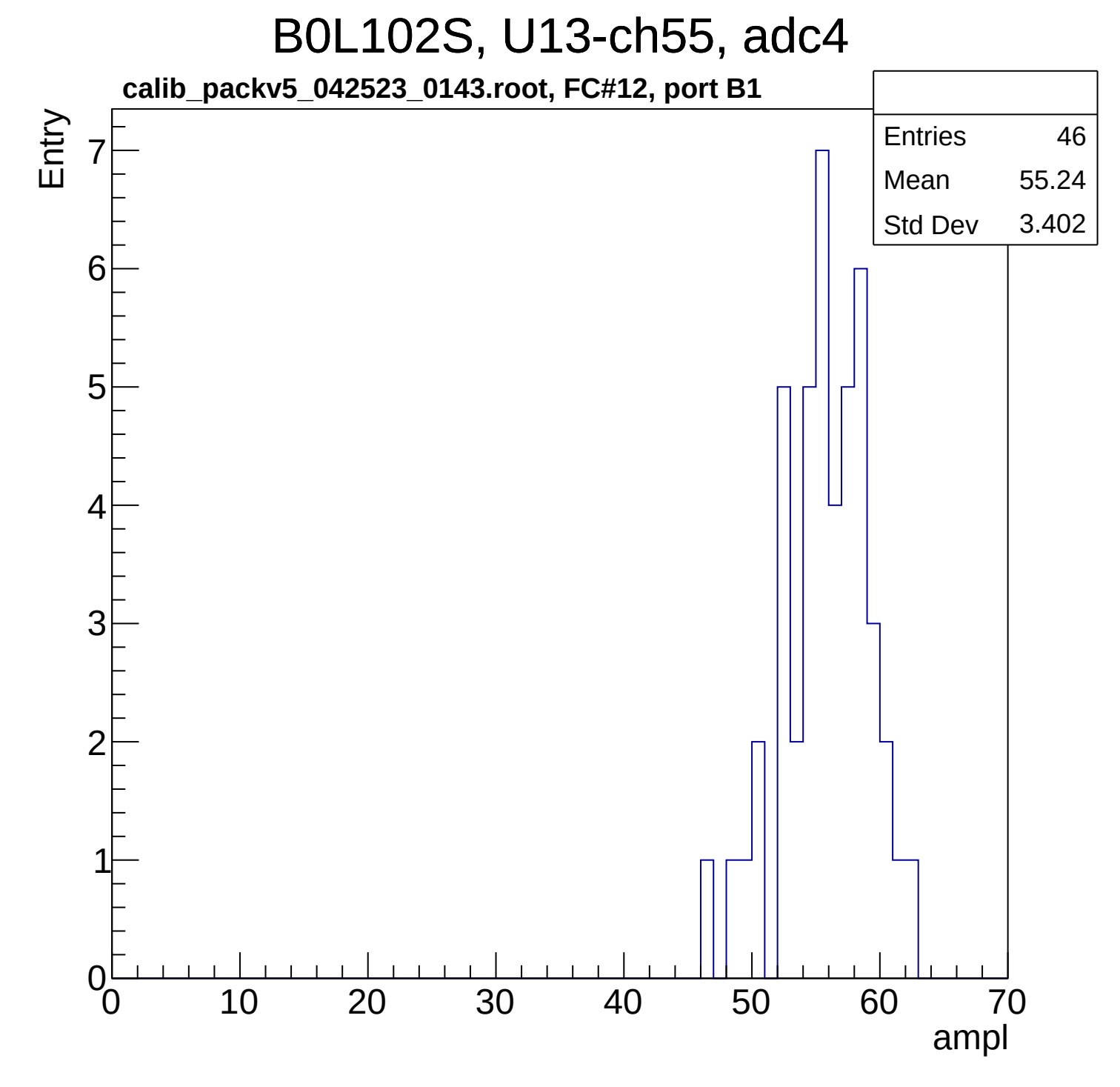
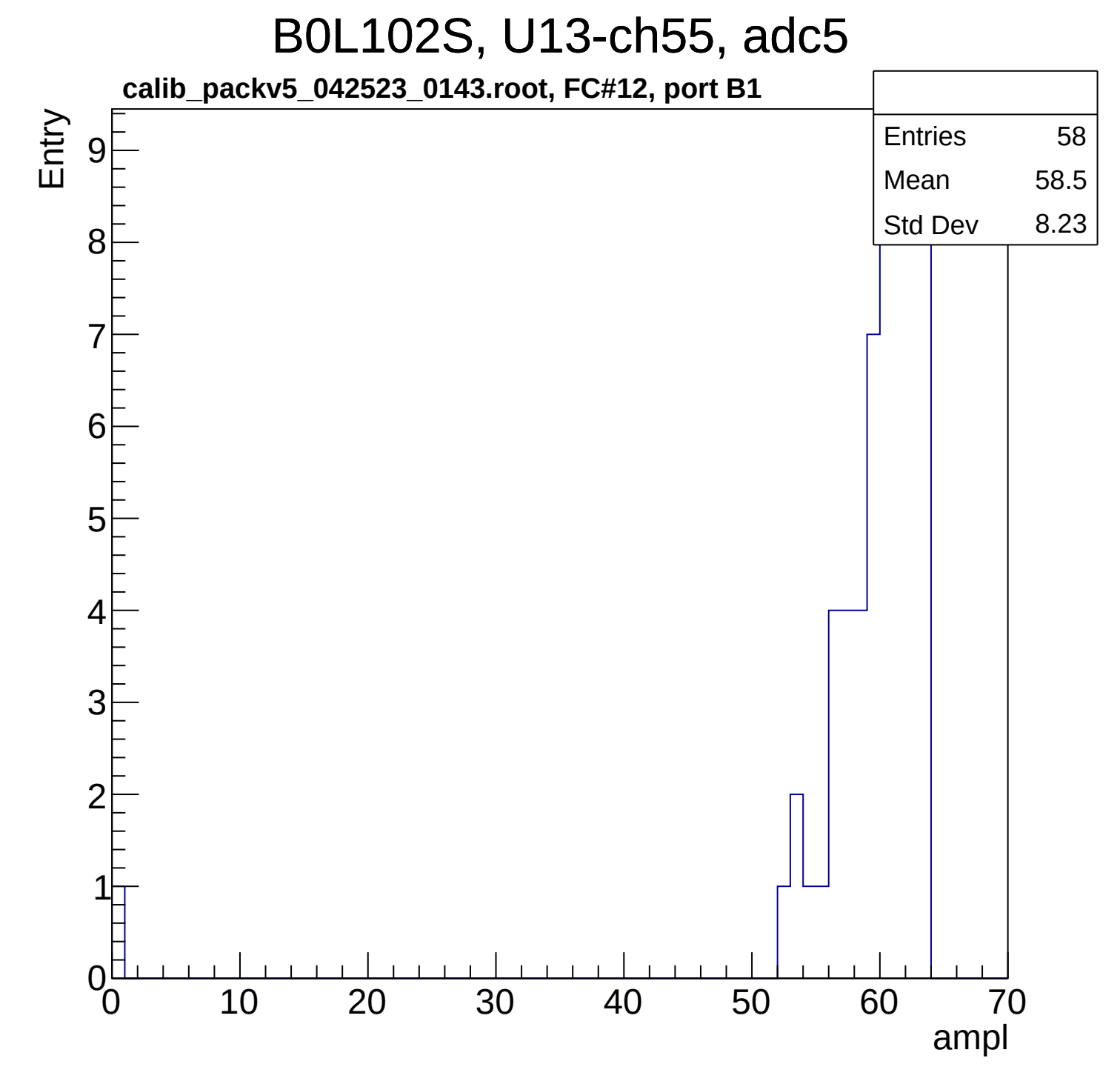
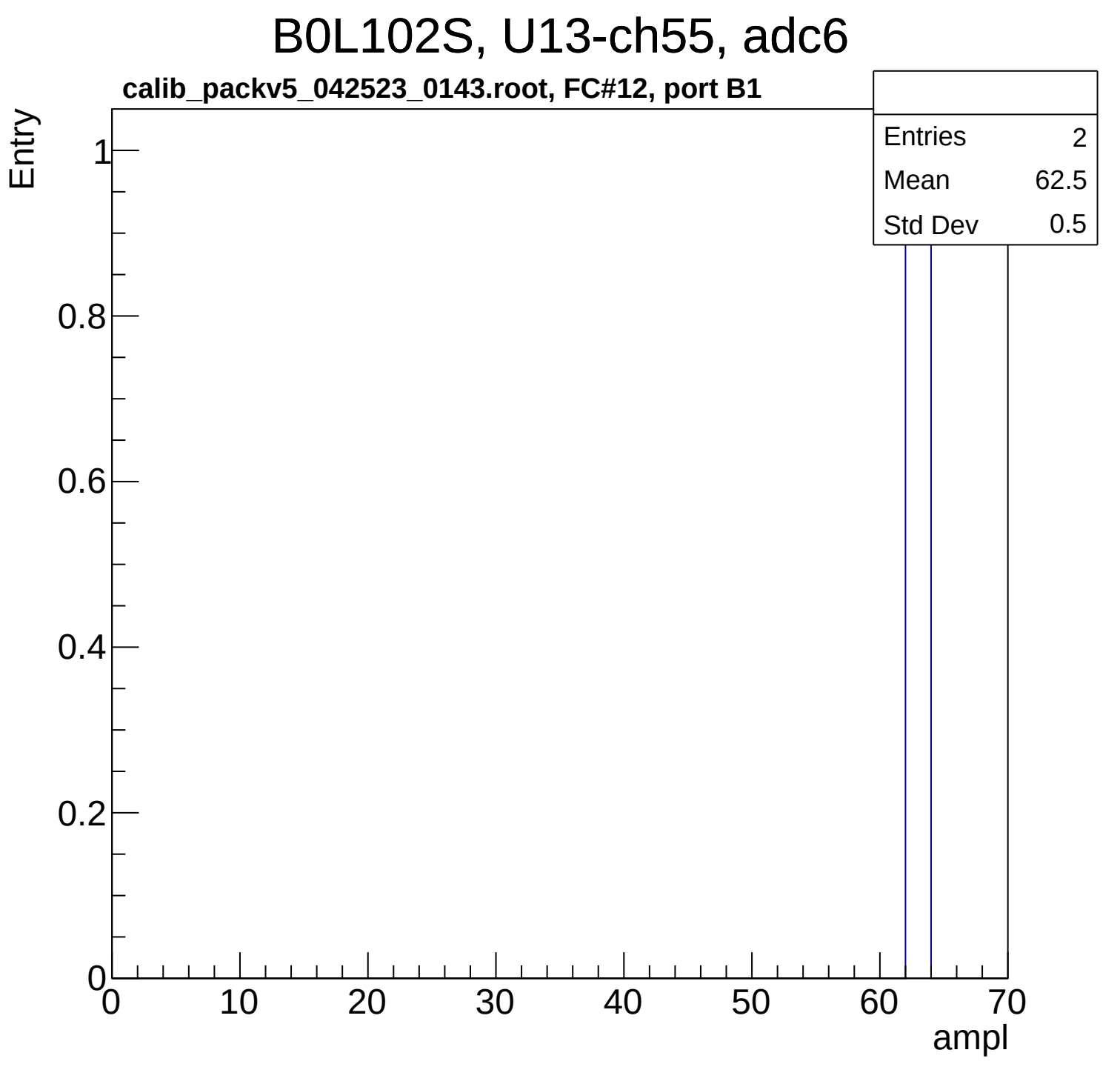
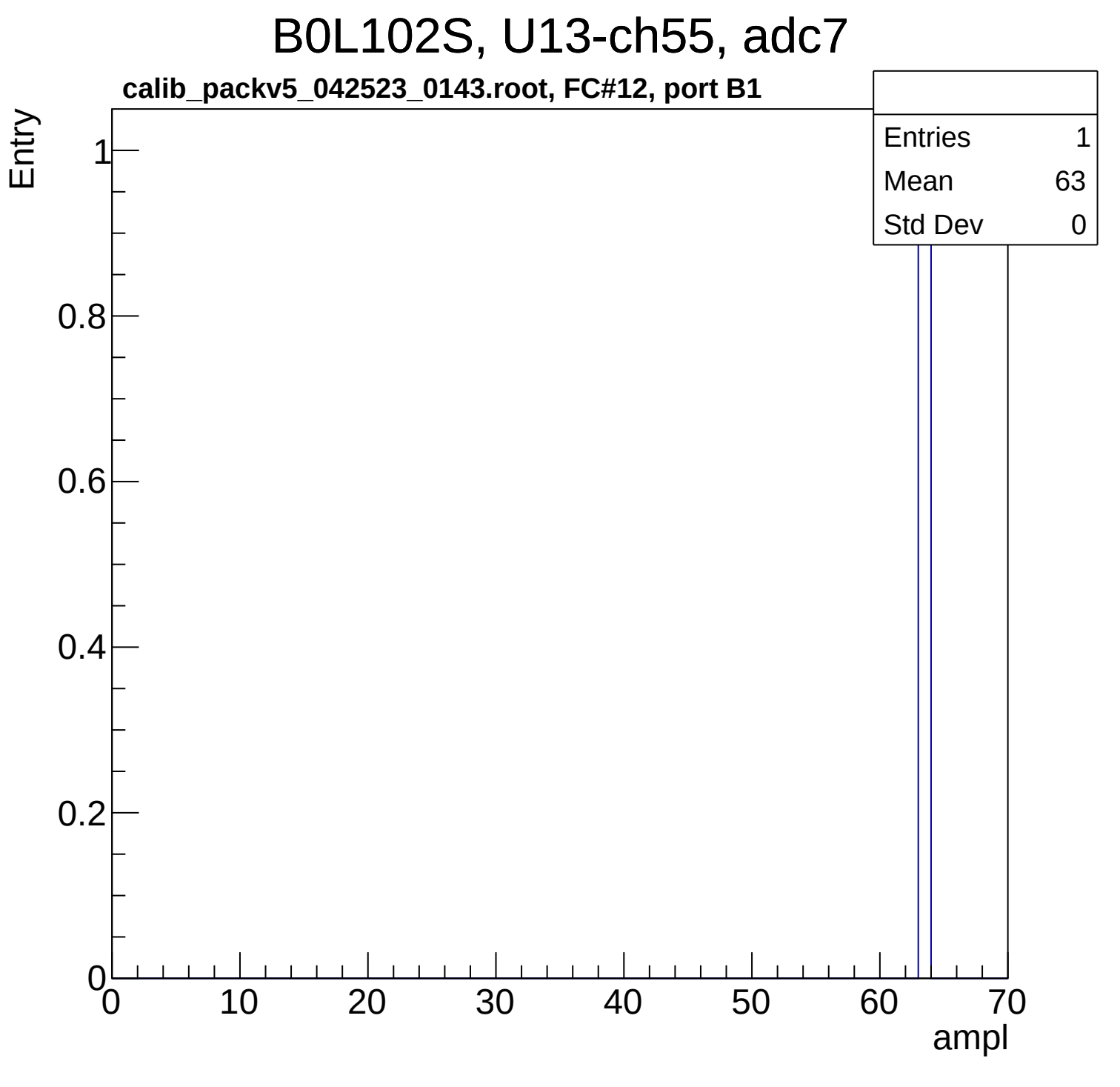
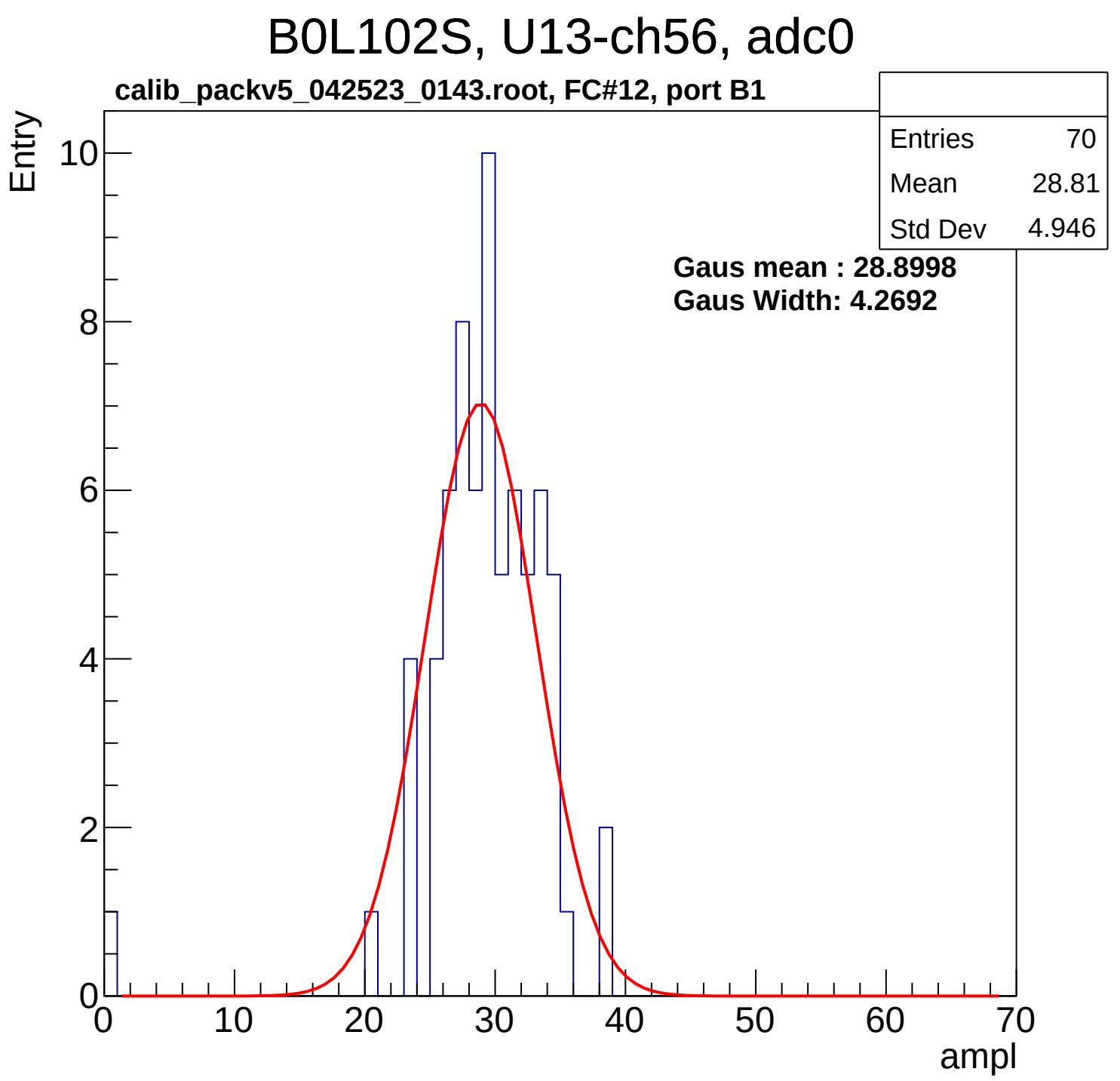
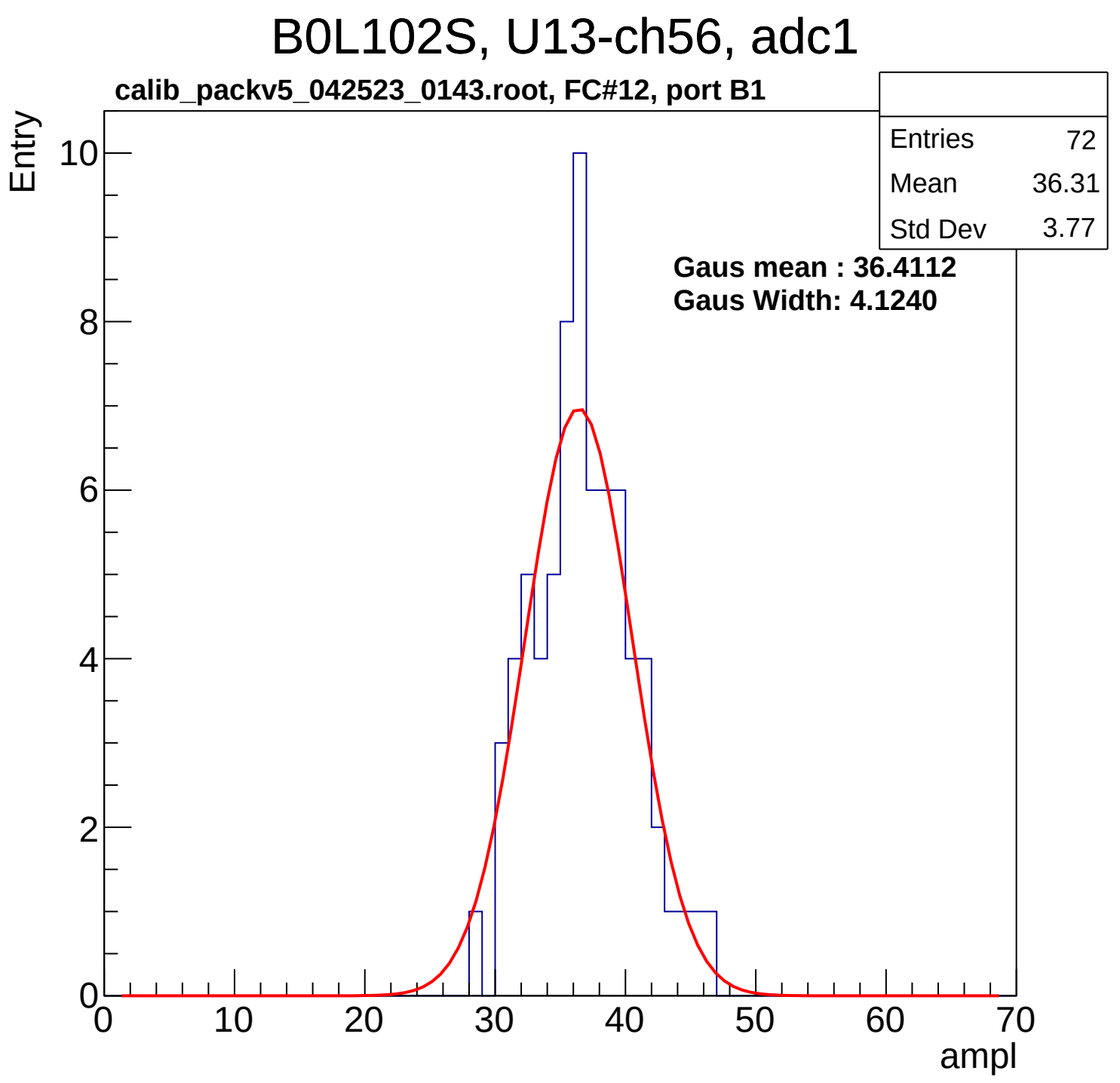

B0L102S, U13-ch55, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 55.24
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

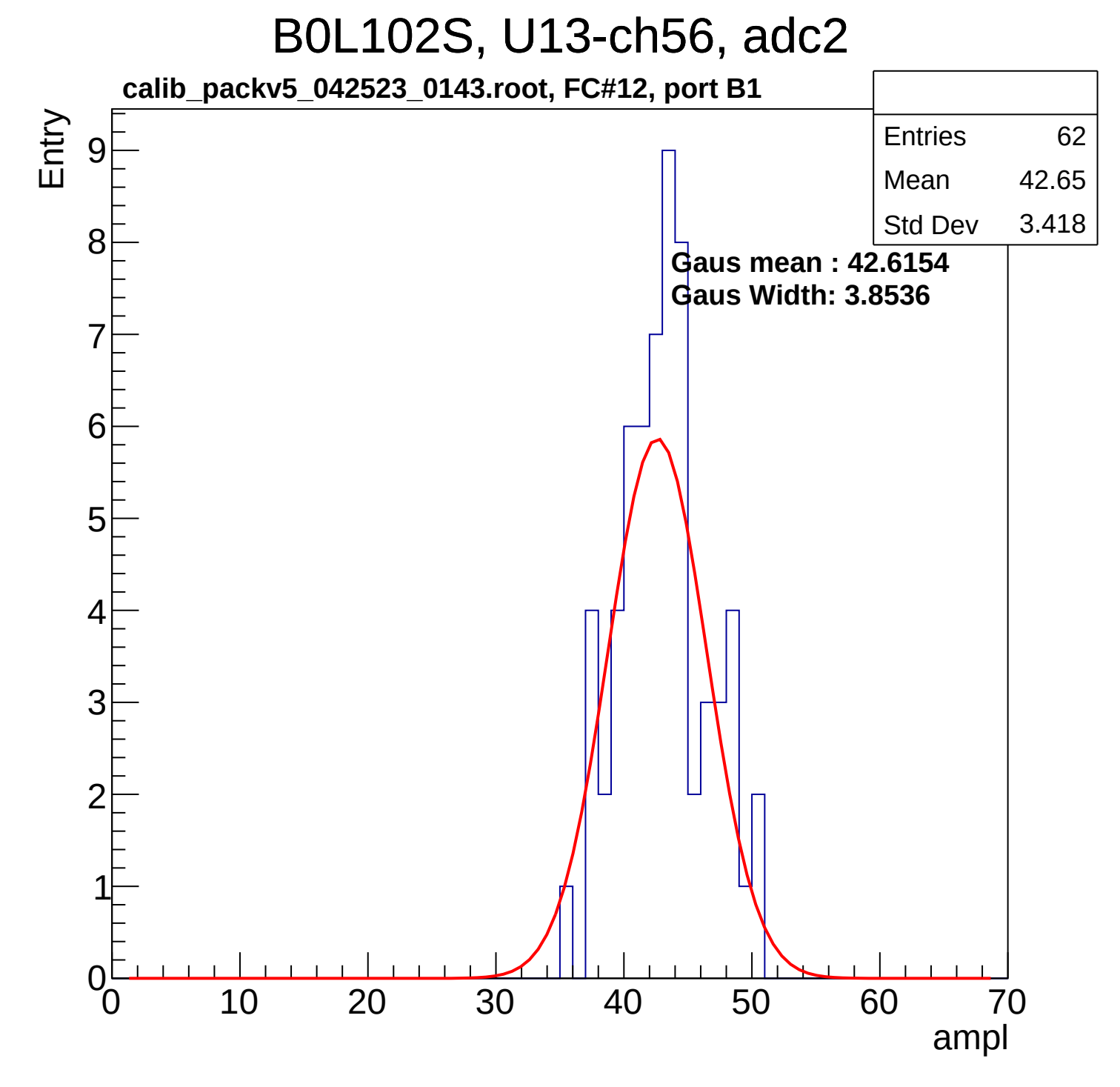

B0L102S, U13-ch55, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 58.5
Std Dev 8.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch55, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch55, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch56, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.81
Std Dev 4.946
Gaus mean : 28.8998
Gaus Width: 4.2692
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch56, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.31
Std Dev 3.77
Gaus mean : 36.4112
Gaus Width: 4.1240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch56, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.65
Std Dev 3.418
Gaus mean : 42.6154
Gaus Width: 3.8536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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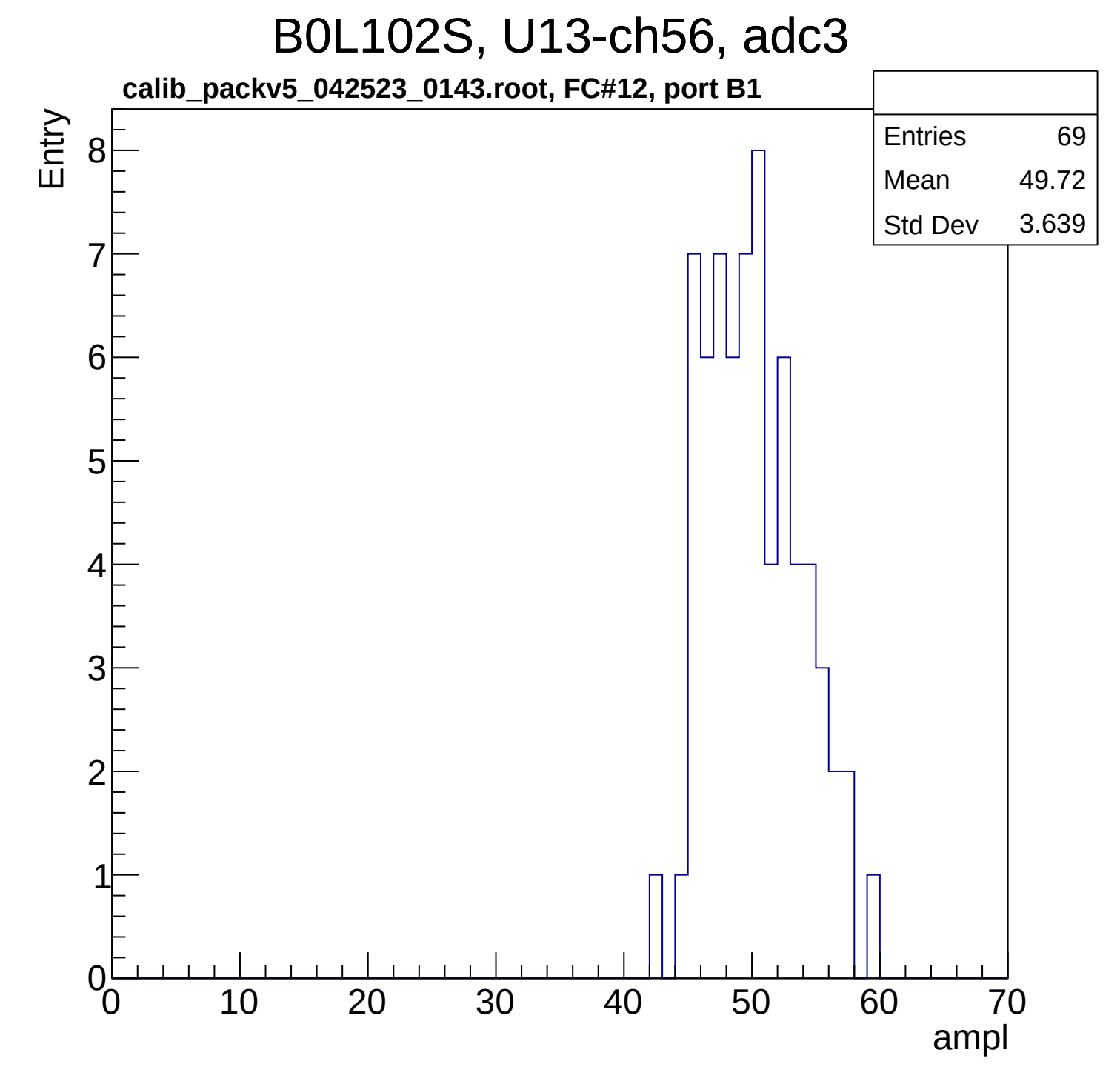
B0L102S, U13-ch56, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.72
Std Dev 3.639
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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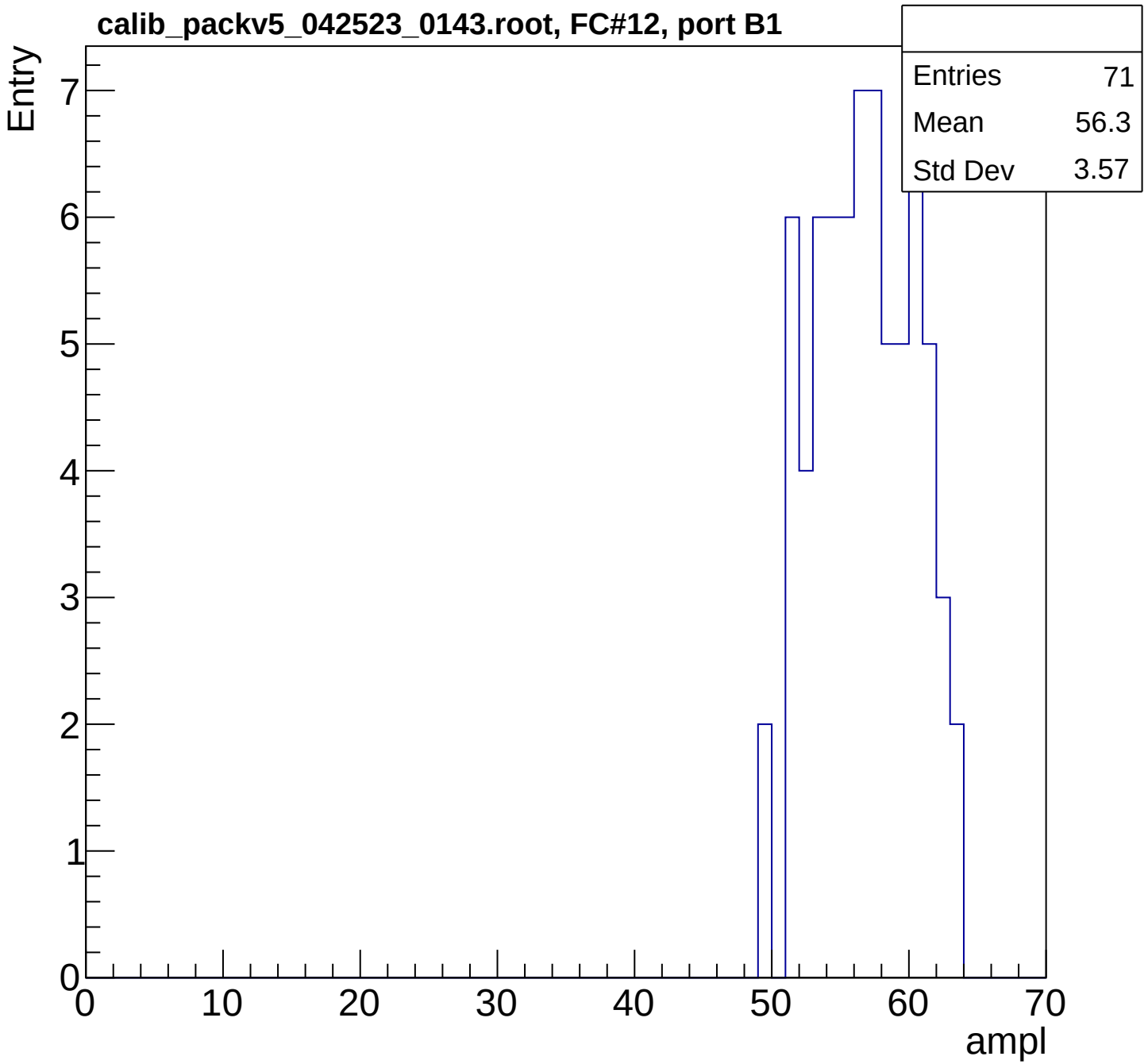
B0L102S, U13-ch56, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 56.3
Std Dev 3.57
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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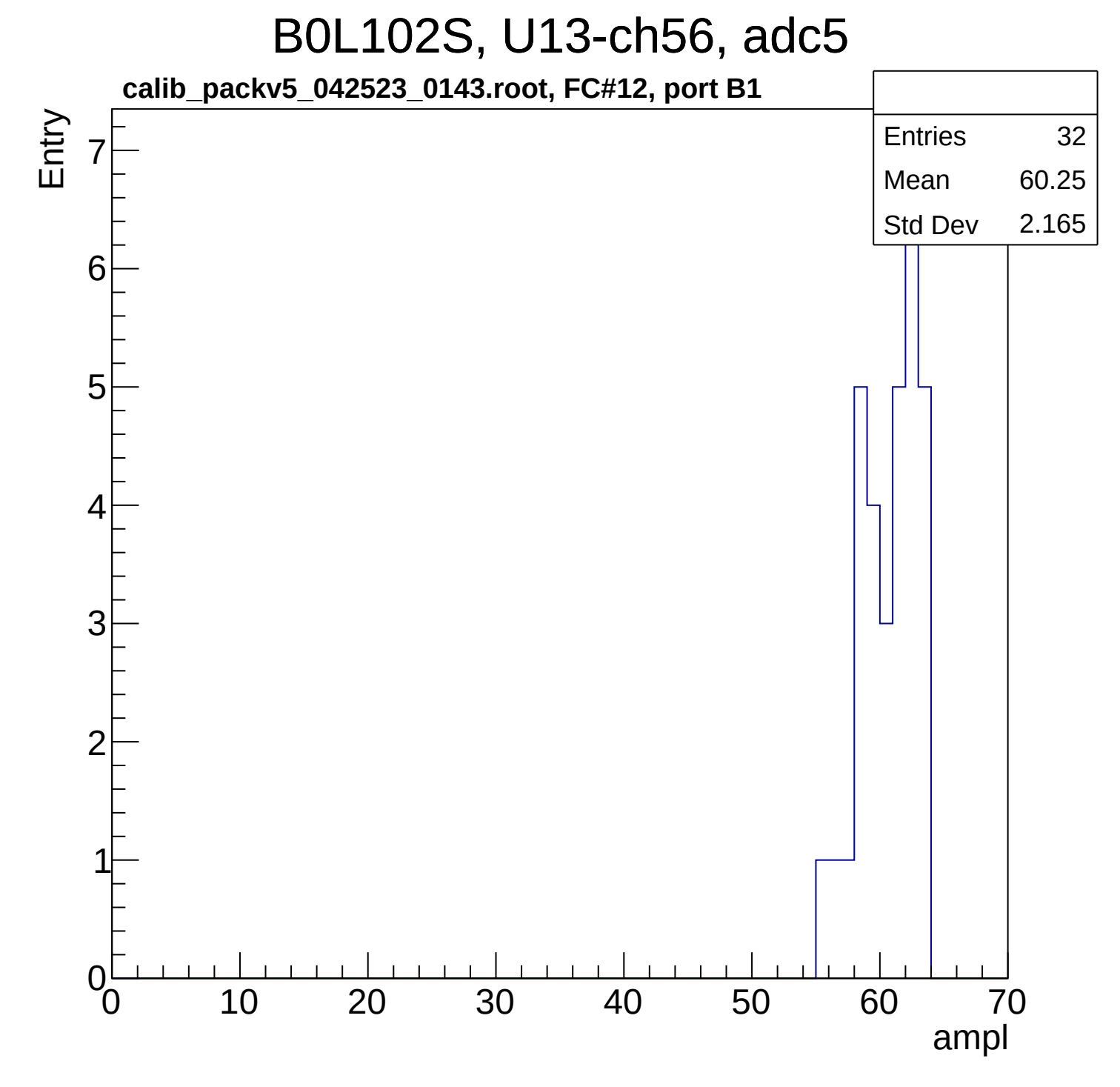
B0L102S, U13-ch56, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 60.25
Std Dev 2.165
Entry
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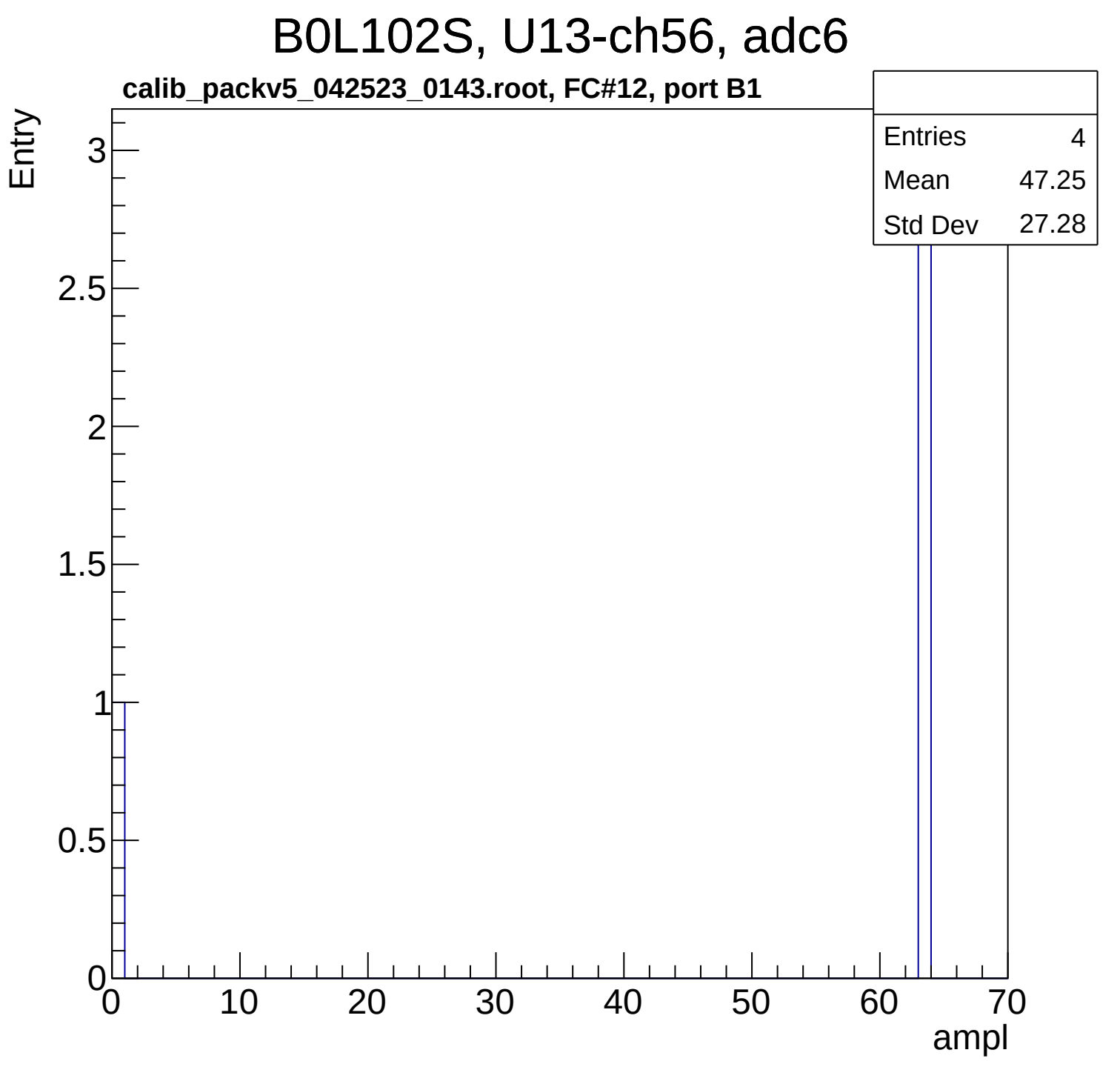
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch56, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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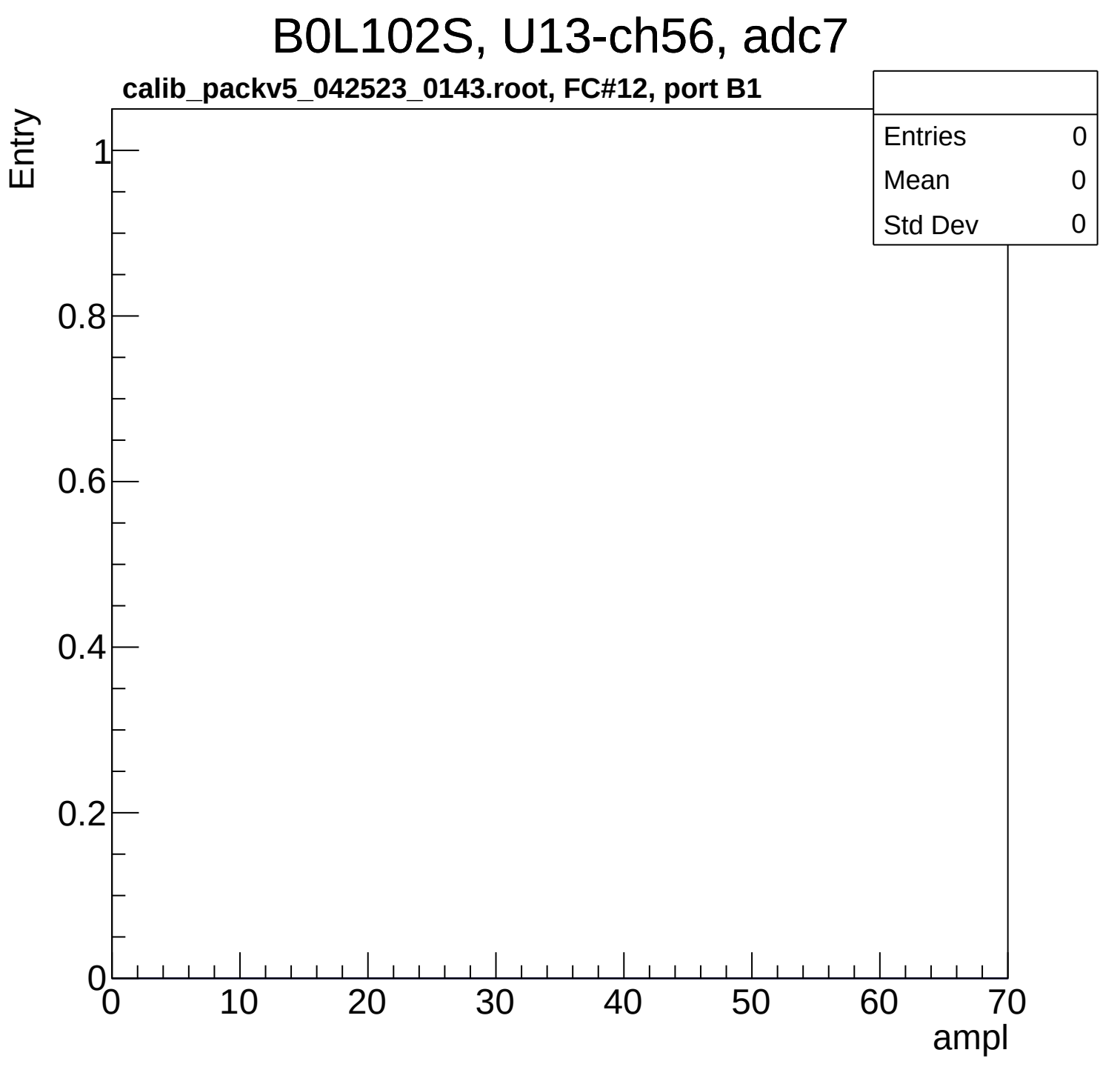
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch56, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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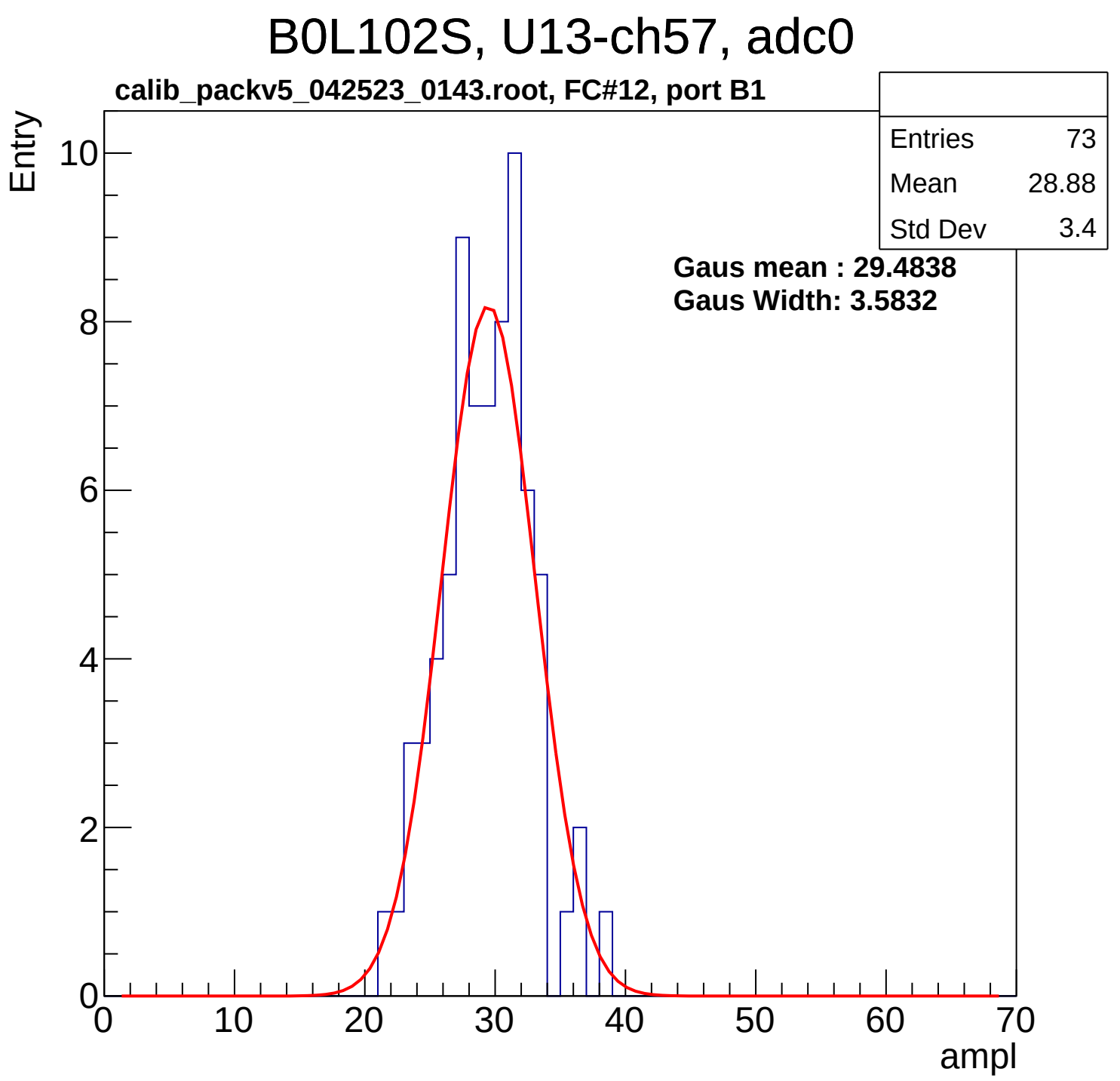
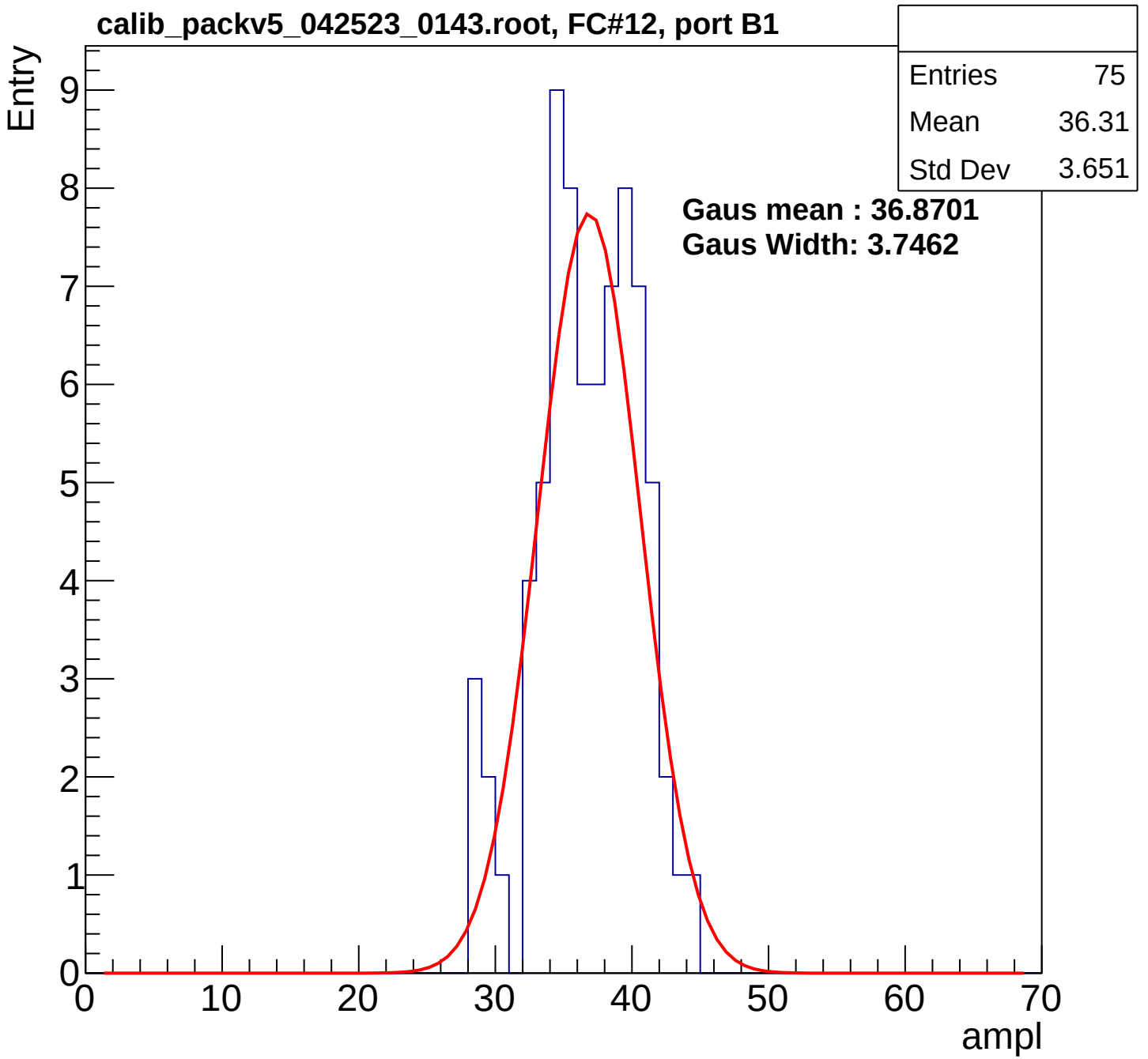
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch57, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.88
Std Dev 3.4
Gaus mean : 29.4838
Gaus Width: 3.5832
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch57, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.31
Std Dev 3.651
Gaus mean : 36.8701
Gaus Width: 3.7462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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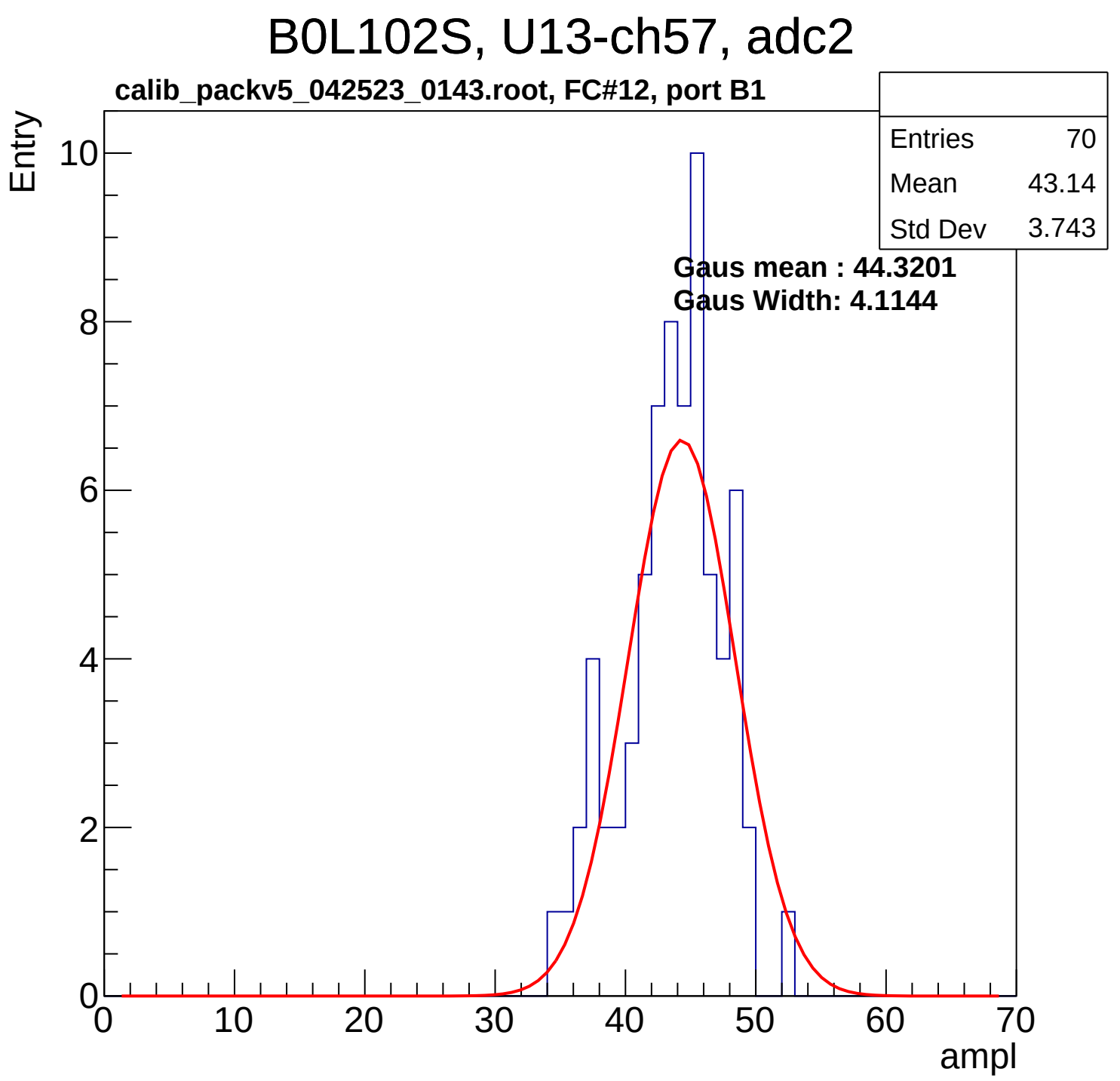
B0L102S, U13-ch57, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.14
Std Dev 3.743
Gaus mean : 44.3201
Gaus Width: 4.1144
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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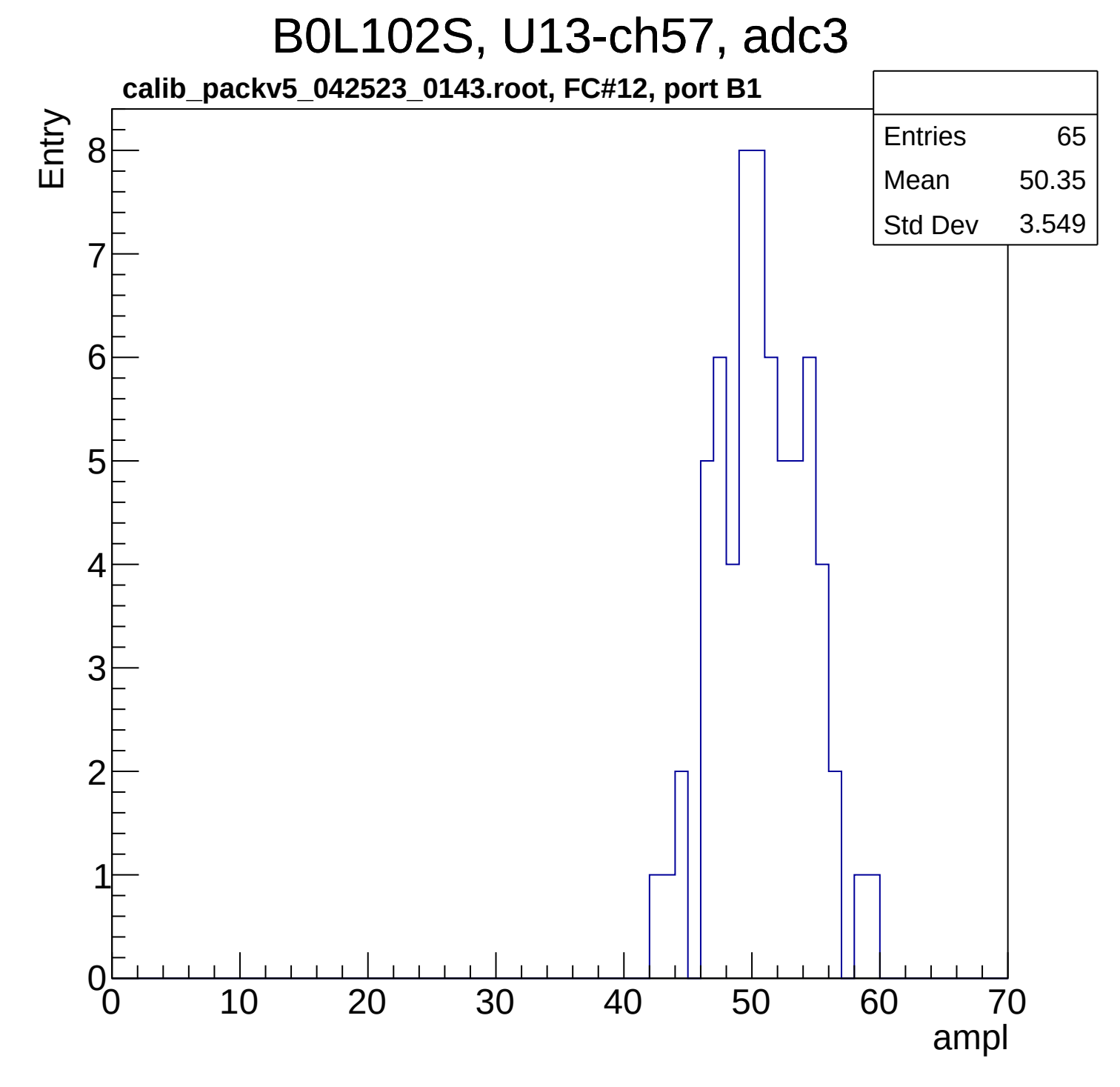
B0L102S, U13-ch57, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.35
Std Dev 3.549
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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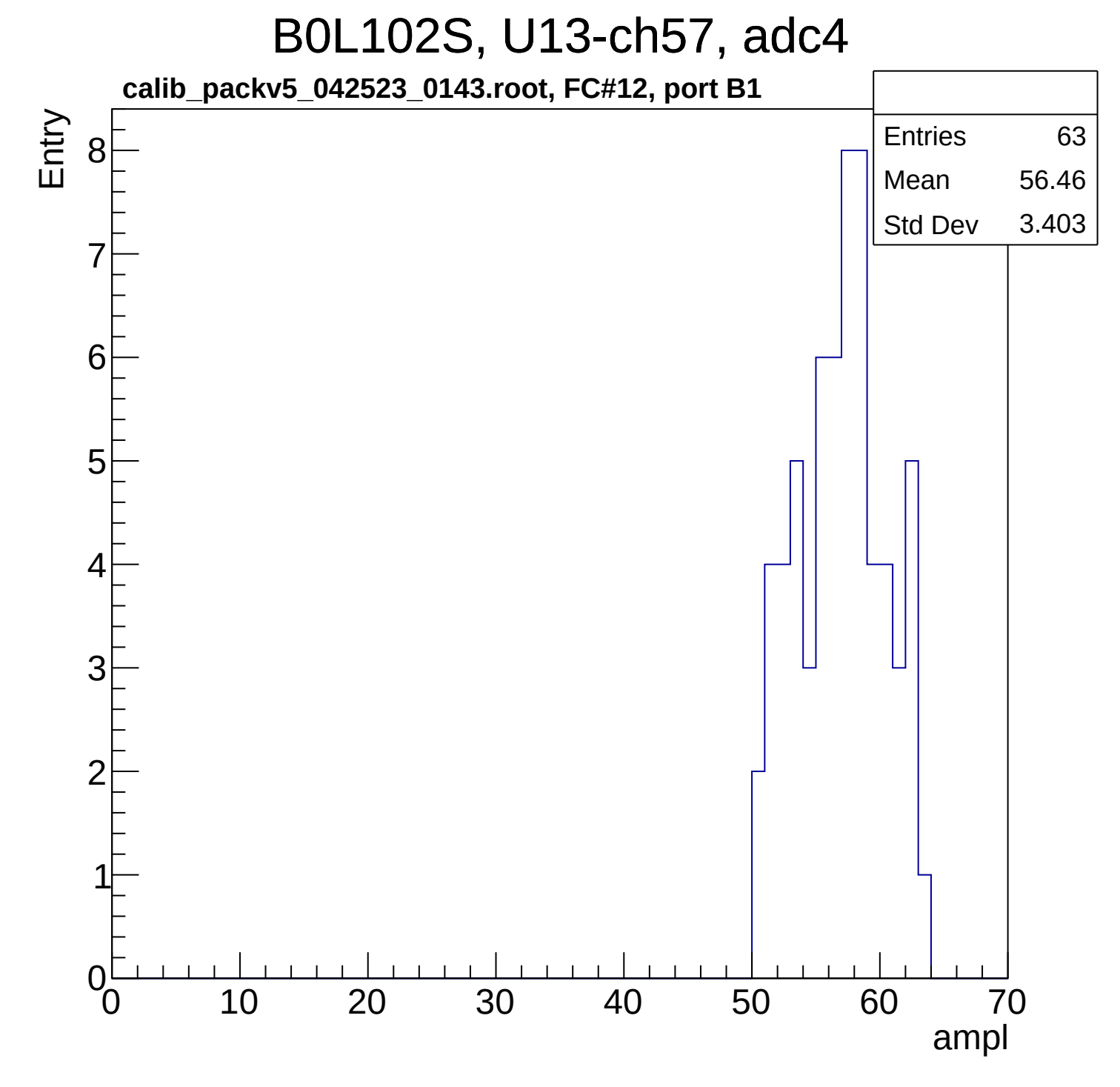
B0L102S, U13-ch57, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 56.46
Std Dev 3.403
Entry
ampl
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1
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3
4
5
6
7
8
0
10
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40
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60
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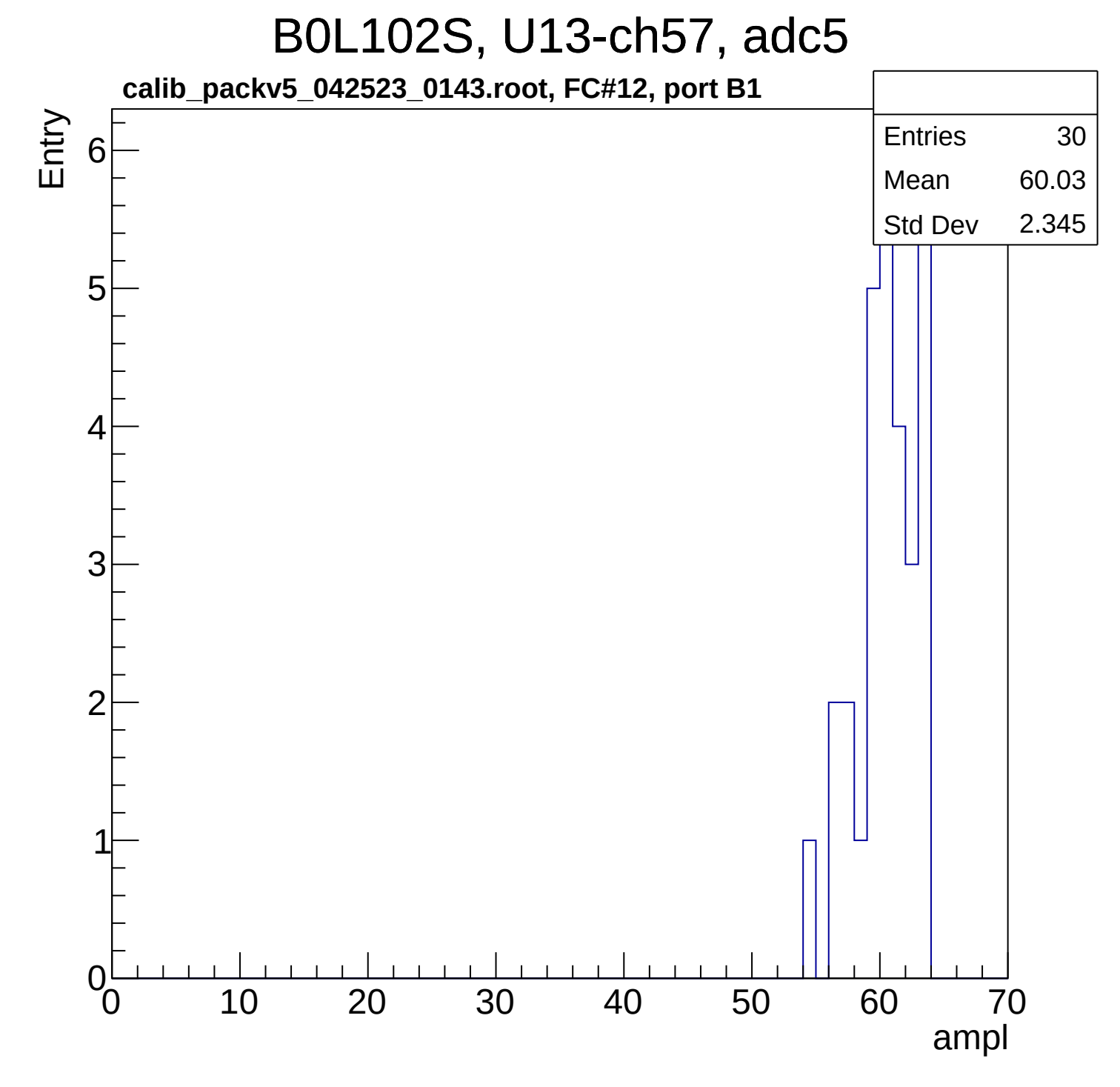
B0L102S, U13-ch57, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 60.03
Std Dev 2.345
Entry
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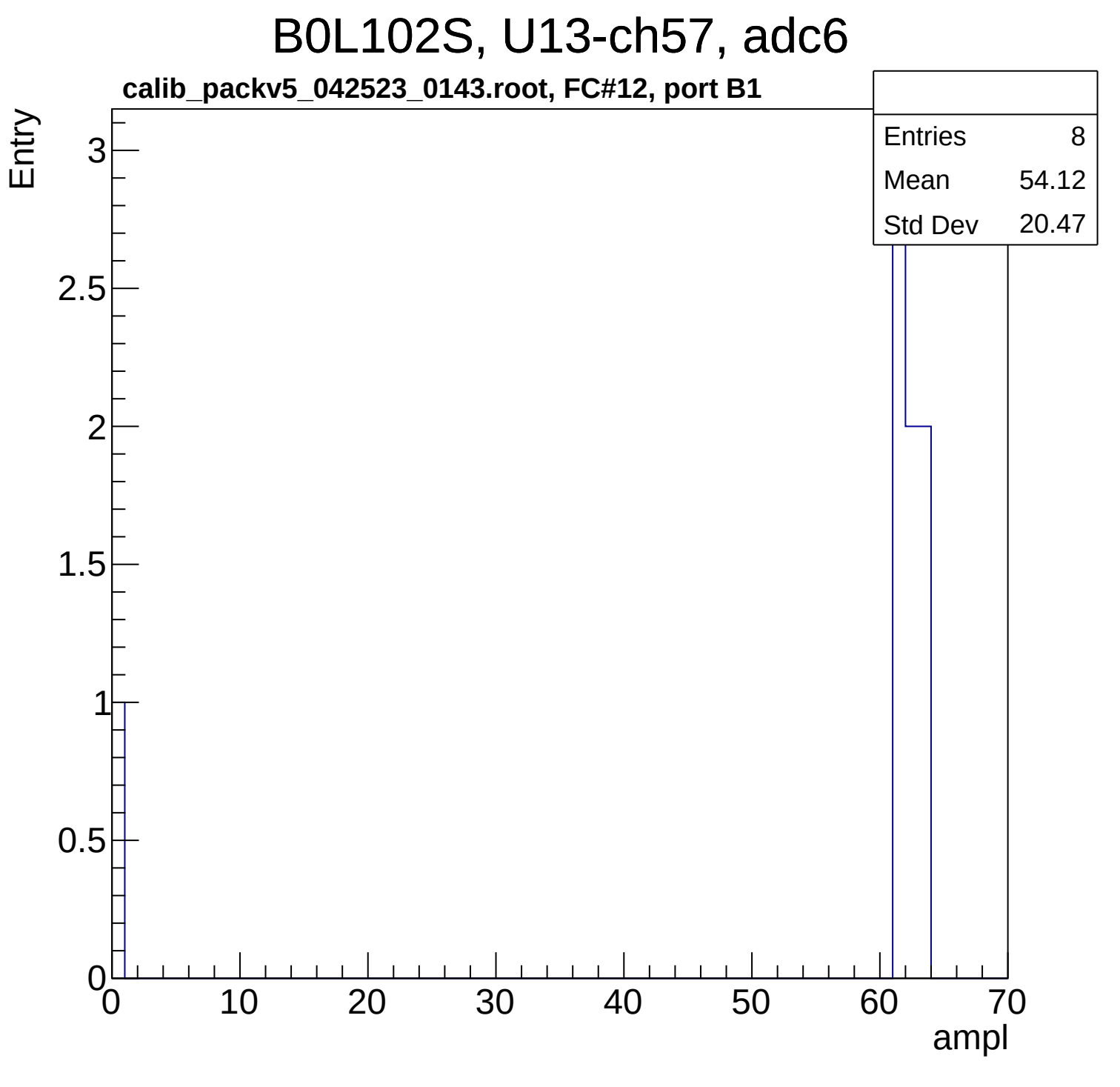
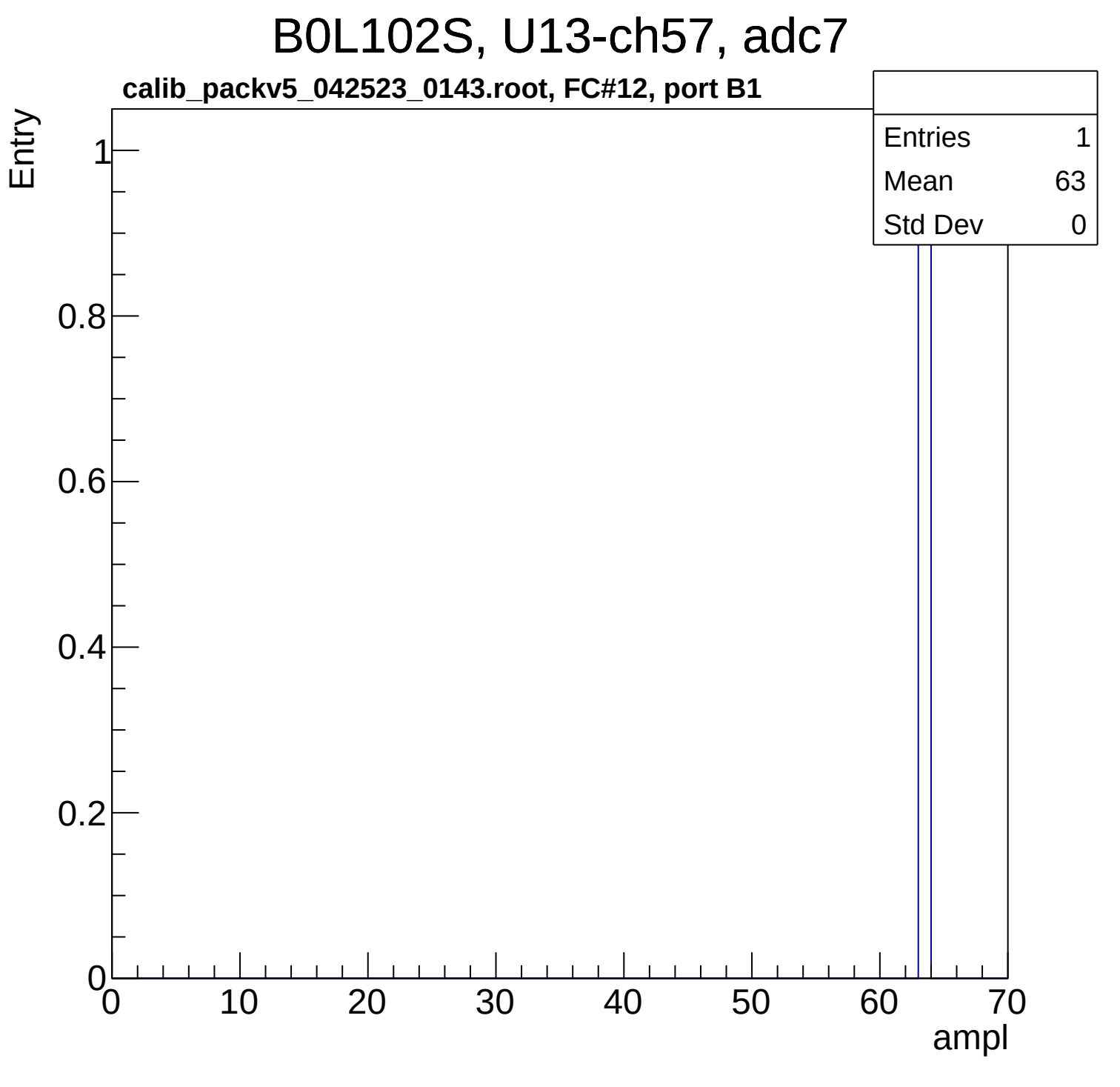
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch57, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.12
Std Dev 20.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch57, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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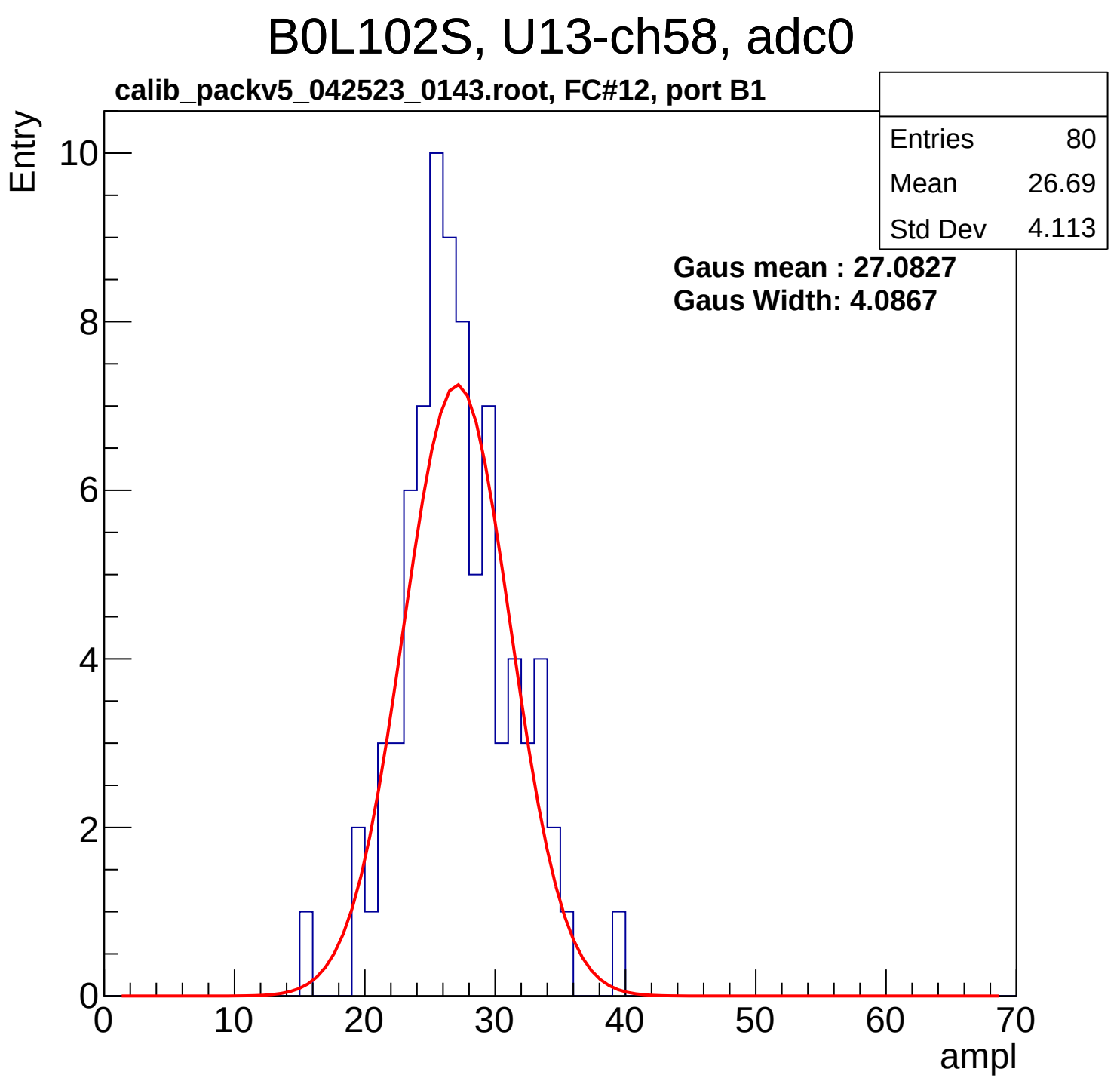
50
60
70
ampl


B0L102S, U13-ch58, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 26.69
Std Dev 4.113
Gaus mean : 27.0827
Gaus Width: 4.0867
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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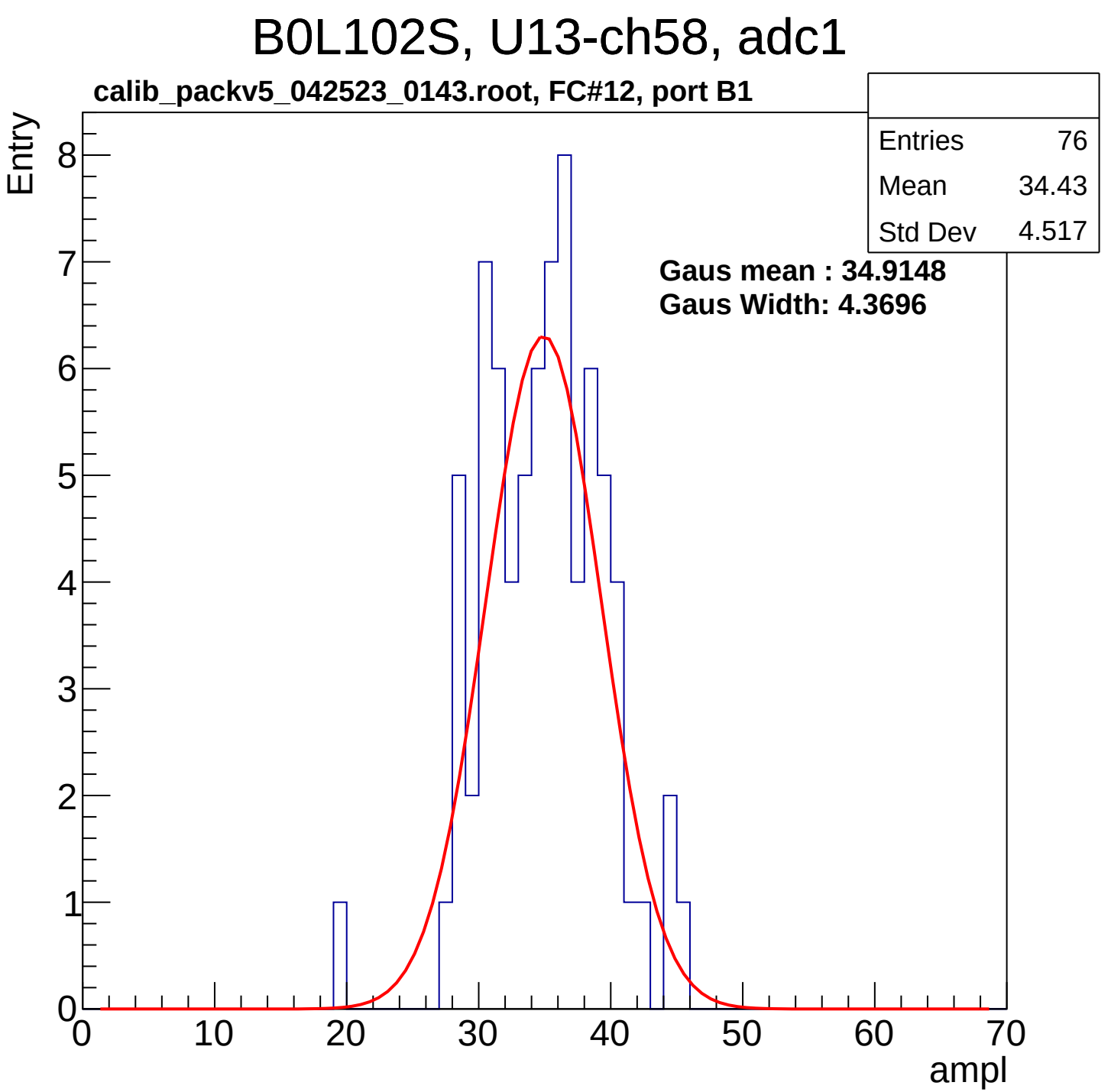
B0L102S, U13-ch58, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 34.43
Std Dev 4.517
Gaus mean : 34.9148
Gaus Width: 4.3696
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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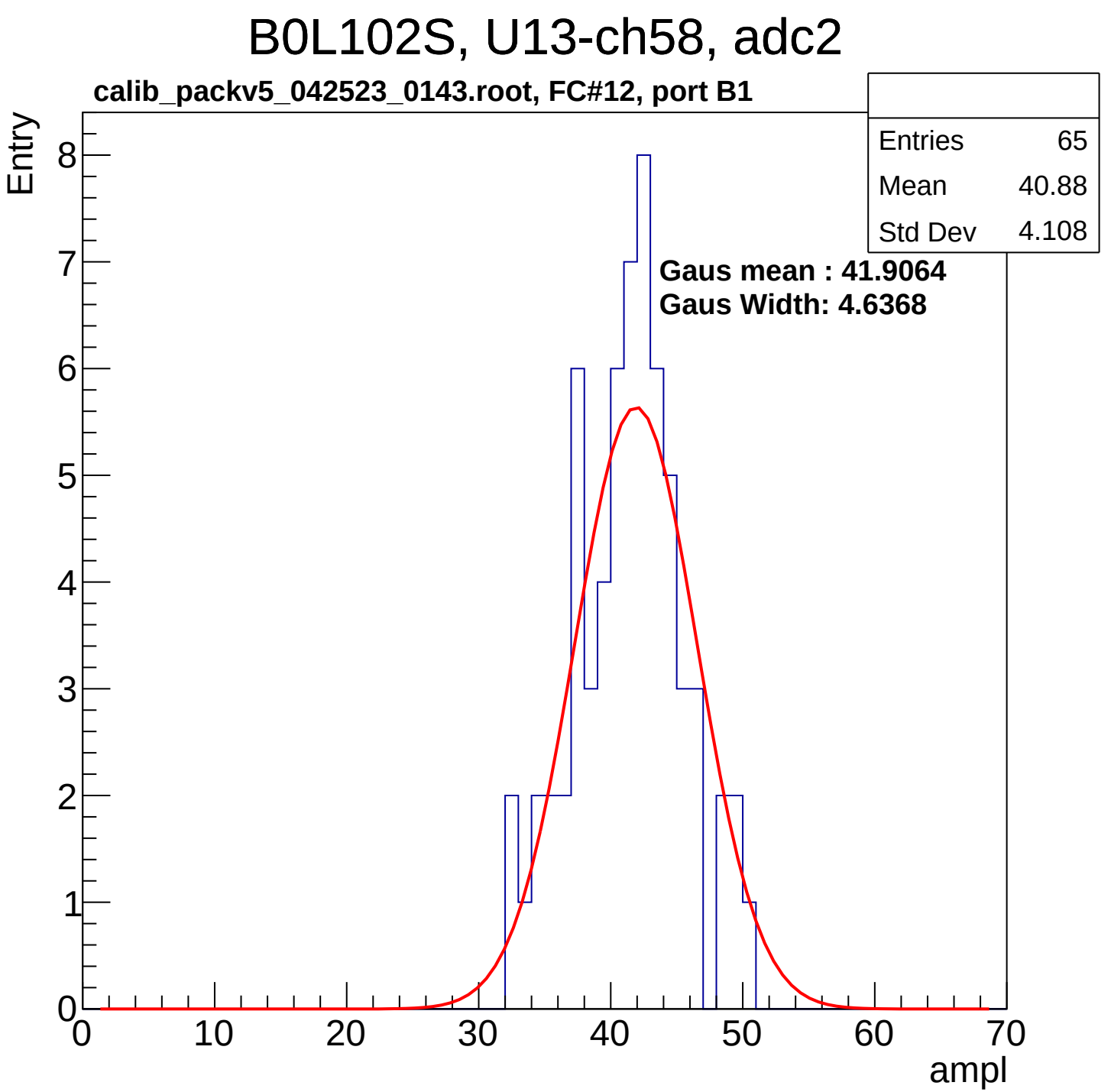
B0L102S, U13-ch58, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 40.88
Std Dev 4.108
Gaus mean : 41.9064
Gaus Width: 4.6368
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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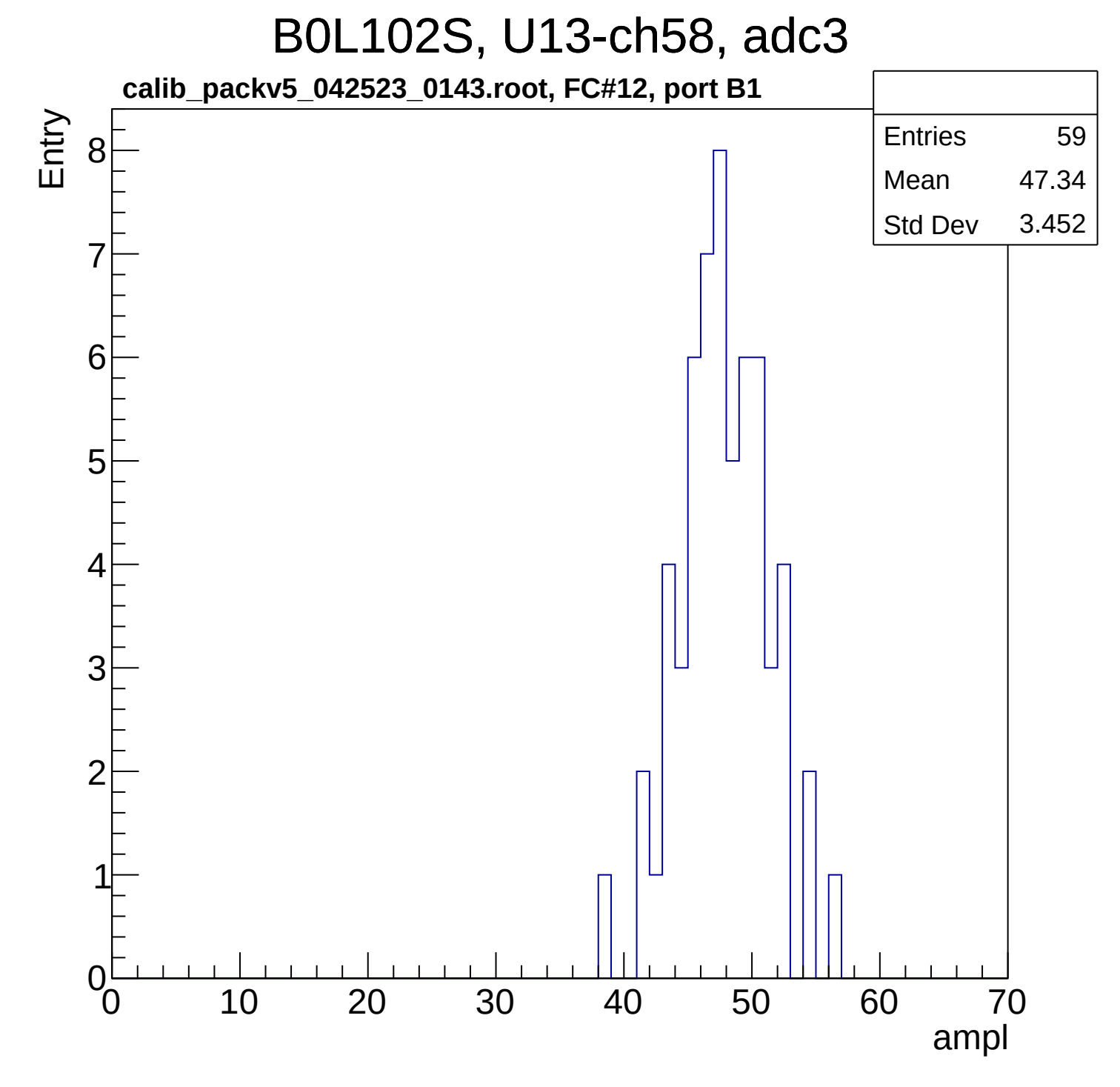
B0L102S, U13-ch58, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 47.34
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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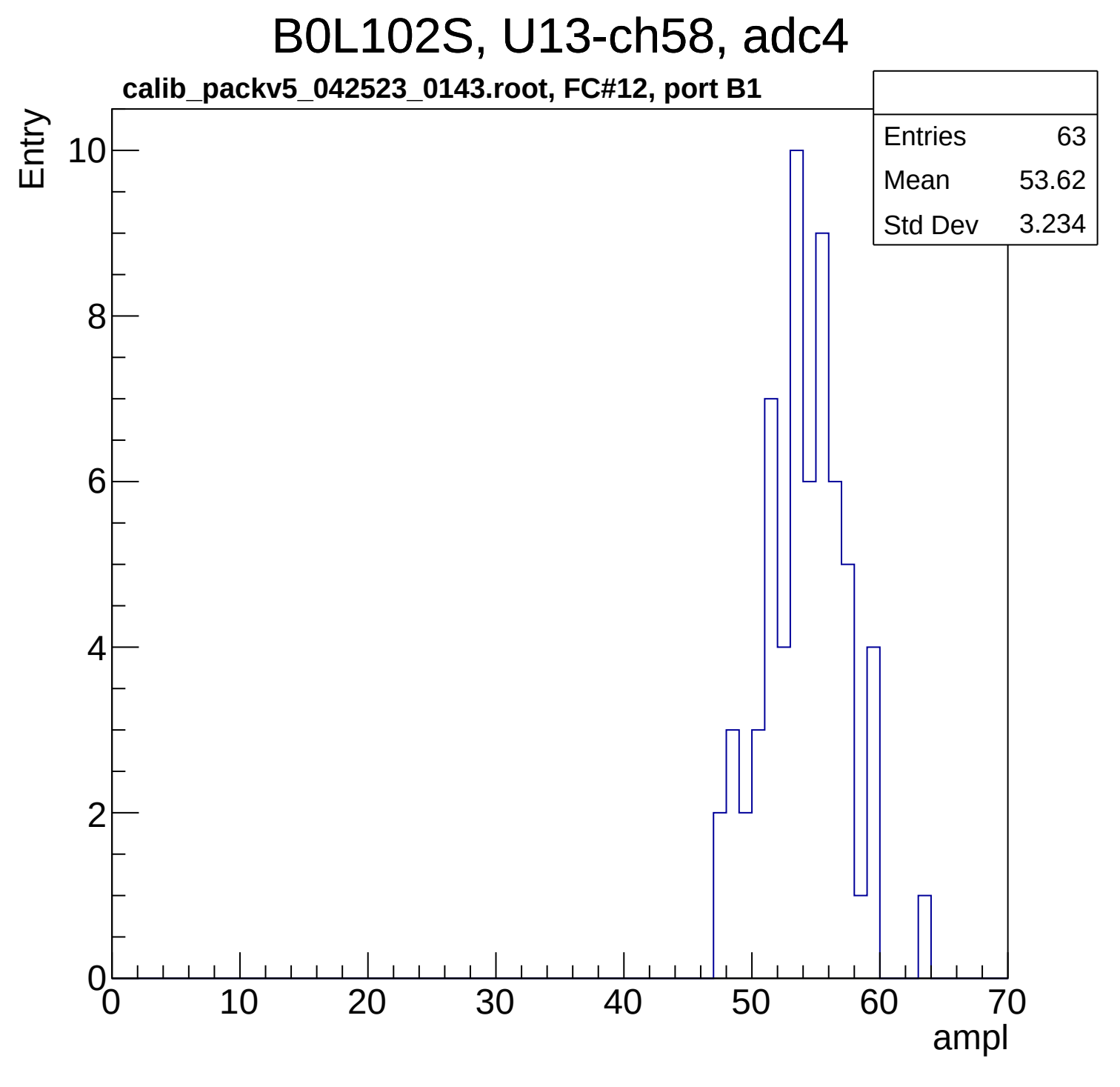
B0L102S, U13-ch58, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 53.62
Std Dev 3.234
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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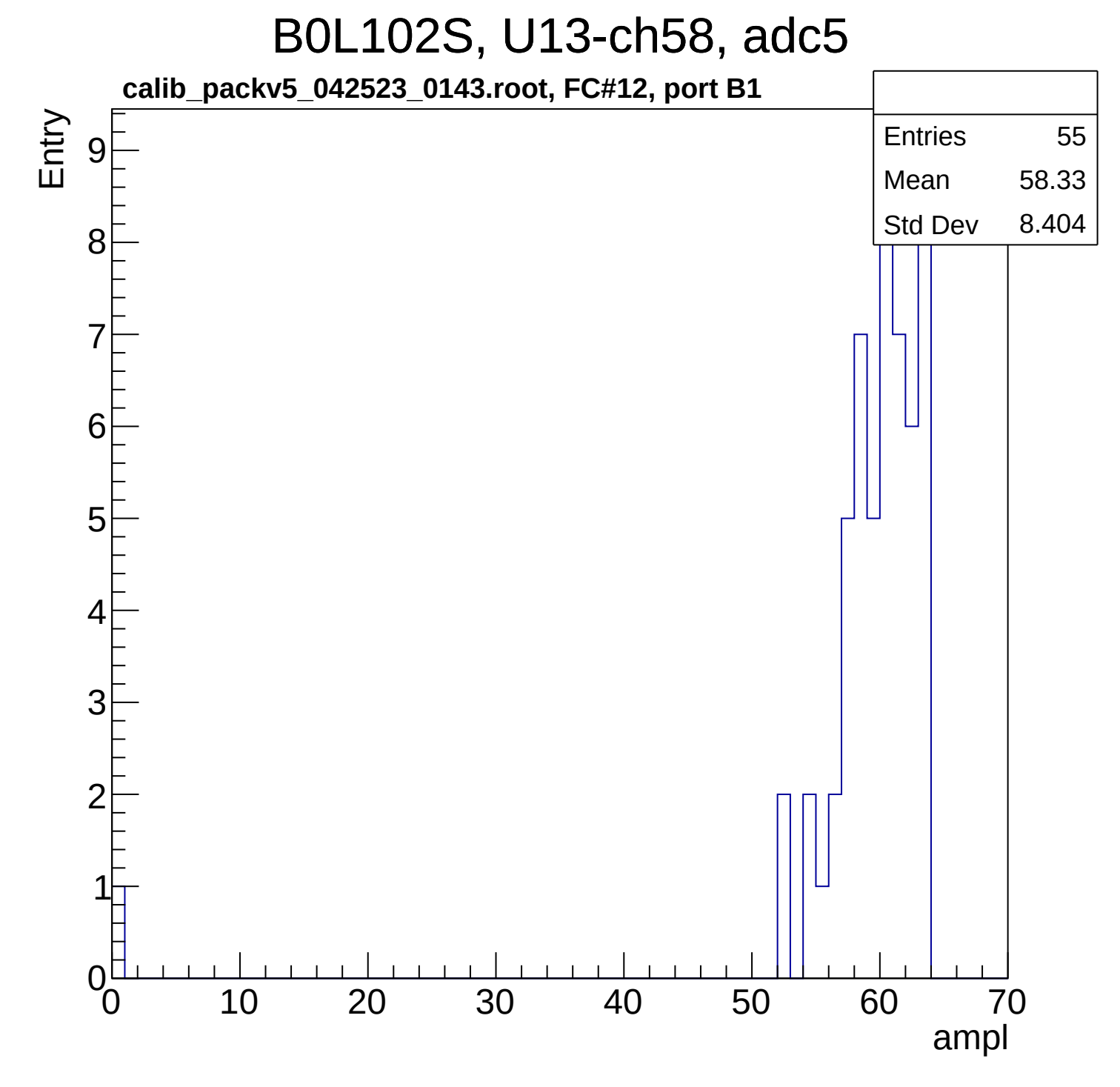
B0L102S, U13-ch58, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.33
Std Dev 8.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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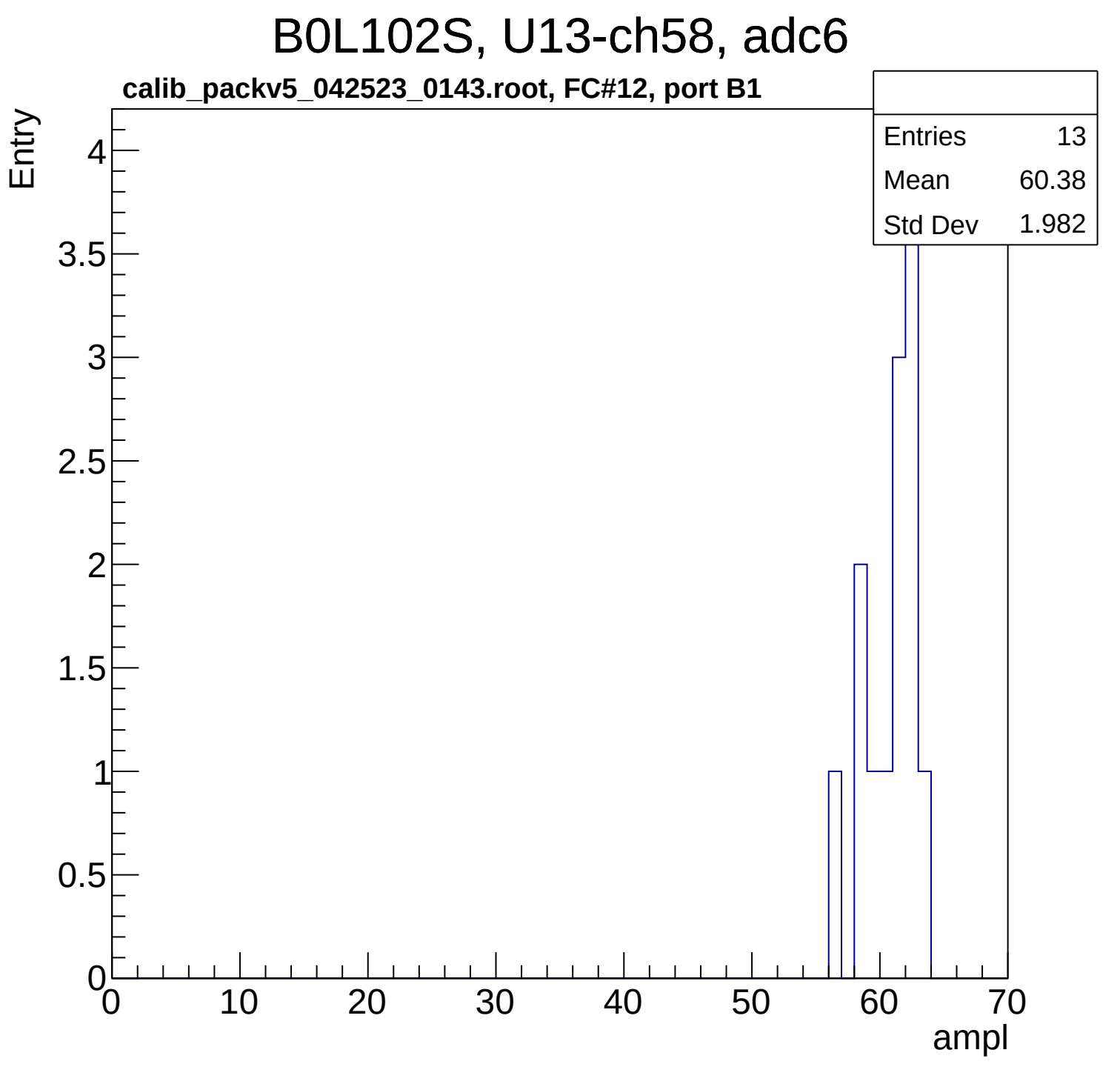
B0L102S, U13-ch58, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 60.38
Std Dev 1.982
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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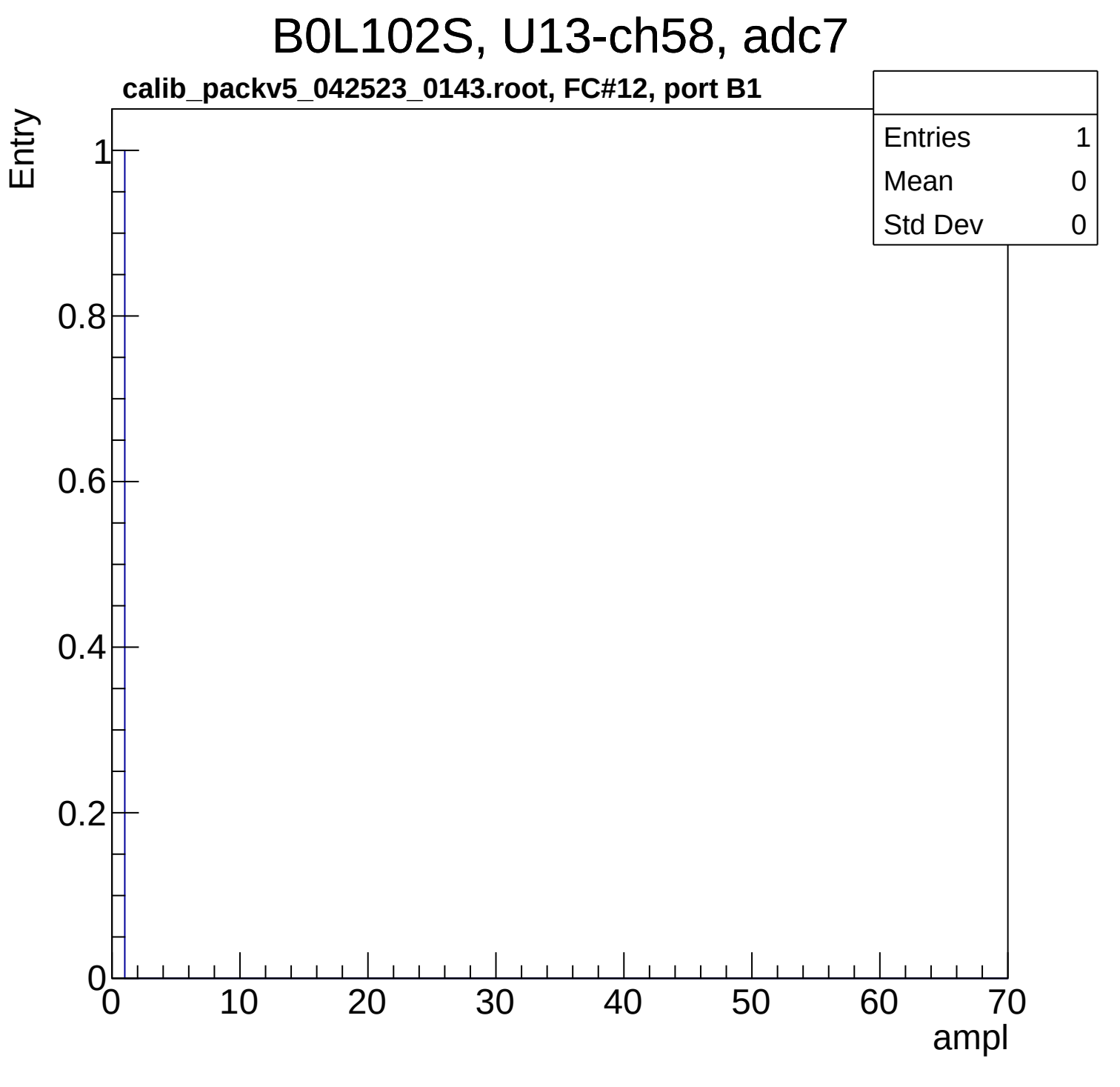
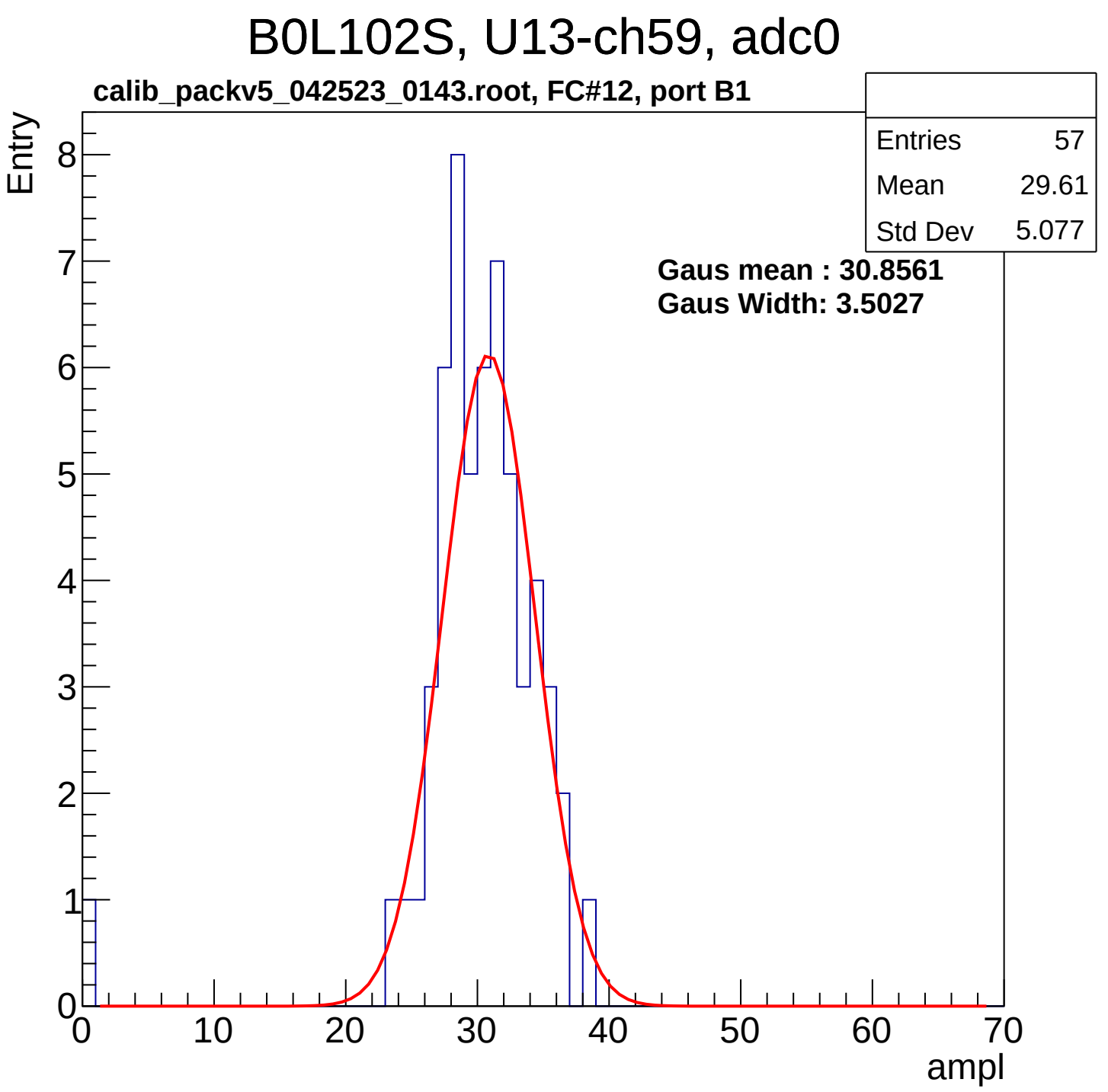
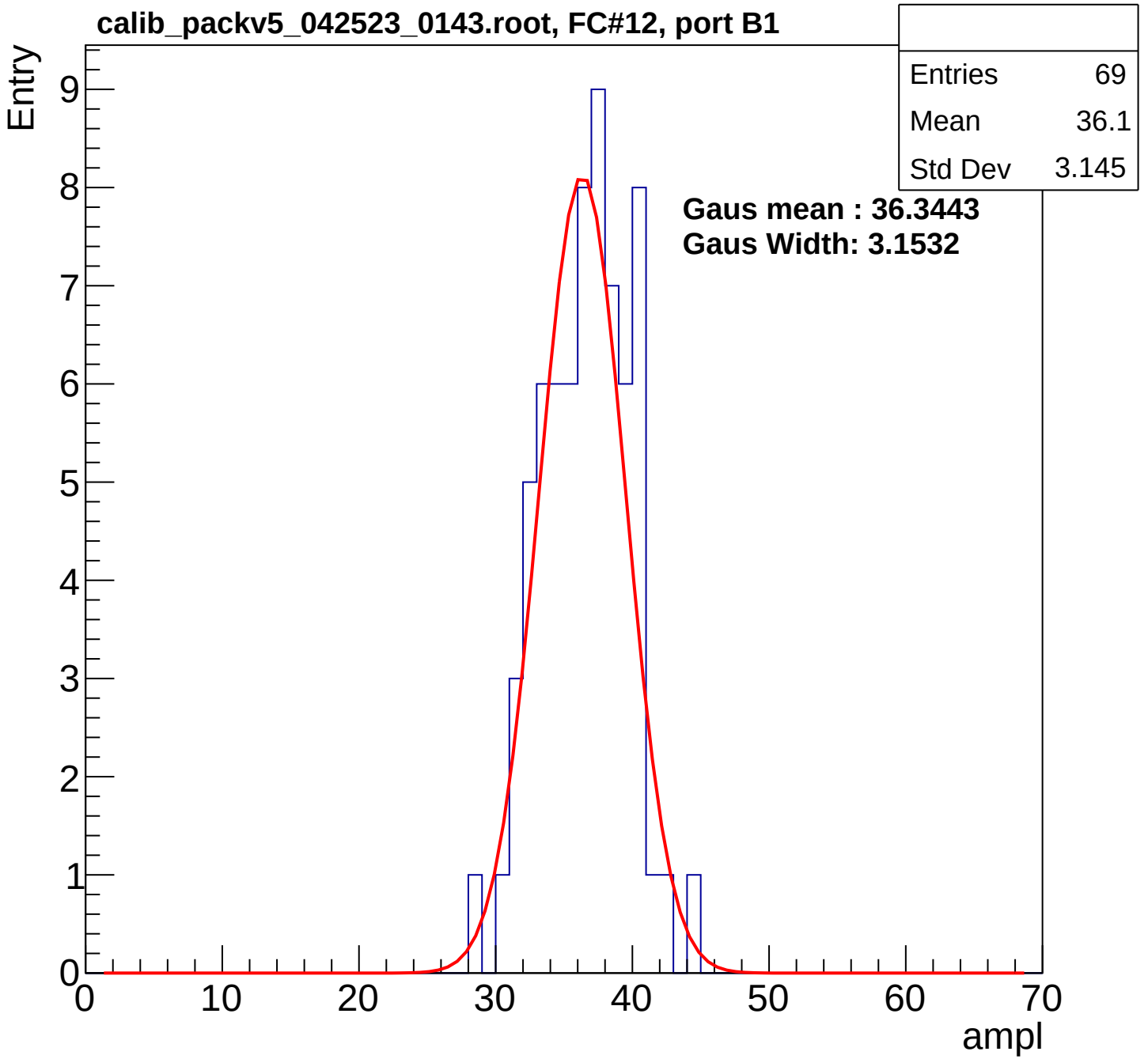

B0L102S, U13-ch58, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch59, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 29.61
Std Dev 5.077
Gaus mean : 30.8561
Gaus Width: 3.5027
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch59, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 36.1
Std Dev 3.145
Gaus mean : 36.3443
Gaus Width: 3.1532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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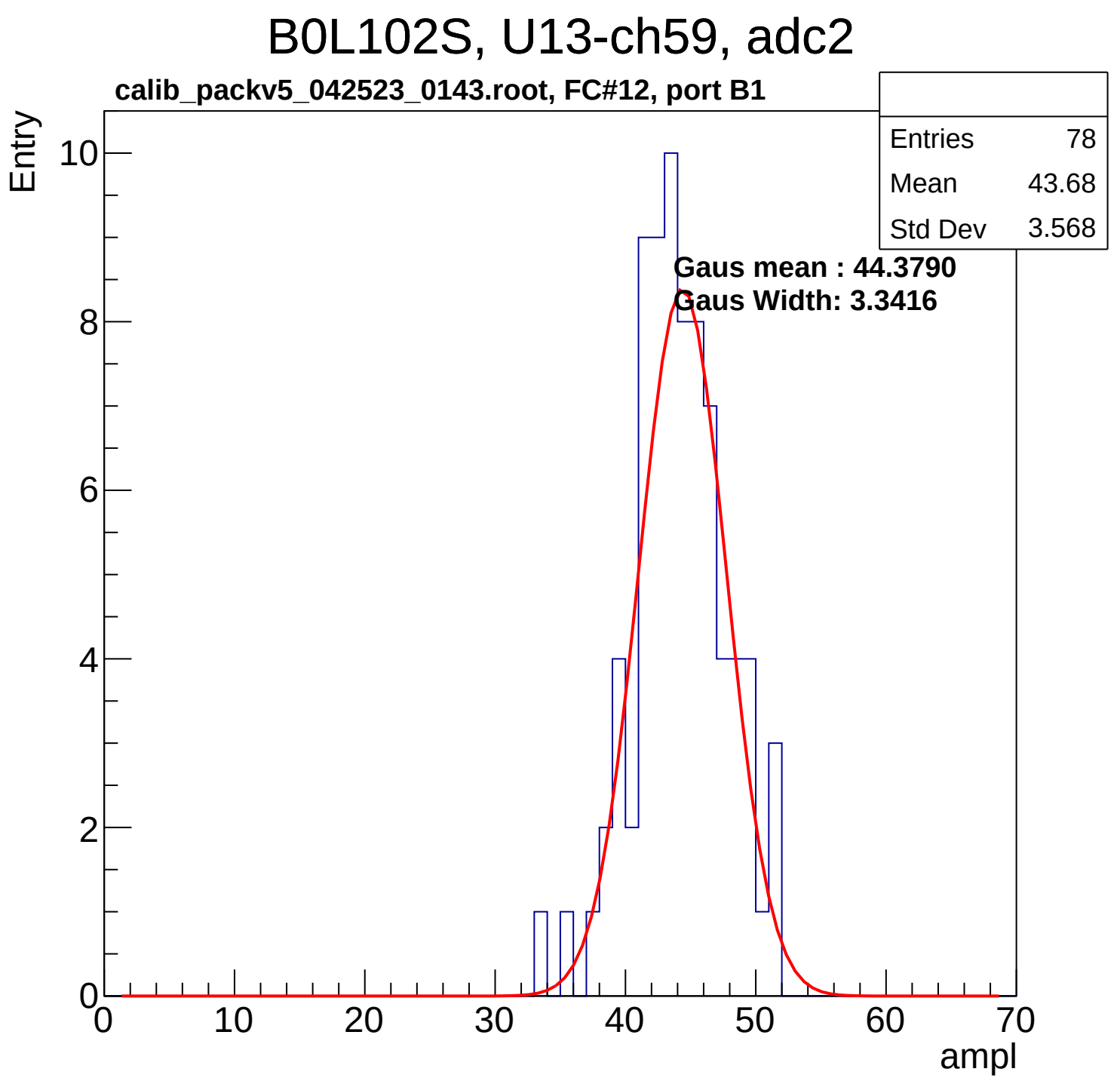
B0L102S, U13-ch59, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 43.68
Std Dev 3.568
Gaus mean : 44.3790
Gaus Width: 3.3416
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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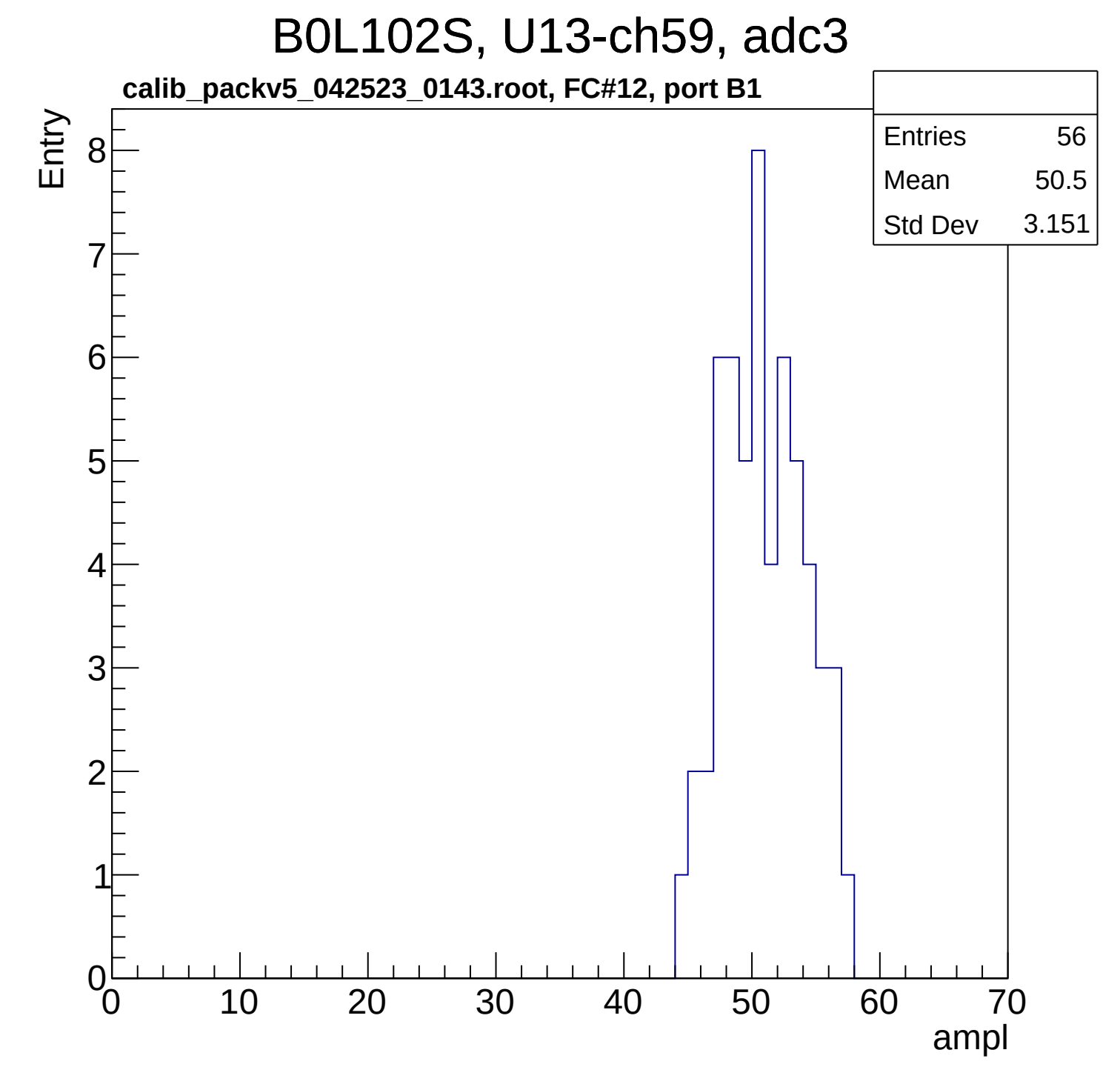
B0L102S, U13-ch59, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 50.5
Std Dev 3.151
Entry
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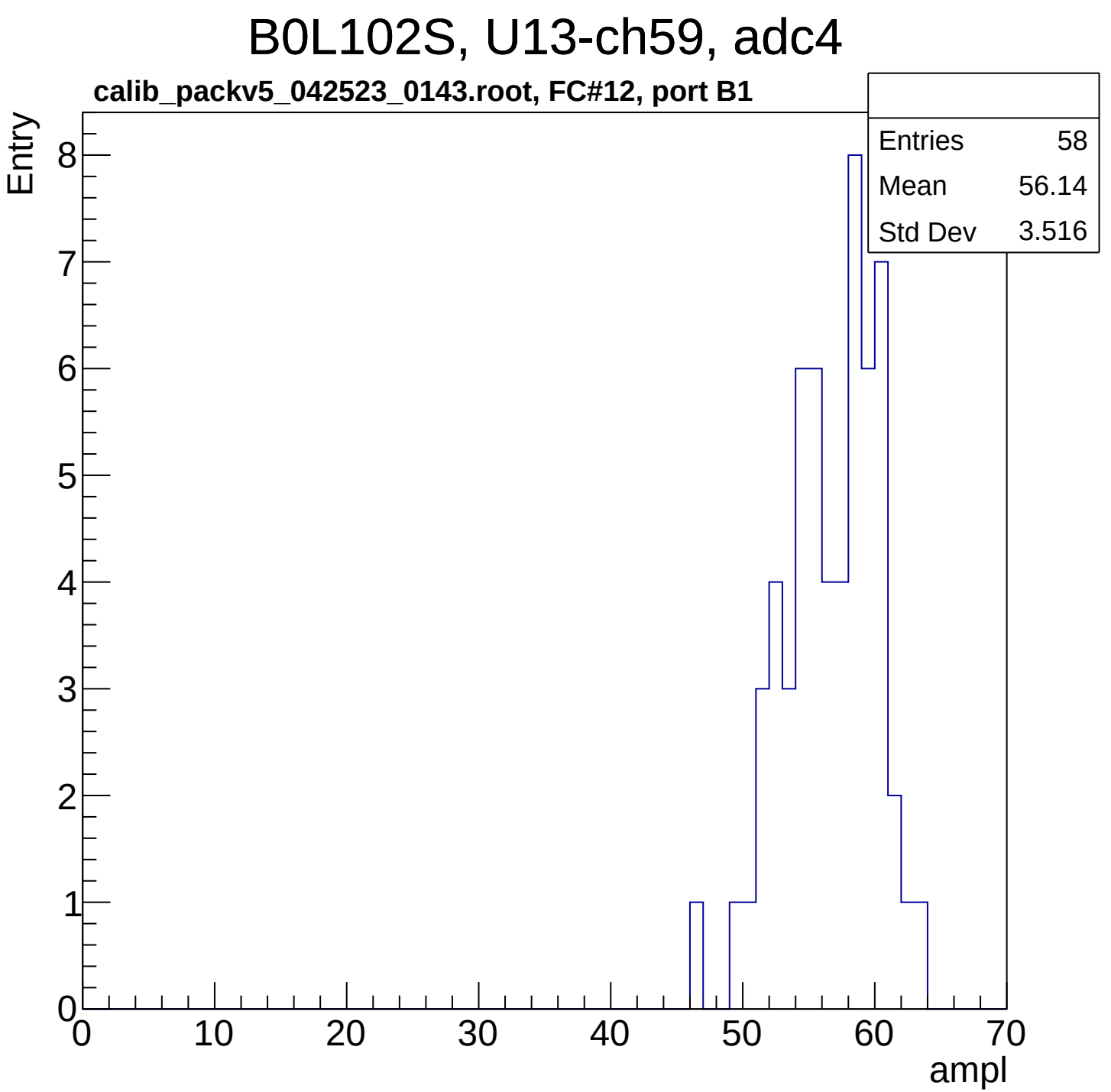
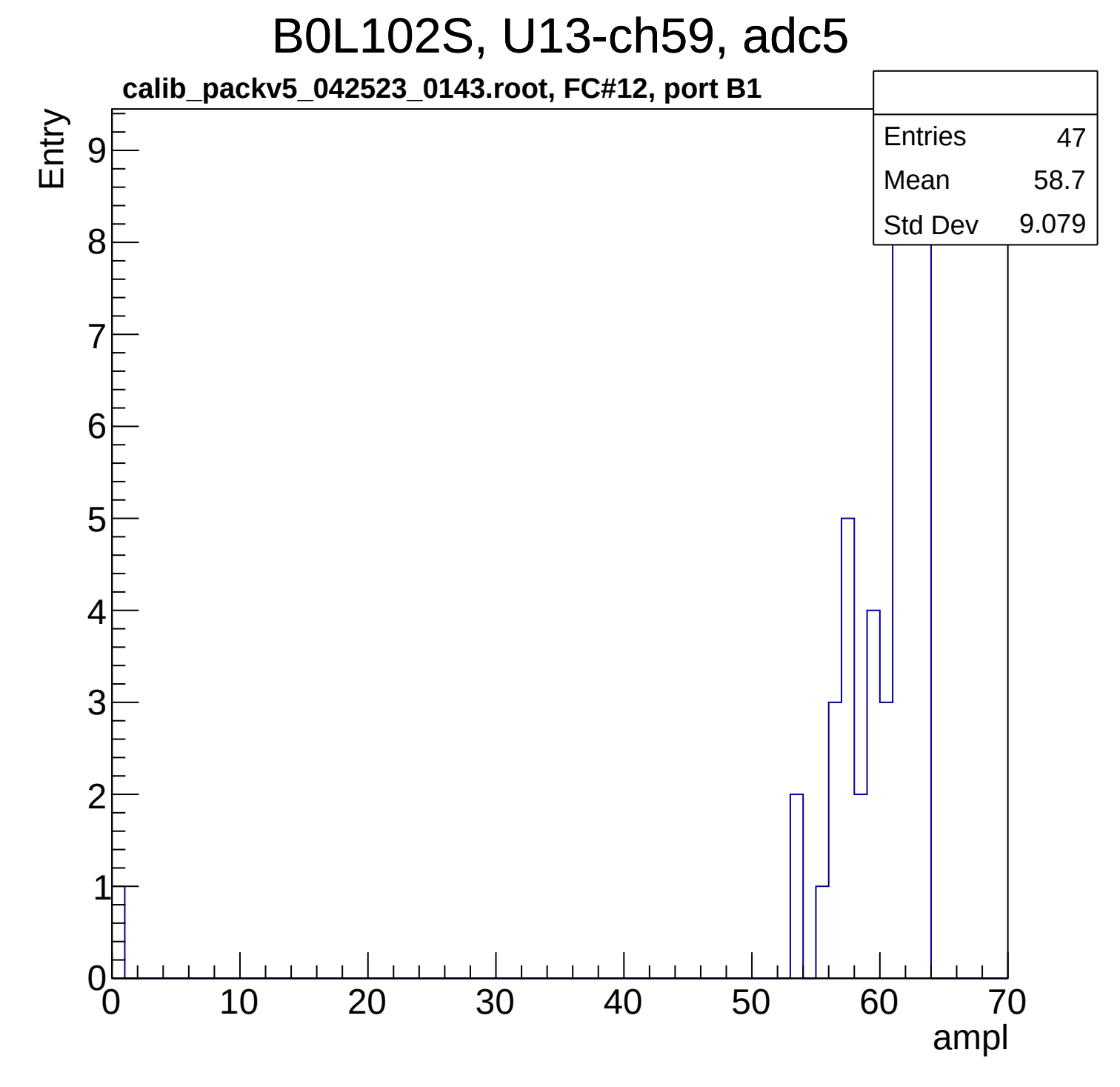
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch59, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 56.14
Std Dev 3.516
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch59, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.7
Std Dev 9.079
Entry
ampl
0
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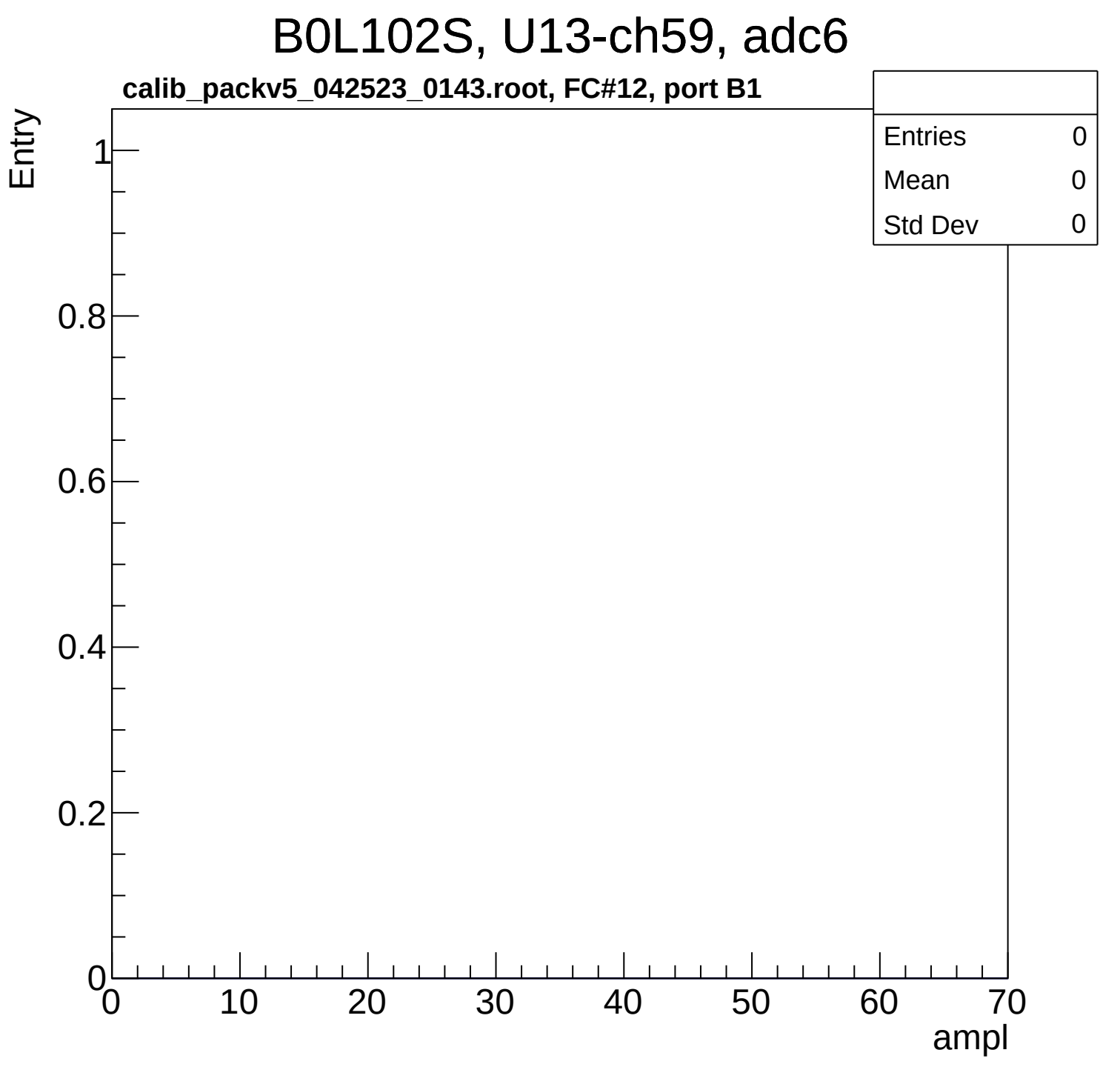
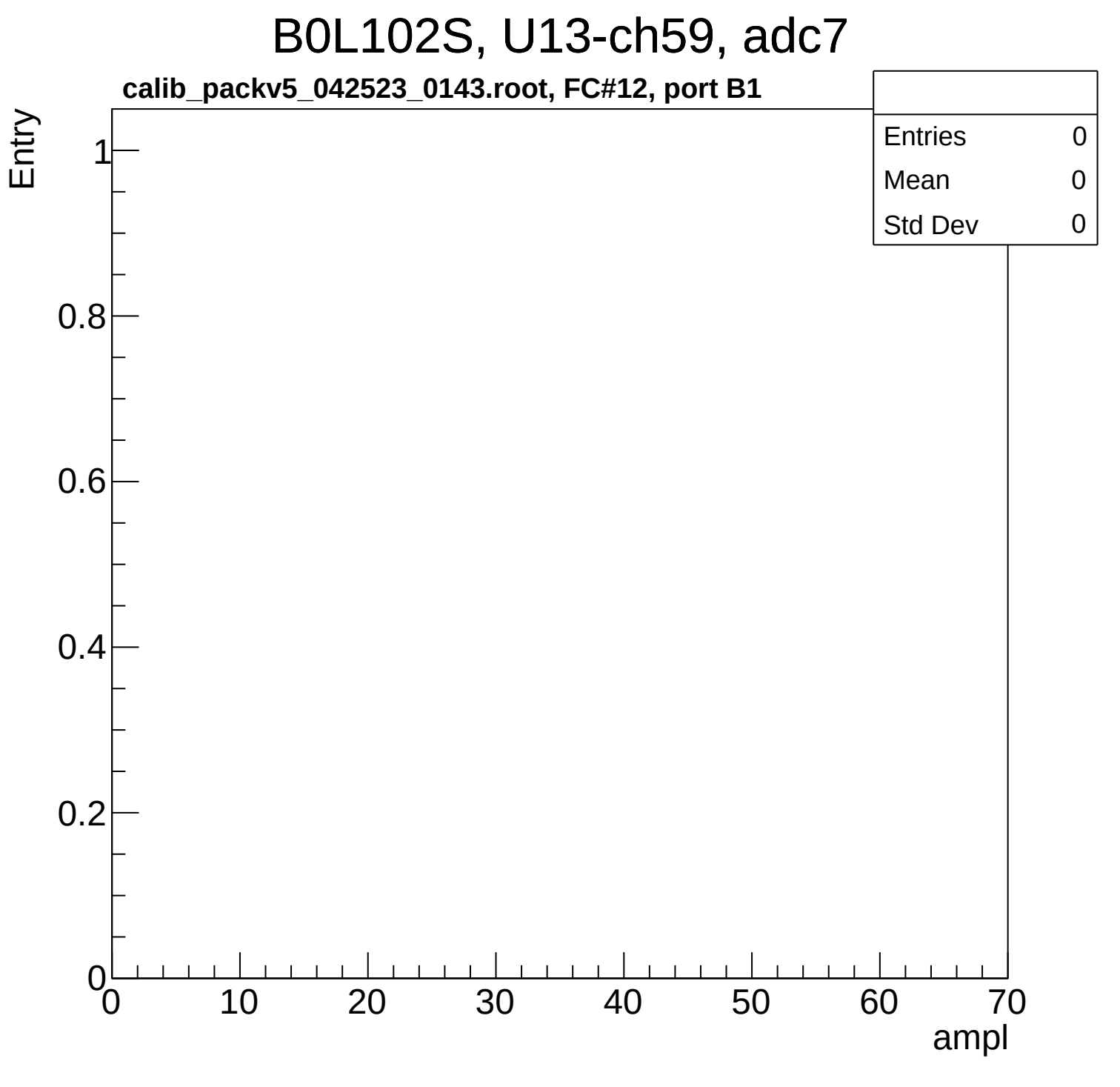
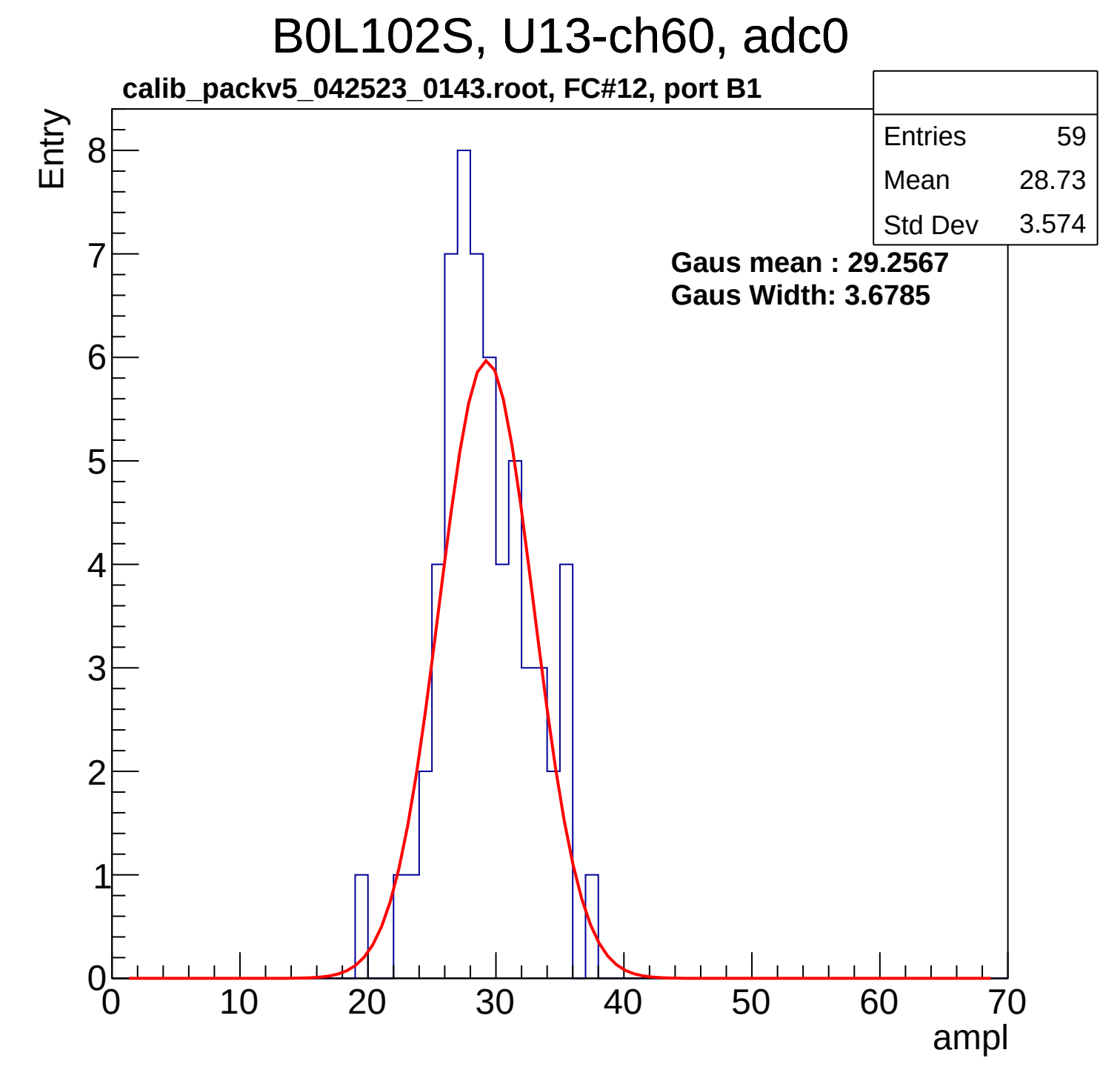
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch59, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch59, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch60, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 28.73
Std Dev 3.574
Gaus mean : 29.2567
Gaus Width: 3.6785
Entry
ampl
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1
2
3
4
5
6
7
8
0
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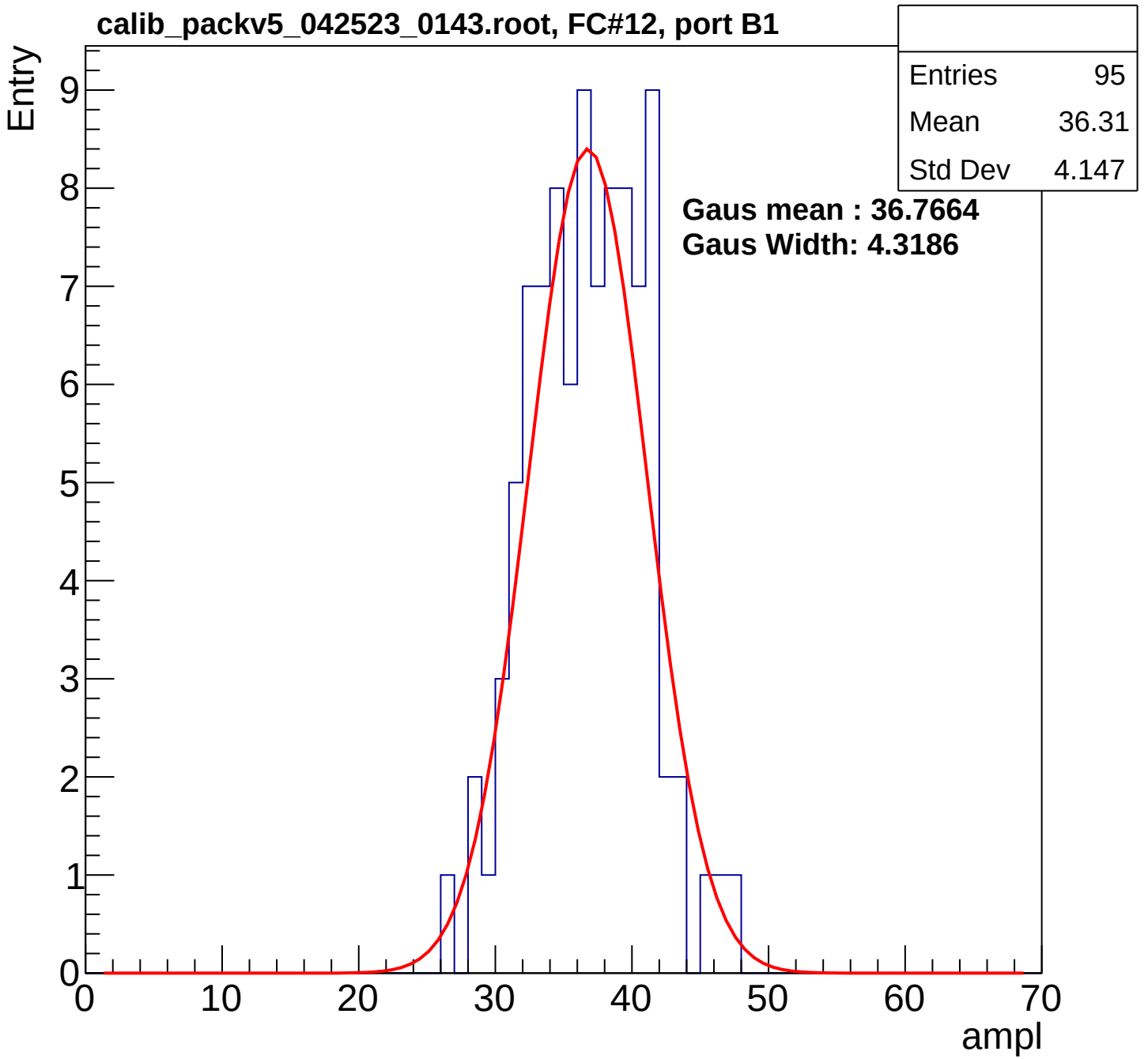
20
30
40
50
60
70


B0L102S, U13-ch60, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 95
Mean 36.31
Std Dev 4.147
Gaus mean : 36.7664
Gaus Width: 4.3186
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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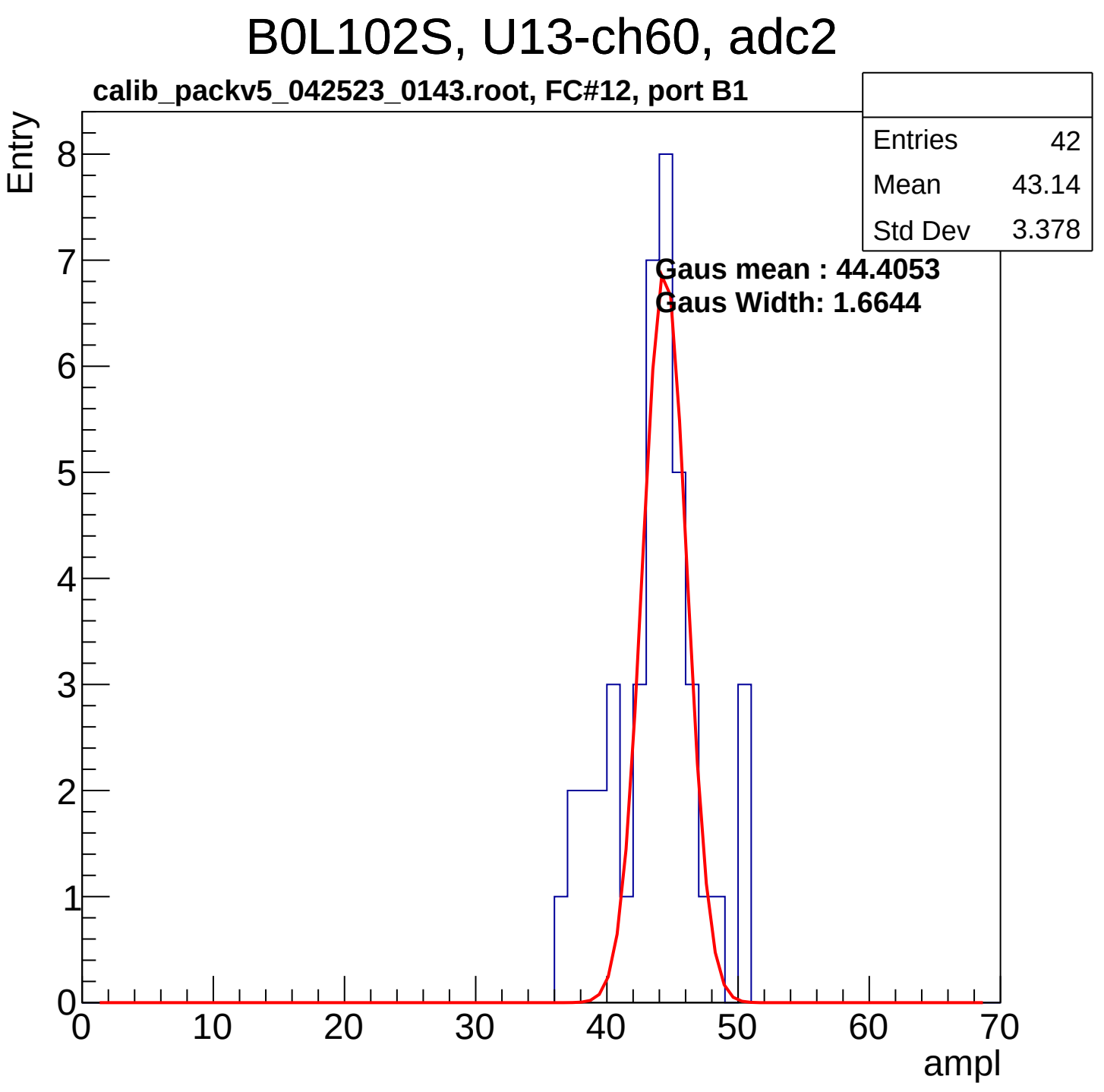
B0L102S, U13-ch60, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 43.14
Std Dev 3.378
Gaus mean : 44.4053
Gaus Width: 1.6644
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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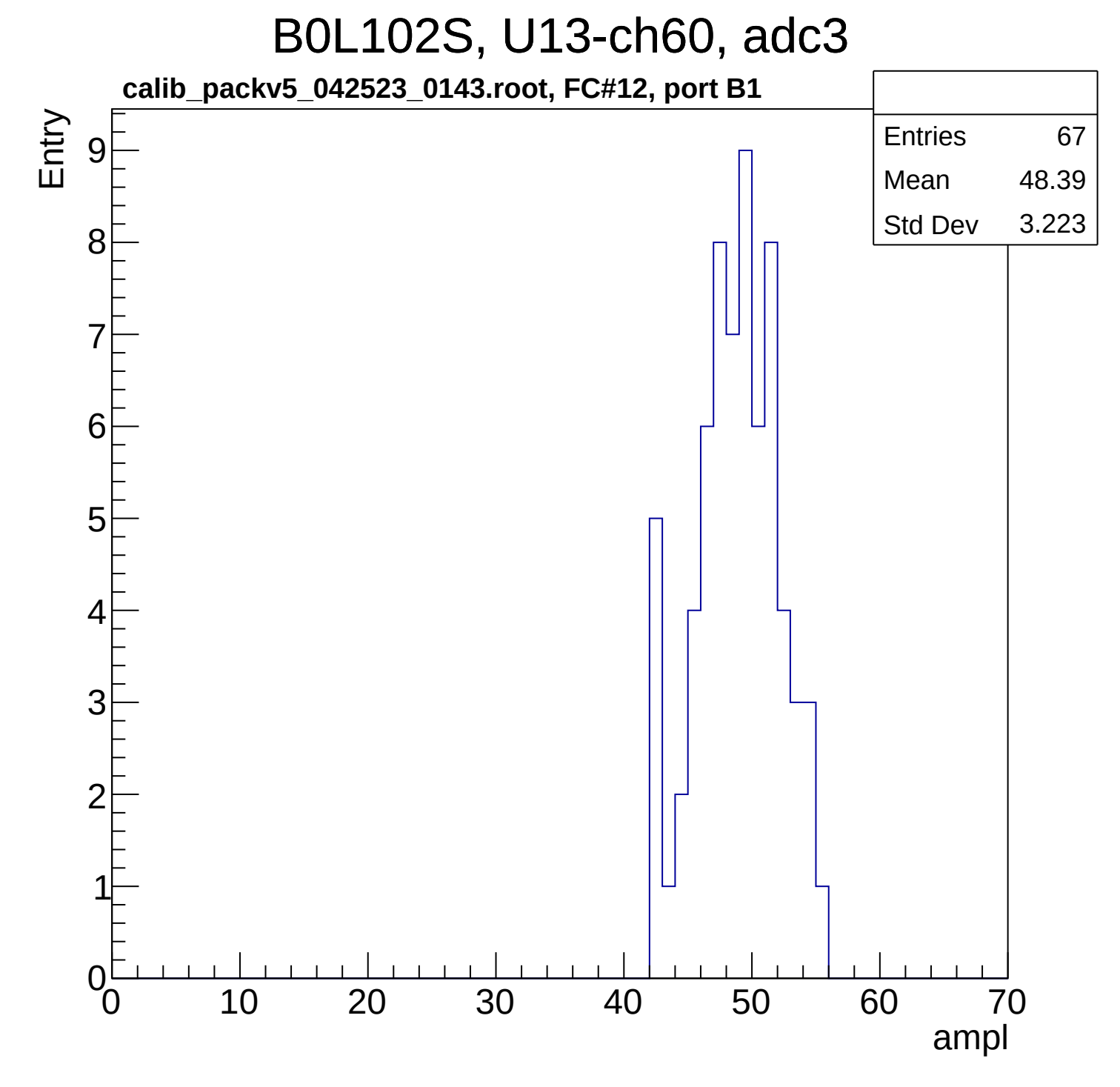
B0L102S, U13-ch60, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 48.39
Std Dev 3.223
Entry
ampl
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3
4
5
6
7
8
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0
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60
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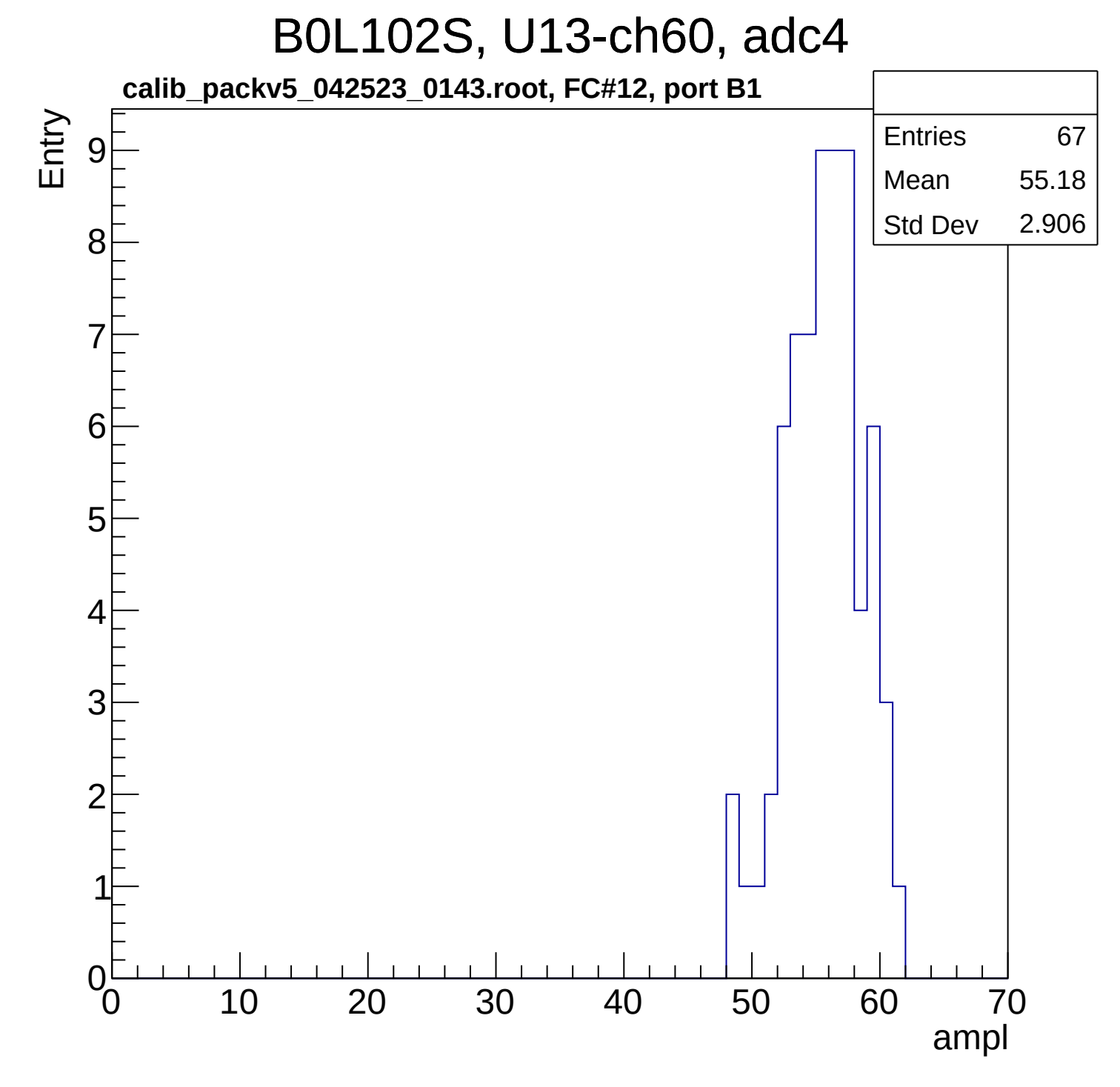
B0L102S, U13-ch60, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.18
Std Dev 2.906
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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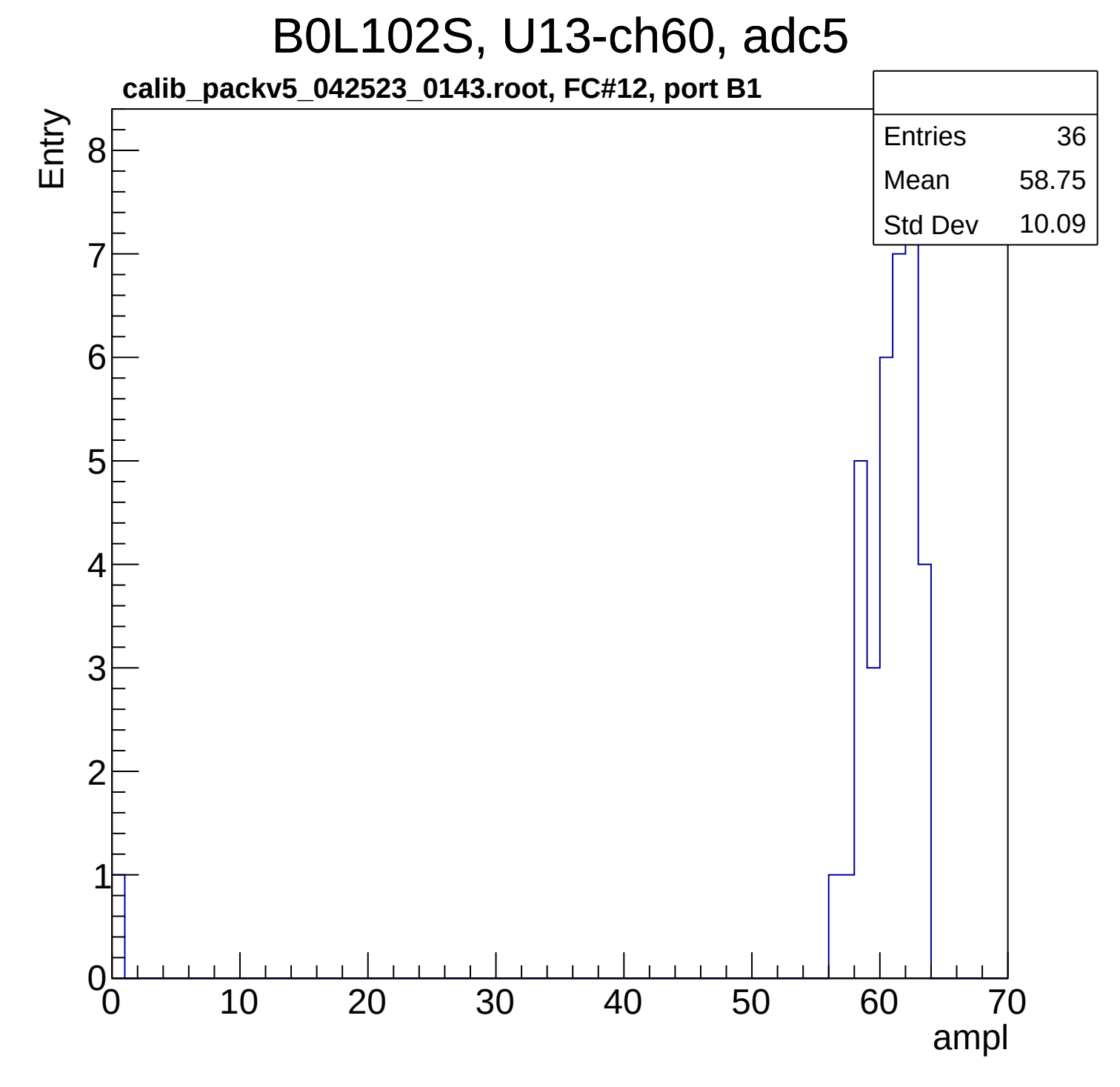
B0L102S, U13-ch60, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.75
Std Dev 10.09
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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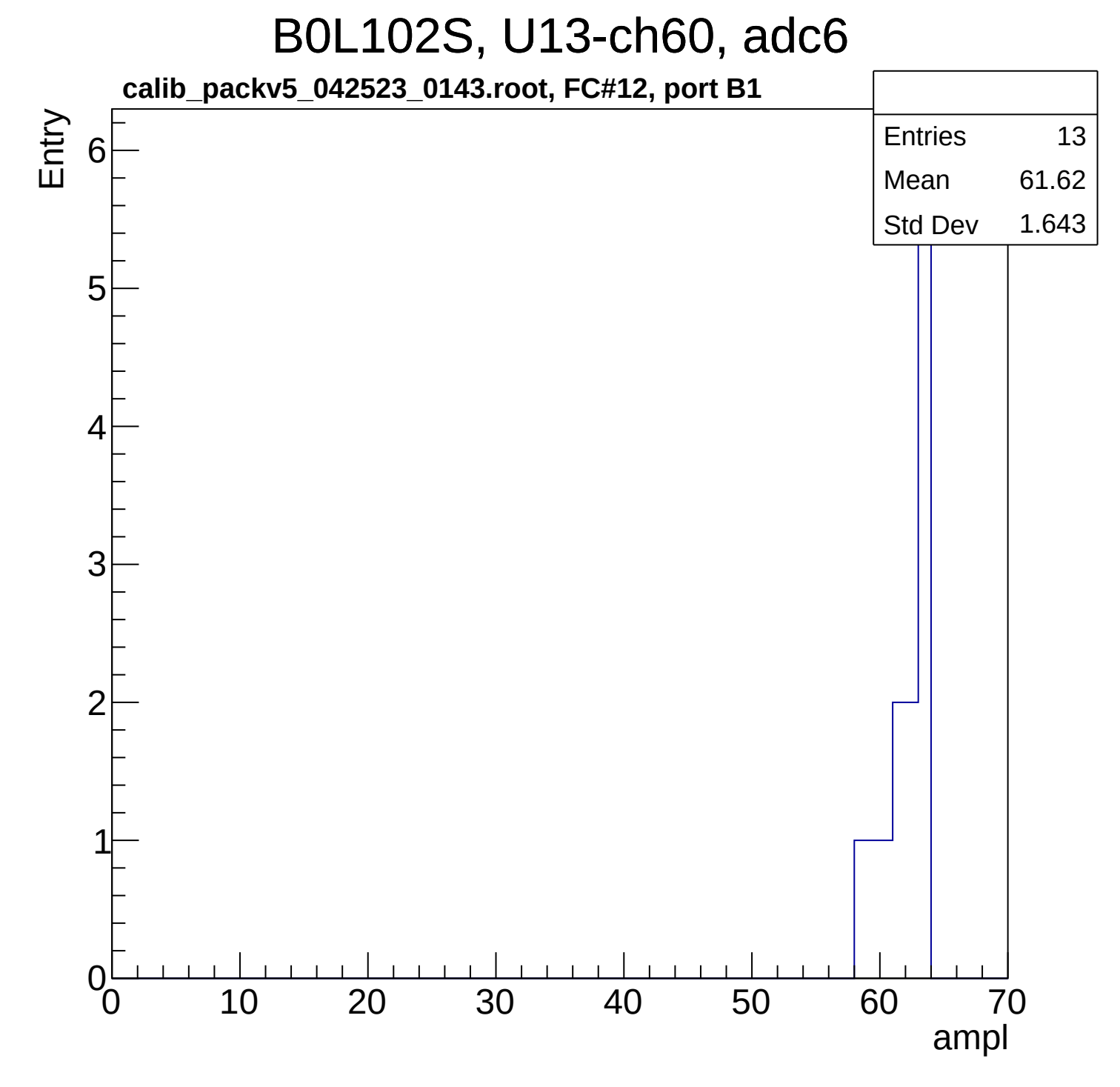
B0L102S, U13-ch60, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 61.62
Std Dev 1.643
Entry
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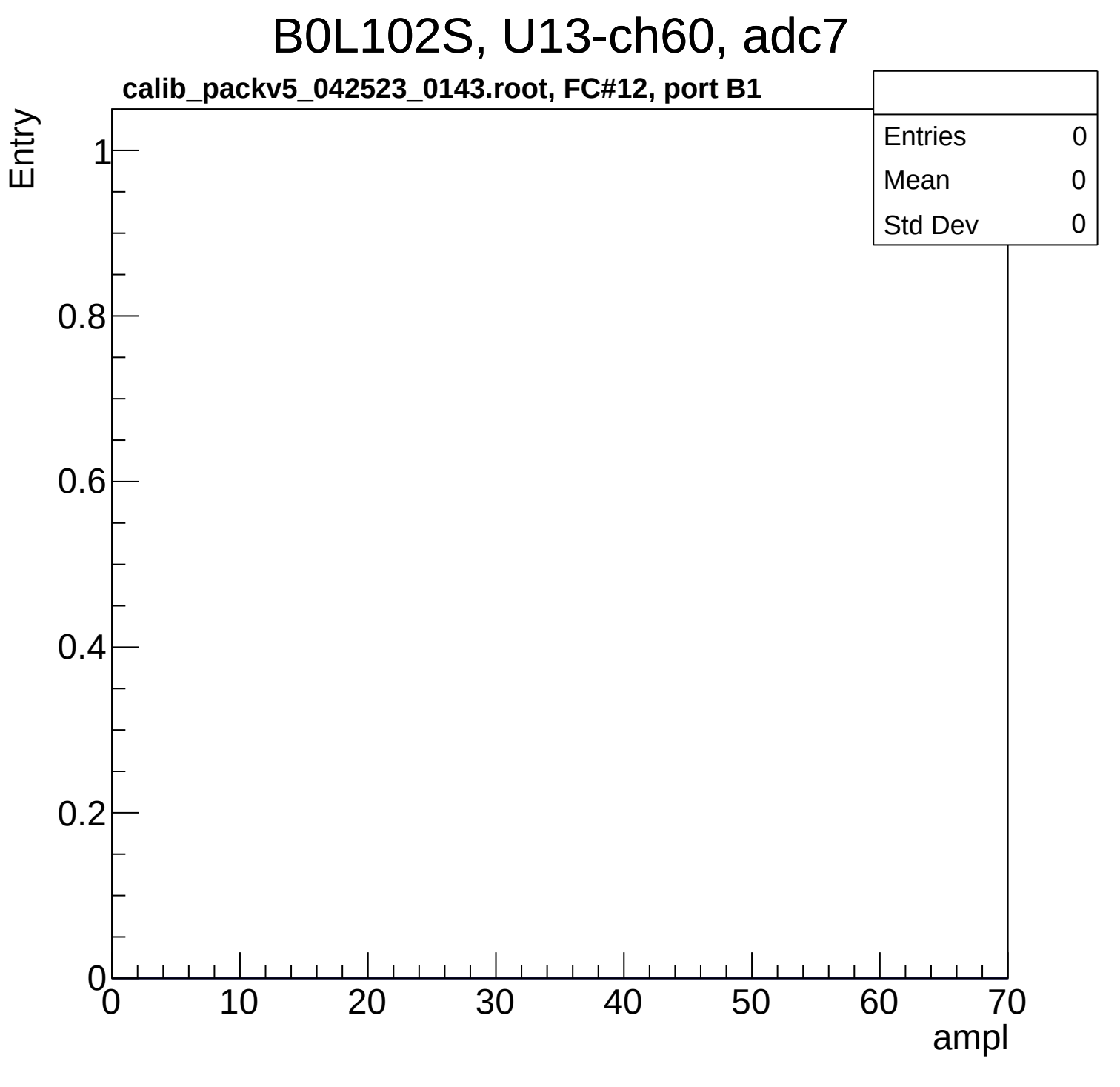
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch60, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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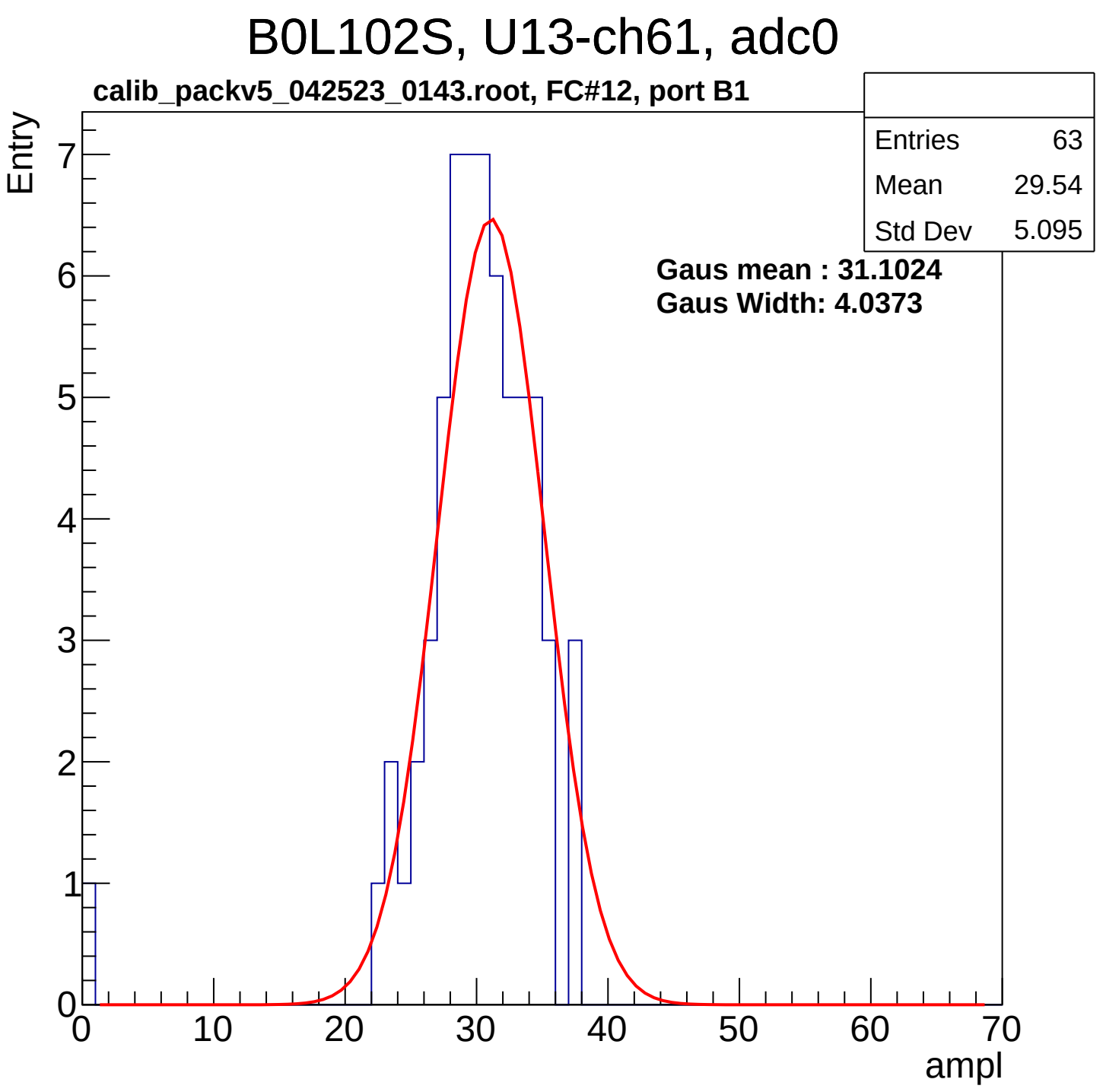
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch61, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 29.54
Std Dev 5.095
Gaus mean : 31.1024
Gaus Width: 4.0373
Entry
ampl
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2
3
4
5
6
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0
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40
50
60
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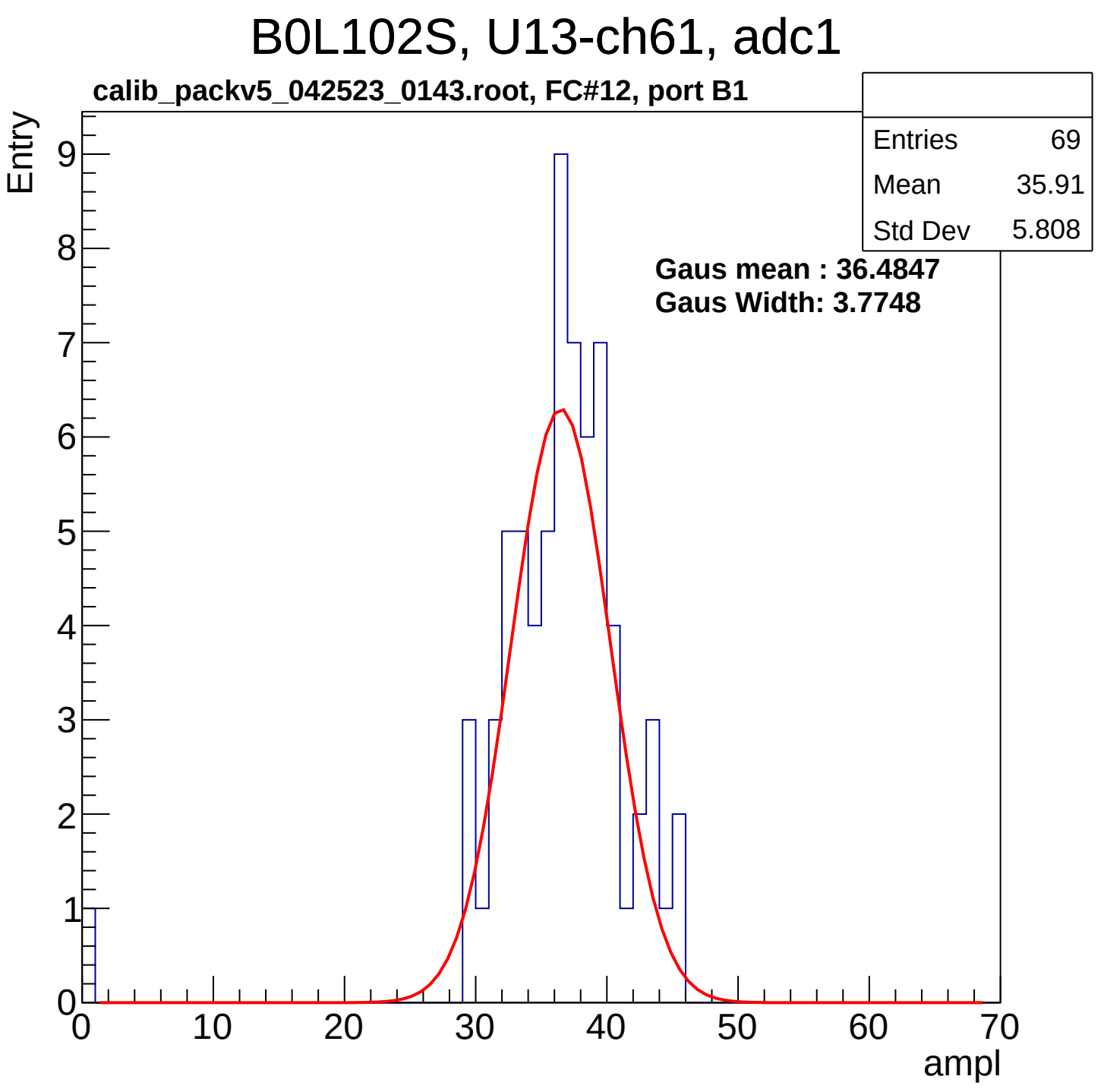
B0L102S, U13-ch61, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 35.91
Std Dev 5.808
Gaus mean : 36.4847
Gaus Width: 3.7748
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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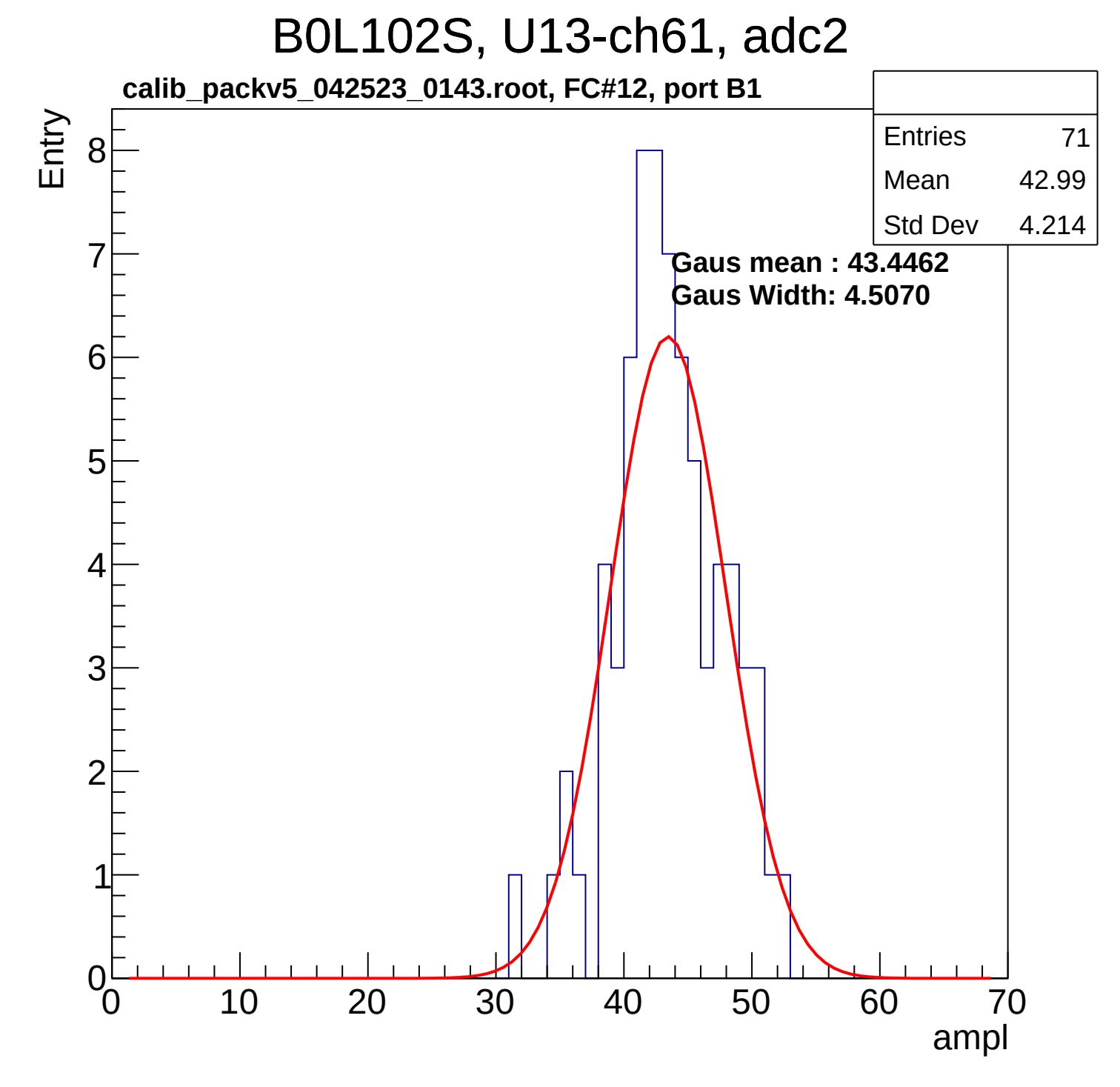
B0L102S, U13-ch61, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.99
Std Dev 4.214
Gaus mean : 43.4462
Gaus Width: 4.5070
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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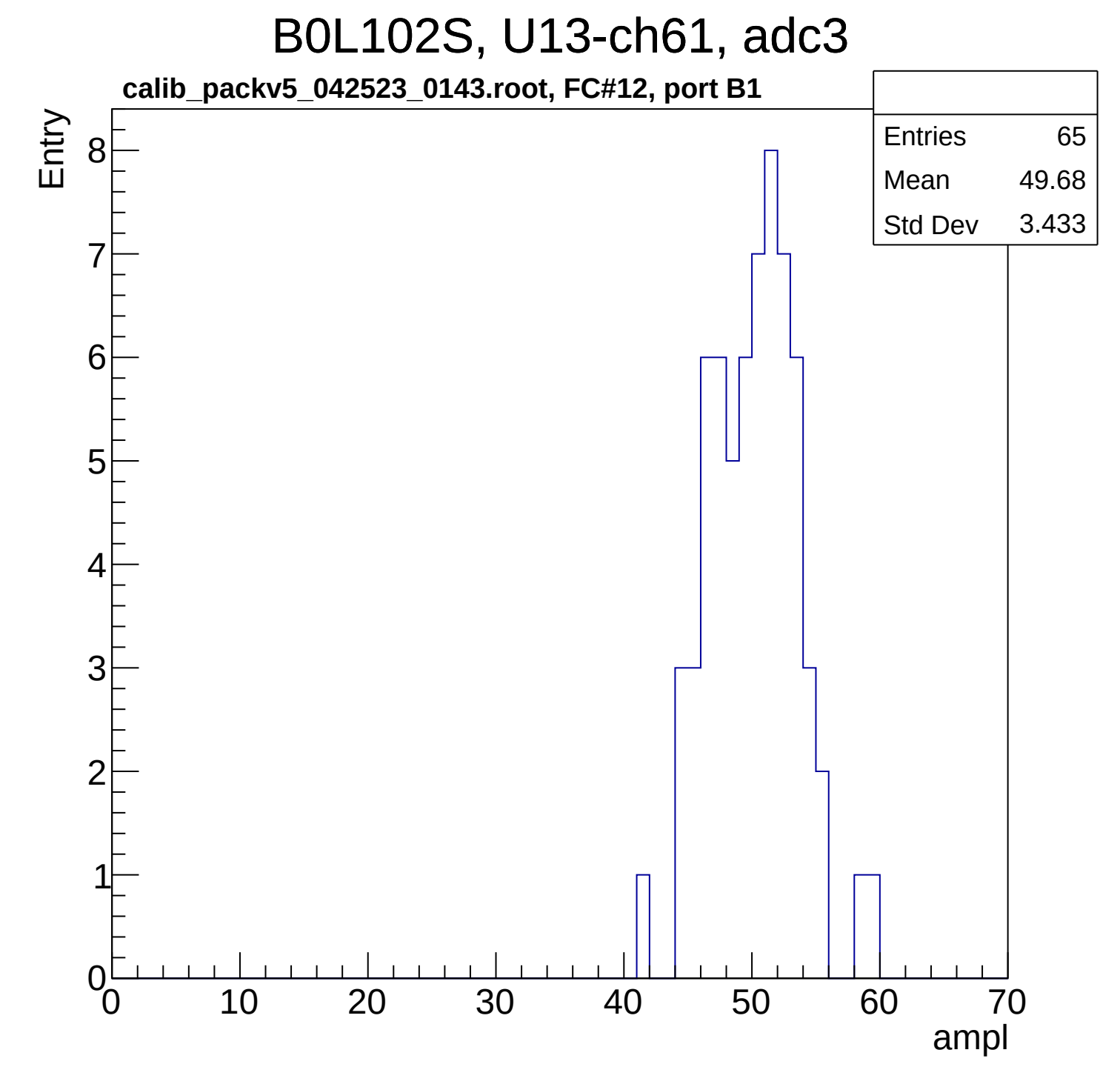
B0L102S, U13-ch61, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.68
Std Dev 3.433
Entry
ampl
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2
3
4
5
6
7
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0
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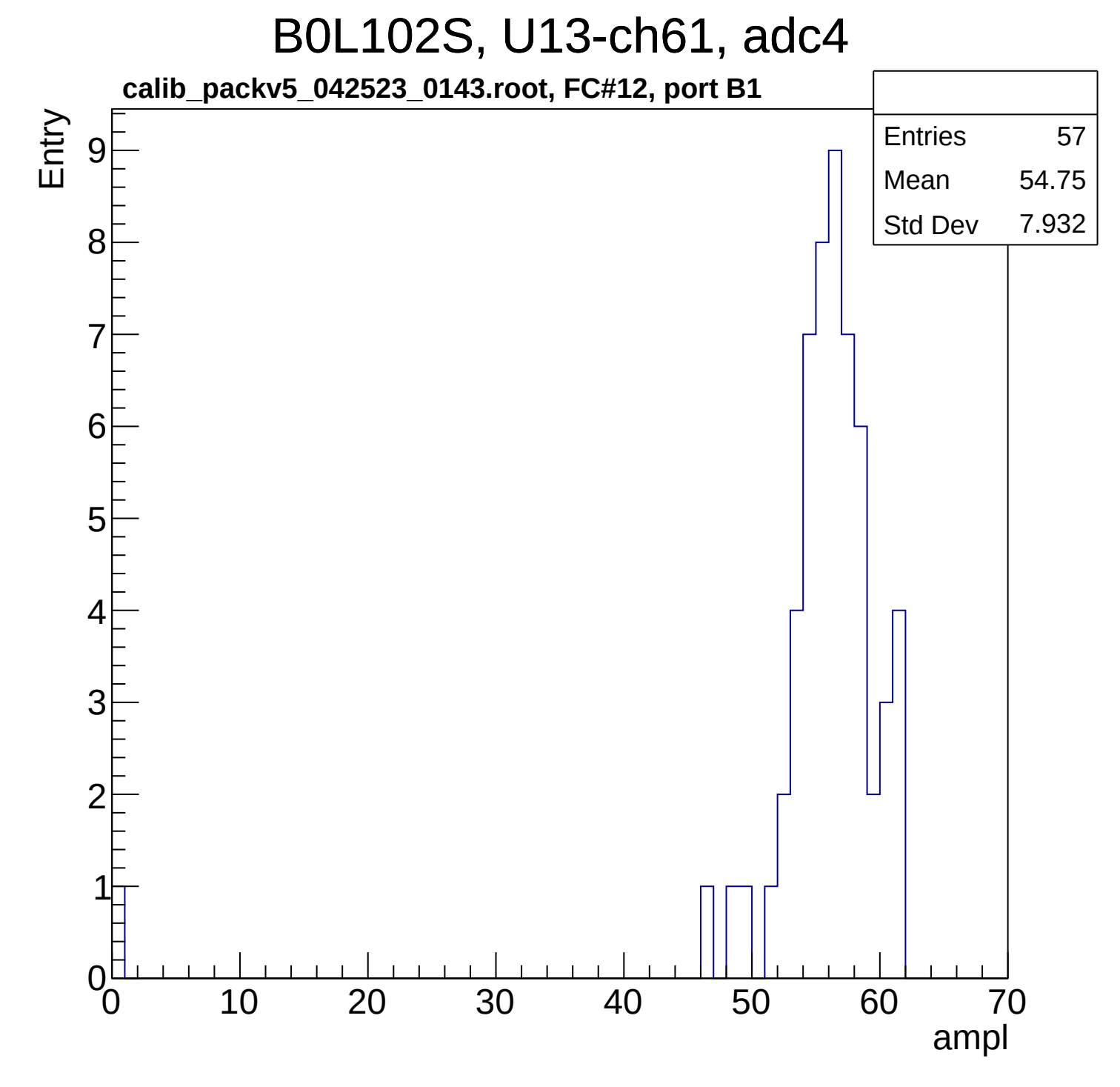
B0L102S, U13-ch61, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.75
Std Dev 7.932
Entry
ampl
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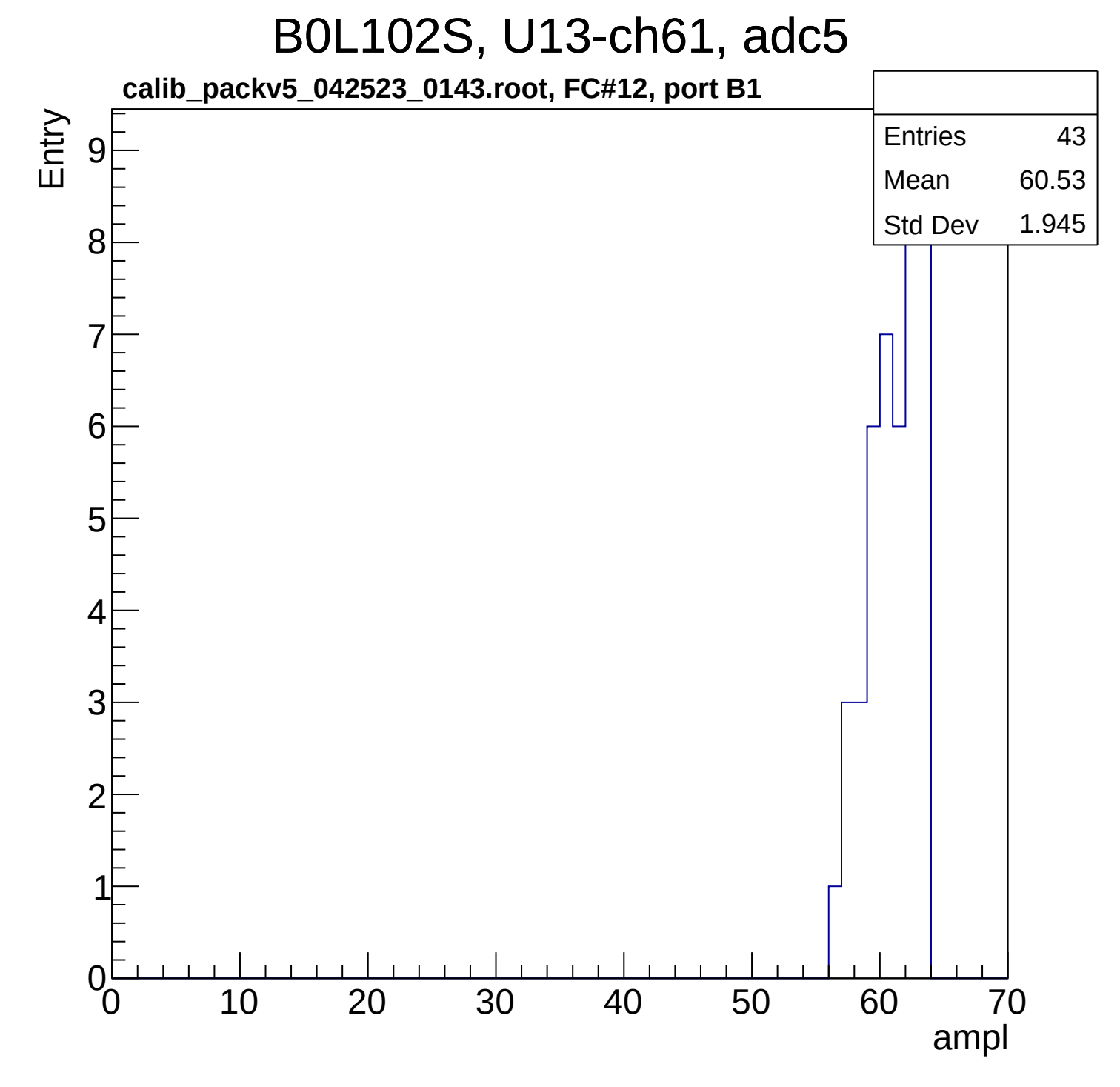
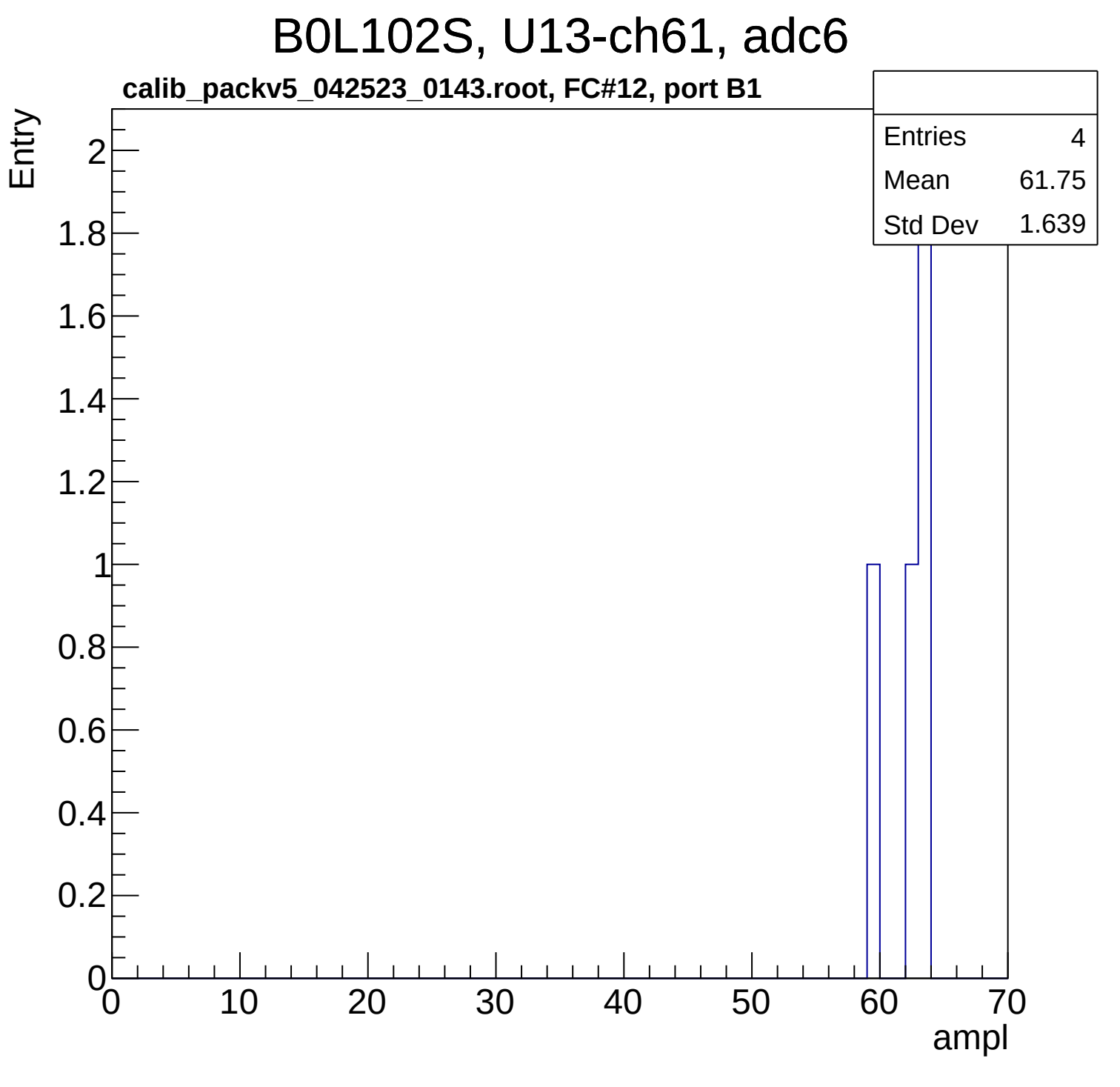
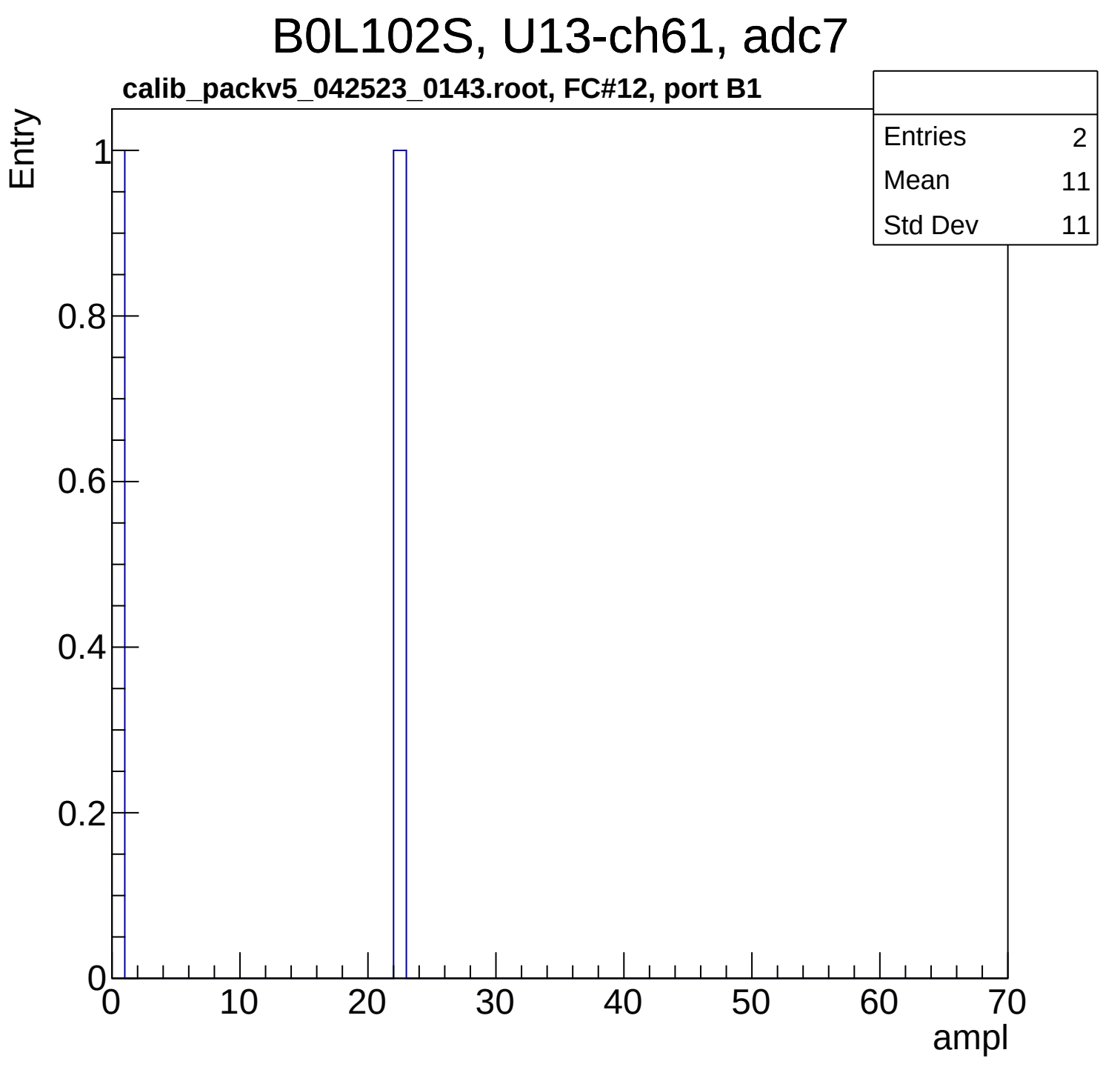
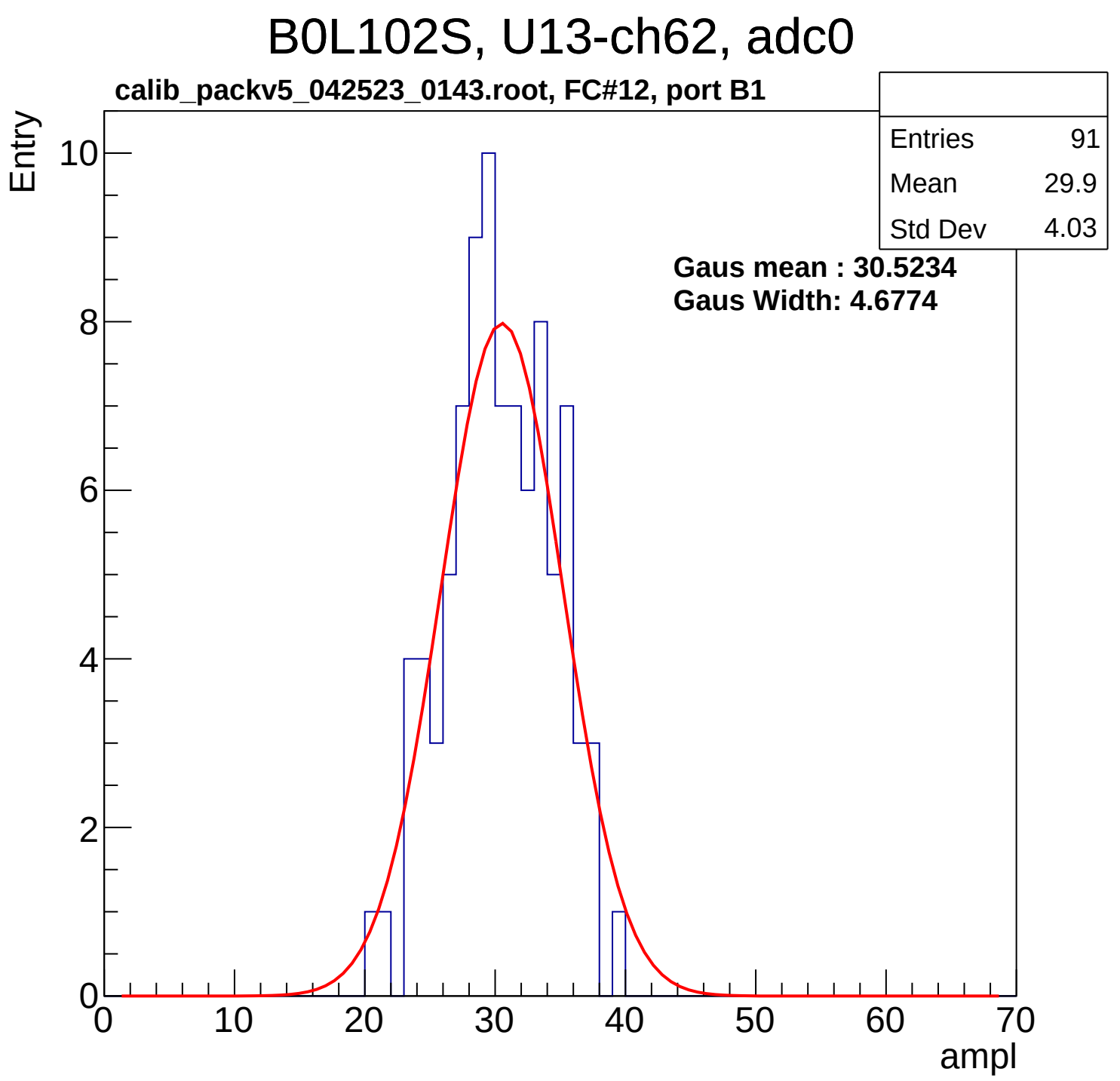
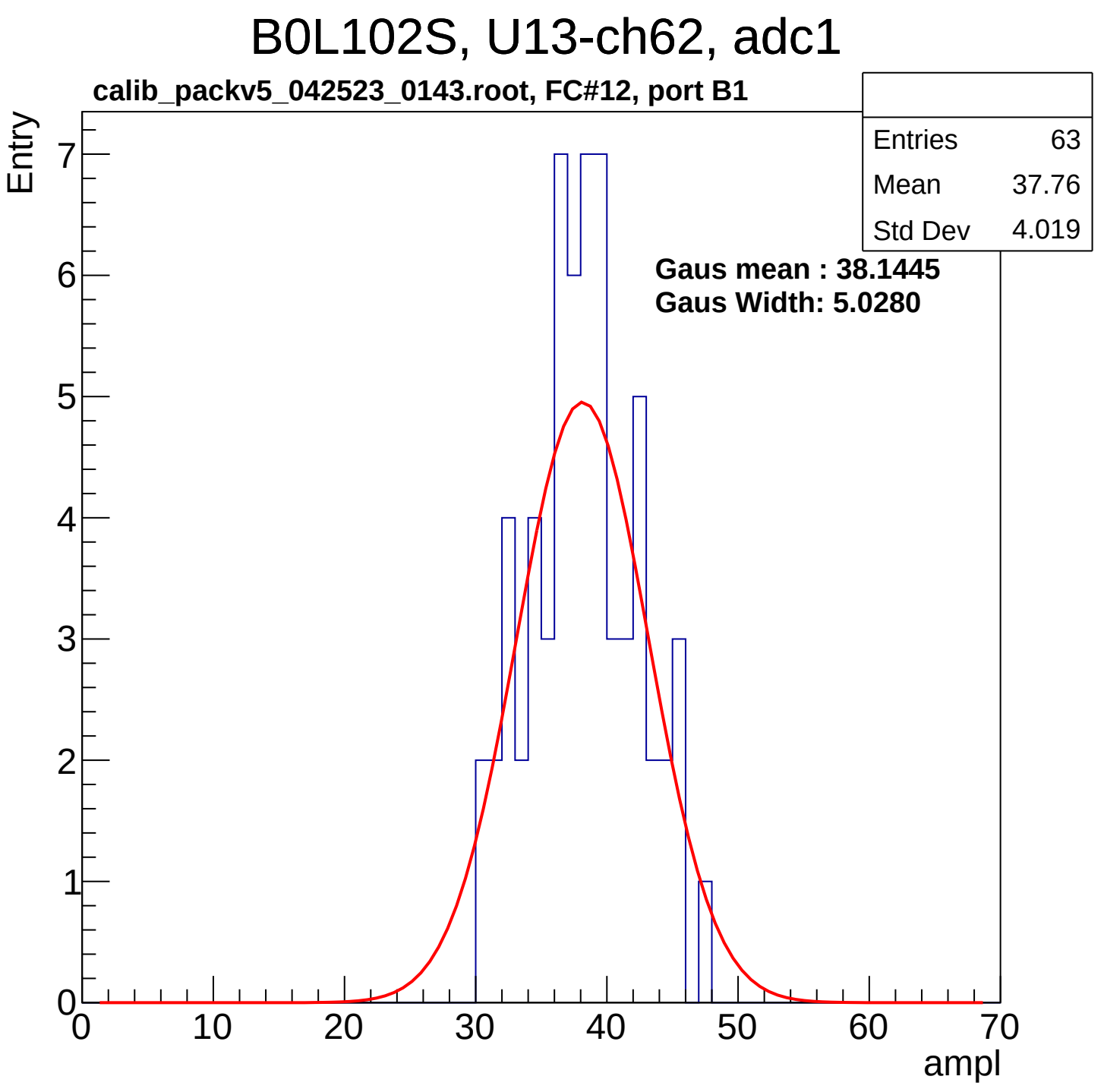
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

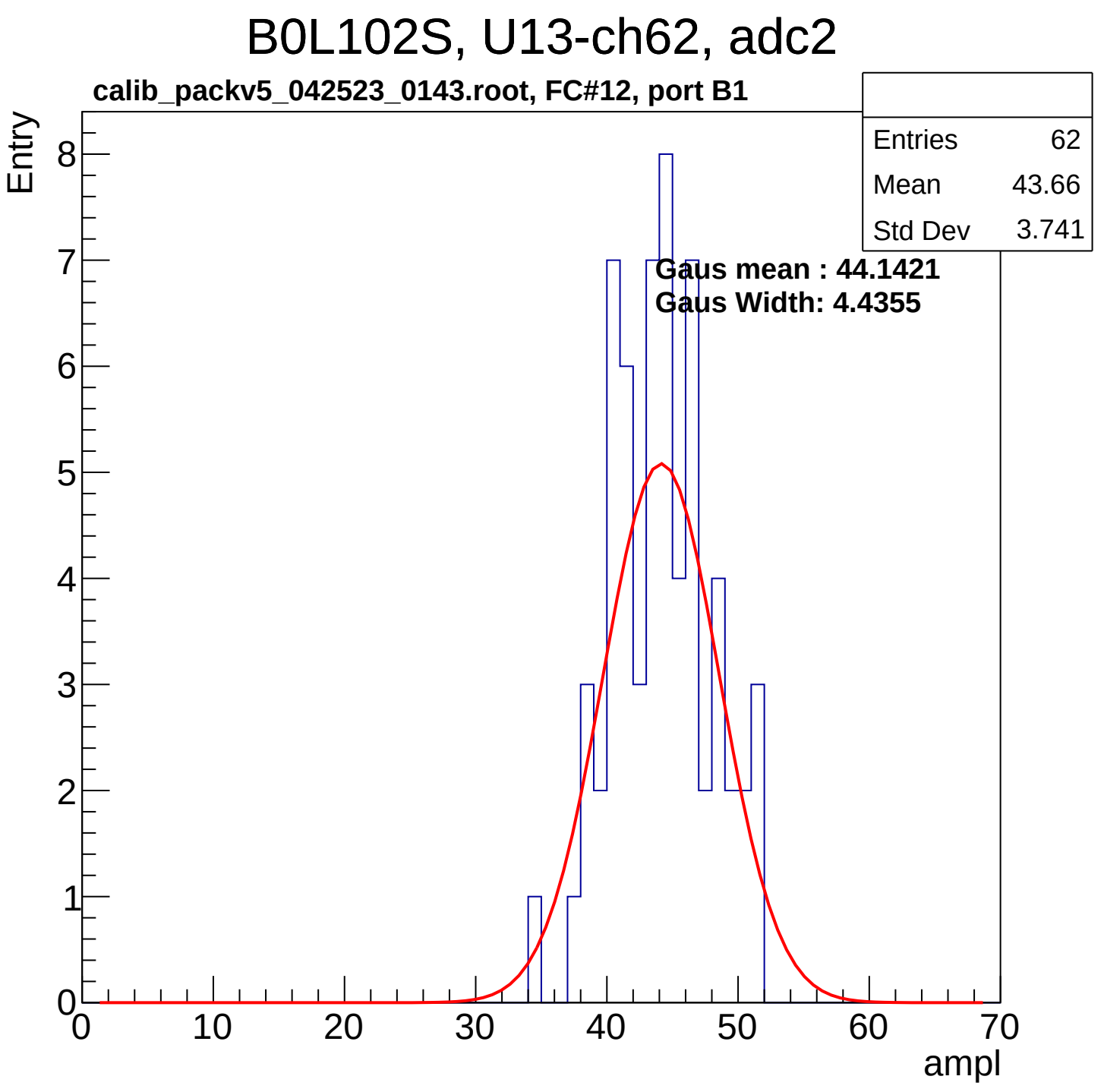

B0L102S, U13-ch61, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.53
Std Dev 1.945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

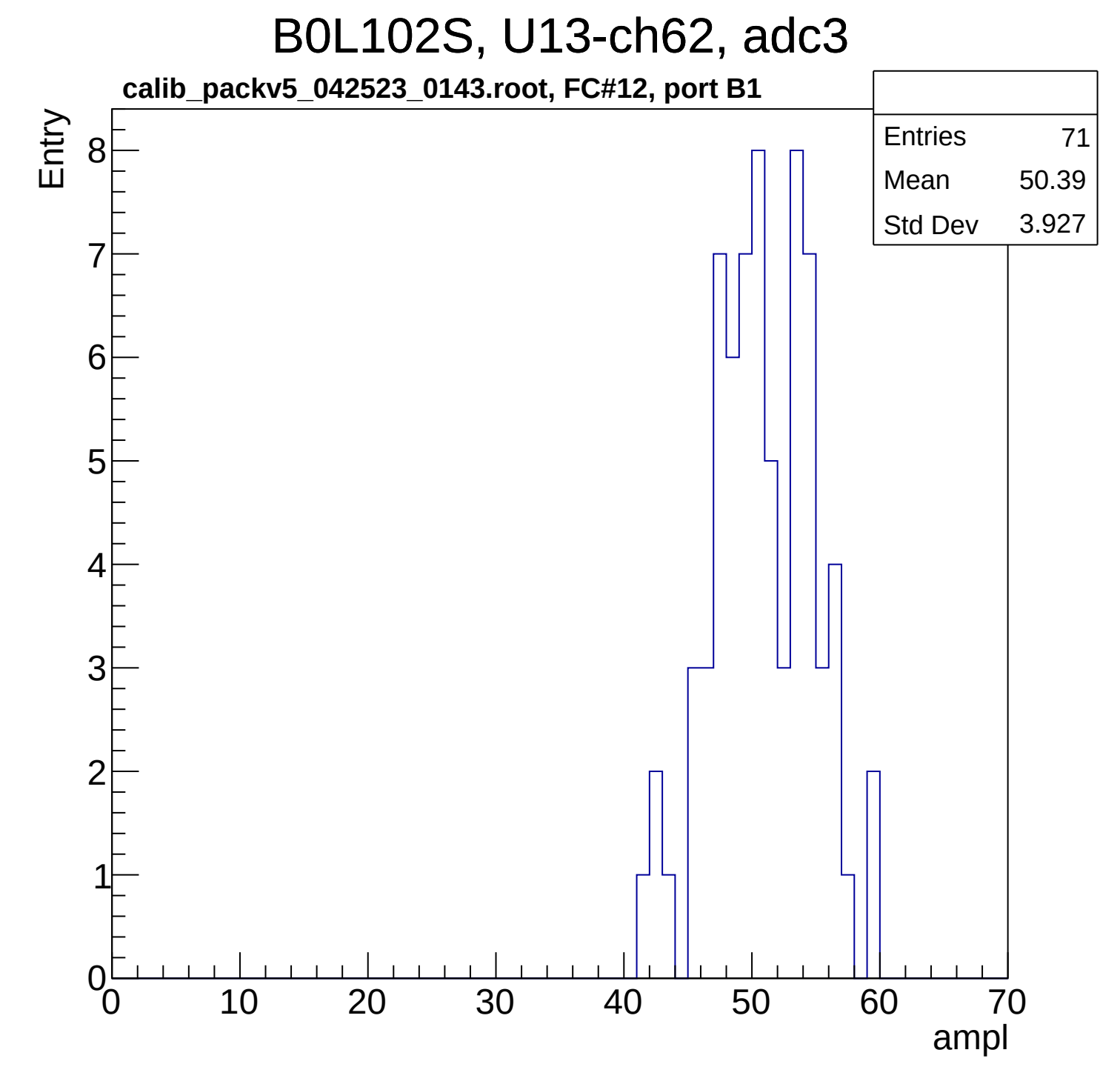

B0L102S, U13-ch61, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

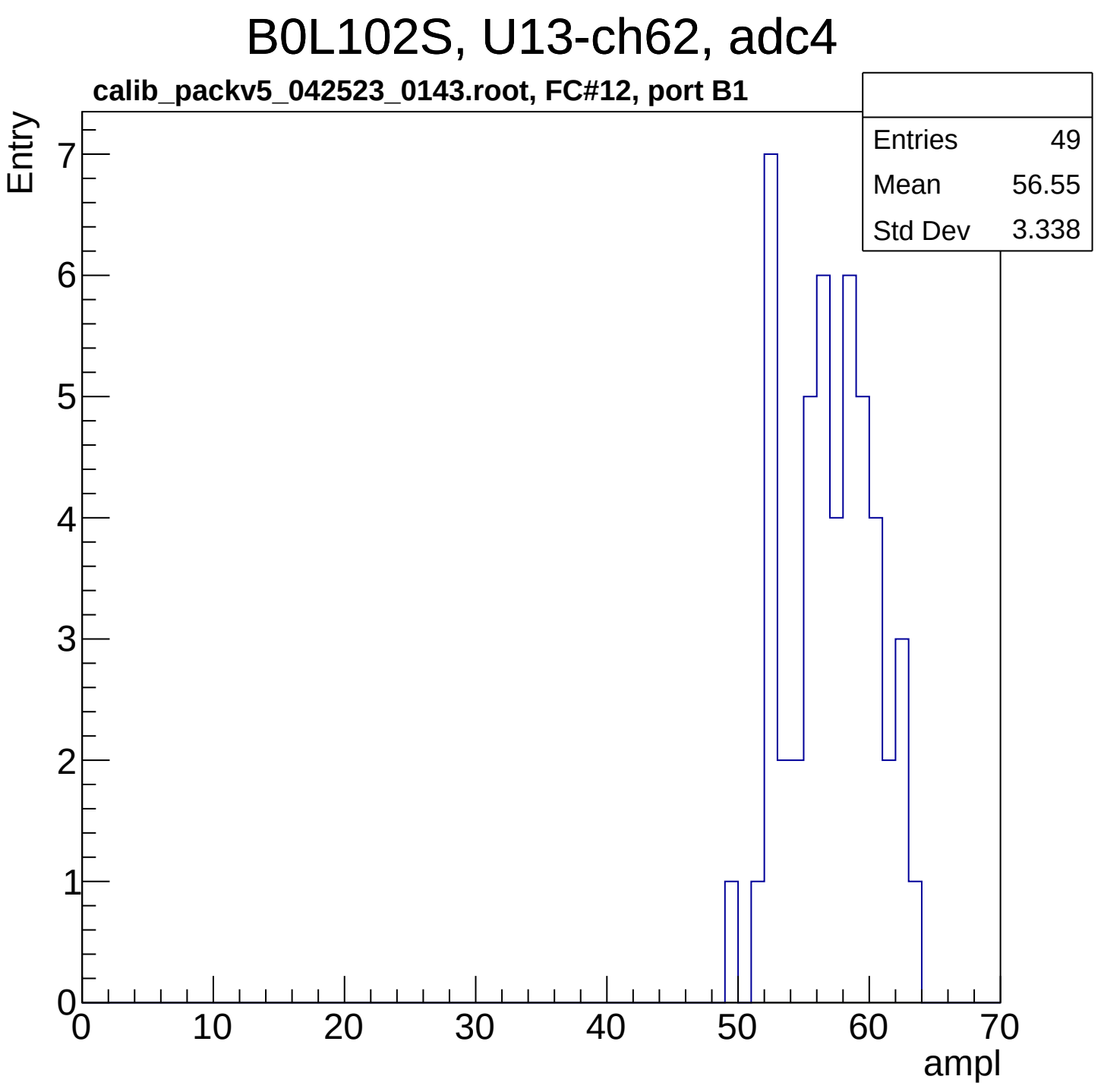

B0L102S, U13-ch61, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

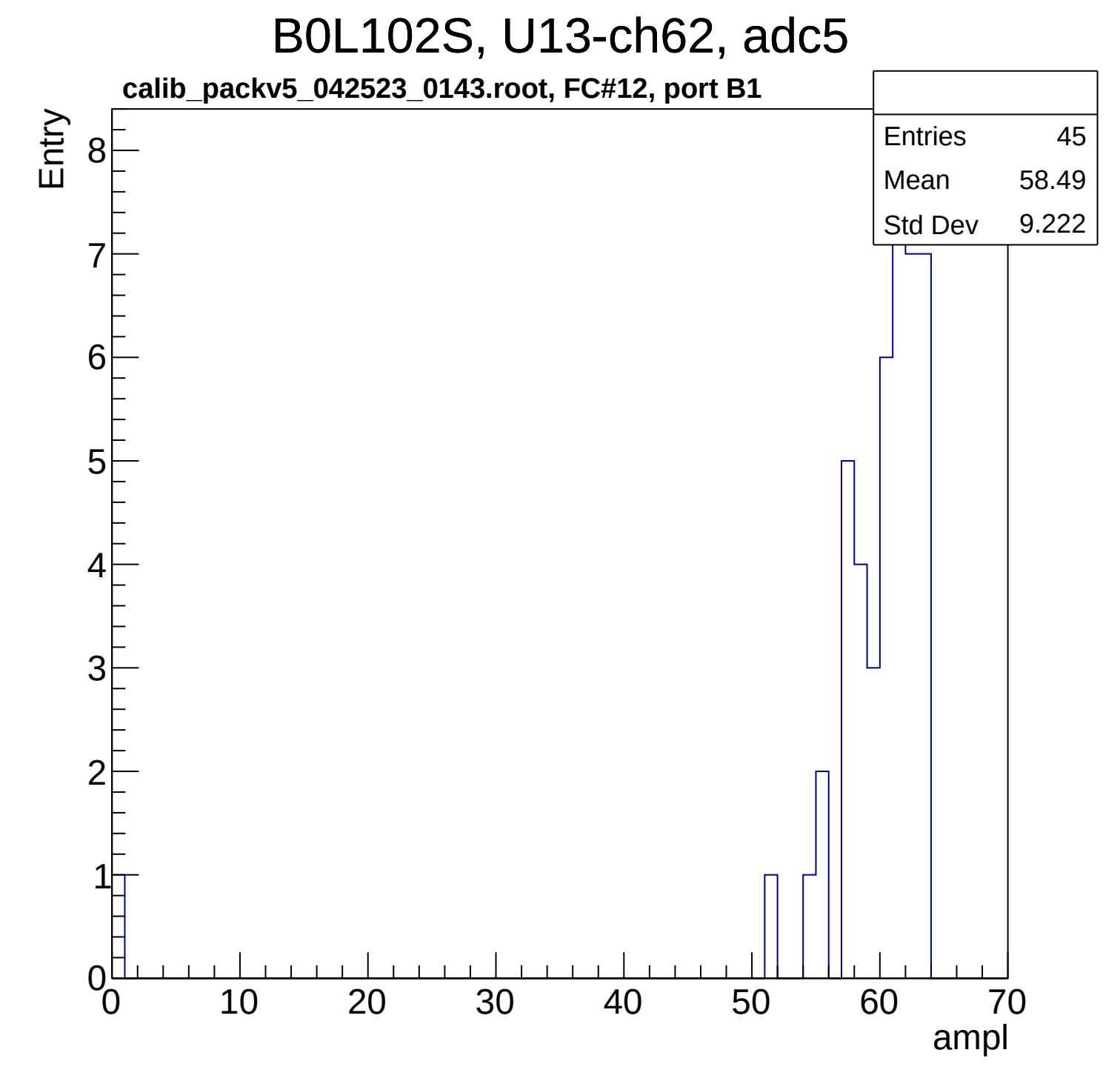

B0L102S, U13-ch62, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 91
Mean 29.9
Std Dev 4.03
Gaus mean : 30.5234
Gaus Width: 4.6774
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

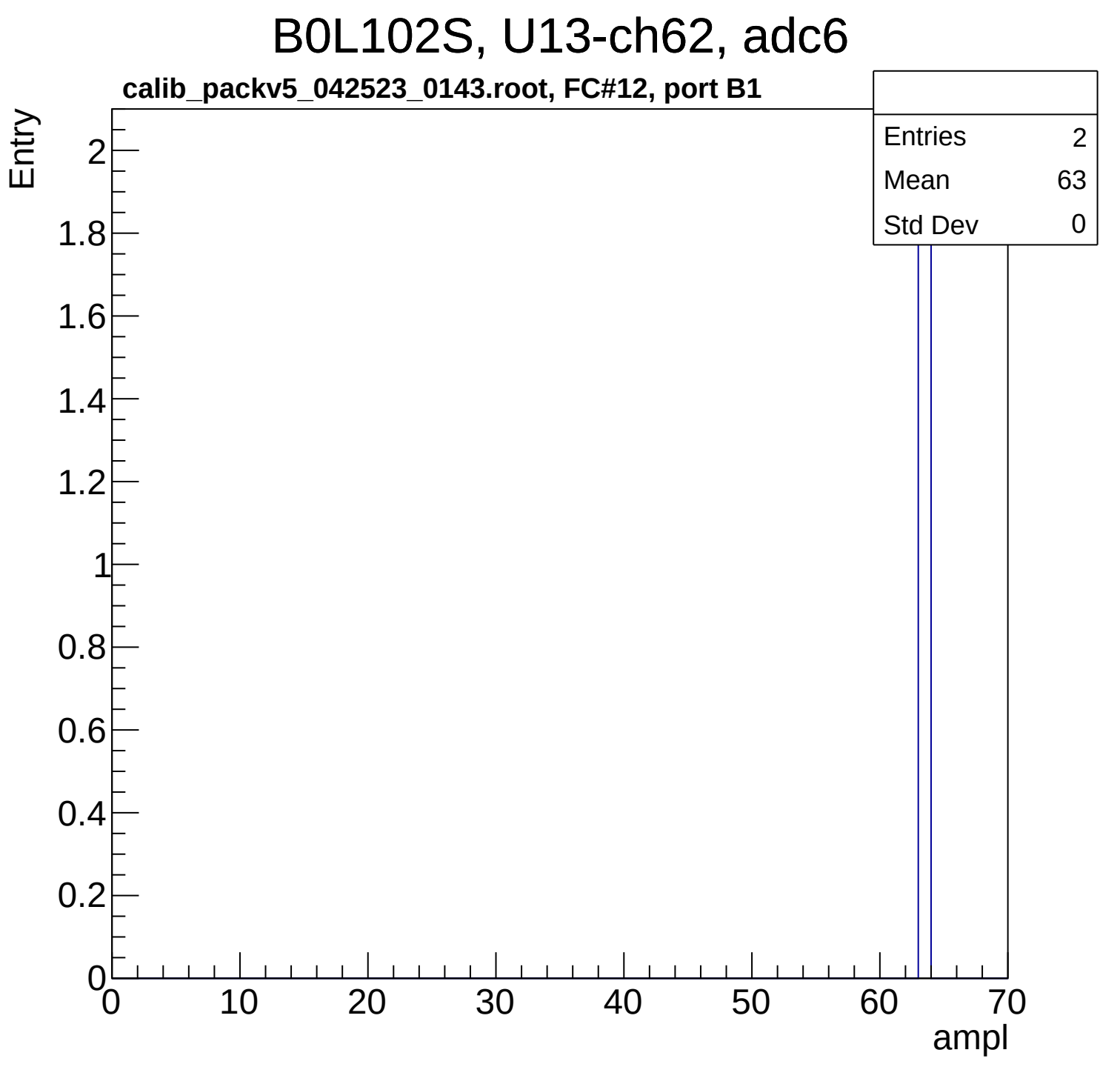

B0L102S, U13-ch62, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 37.76
Std Dev 4.019
Gaus mean : 38.1445
Gaus Width: 5.0280
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

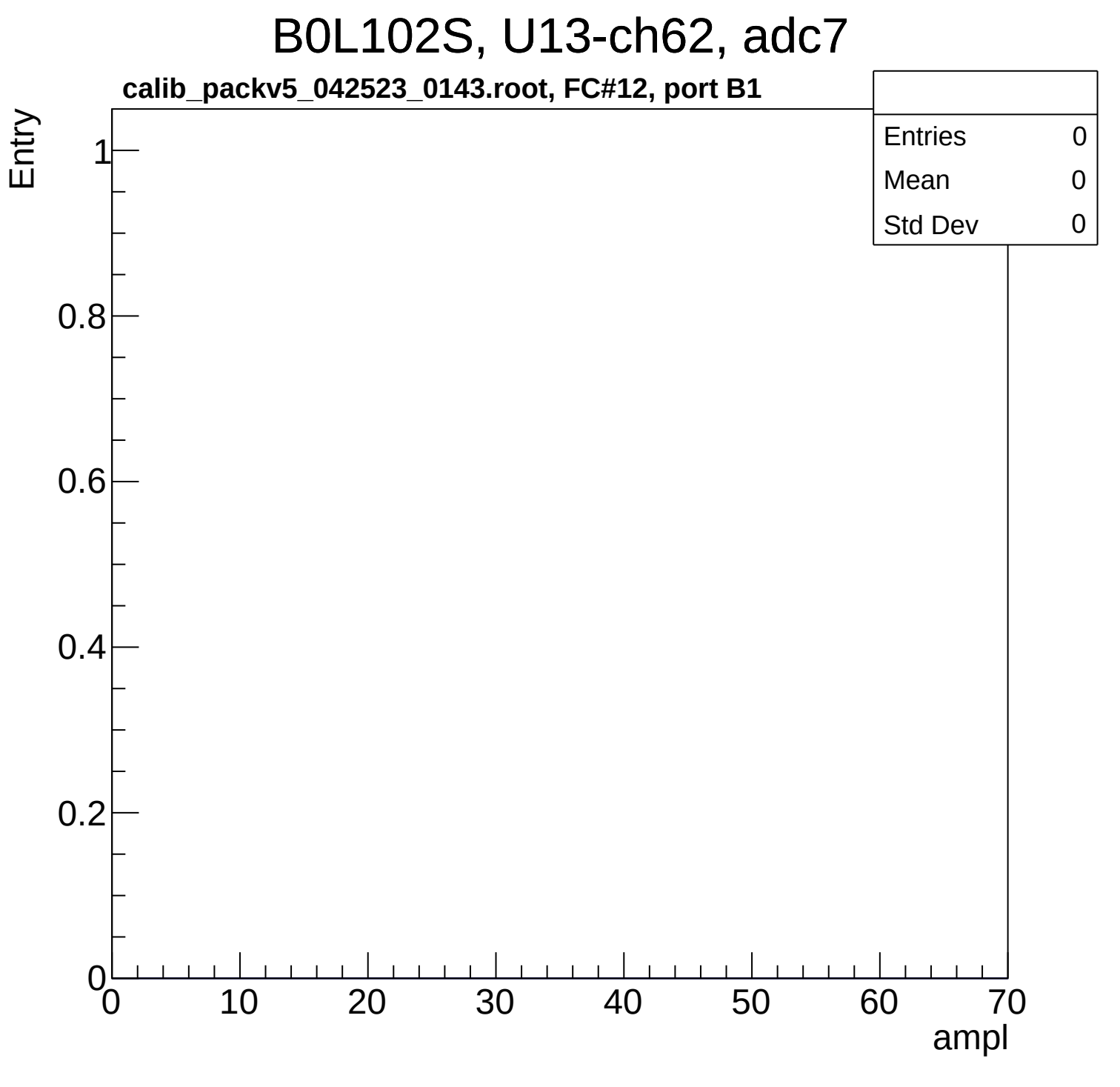

B0L102S, U13-ch62, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 43.66
Std Dev 3.741
Gaus mean : 44.1421
Gaus Width: 4.4355
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

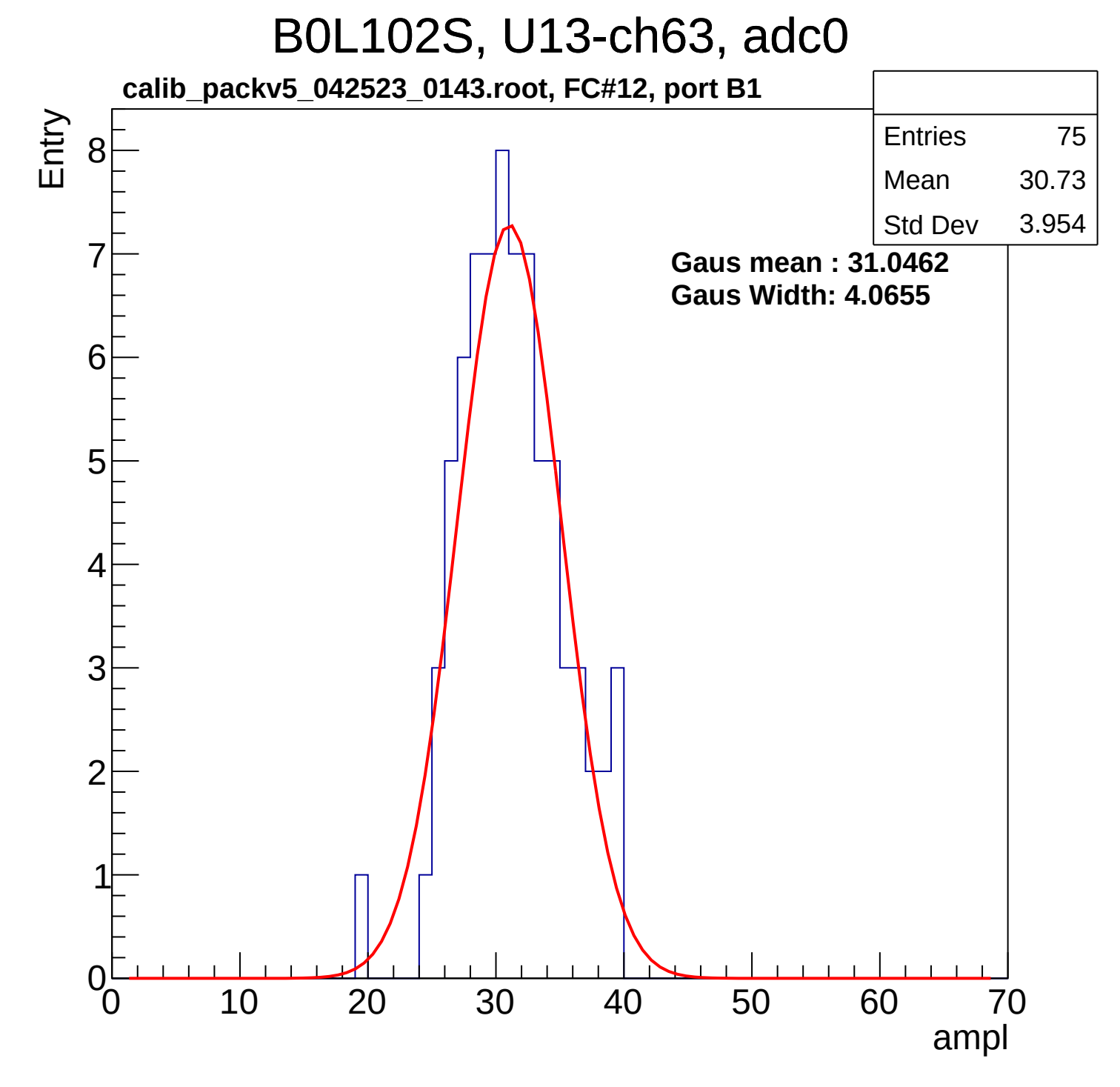

B0L102S, U13-ch62, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.39
Std Dev 3.927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch62, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 56.55
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch62, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.49
Std Dev 9.222
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch62, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch62, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch63, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 30.73
Std Dev 3.954
Gaus mean : 31.0462
Gaus Width: 4.0655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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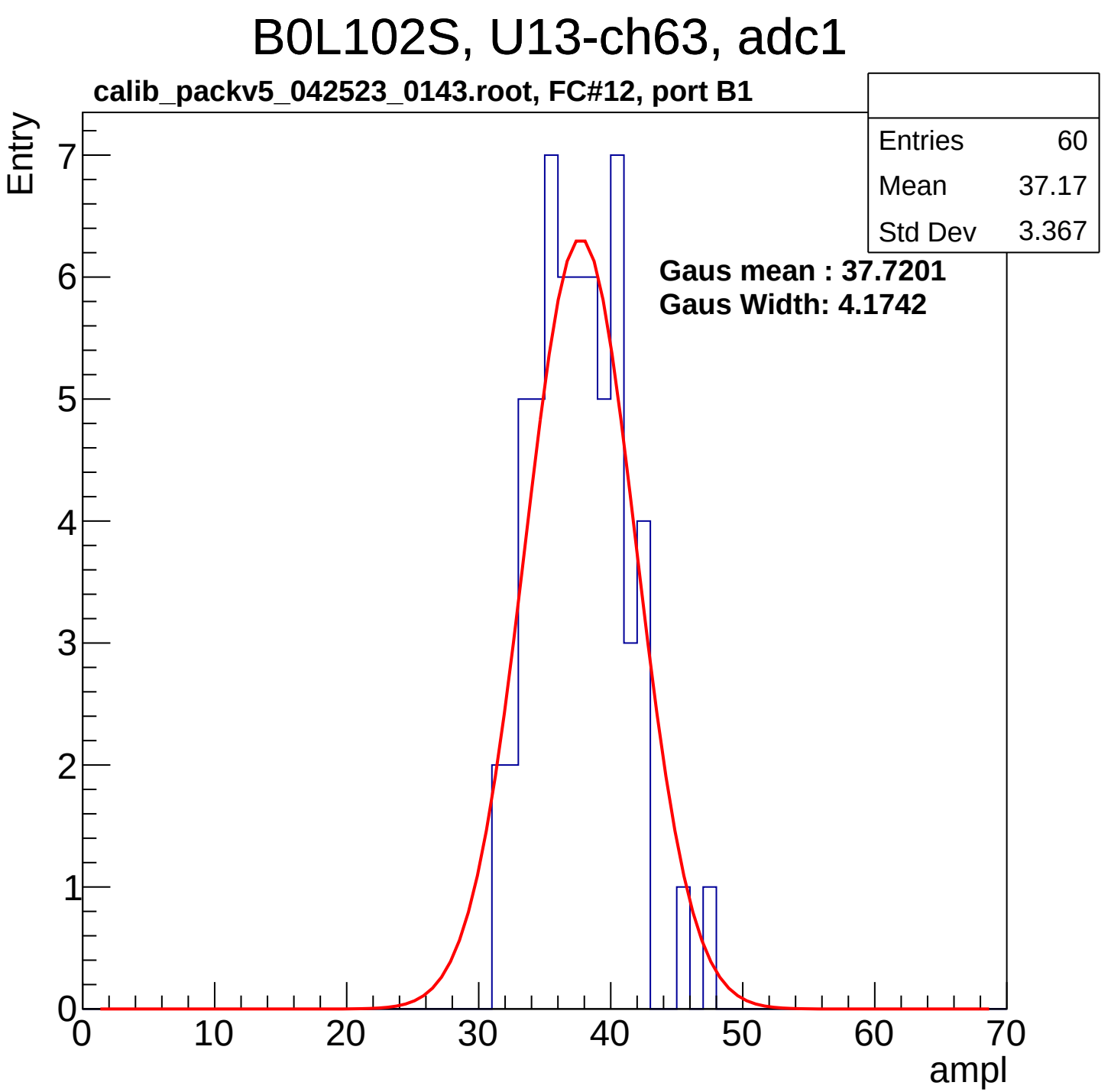
B0L102S, U13-ch63, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 37.17
Std Dev 3.367
Gaus mean : 37.7201
Gaus Width: 4.1742
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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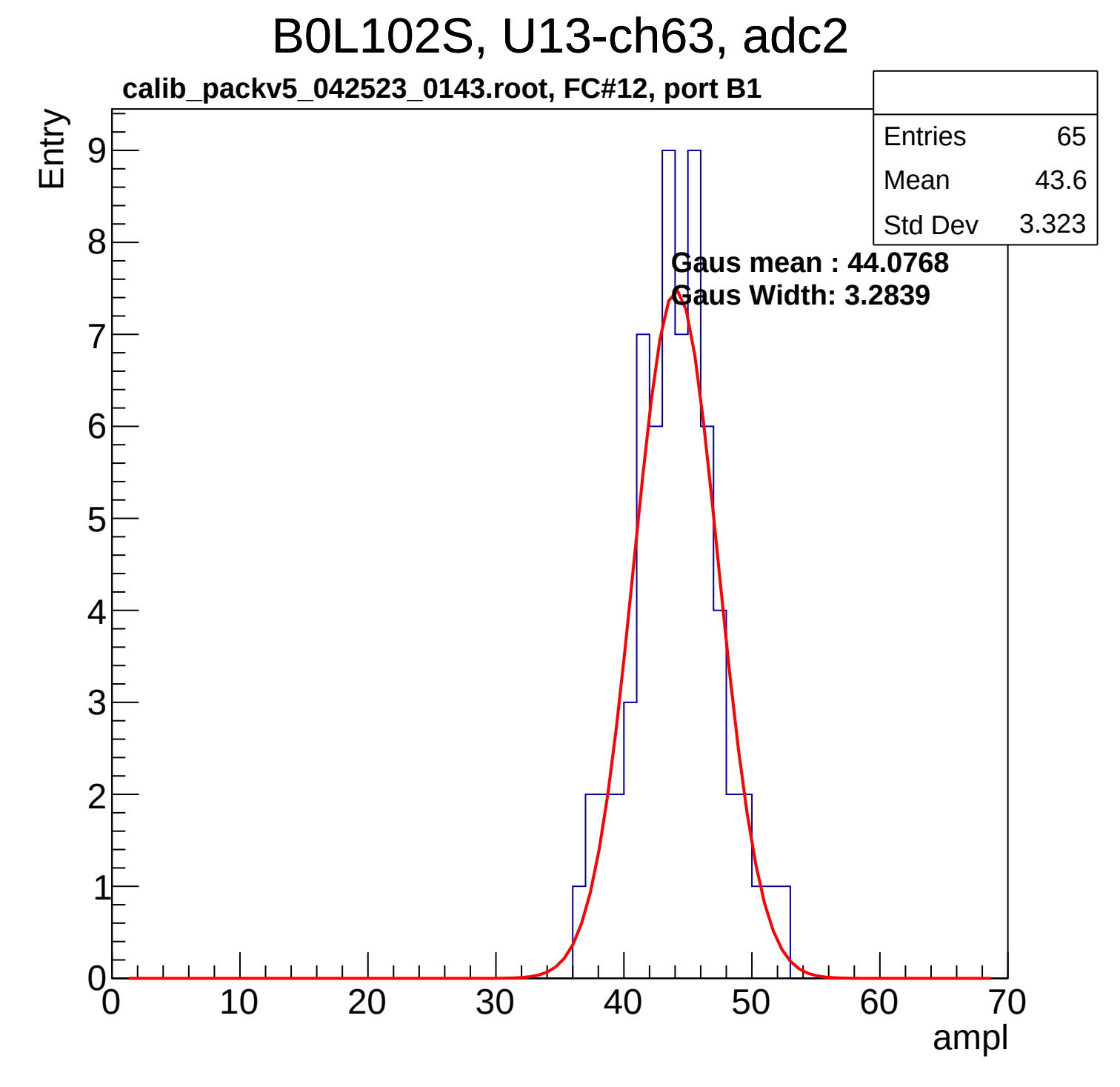
B0L102S, U13-ch63, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.6
Std Dev 3.323
Gaus mean : 44.0768
Gaus Width: 3.2839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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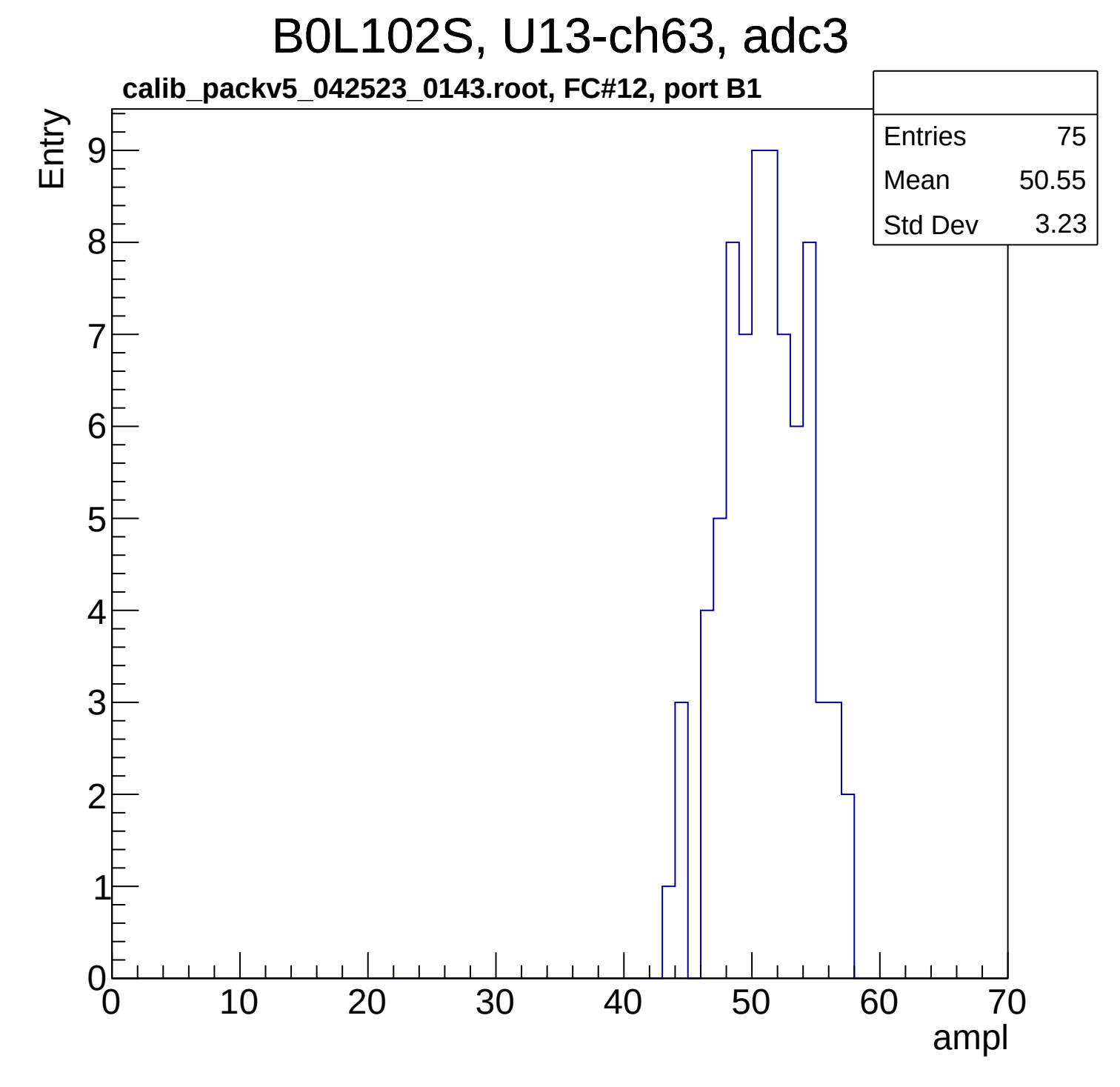
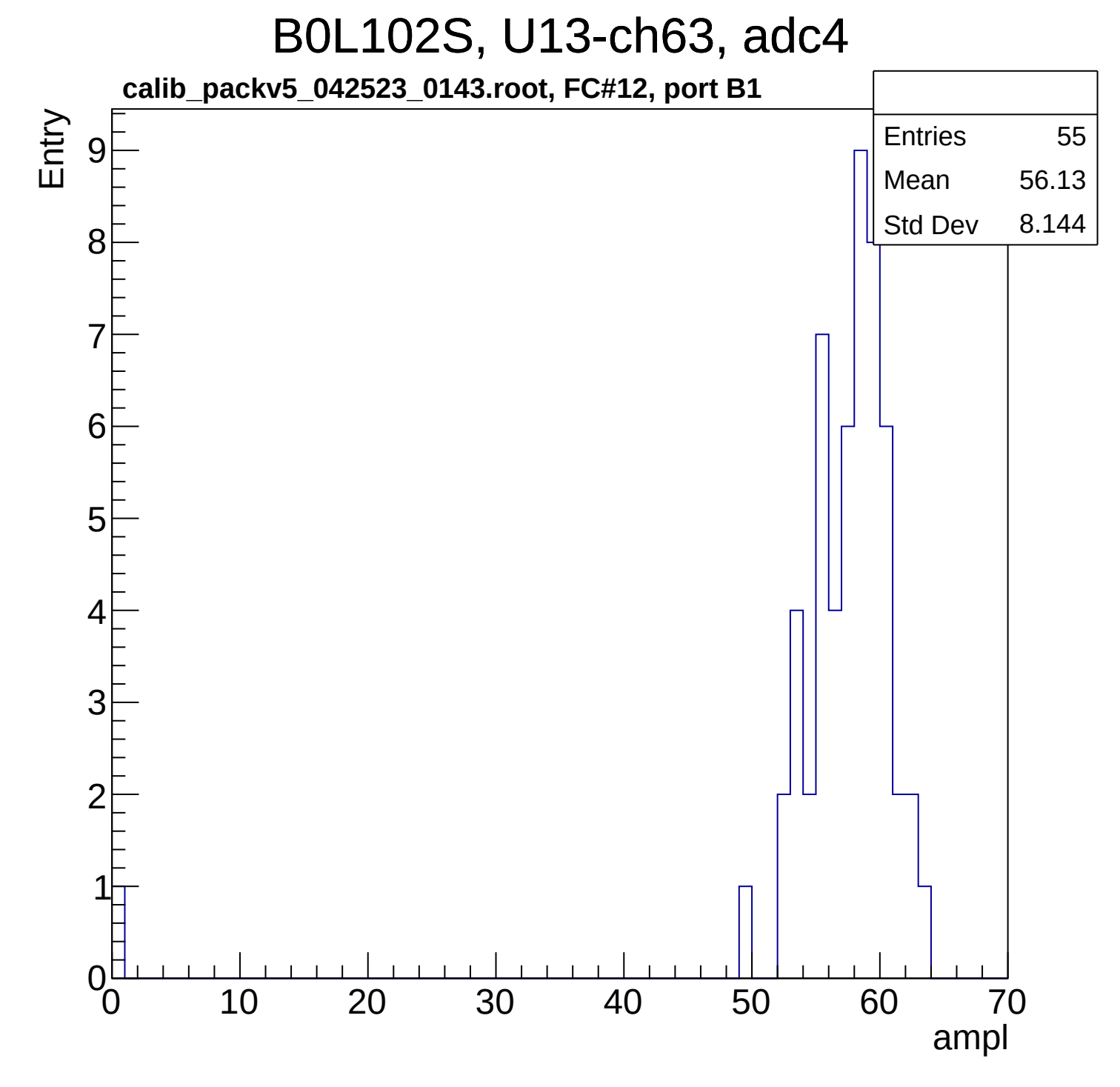
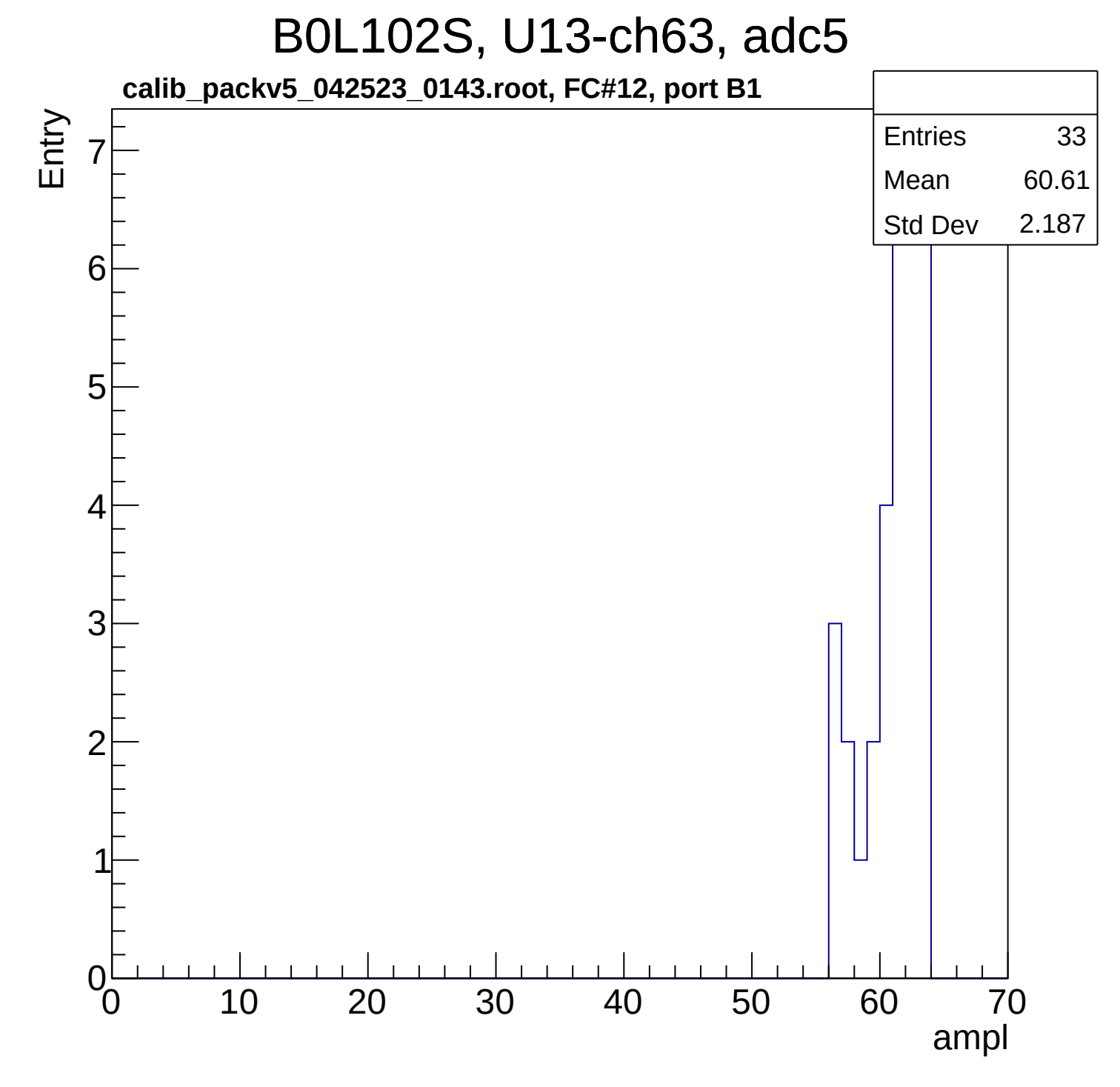
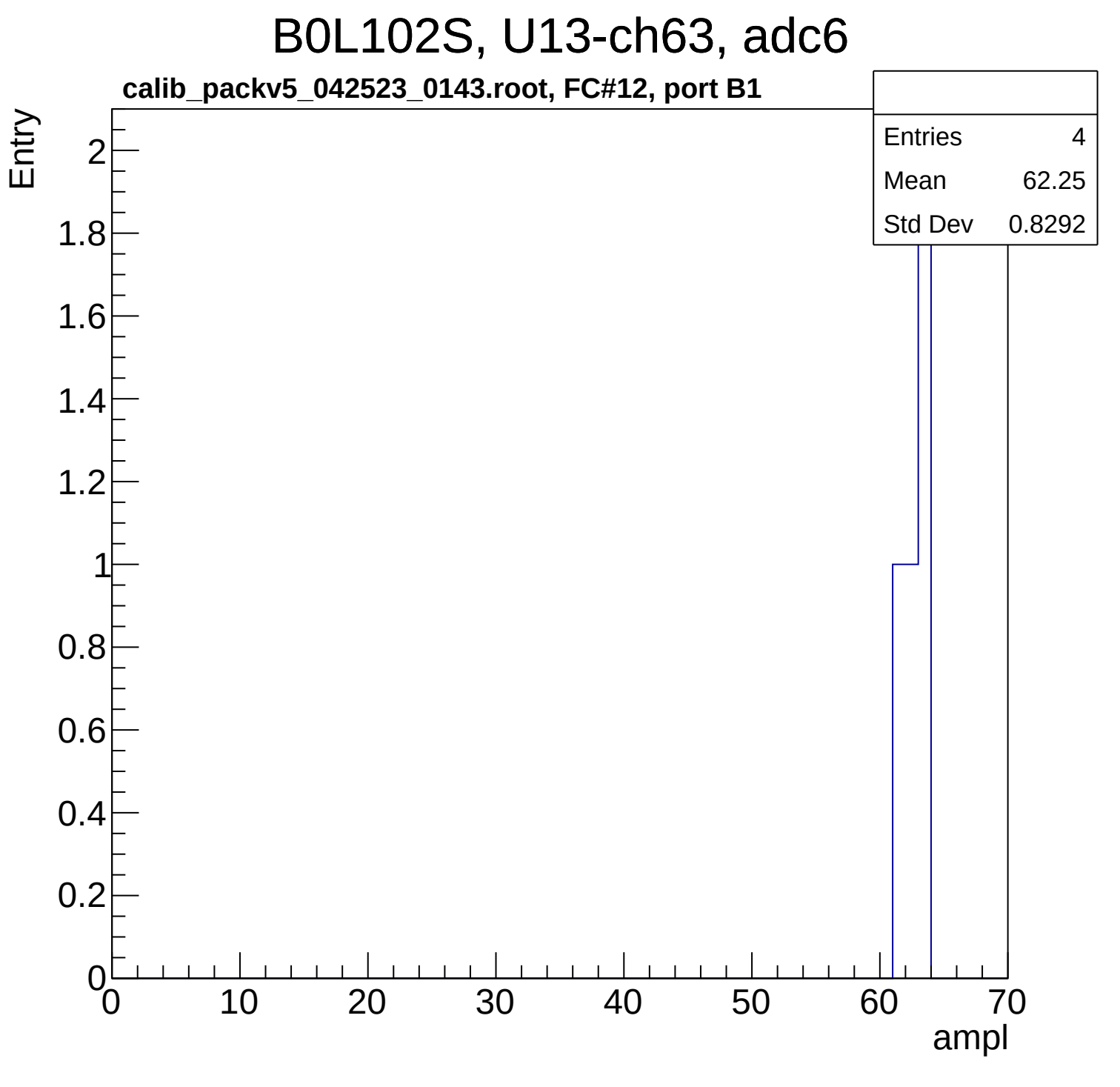
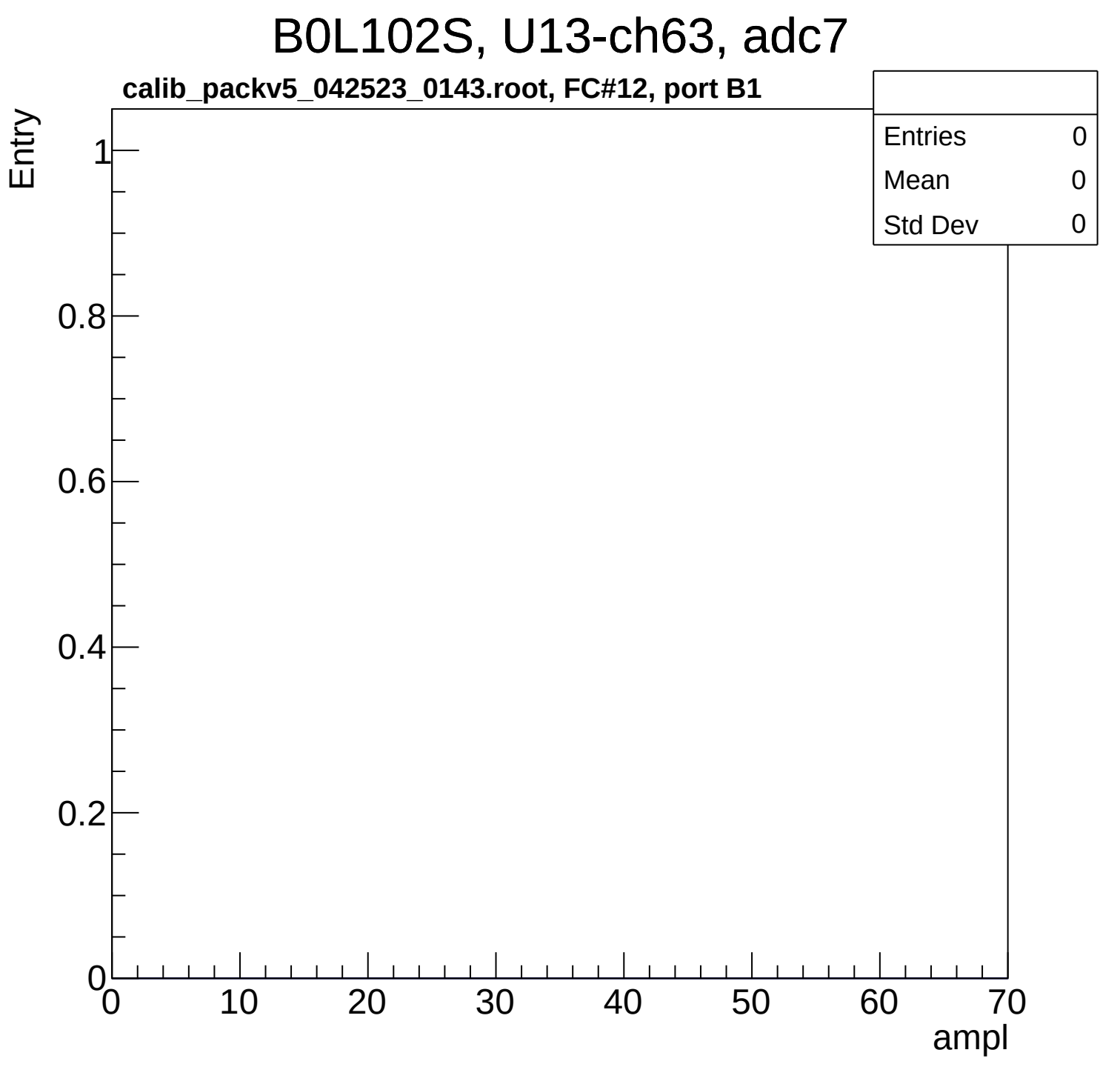
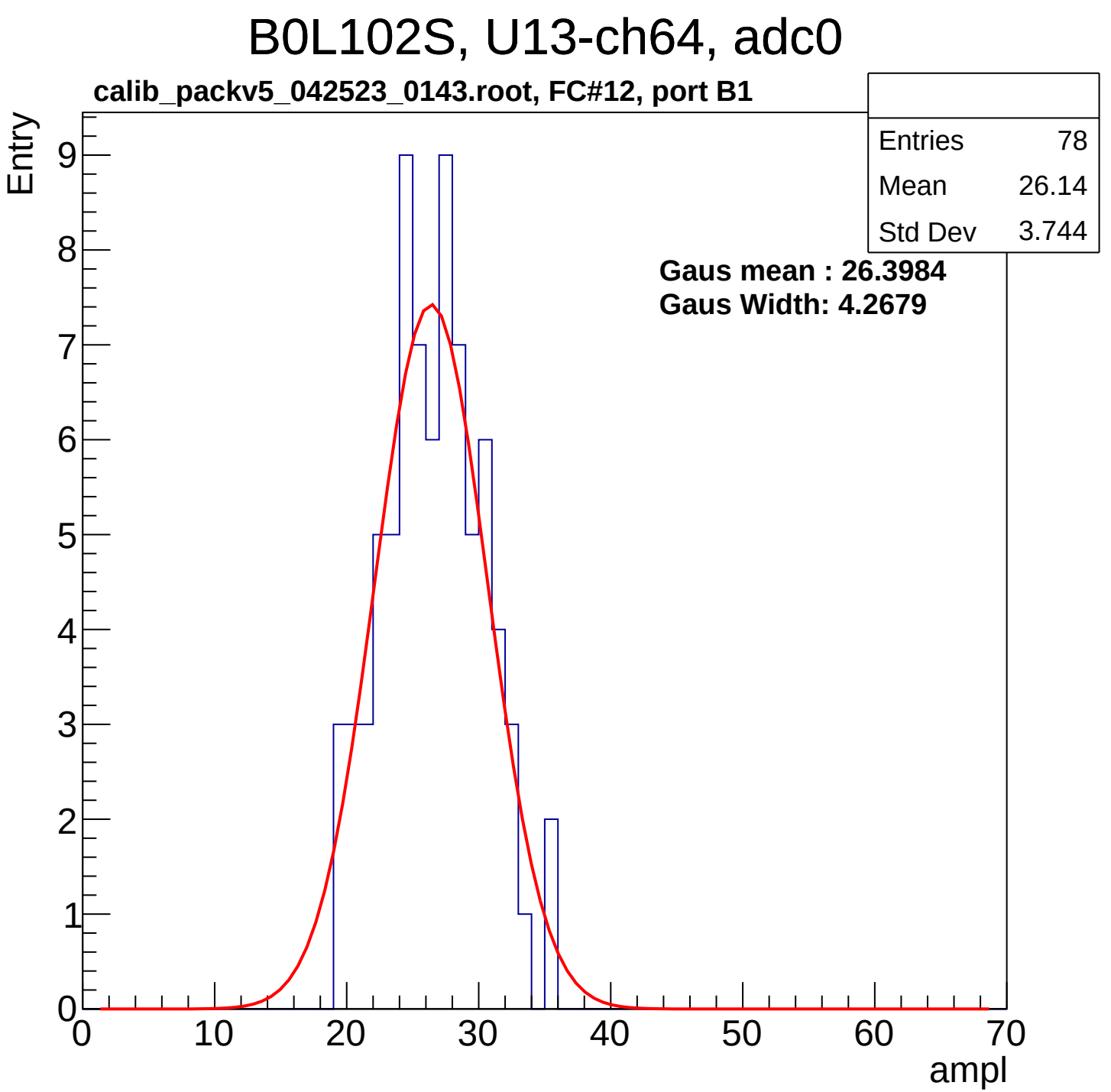

B0L102S, U13-ch63, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 50.55
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch63, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.13
Std Dev 8.144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch63, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.61
Std Dev 2.187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch63, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch63, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch64, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 26.14
Std Dev 3.744
Gaus mean : 26.3984
Gaus Width: 4.2679
Entry
ampl
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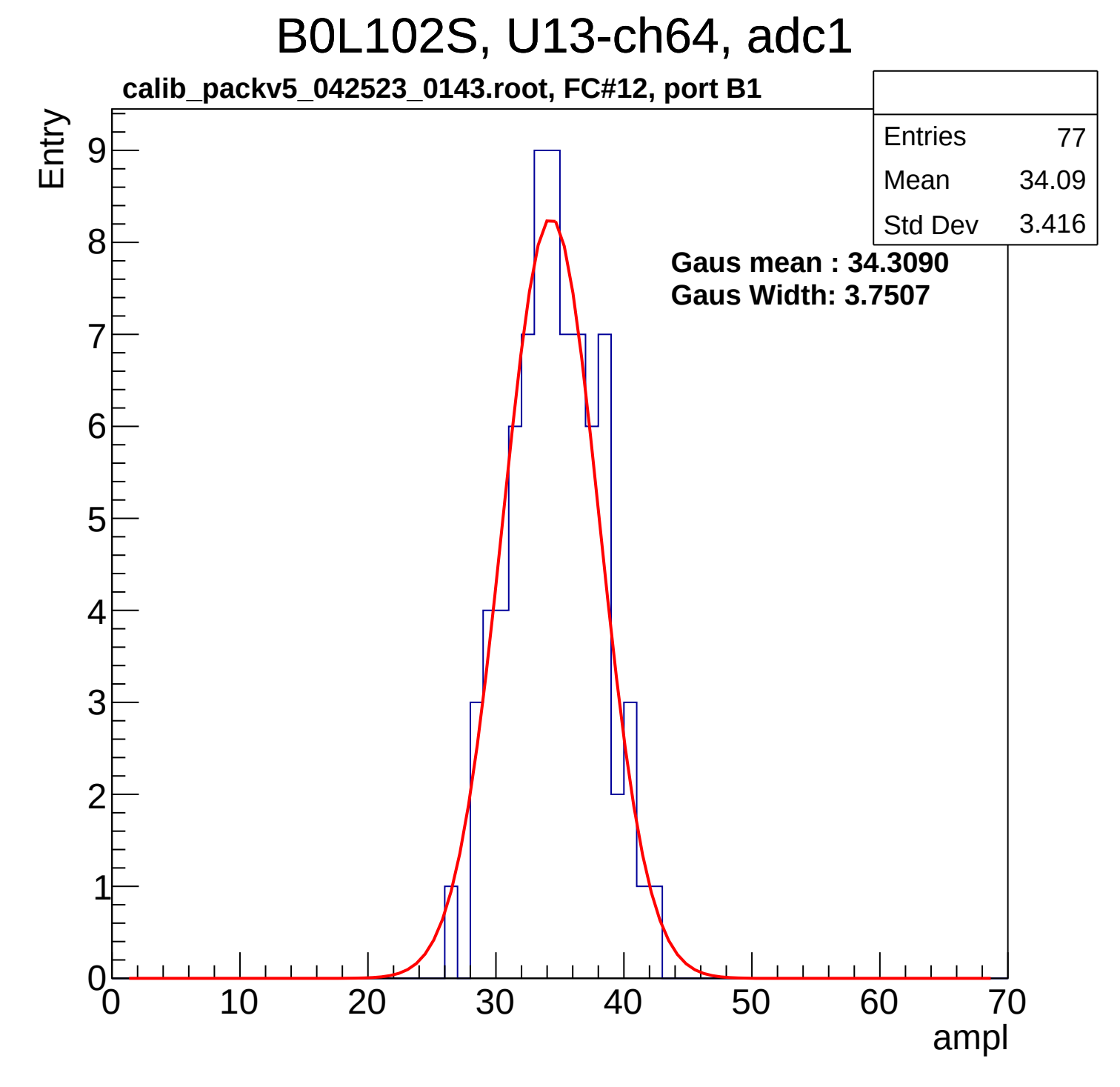
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch64, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.09
Std Dev 3.416
Gaus mean : 34.3090
Gaus Width: 3.7507
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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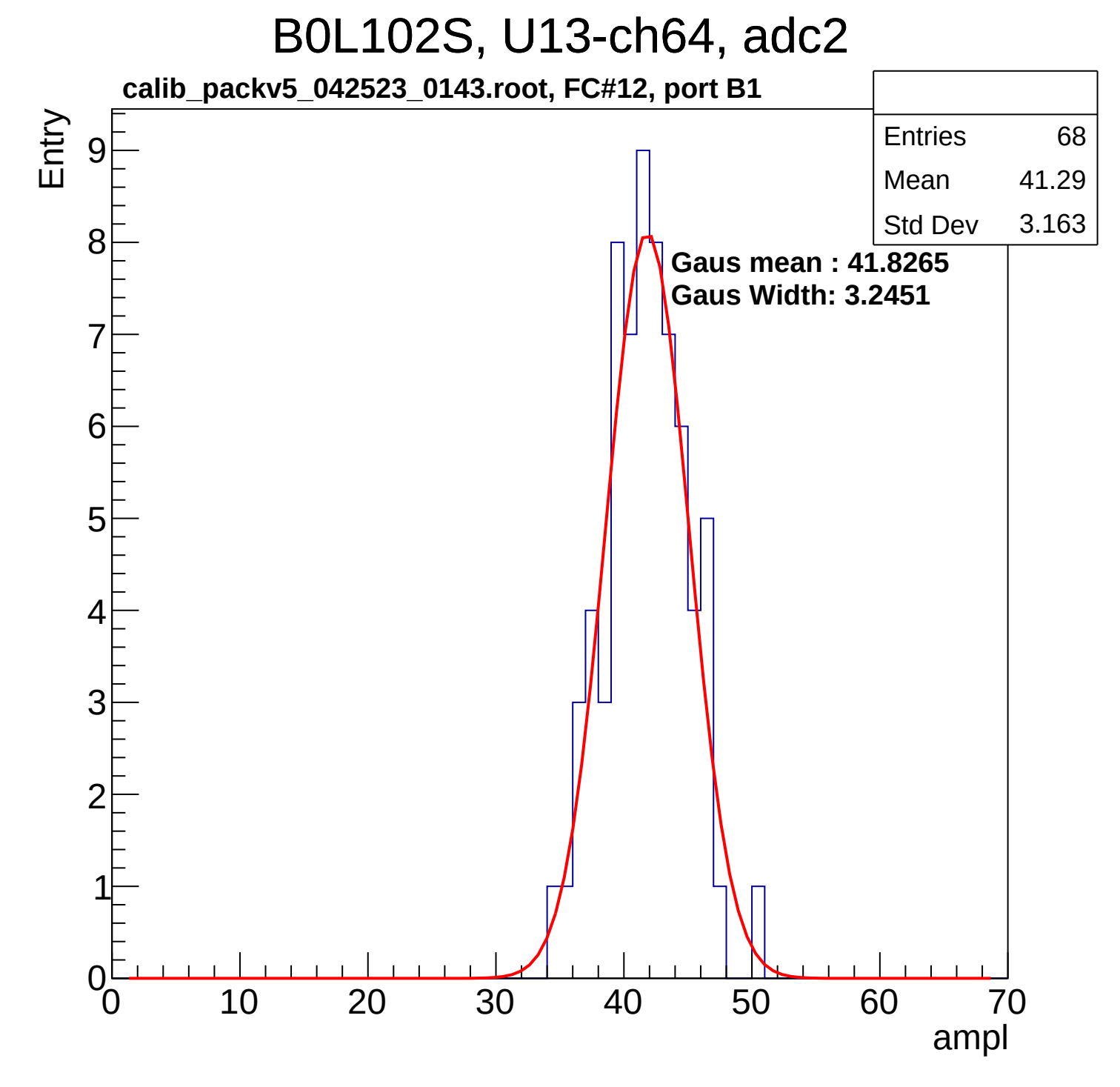
40
50
60
70


B0L102S, U13-ch64, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.29
Std Dev 3.163
Gaus mean : 41.8265
Gaus Width: 3.2451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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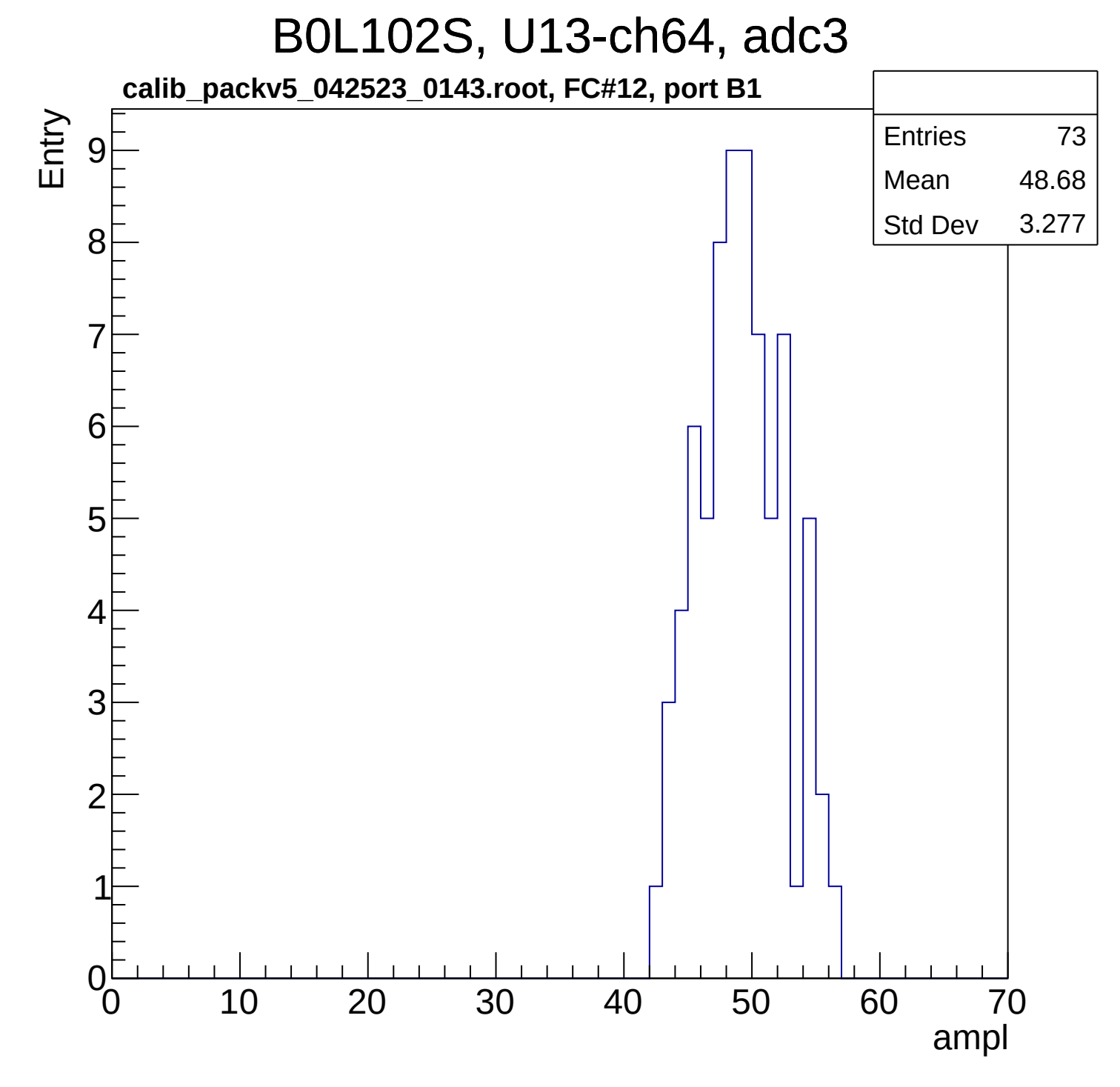
B0L102S, U13-ch64, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 48.68
Std Dev 3.277
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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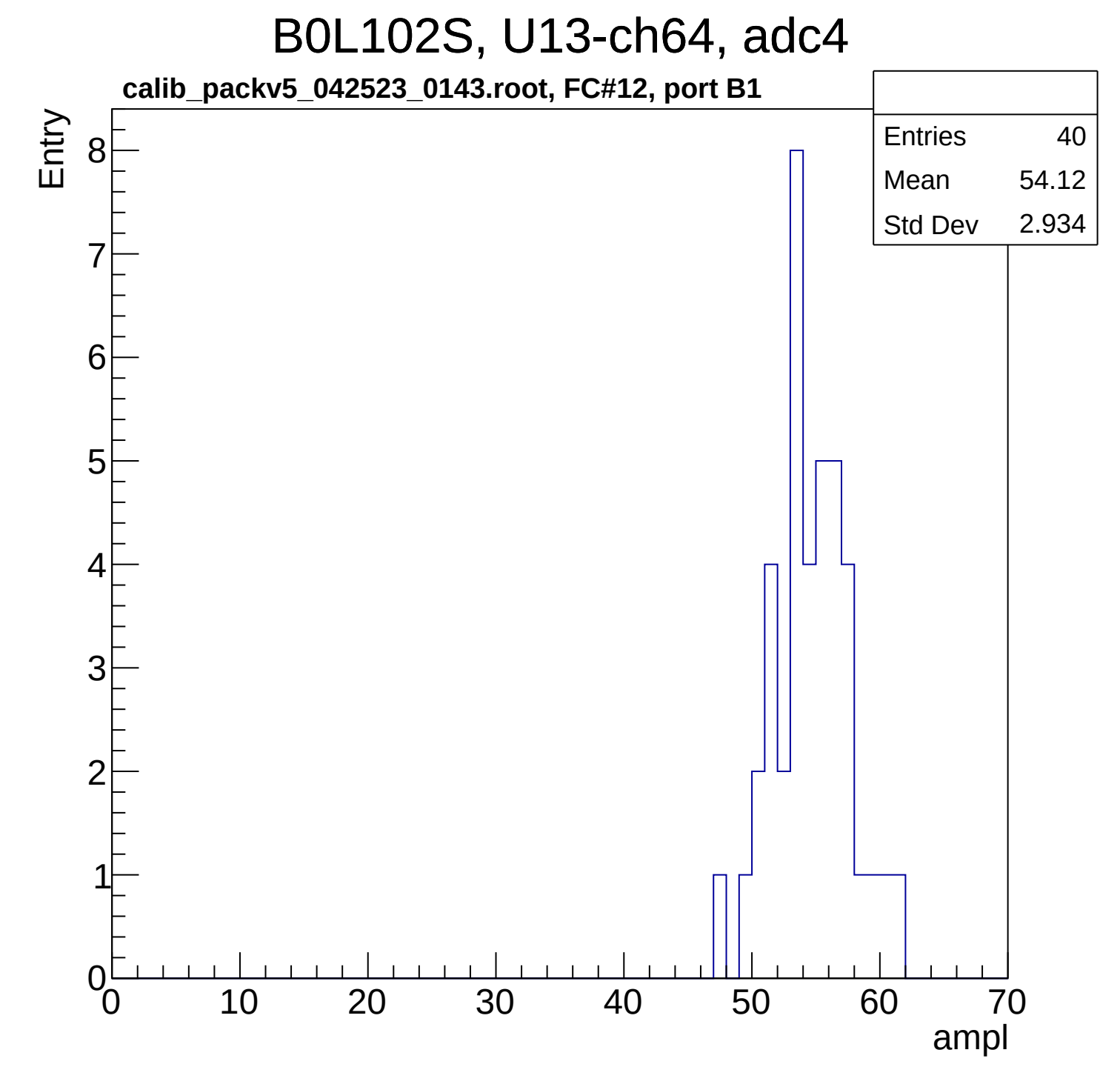
B0L102S, U13-ch64, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 54.12
Std Dev 2.934
Entry
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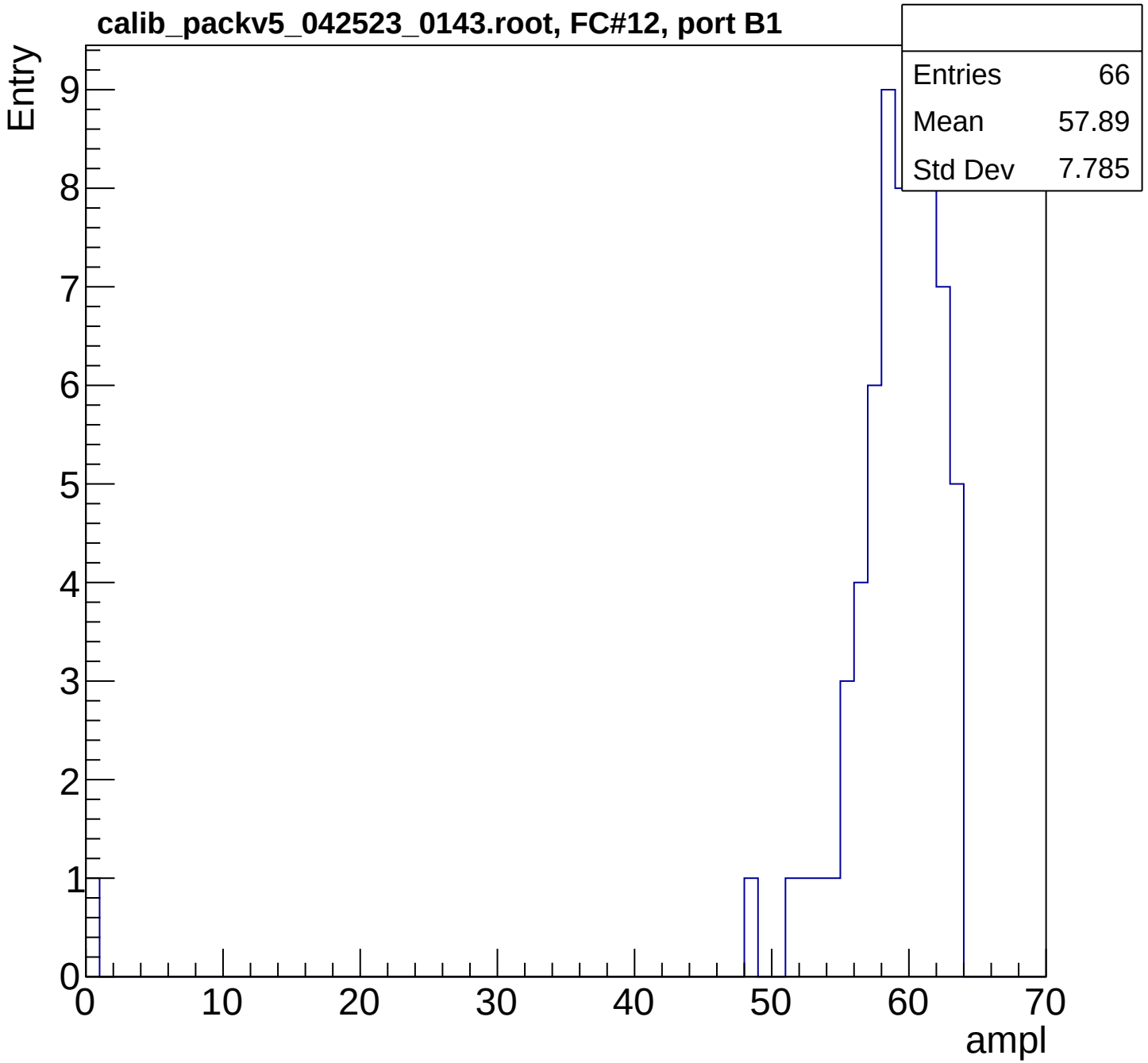
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch64, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 57.89
Std Dev 7.785
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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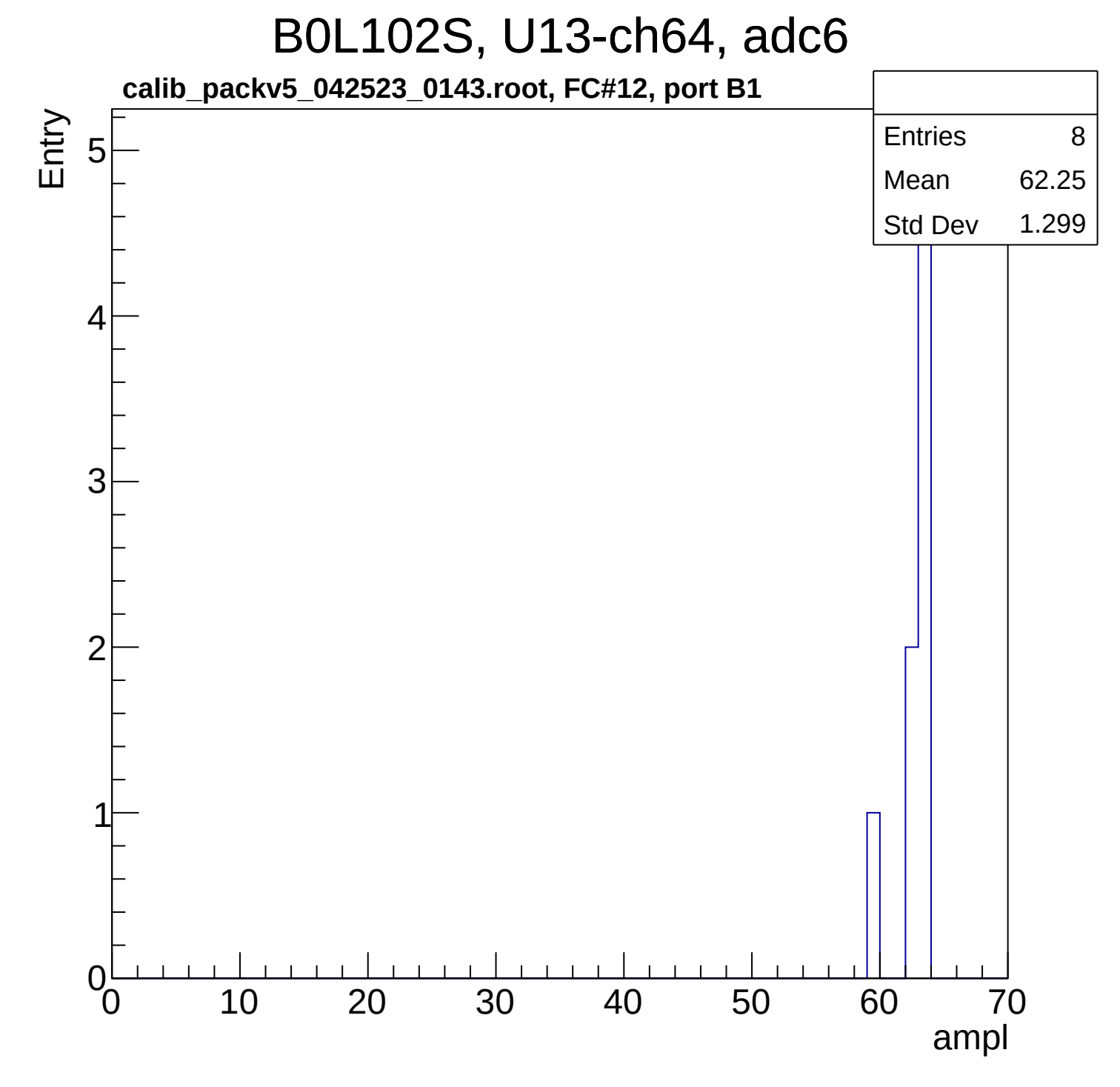
B0L102S, U13-ch64, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.25
Std Dev 1.299
Entry
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3
4
5
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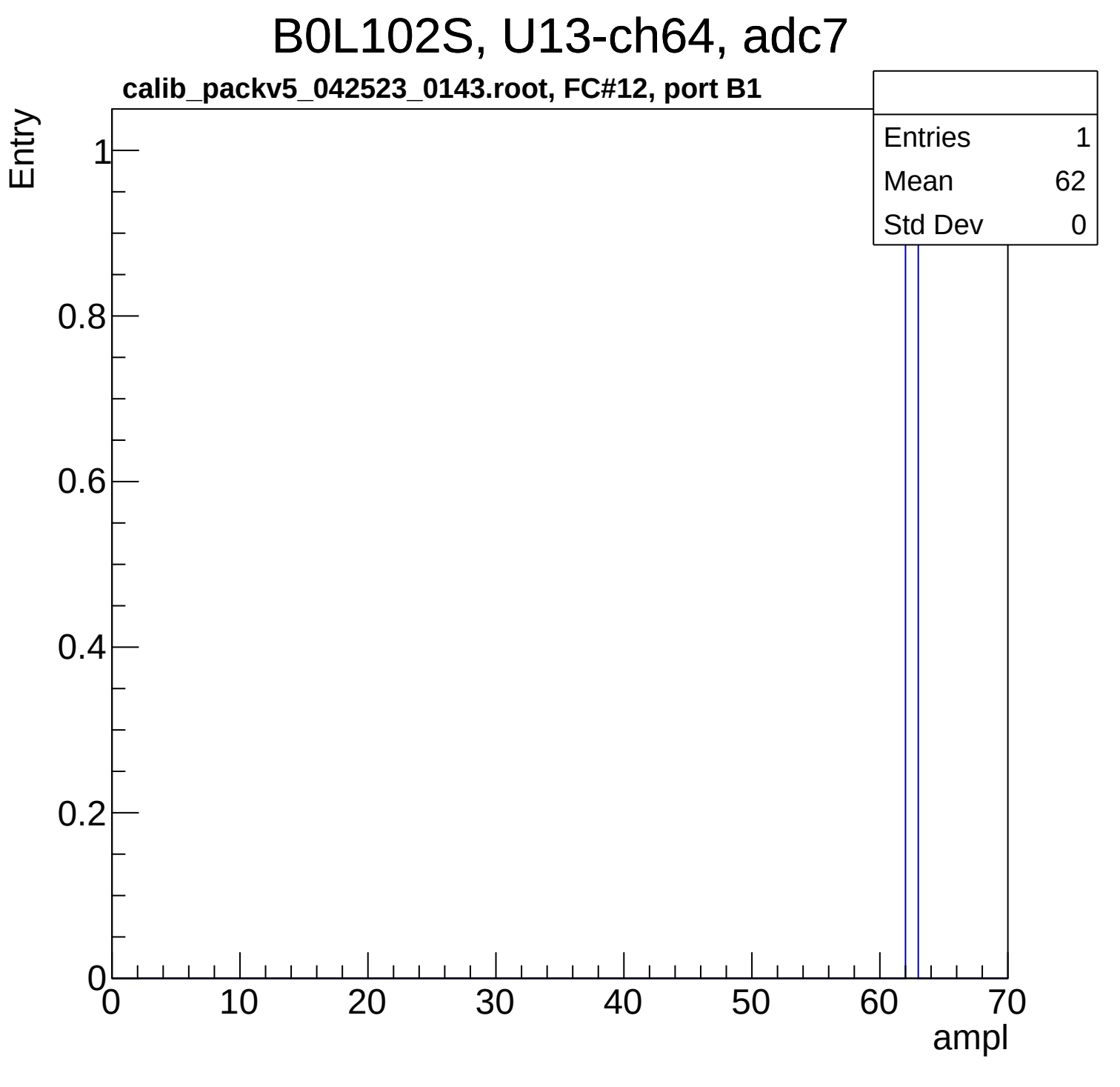
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch64, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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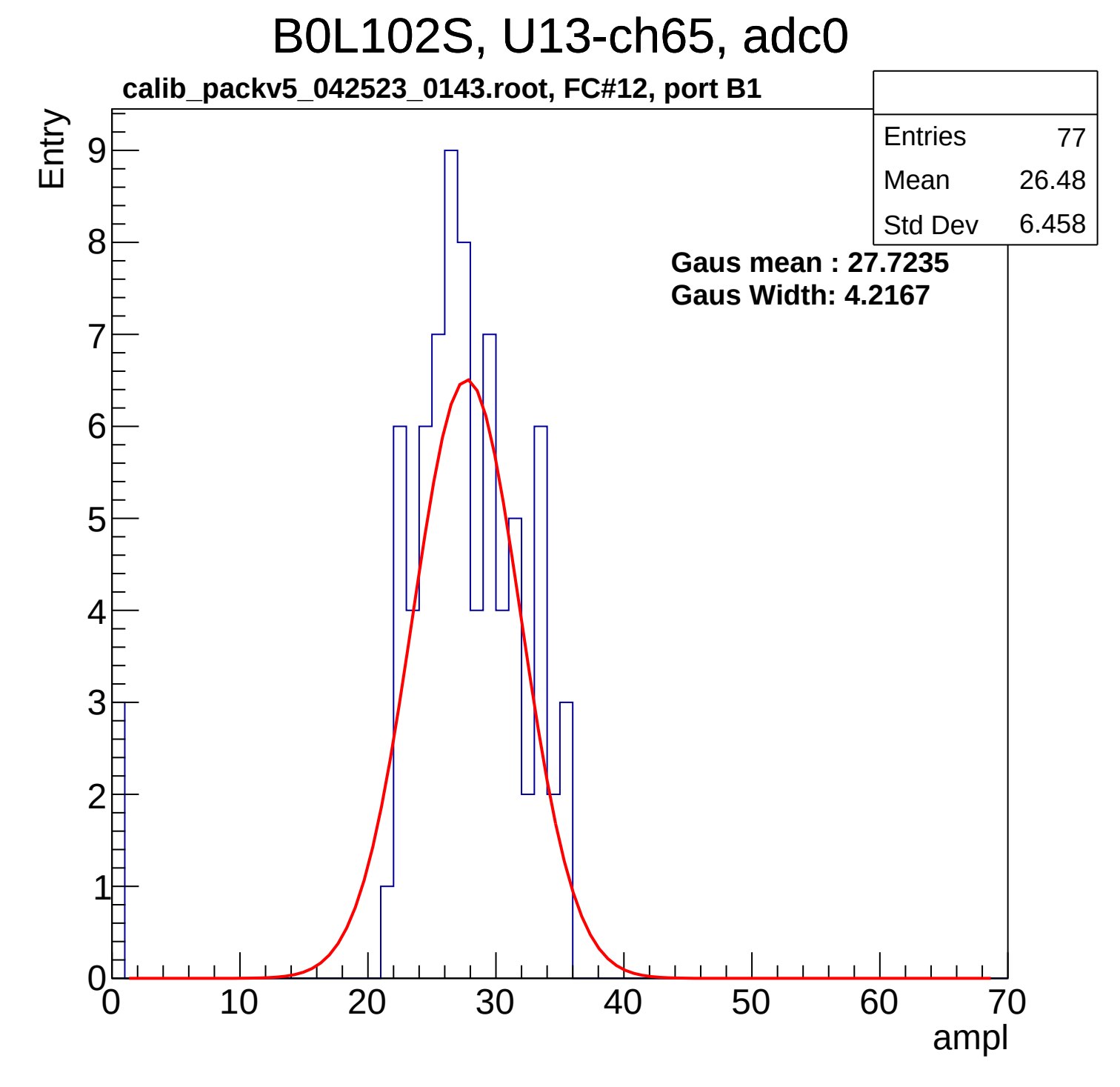
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch65, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 26.48
Std Dev 6.458
Gaus mean : 27.7235
Gaus Width: 4.2167
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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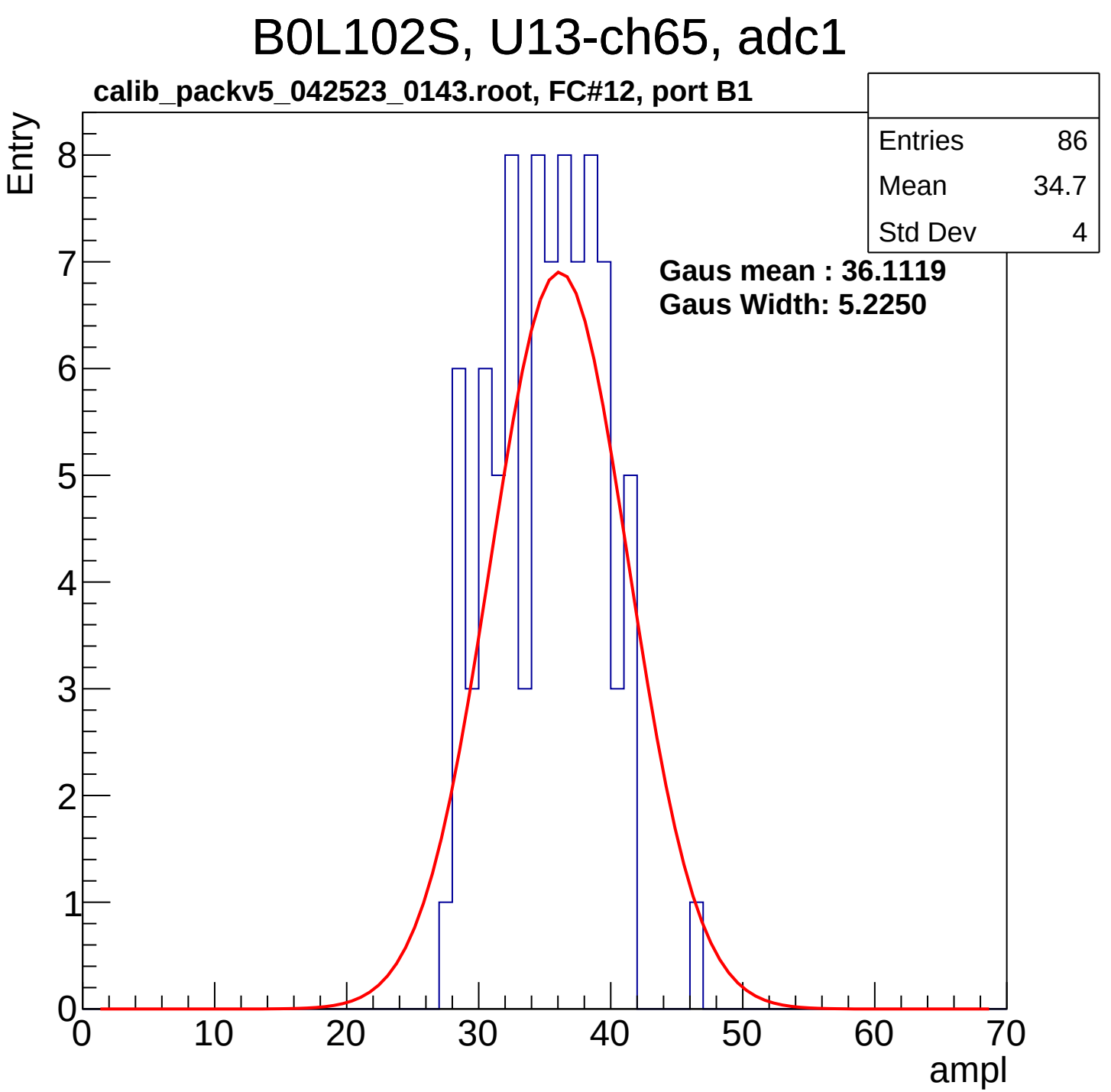
B0L102S, U13-ch65, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 34.7
Std Dev 4
Gaus mean : 36.1119
Gaus Width: 5.2250
Entry
ampl
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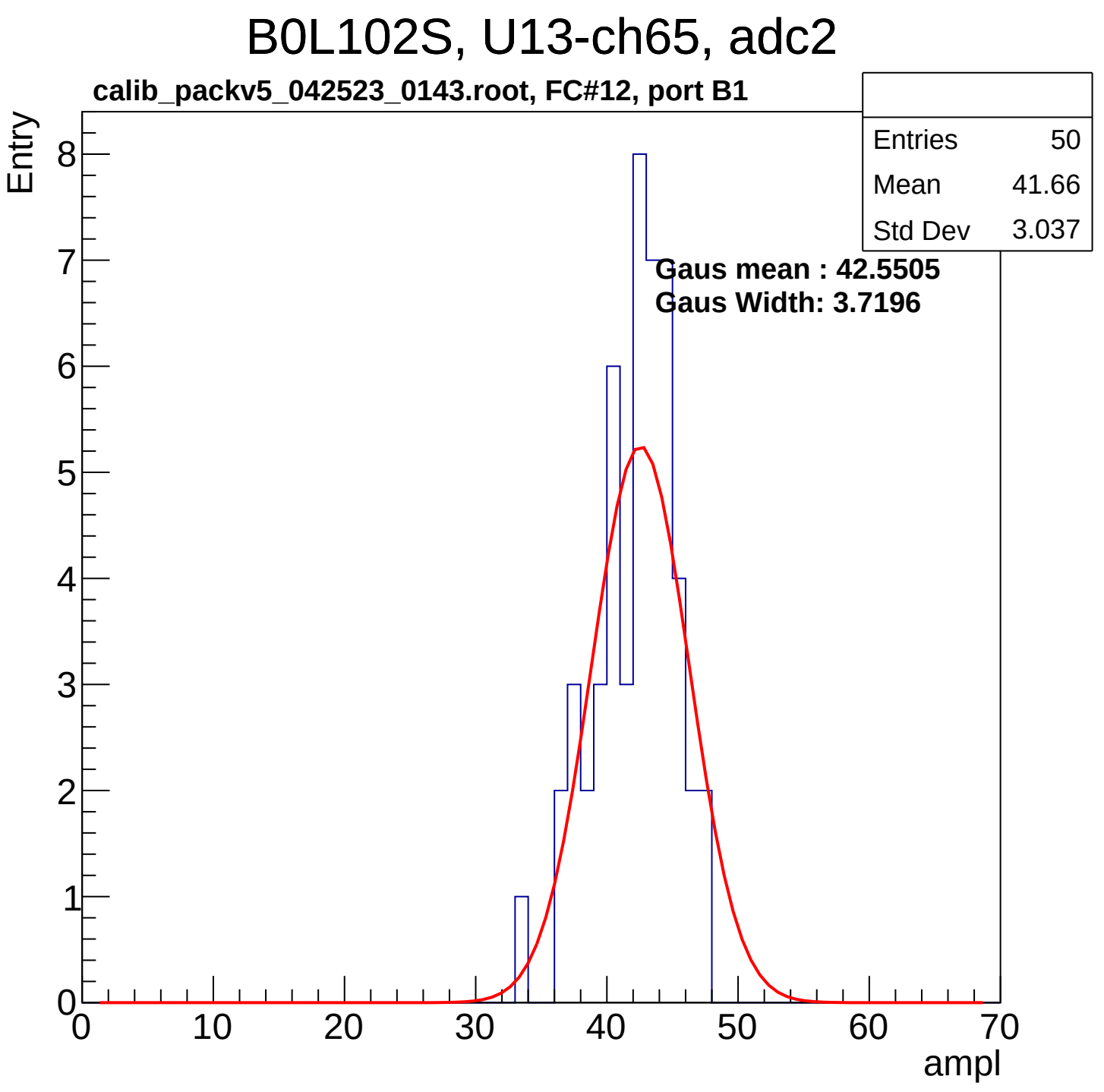
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch65, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 41.66
Std Dev 3.037
Gaus mean : 42.5505
Gaus Width: 3.7196
Entry
ampl
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1
2
3
4
5
6
7
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0
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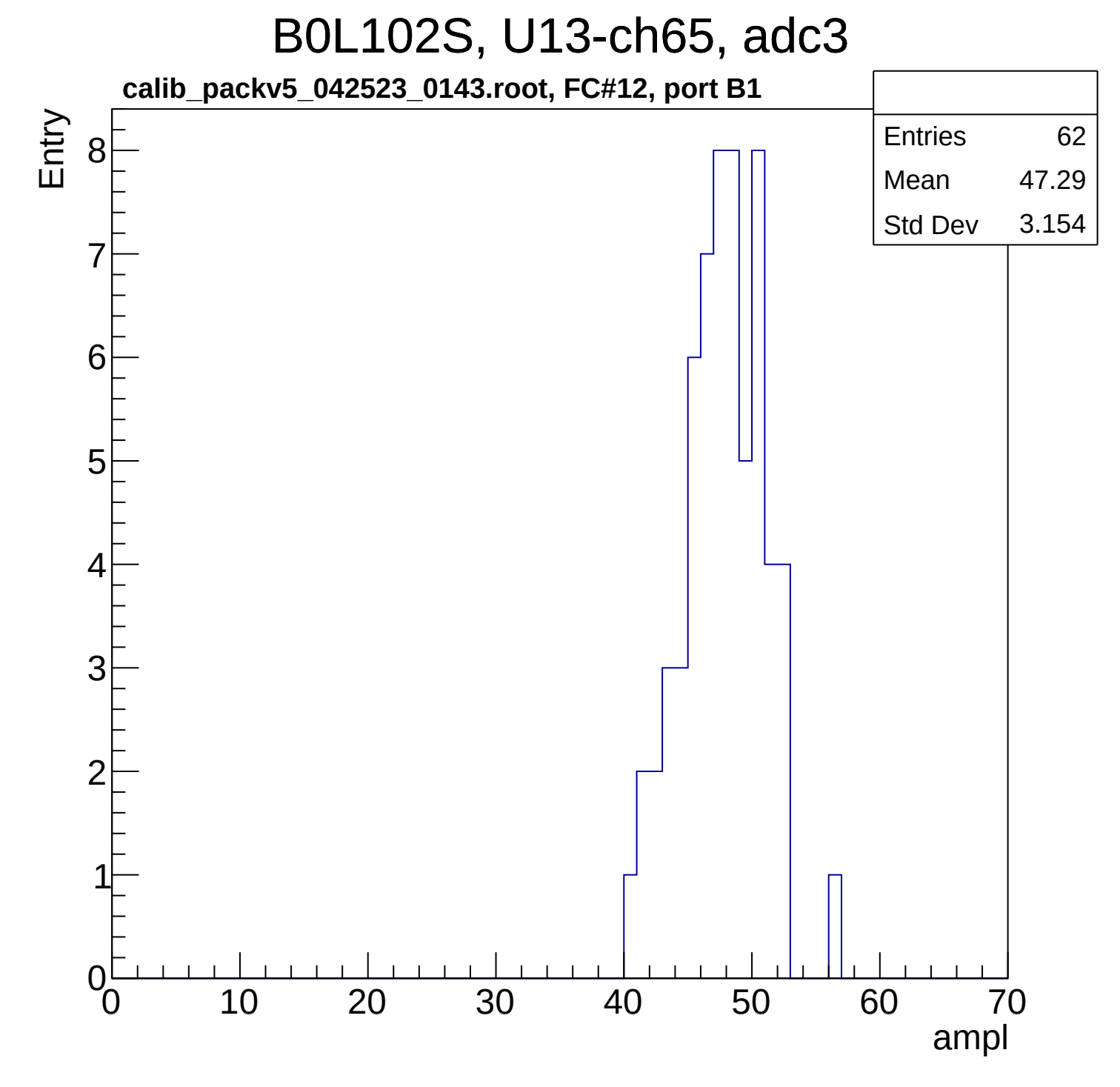
B0L102S, U13-ch65, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 47.29
Std Dev 3.154
Entry
ampl
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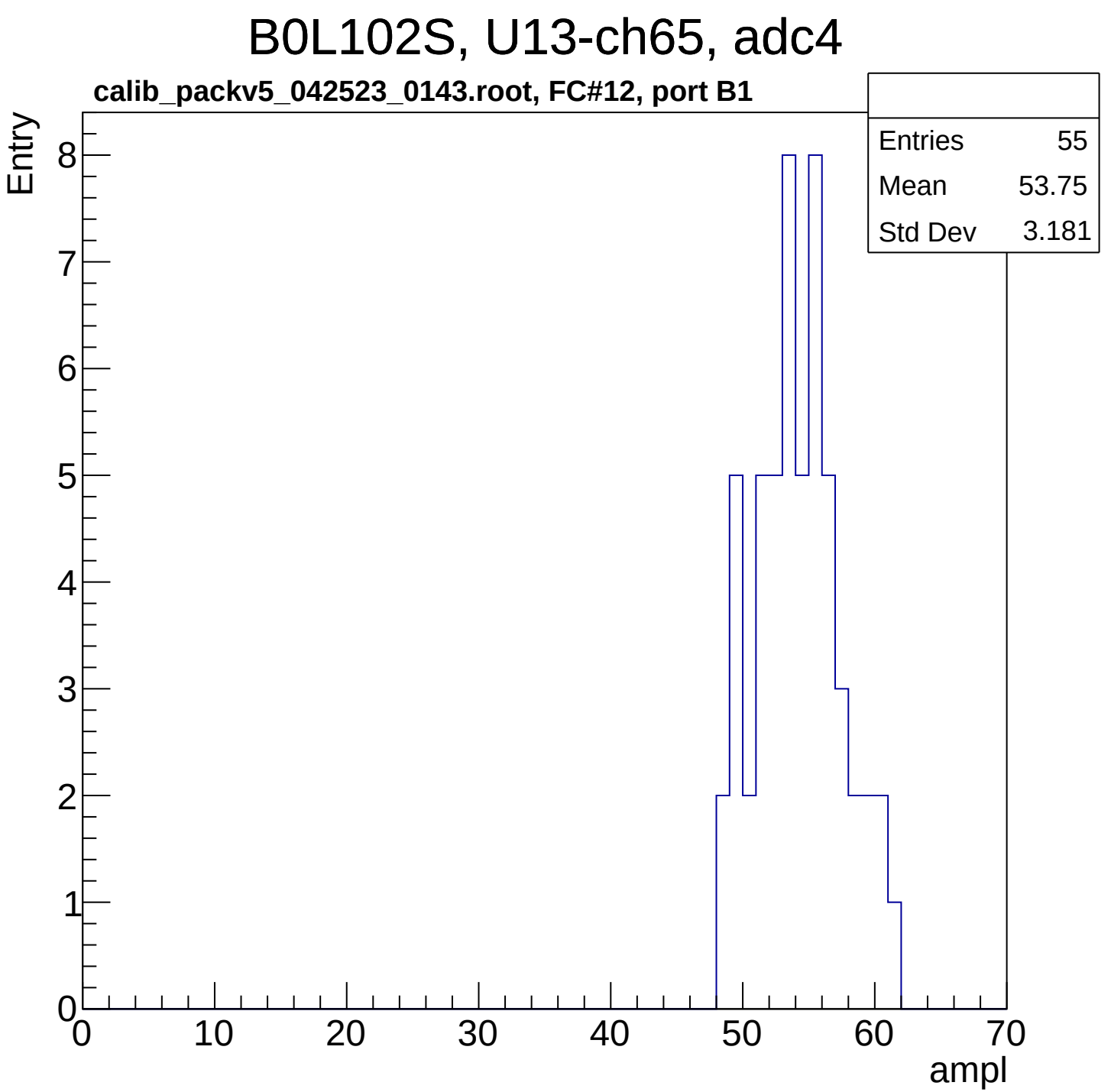
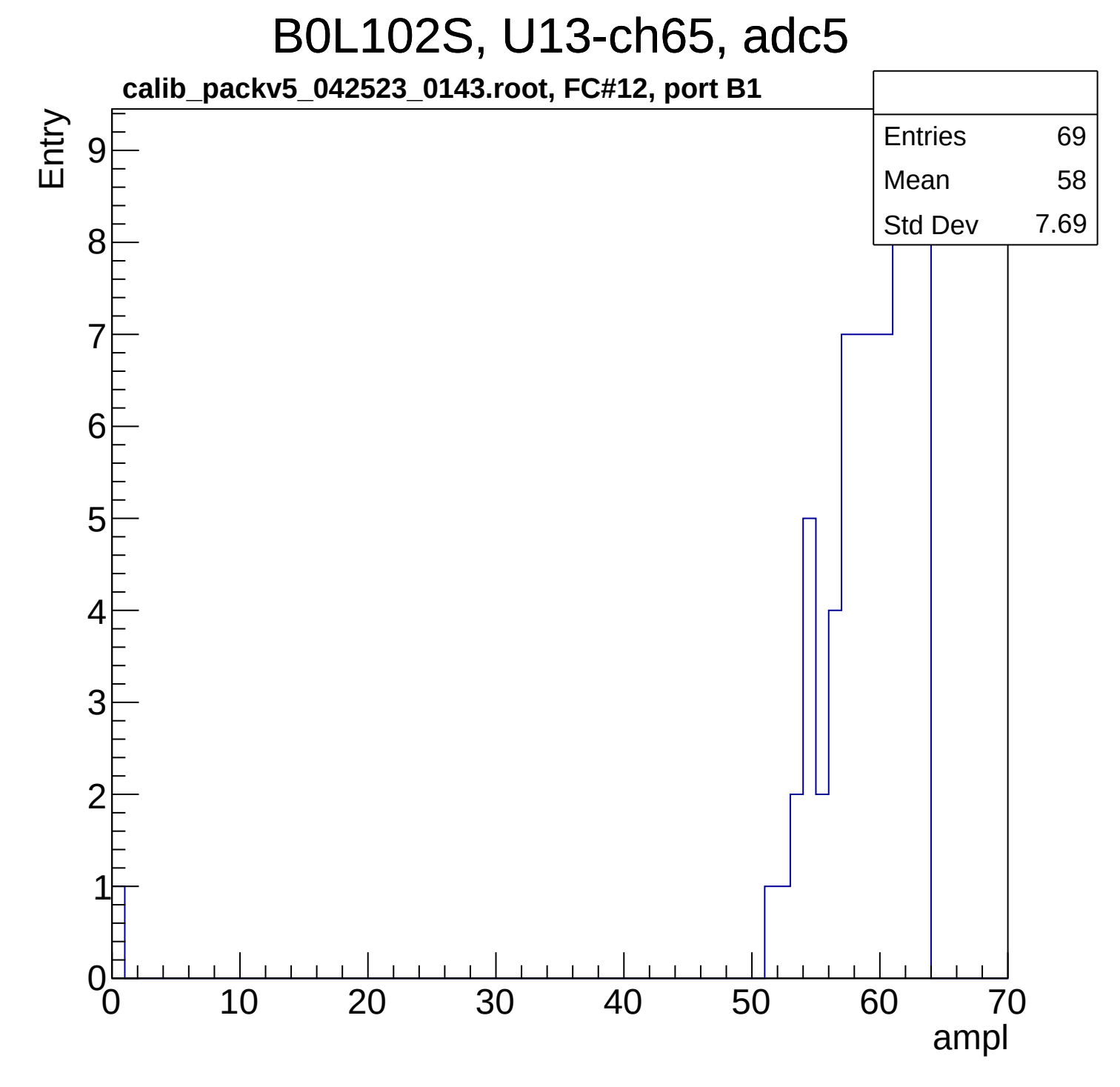

B0L102S, U13-ch65, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 53.75
Std Dev 3.181
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch65, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 58
Std Dev 7.69
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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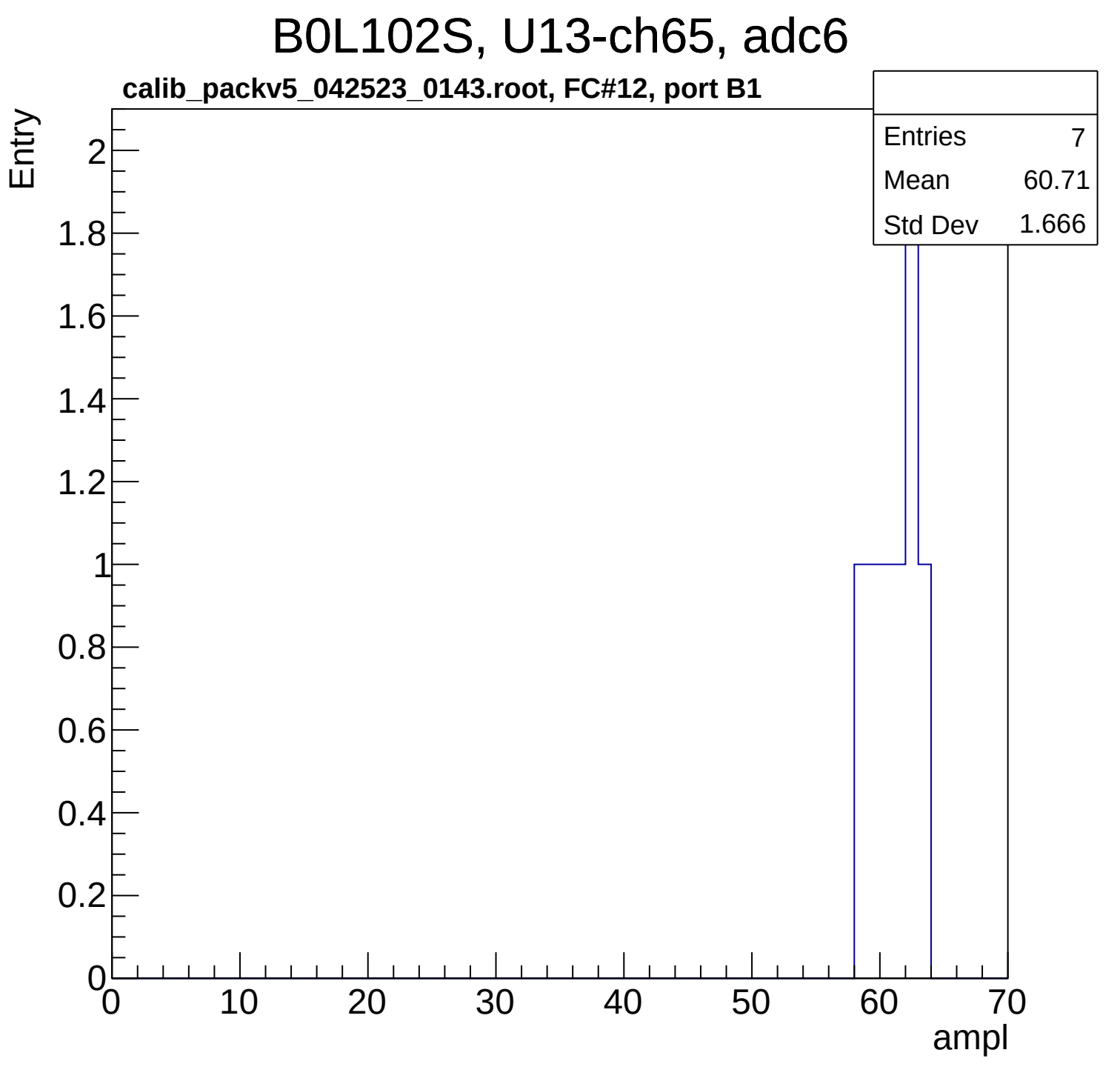
B0L102S, U13-ch65, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 60.71
Std Dev 1.666
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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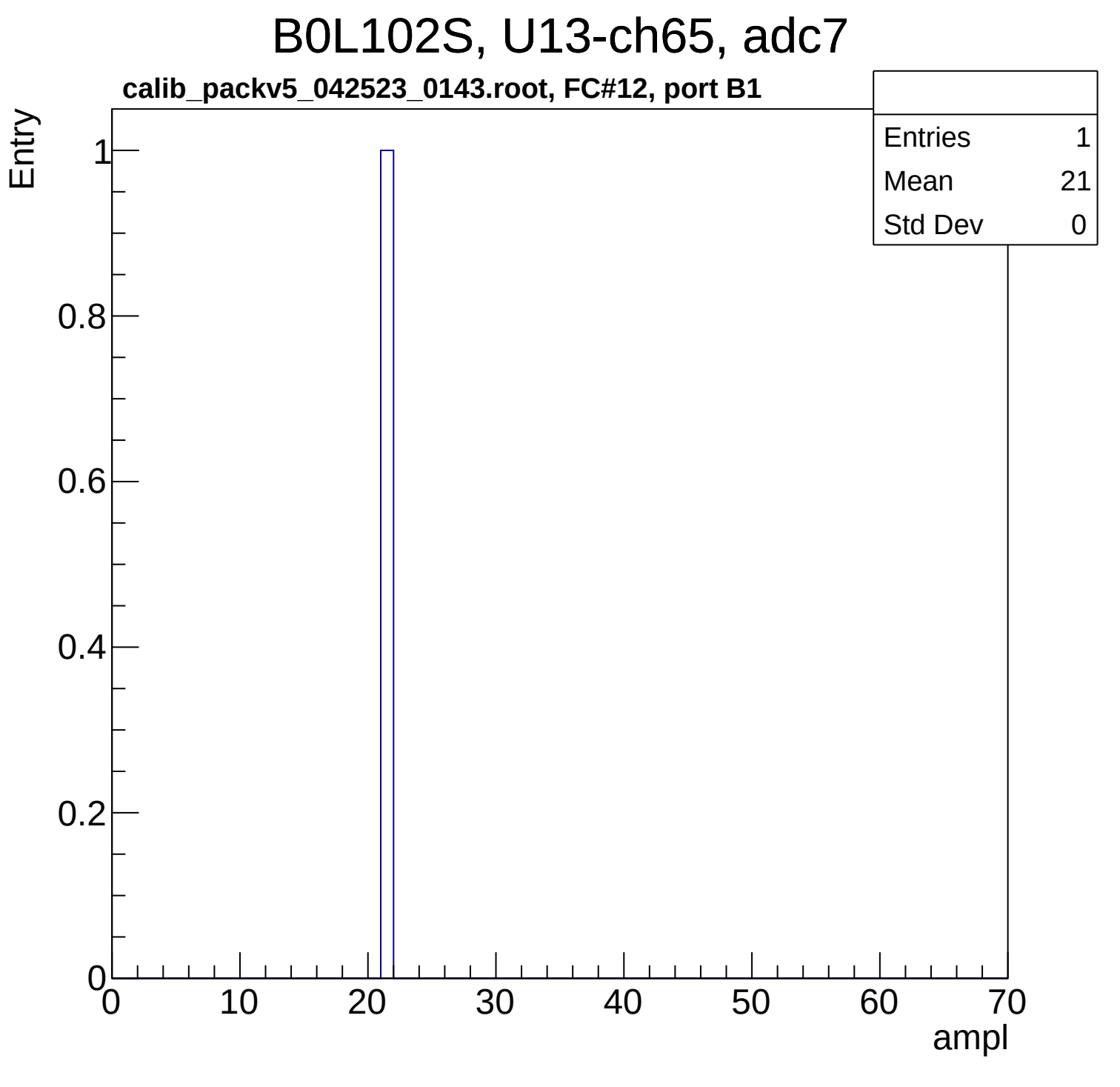
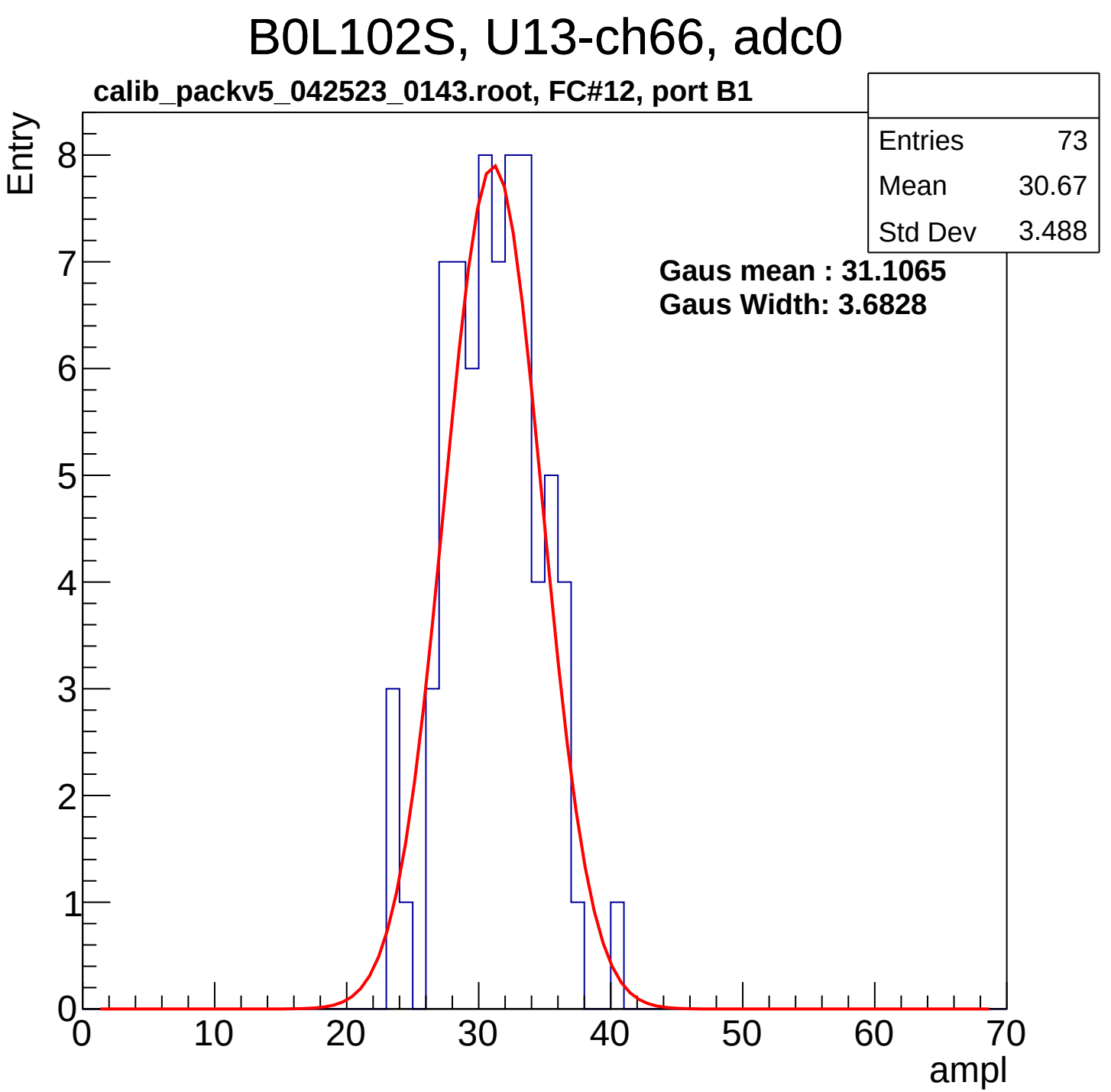
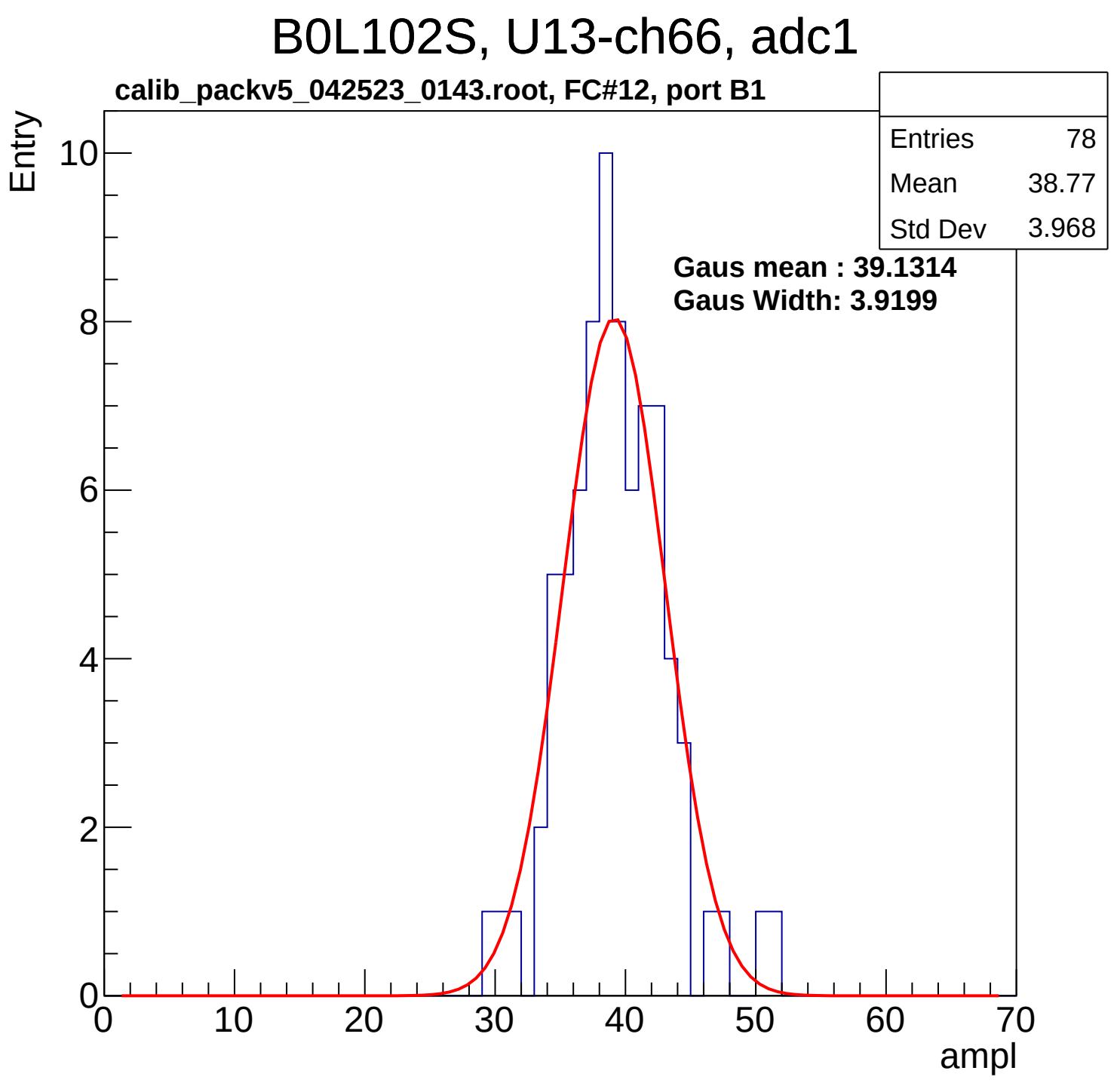
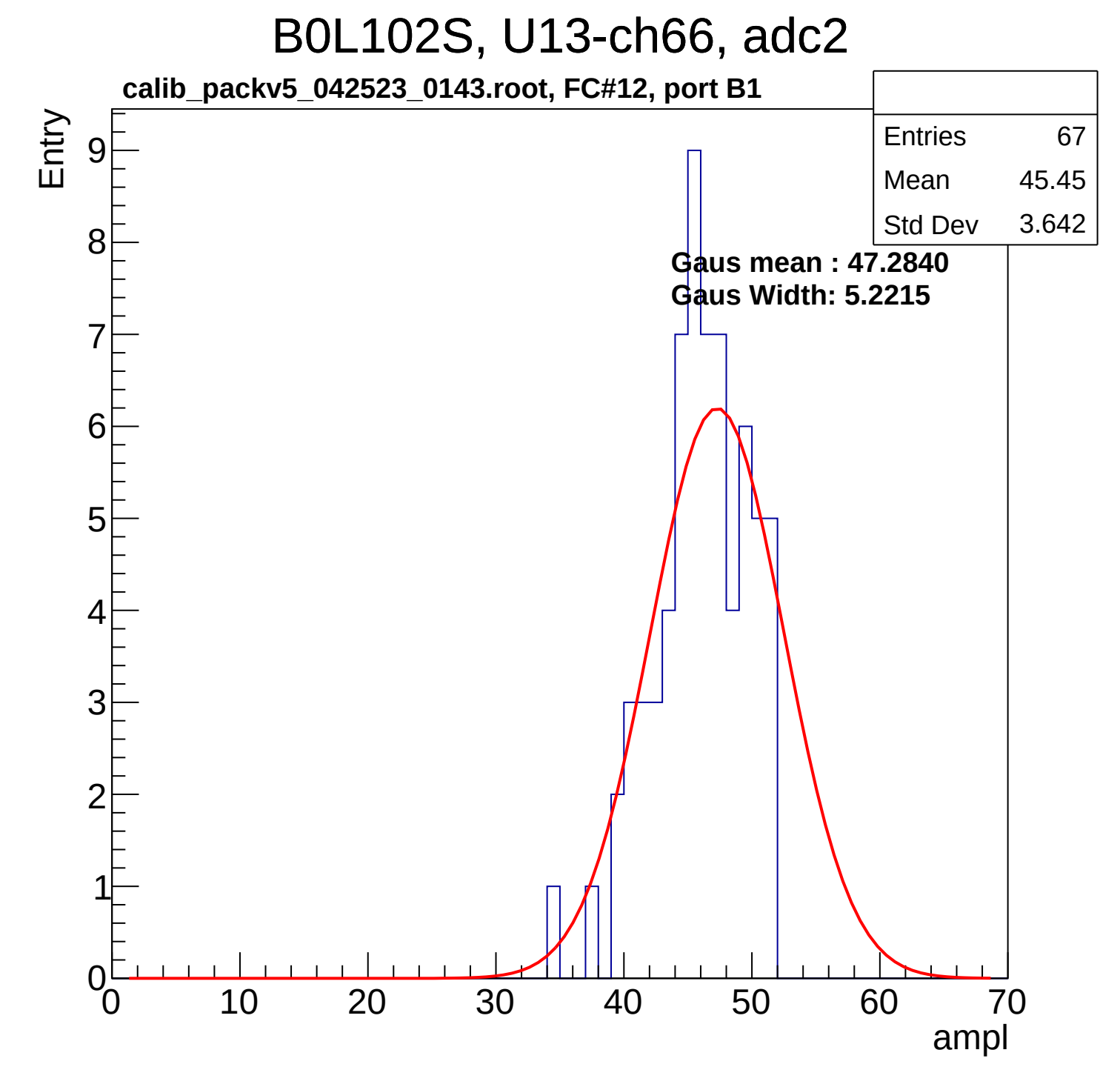
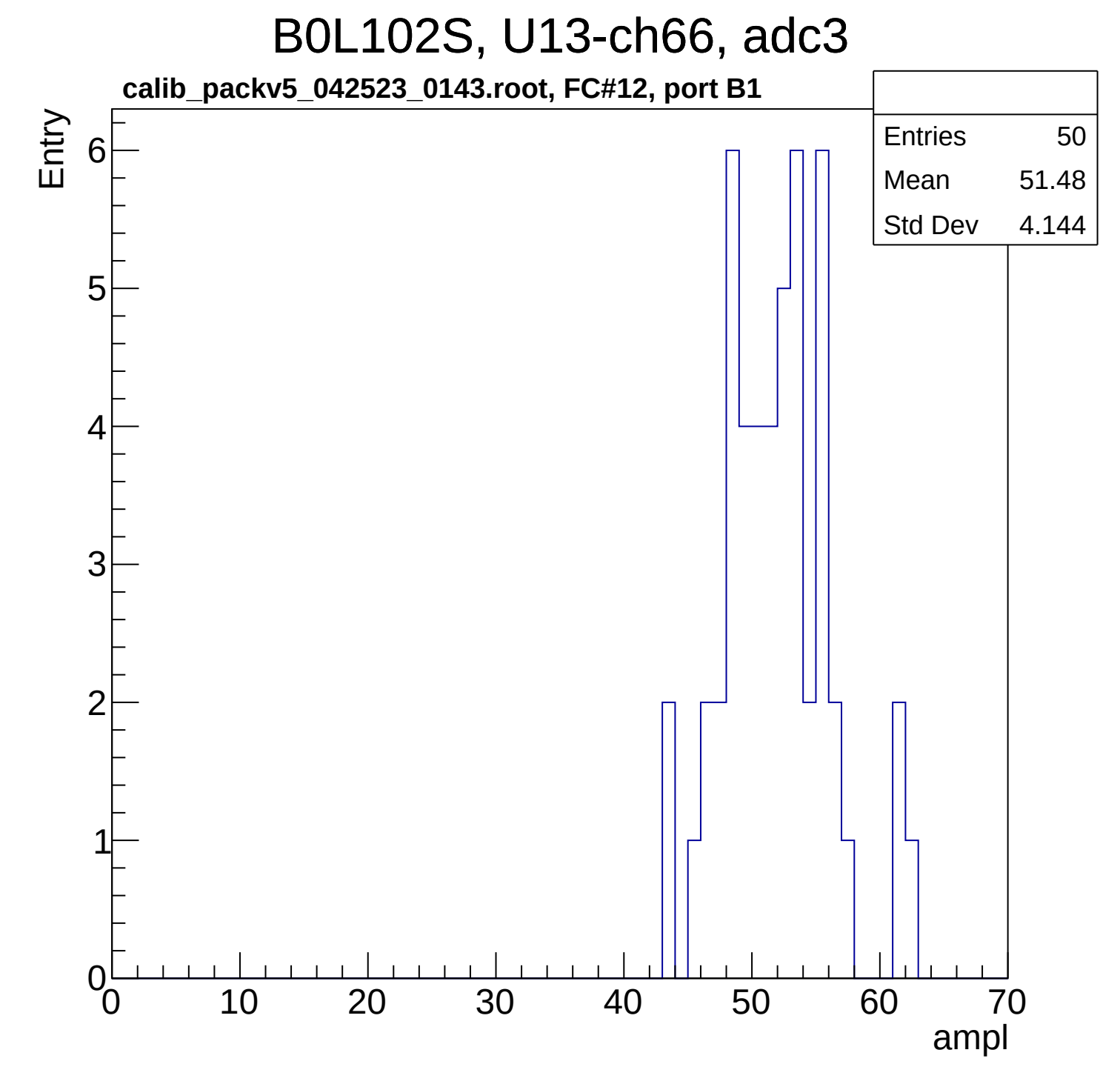
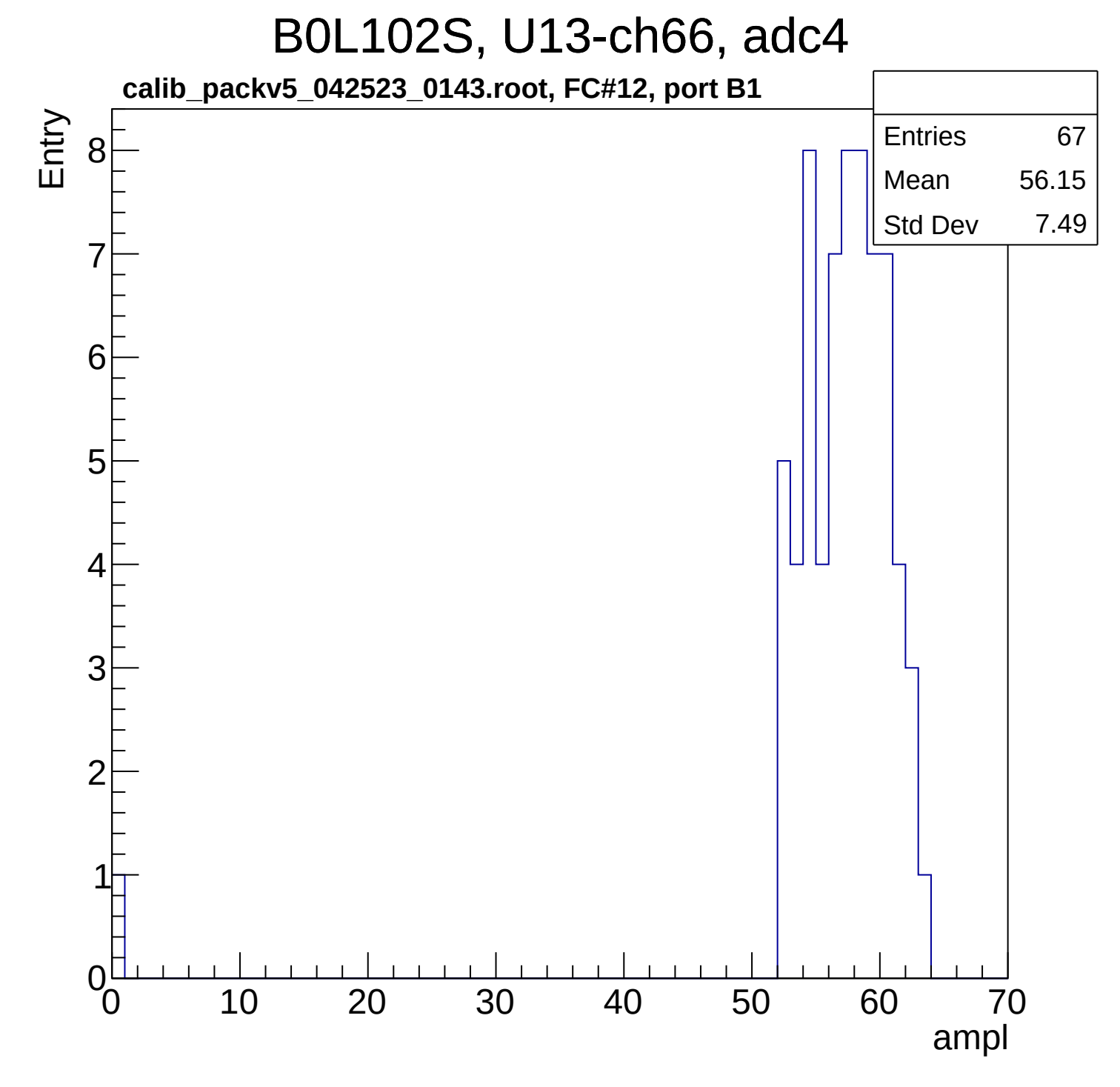

B0L102S, U13-ch65, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

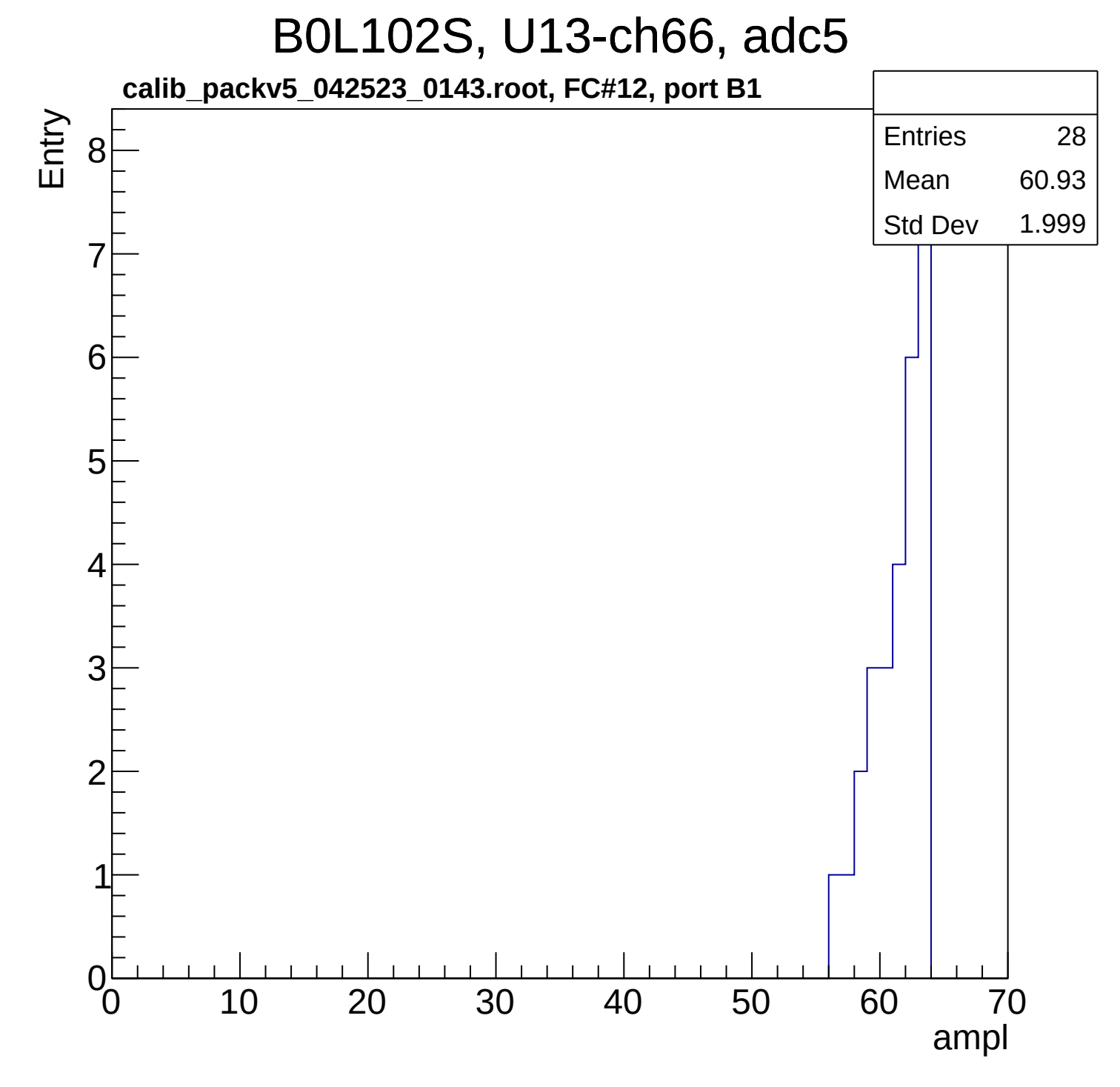

B0L102S, U13-ch66, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 30.67
Std Dev 3.488
Gaus mean : 31.1065
Gaus Width: 3.6828
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

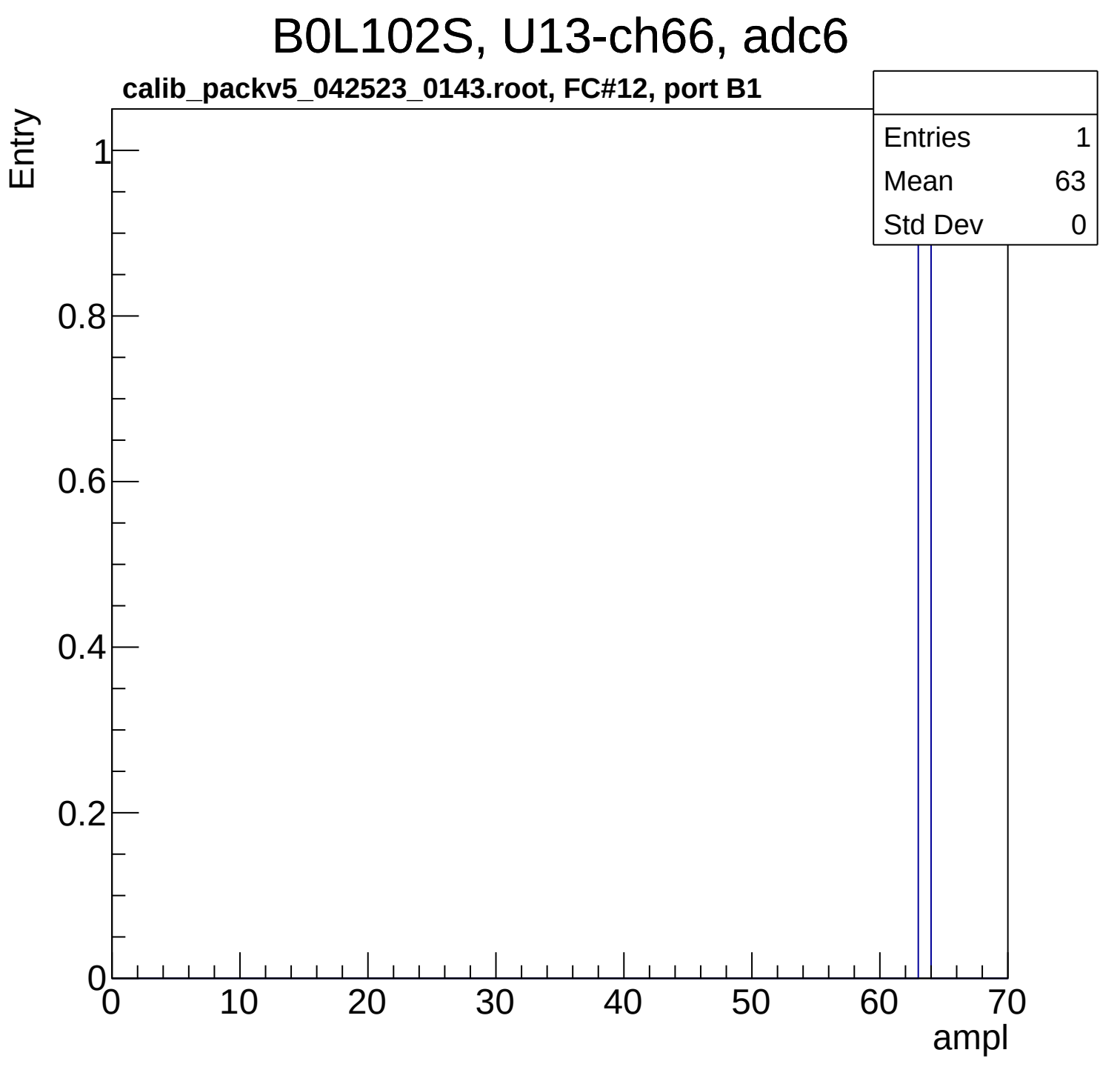

B0L102S, U13-ch66, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 38.77
Std Dev 3.968
Gaus mean : 39.1314
Gaus Width: 3.9199
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

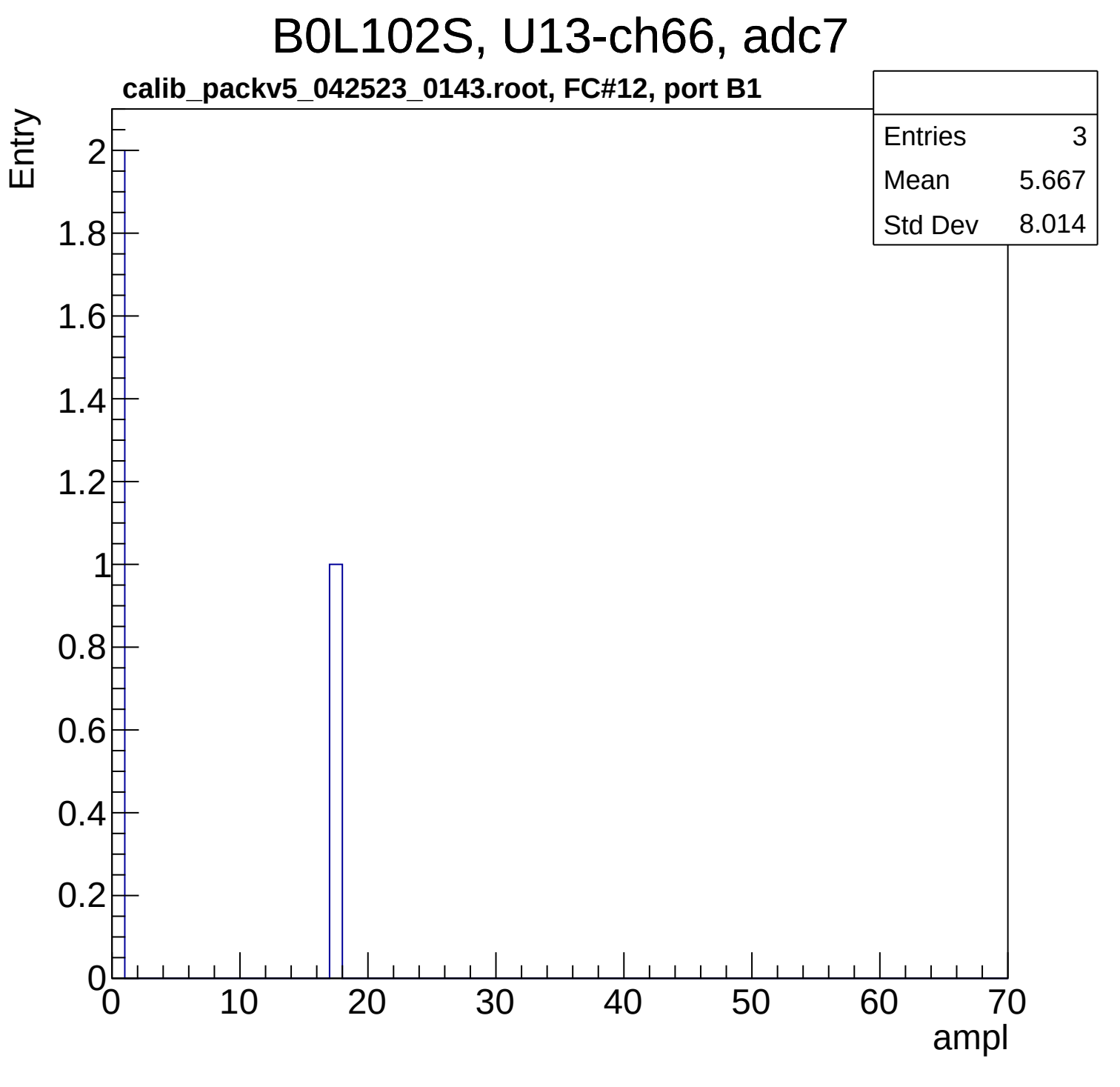

B0L102S, U13-ch66, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 45.45
Std Dev 3.642
Gaus mean : 47.2840
Gaus Width: 5.2215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

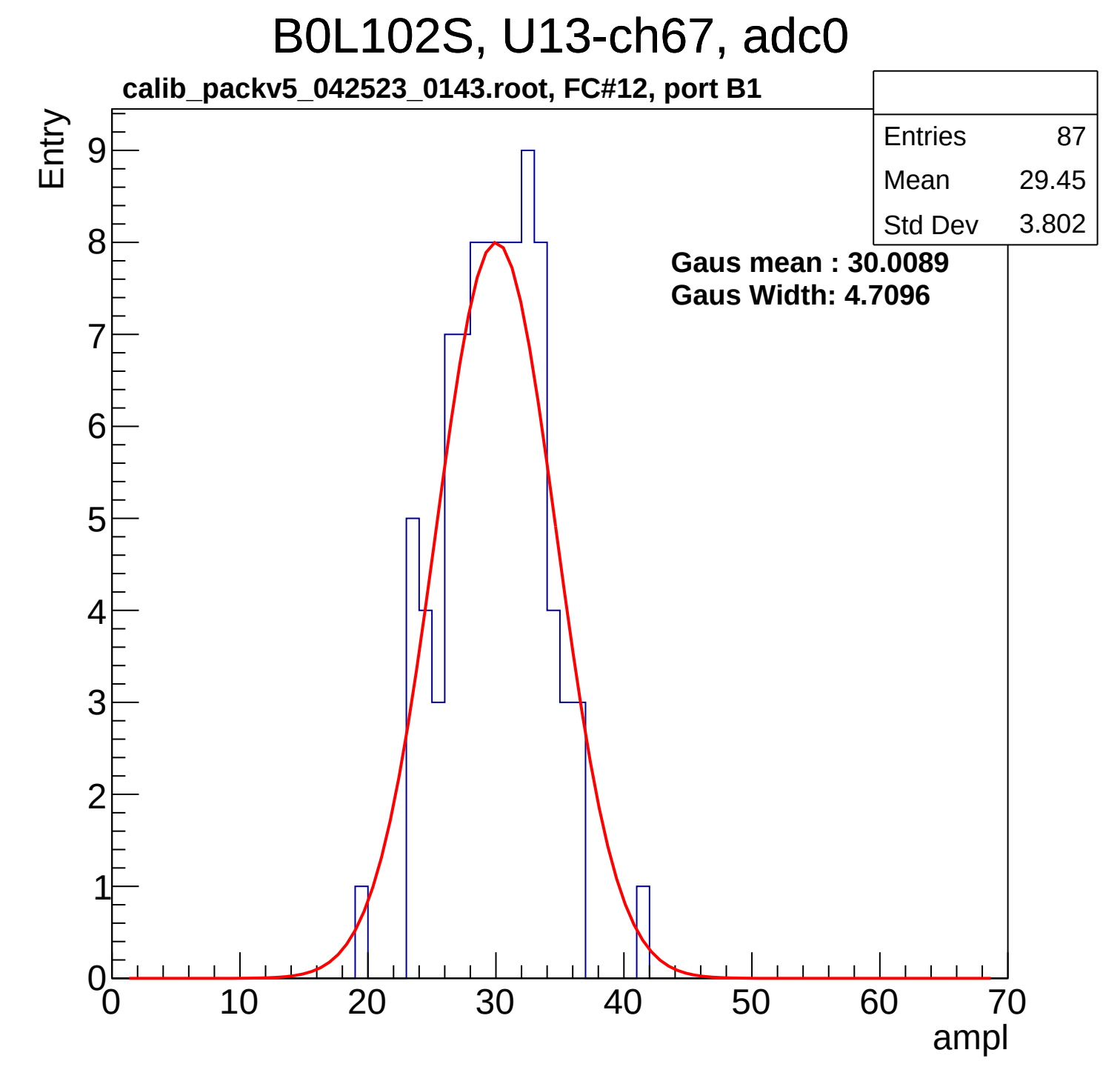

B0L102S, U13-ch66, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 51.48
Std Dev 4.144
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch66, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 56.15
Std Dev 7.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch66, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 28
Mean 60.93
Std Dev 1.999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch66, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch66, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 5.667
Std Dev 8.014
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch67, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 29.45
Std Dev 3.802
Gaus mean : 30.0089
Gaus Width: 4.7096
Entry
ampl
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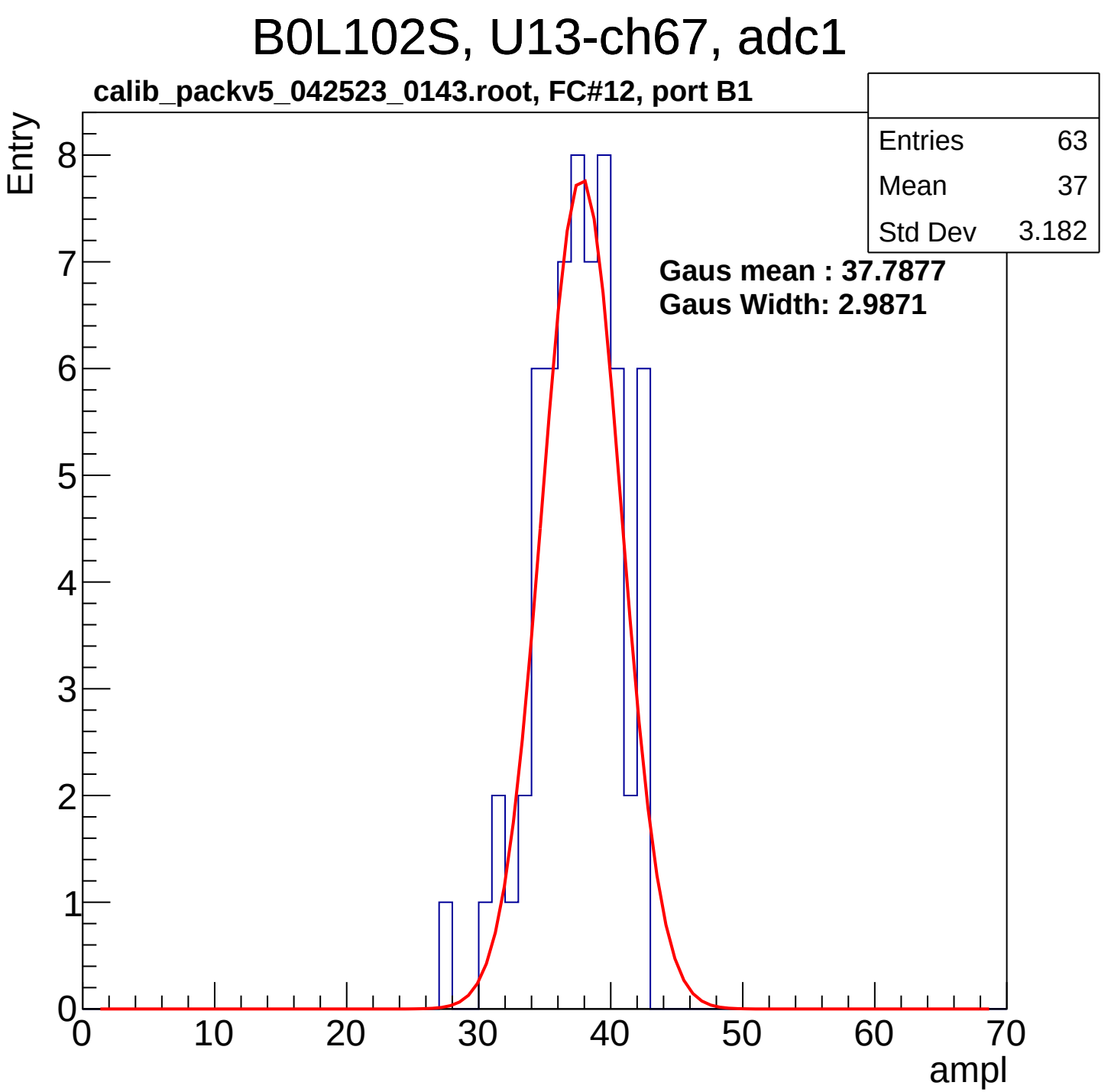
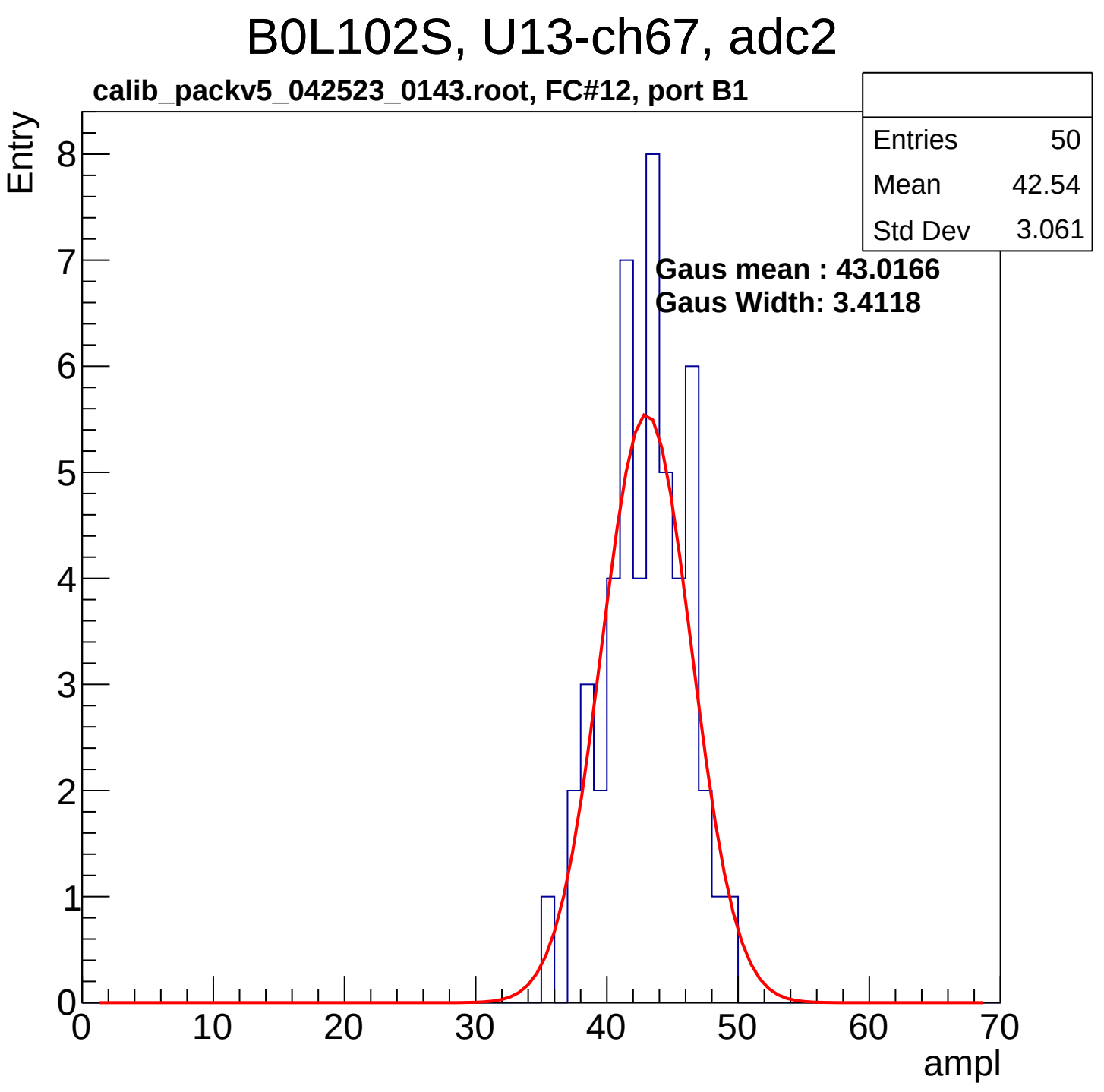
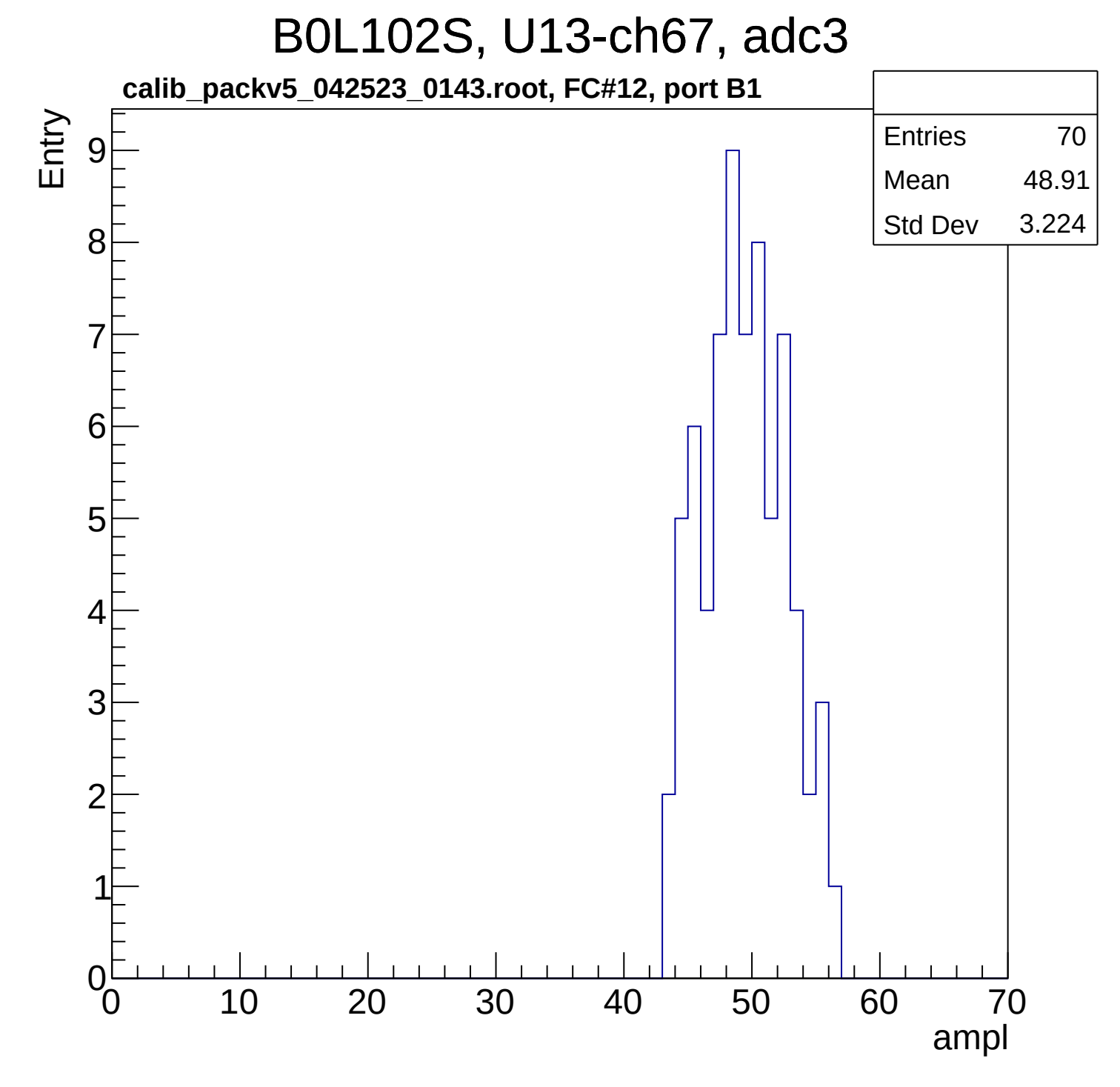
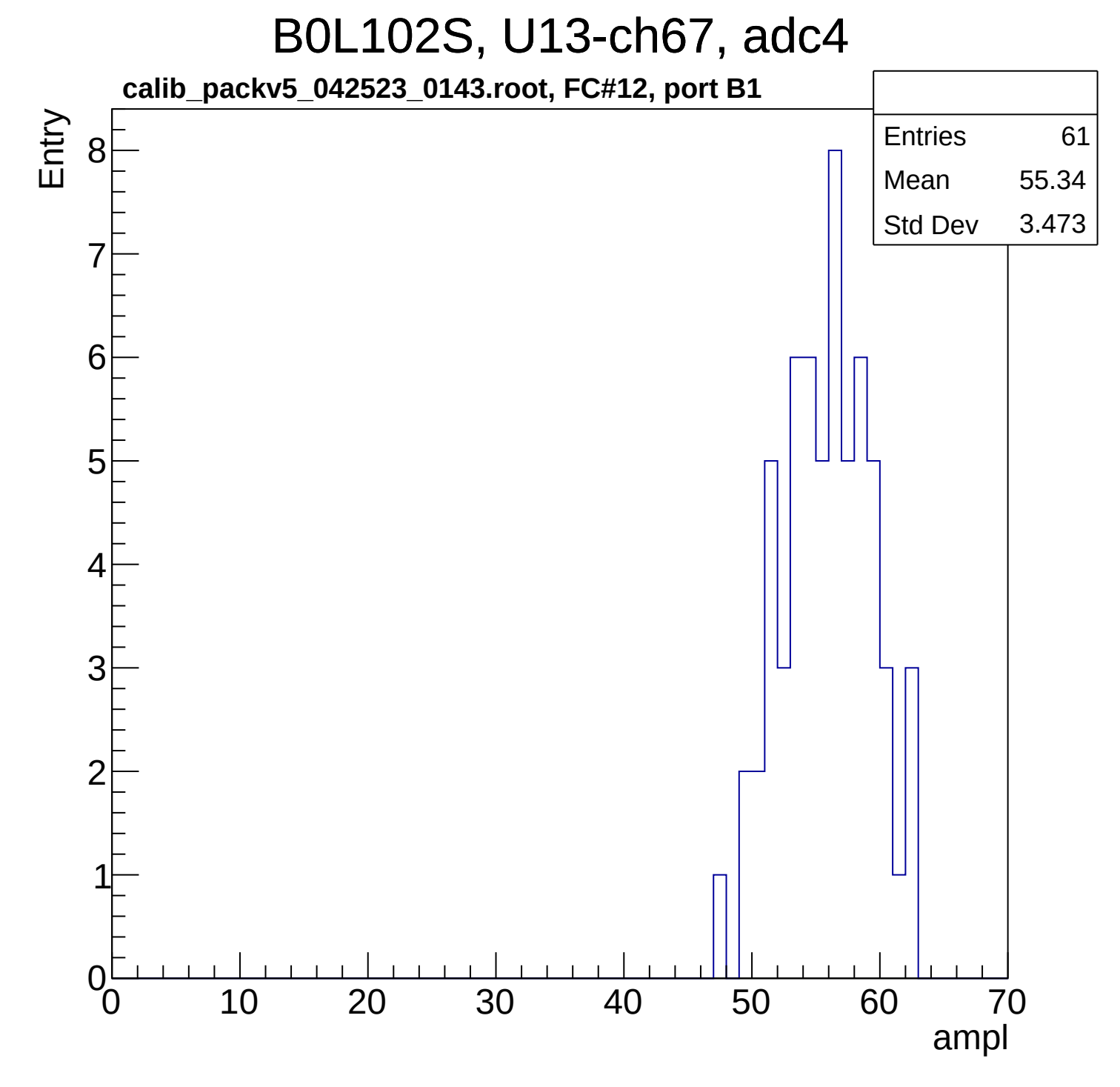
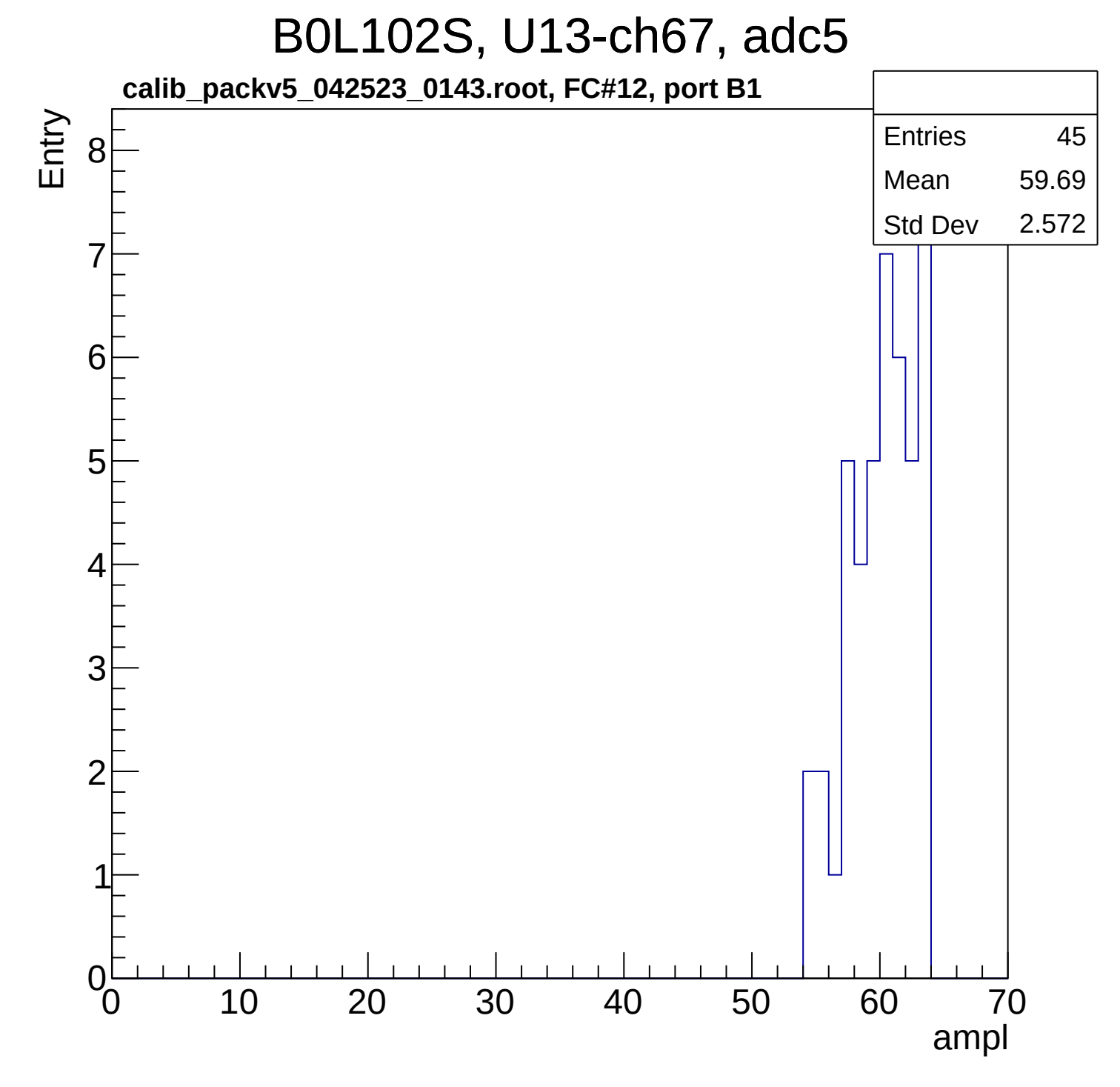
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

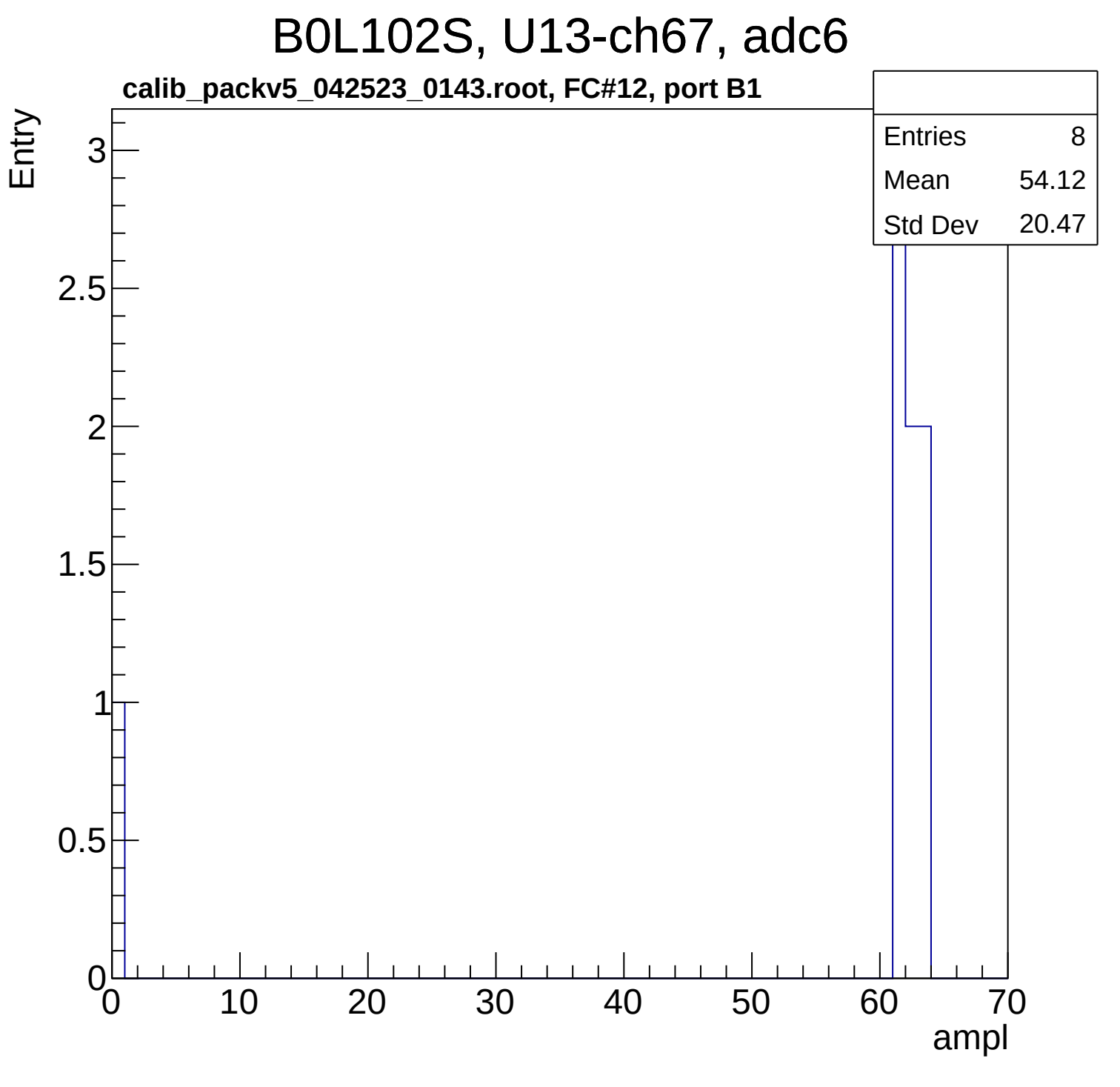

B0L102S, U13-ch67, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 37
Std Dev 3.182
Gaus mean : 37.7877
Gaus Width: 2.9871
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

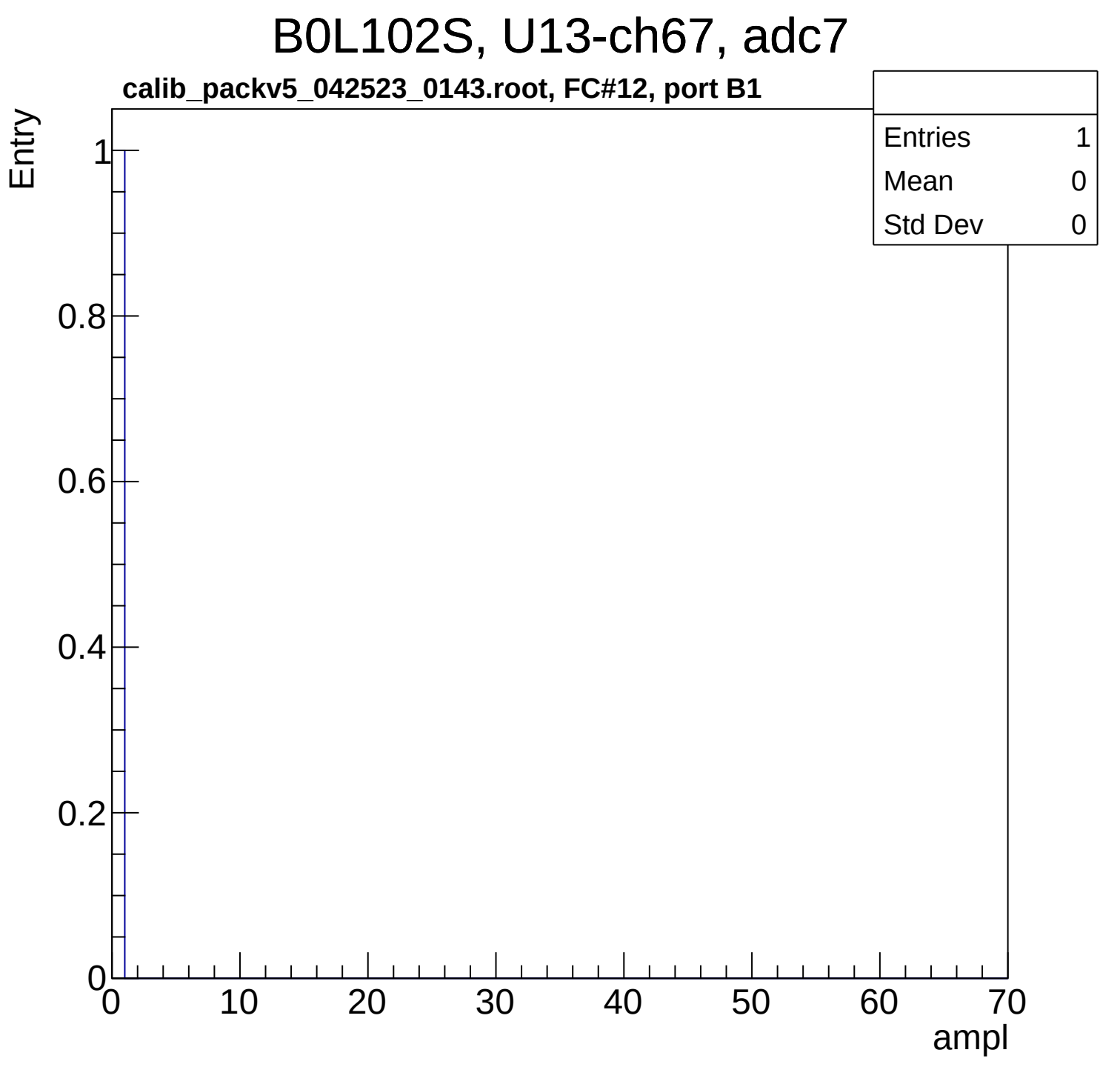

B0L102S, U13-ch67, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 42.54
Std Dev 3.061
Gaus mean : 43.0166
Gaus Width: 3.4118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

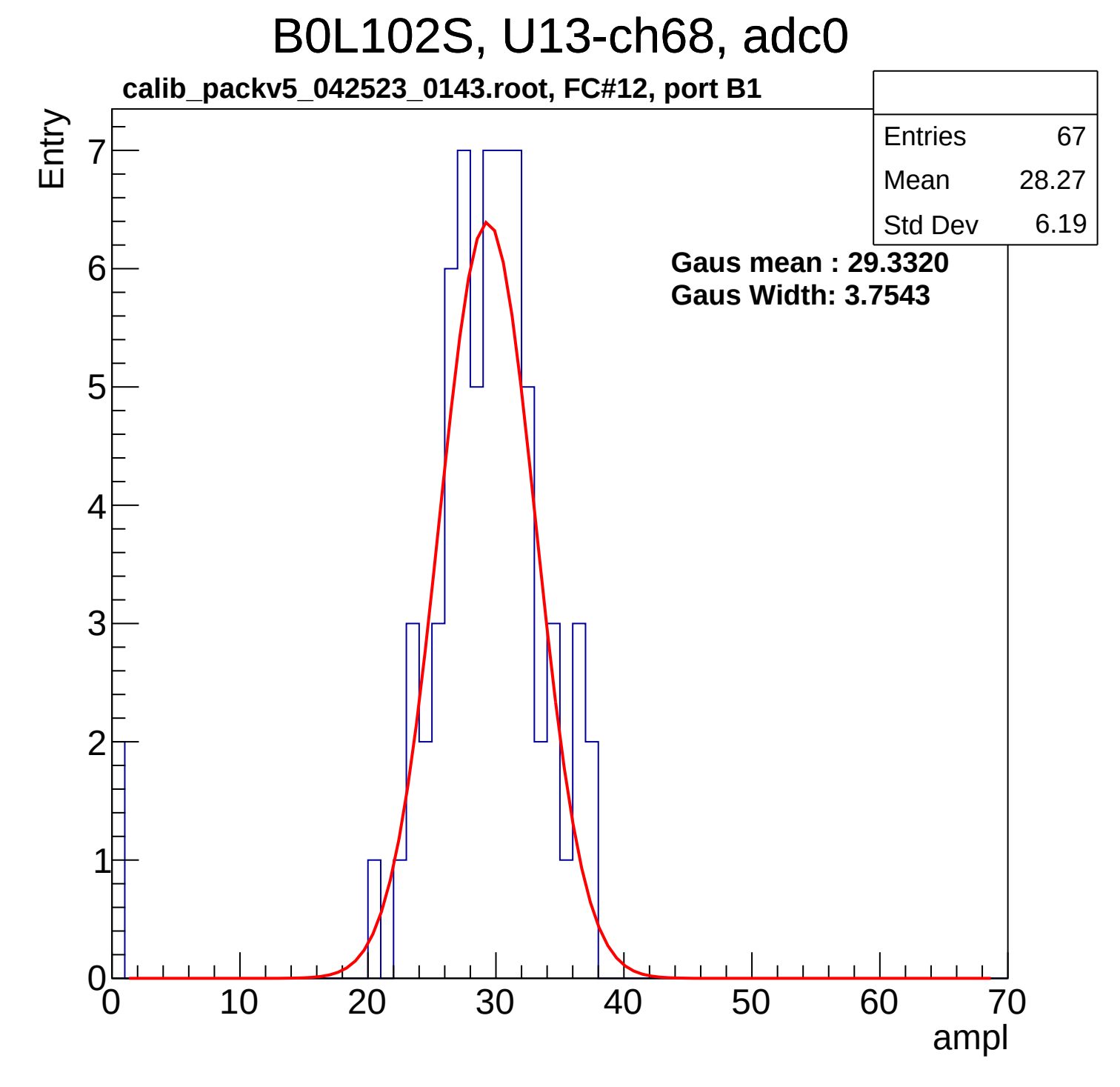

B0L102S, U13-ch67, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.91
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

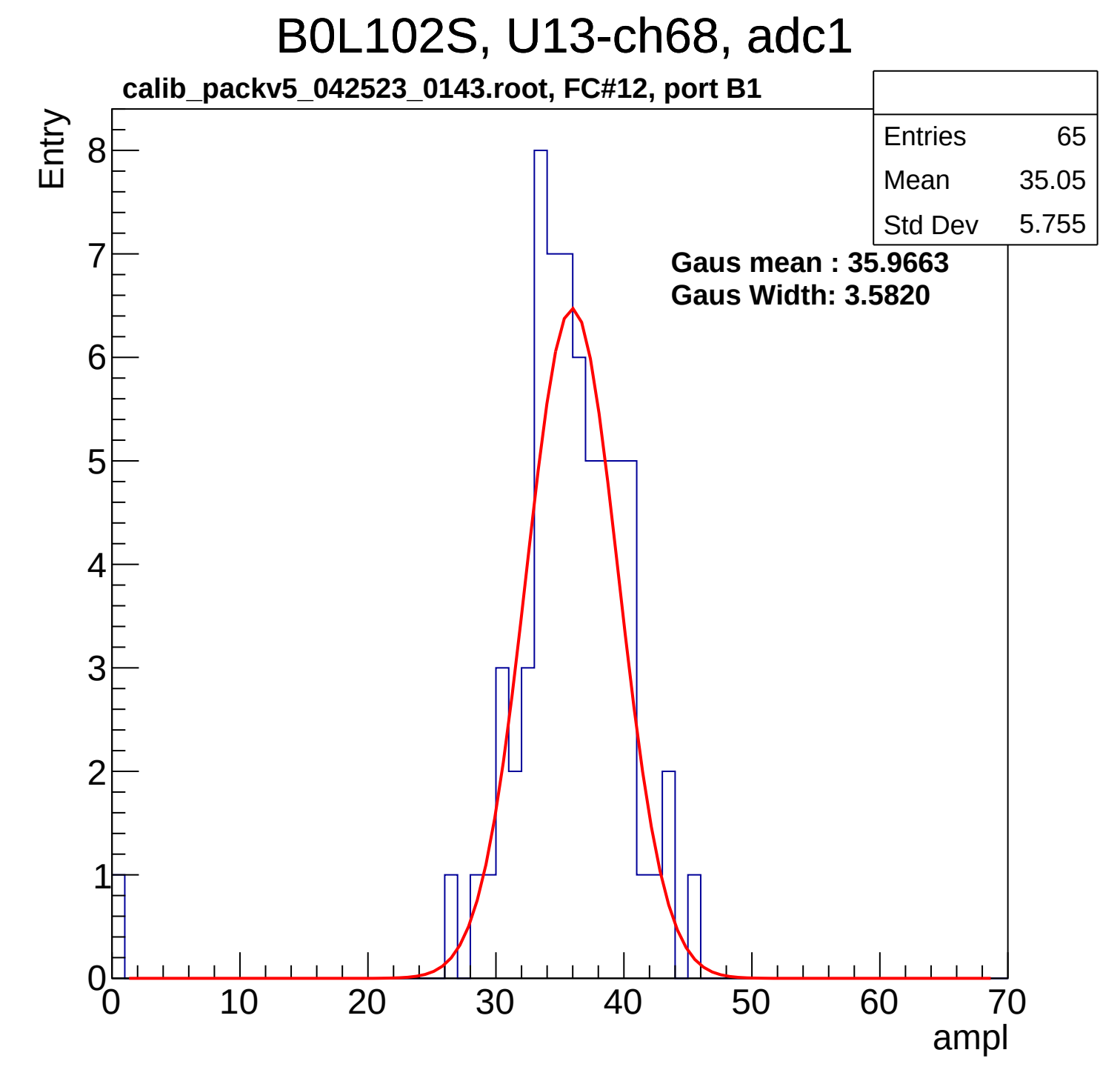

B0L102S, U13-ch67, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.34
Std Dev 3.473
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

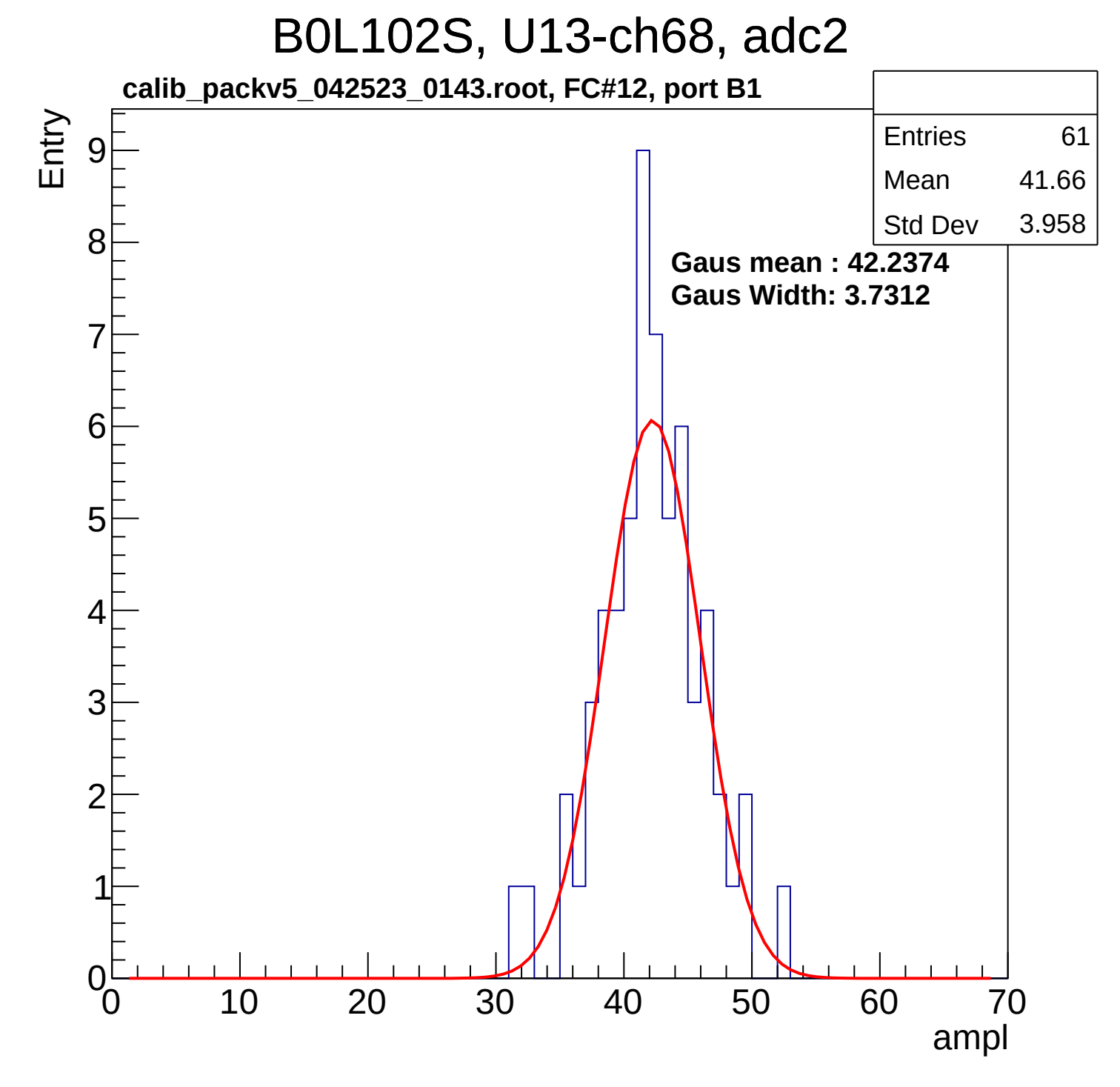

B0L102S, U13-ch67, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 59.69
Std Dev 2.572
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch67, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.12
Std Dev 20.47
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch67, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch68, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.27
Std Dev 6.19
Gaus mean : 29.3320
Gaus Width: 3.7543
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch68, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.05
Std Dev 5.755
Gaus mean : 35.9663
Gaus Width: 3.5820
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch68, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.66
Std Dev 3.958
Gaus mean : 42.2374
Gaus Width: 3.7312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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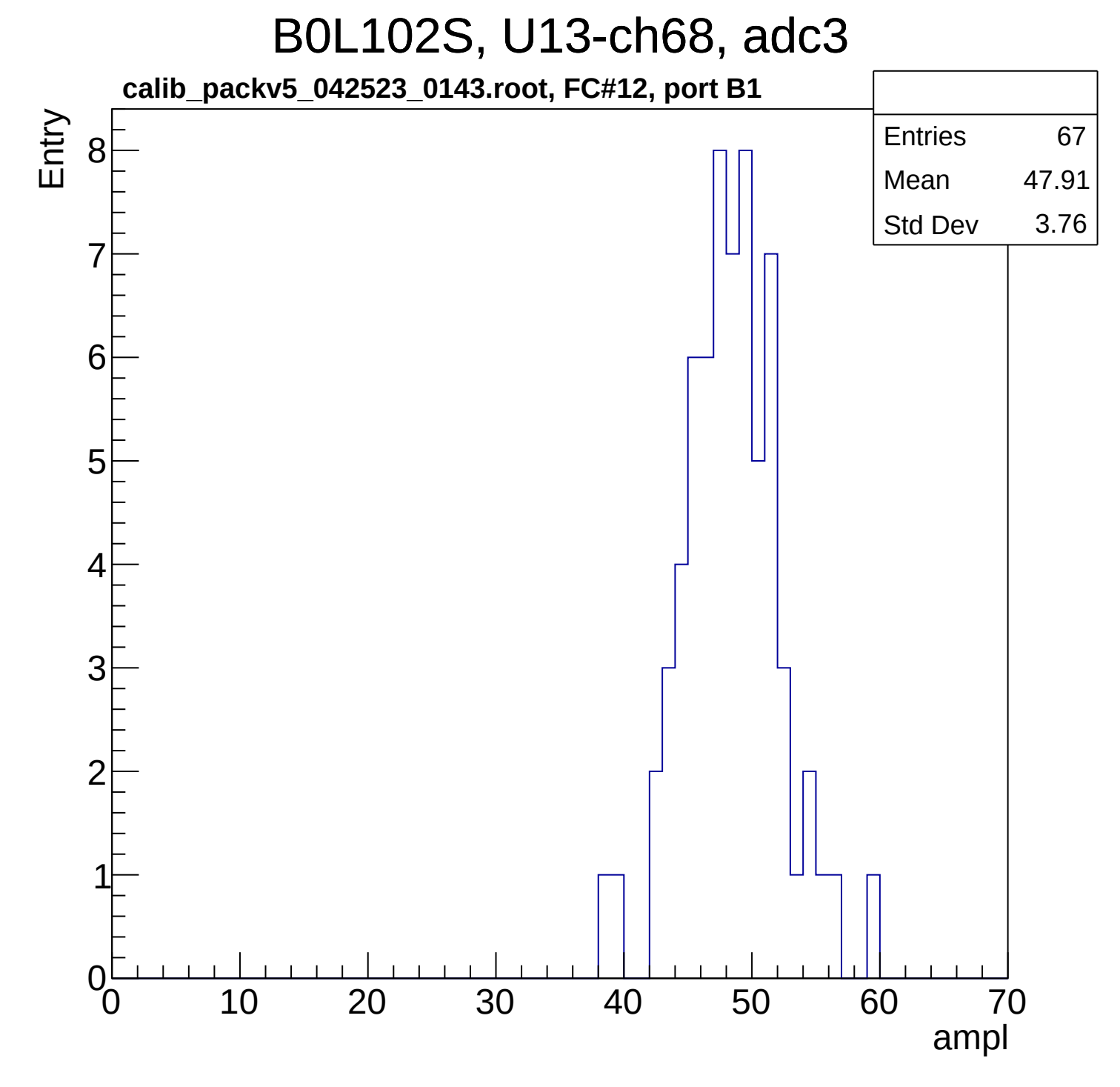
20
30
40
50
60
70


B0L102S, U13-ch68, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 47.91
Std Dev 3.76
Entry
ampl
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1
2
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4
5
6
7
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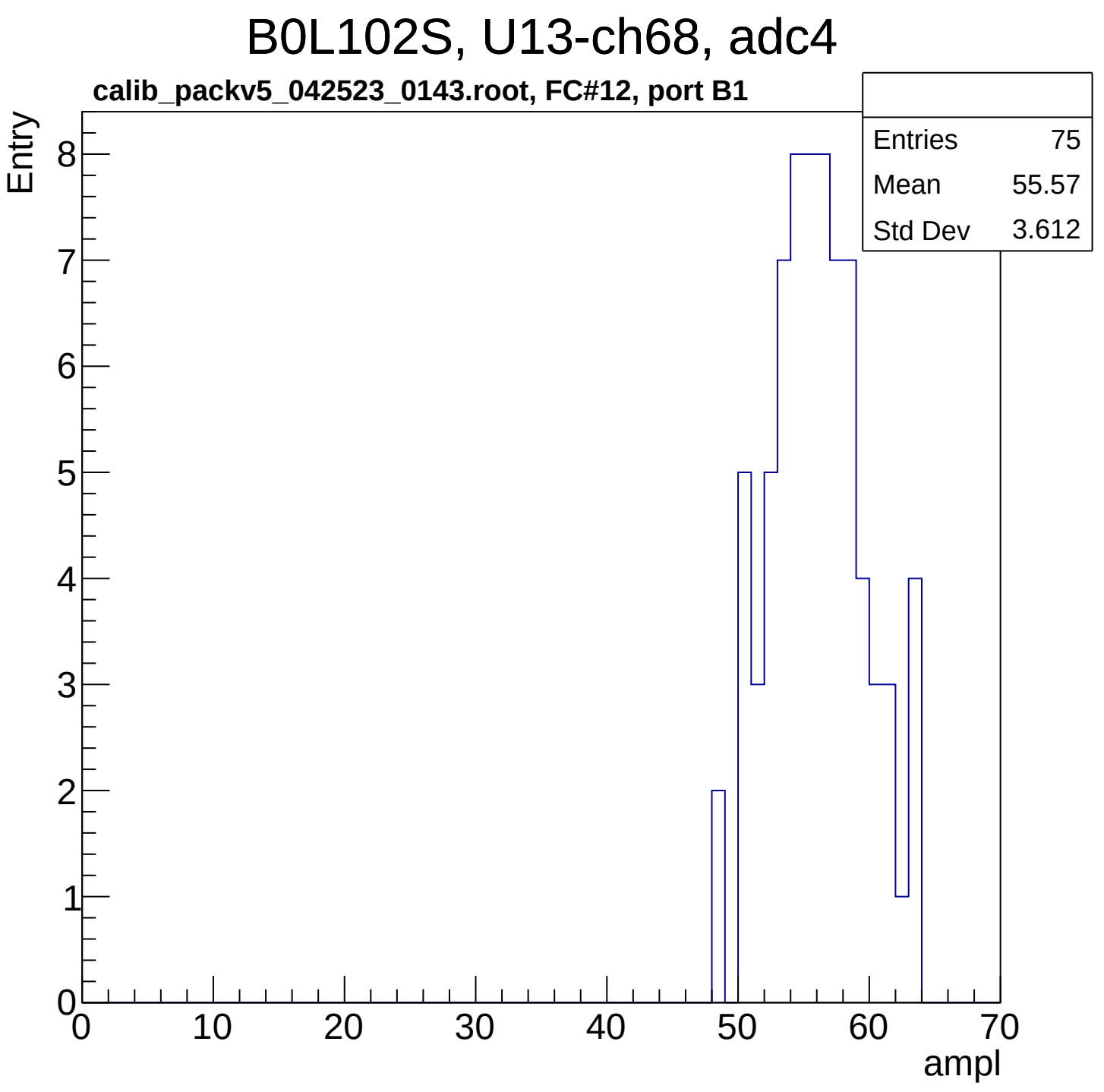
0
10
20
30
40
50
60
70


B0L102S, U13-ch68, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 55.57
Std Dev 3.612
Entry
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2
3
4
5
6
7
8
0
10
20
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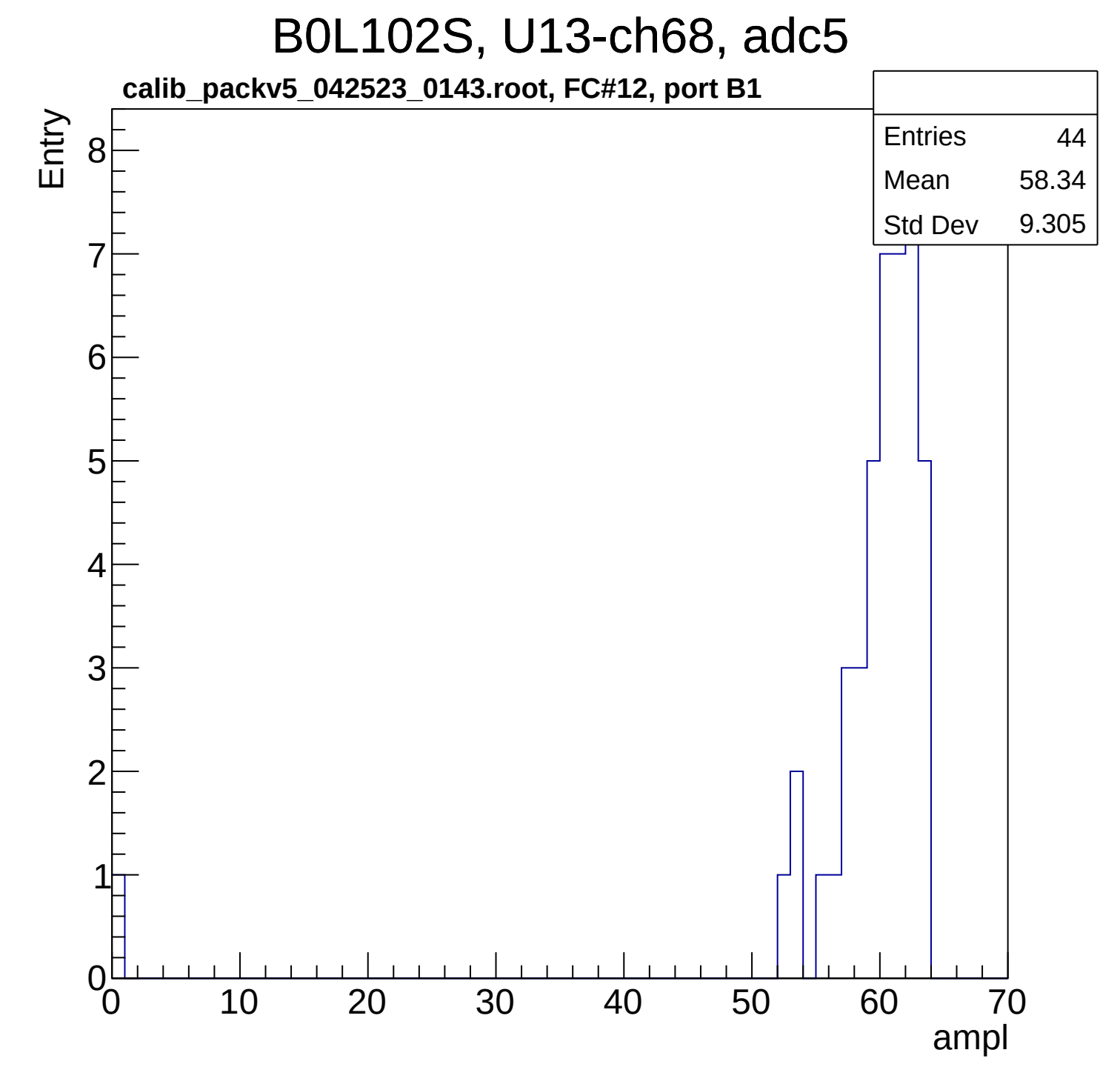
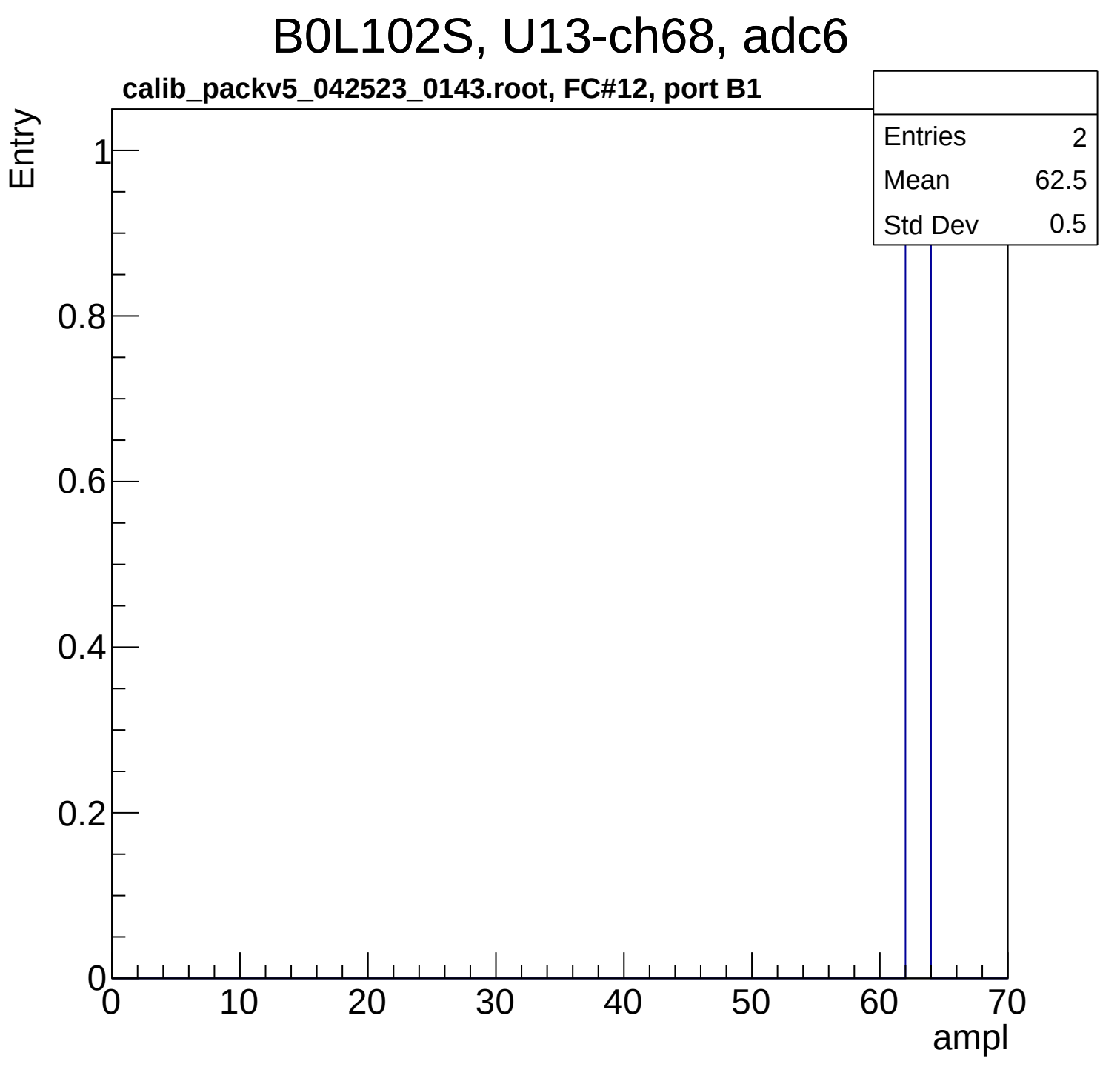
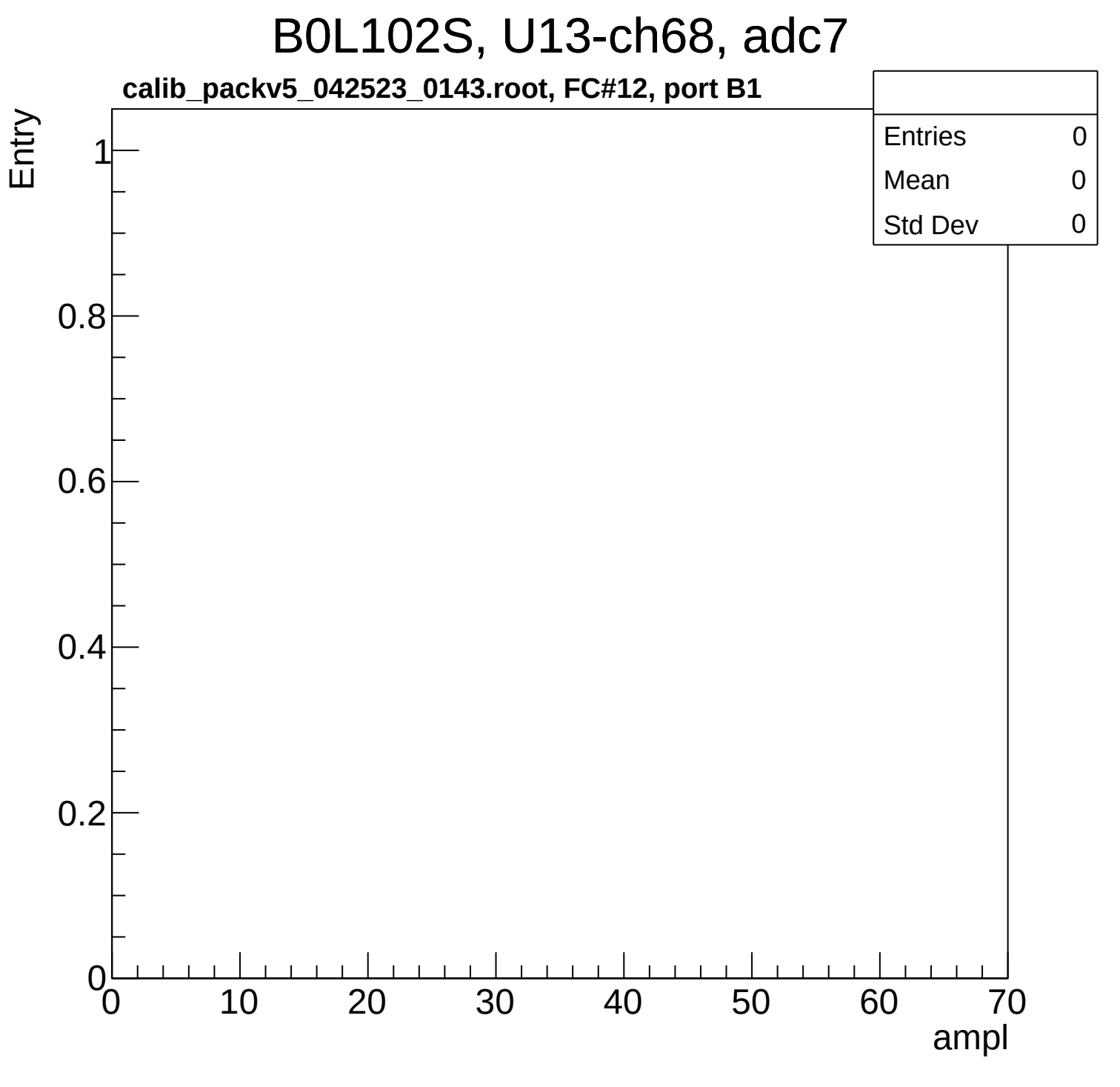
50
60
70
ampl


B0L102S, U13-ch68, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.34
Std Dev 9.305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch68, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch68, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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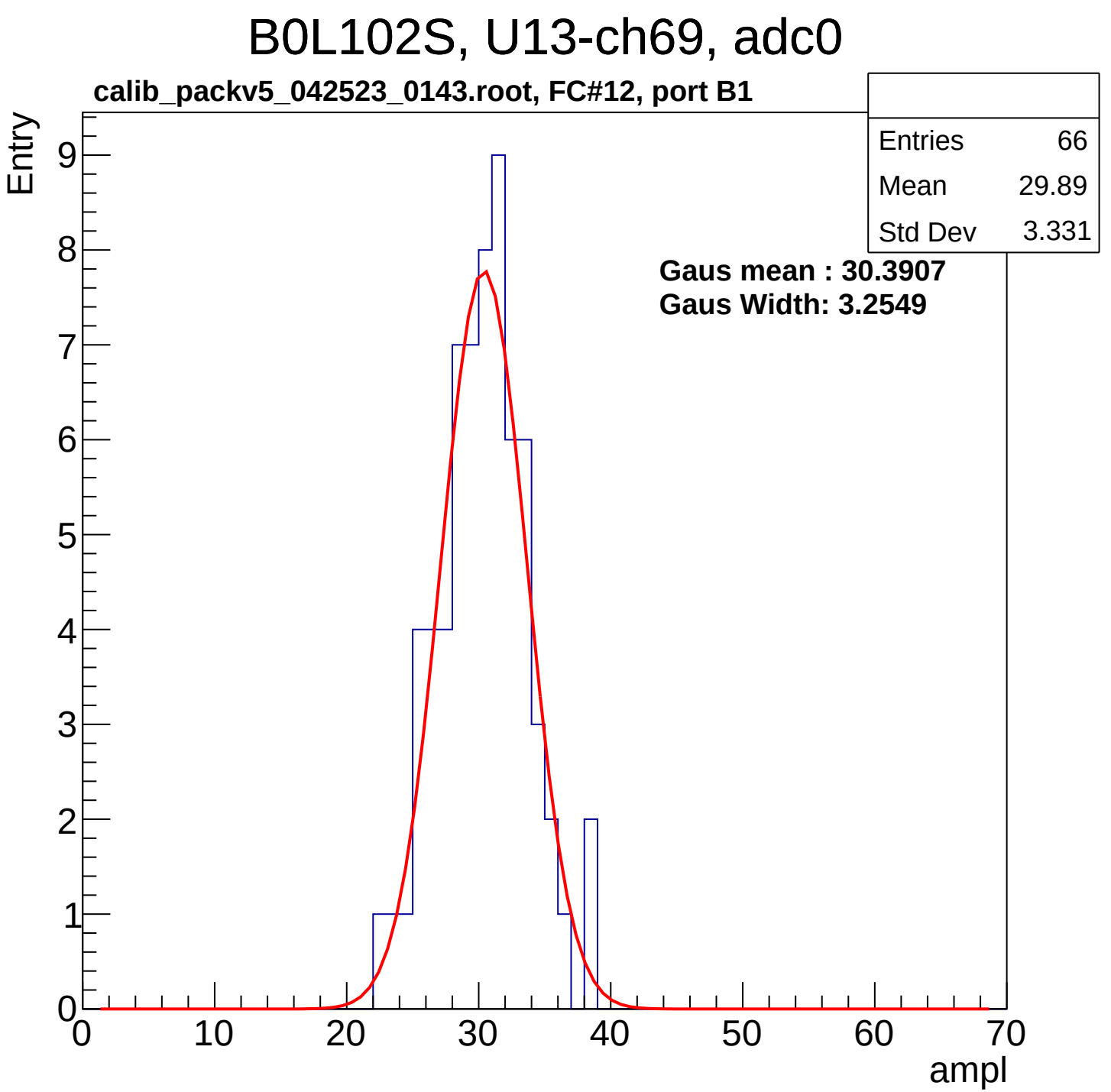
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch69, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 29.89
Std Dev 3.331
Gaus mean : 30.3907
Gaus Width: 3.2549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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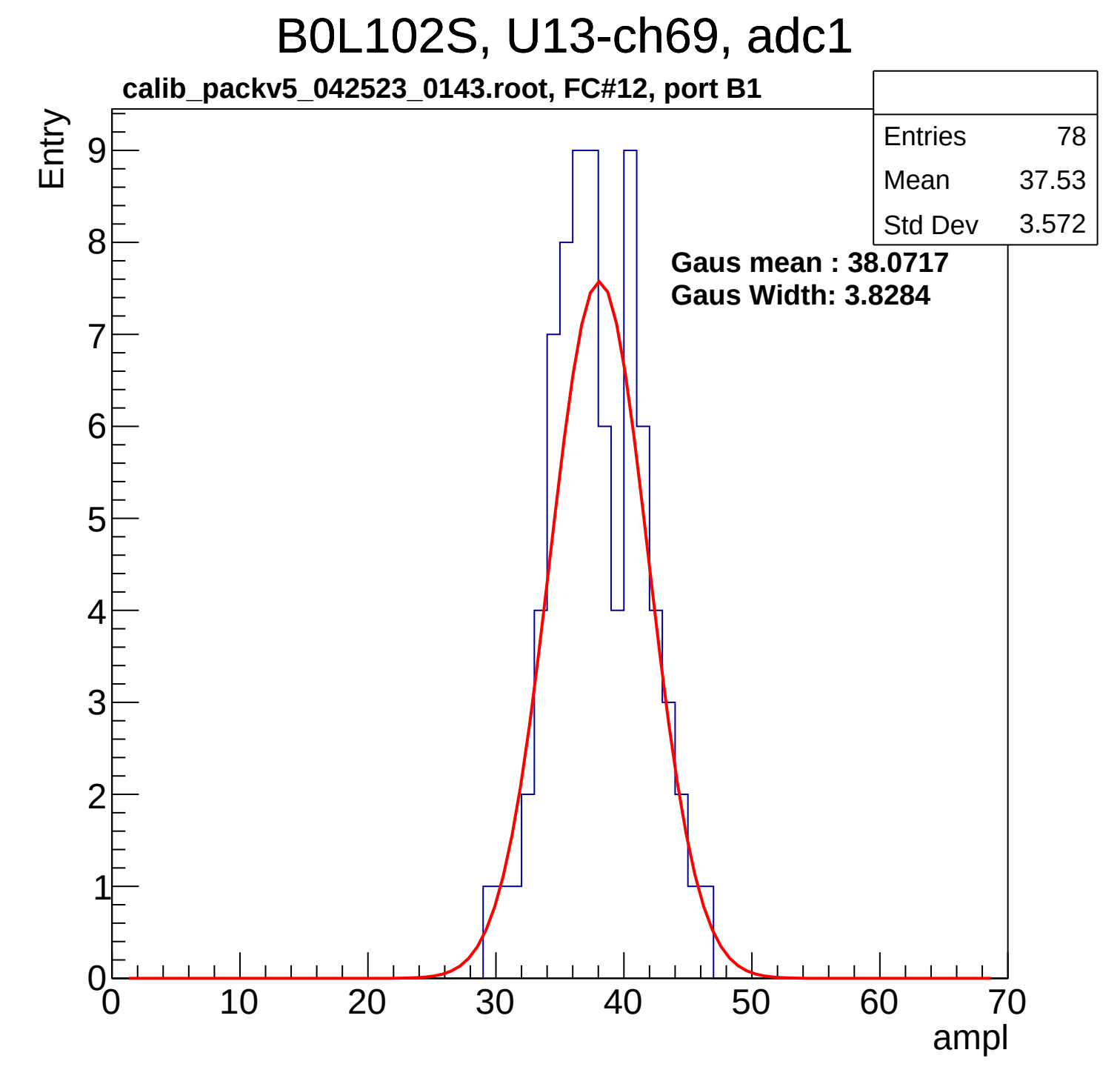
B0L102S, U13-ch69, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 37.53
Std Dev 3.572
Gaus mean : 38.0717
Gaus Width: 3.8284
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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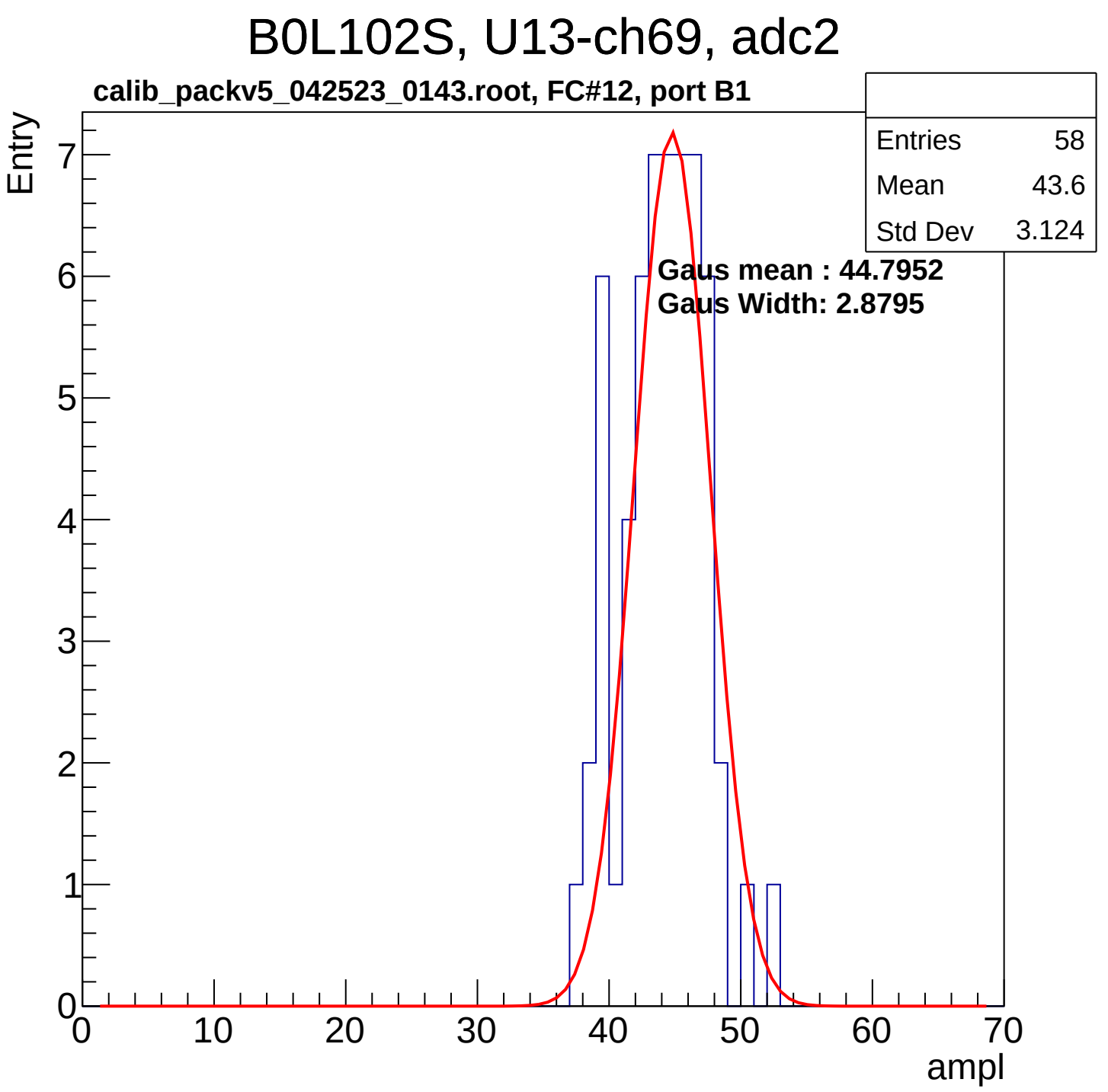
B0L102S, U13-ch69, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 43.6
Std Dev 3.124
Gaus mean : 44.7952
Gaus Width: 2.8795
Entry
ampl
0
1
2
3
4
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6
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0
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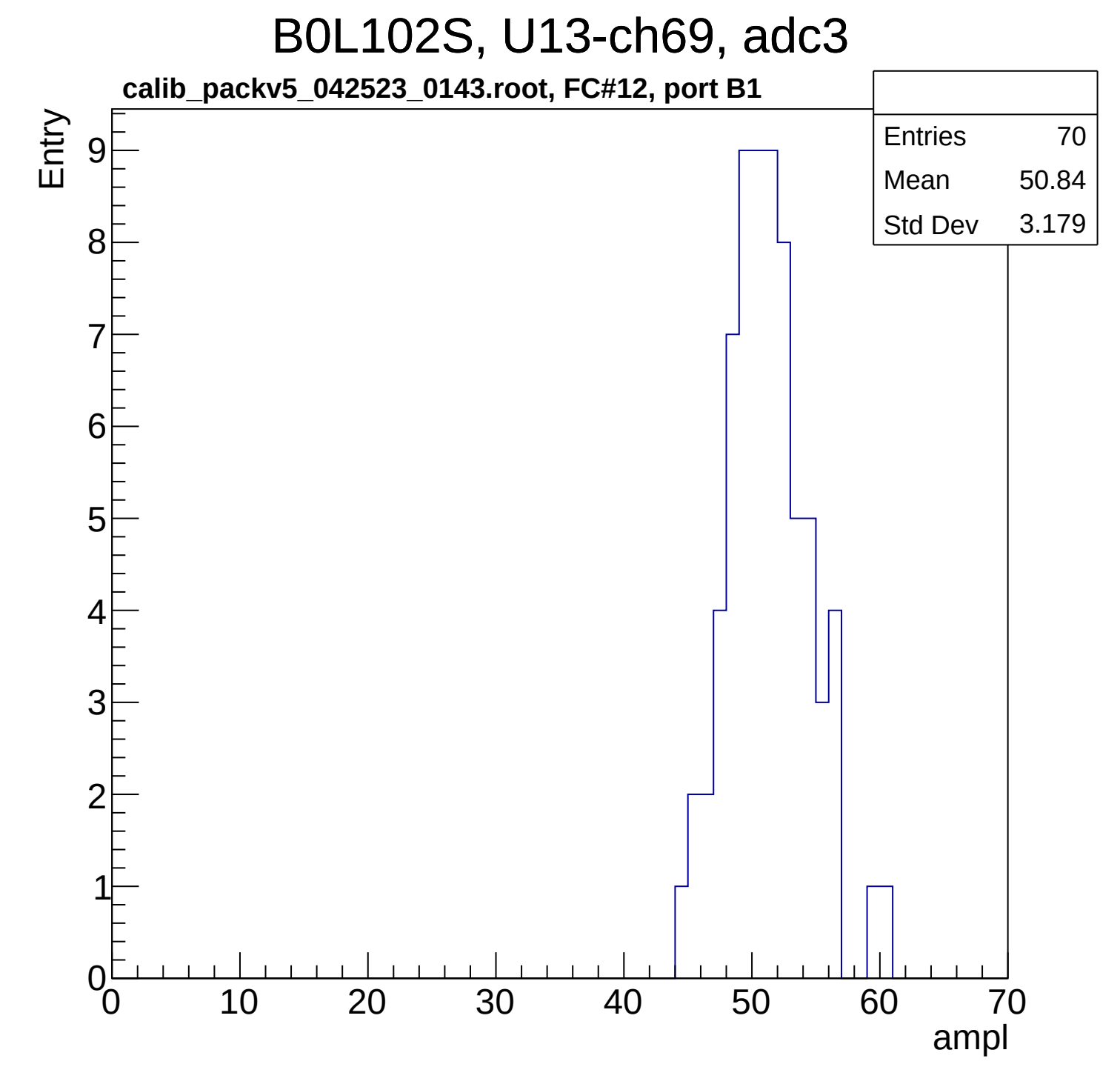
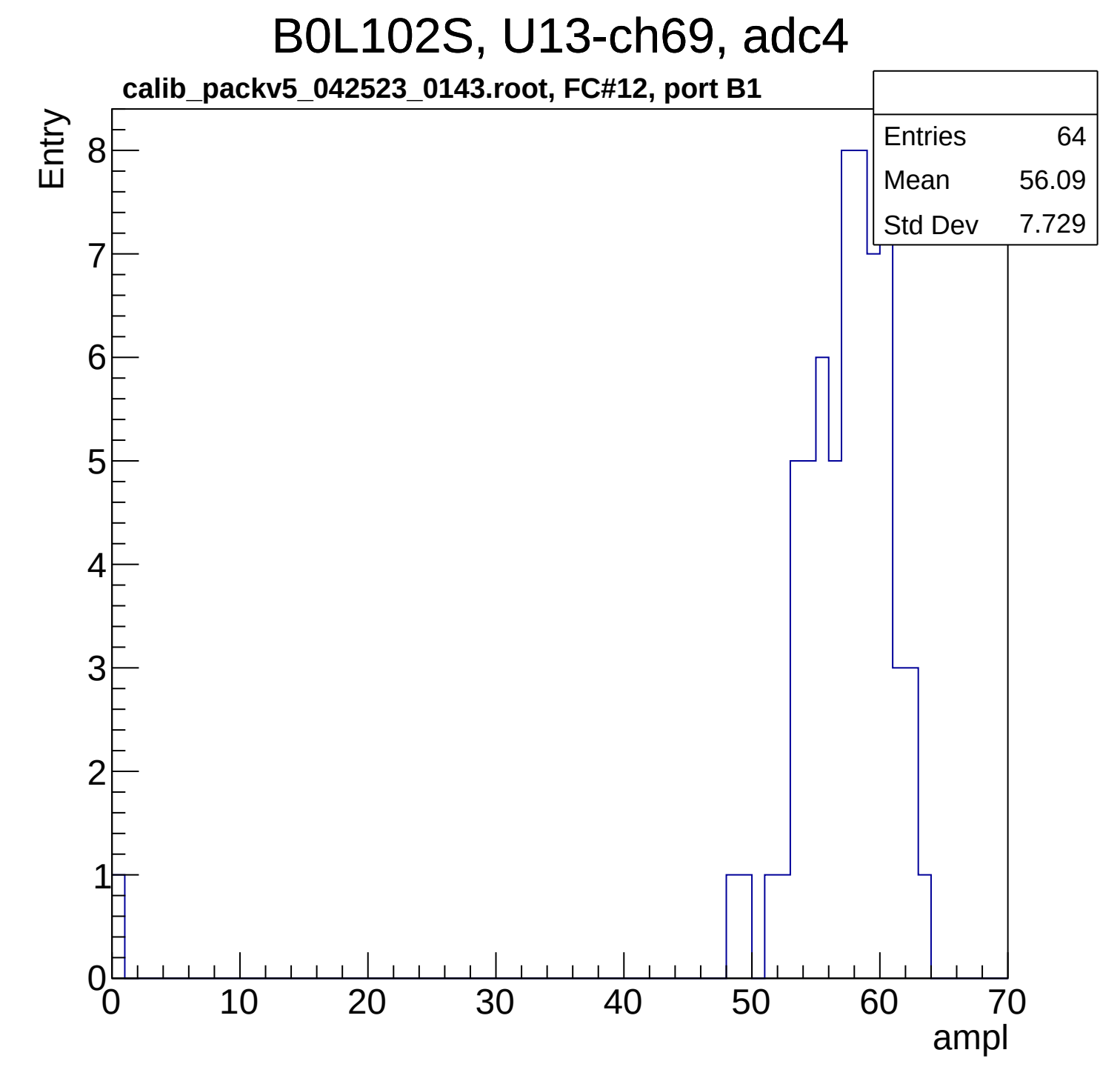

B0L102S, U13-ch69, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 50.84
Std Dev 3.179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch69, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.09
Std Dev 7.729
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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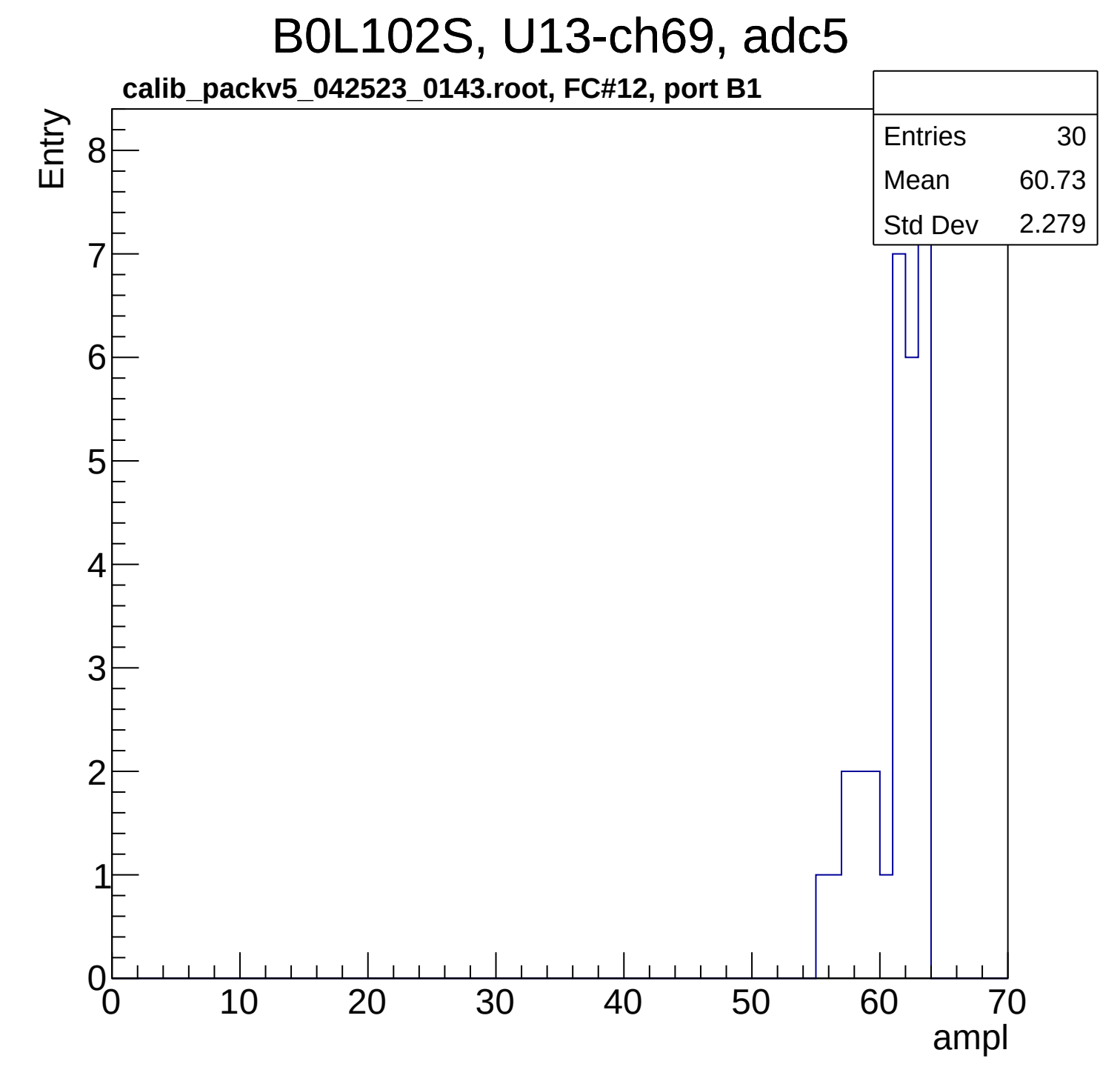
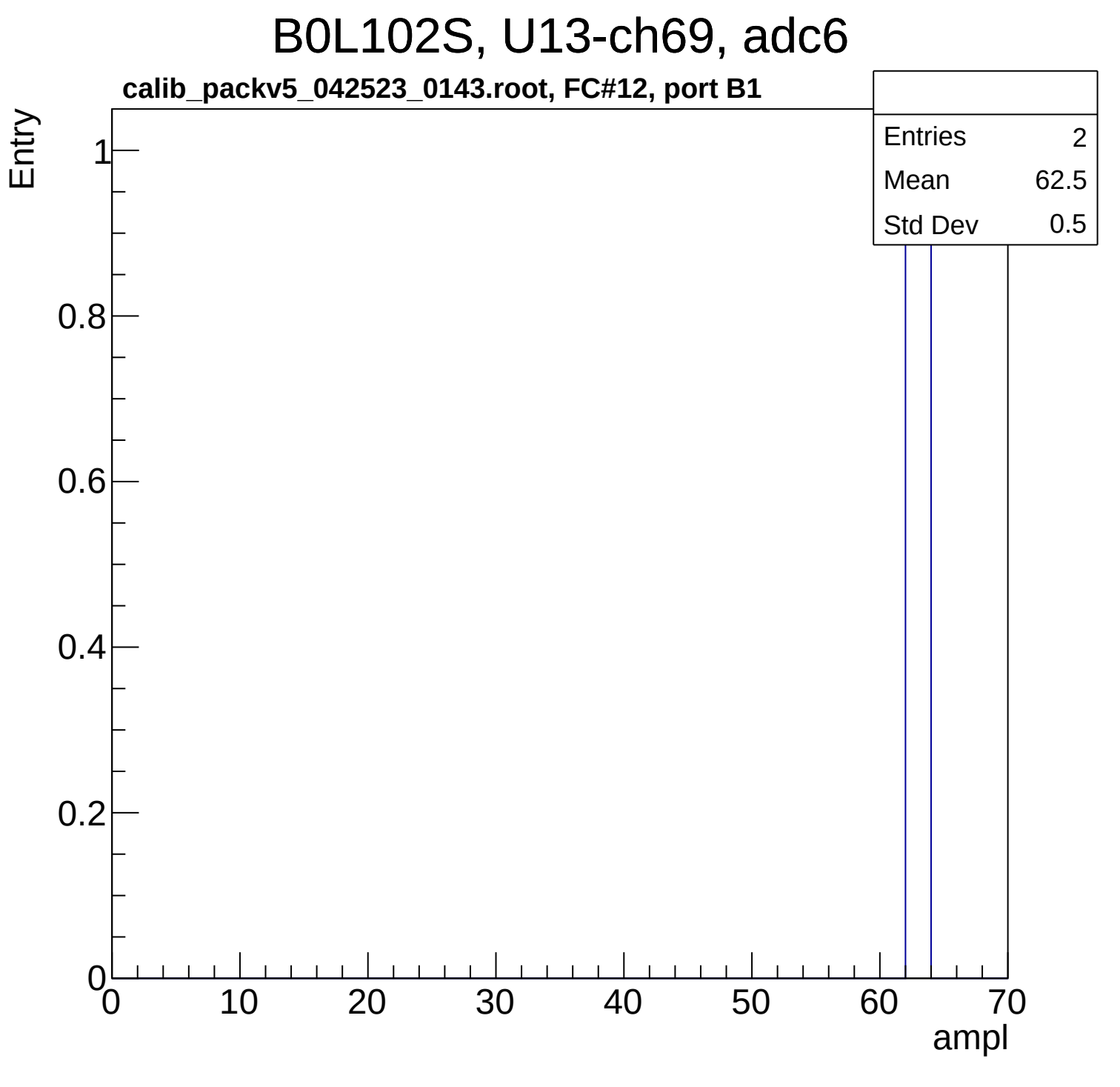
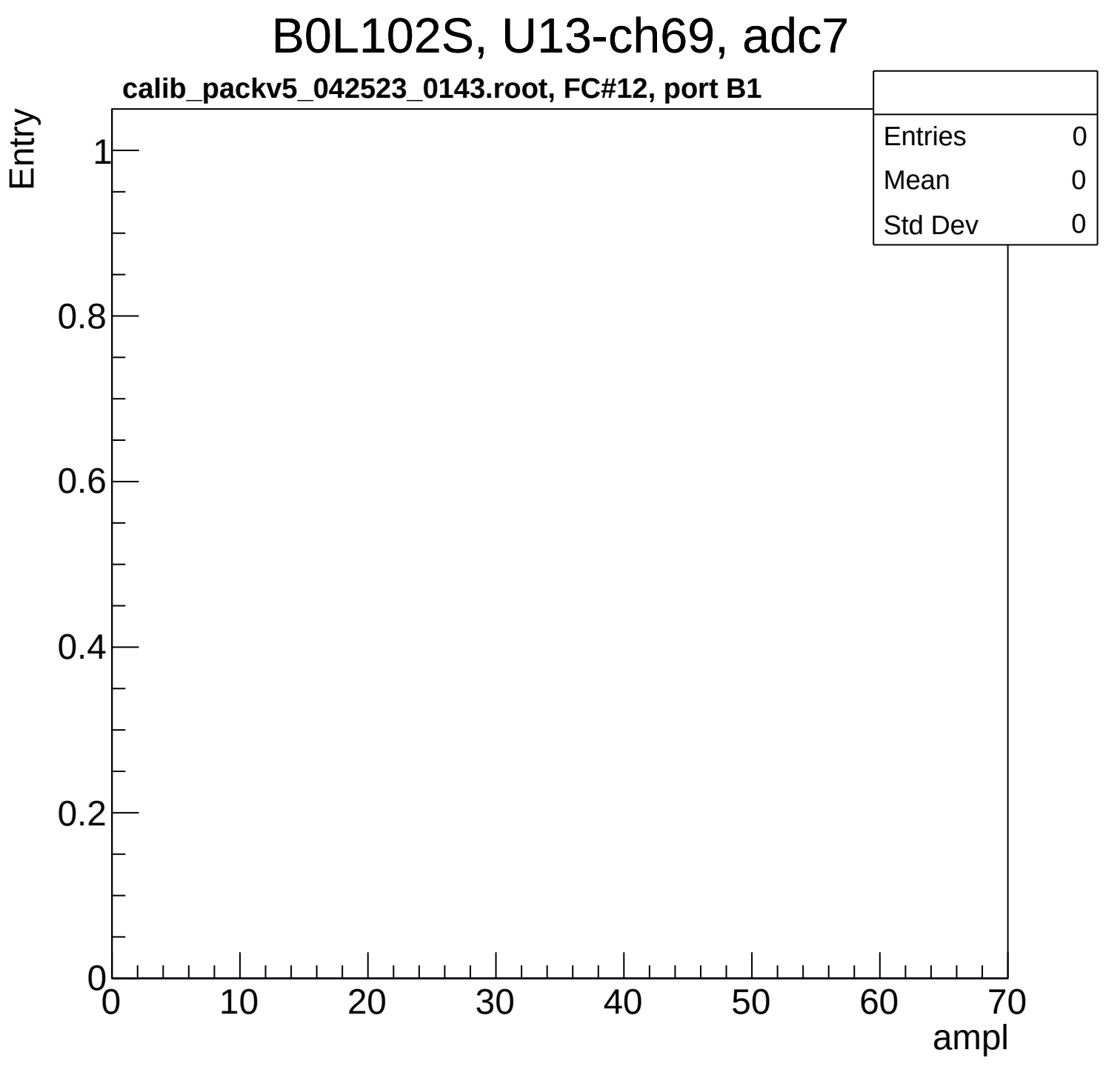
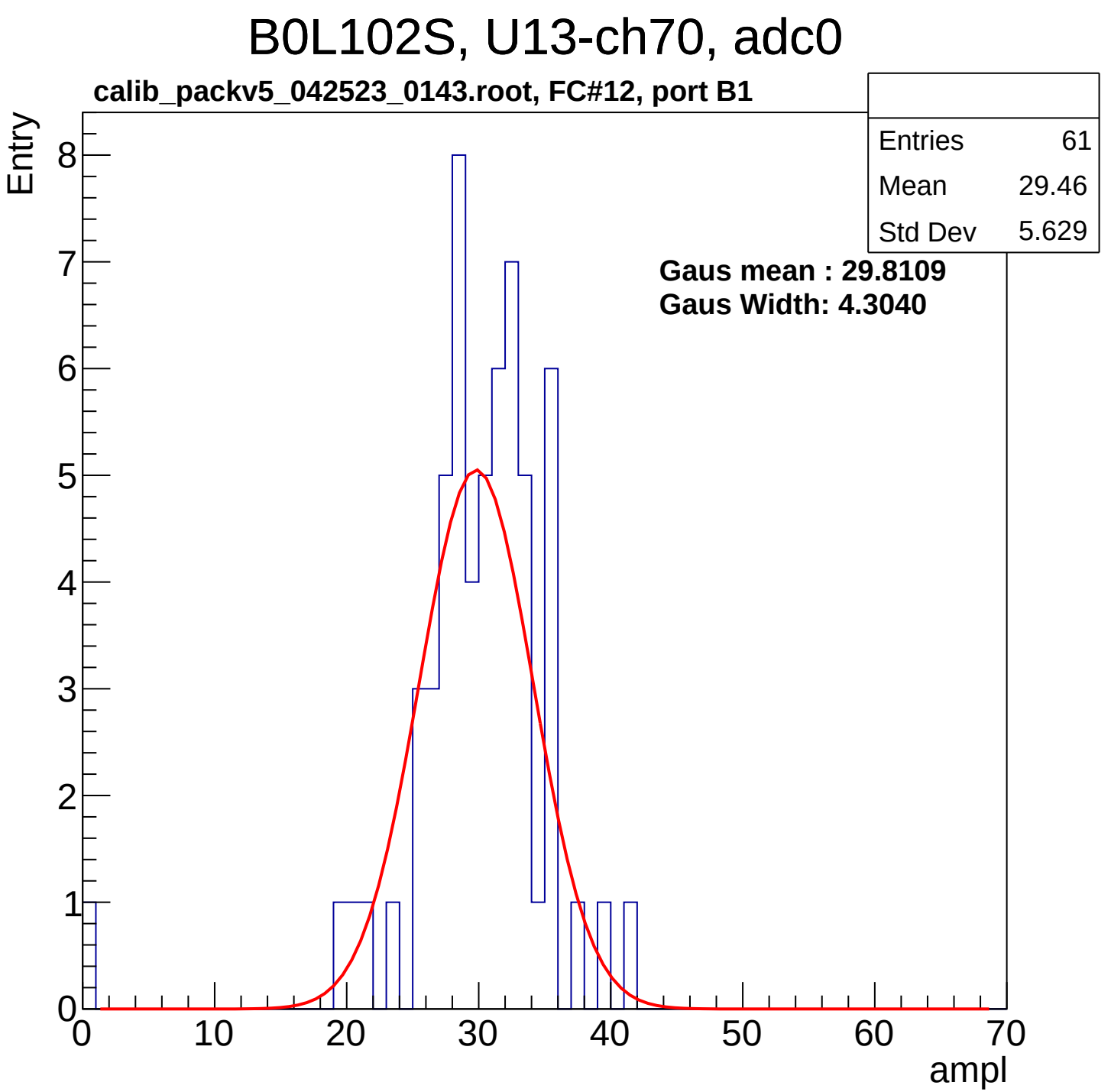
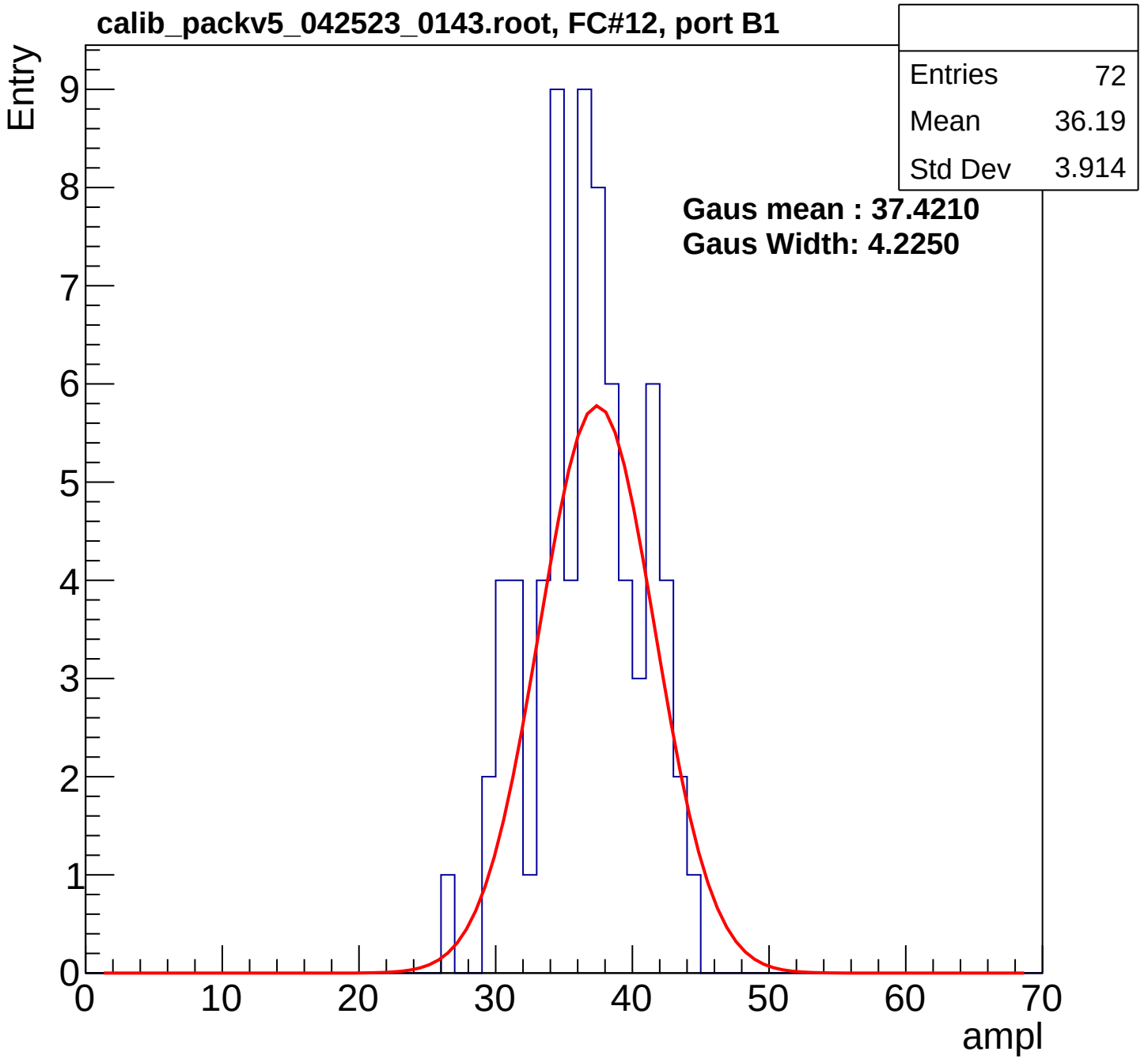

B0L102S, U13-ch69, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 60.73
Std Dev 2.279
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch69, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch69, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch70, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 29.46
Std Dev 5.629
Gaus mean : 29.8109
Gaus Width: 4.3040
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch70, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.19
Std Dev 3.914
Gaus mean : 37.4210
Gaus Width: 4.2250
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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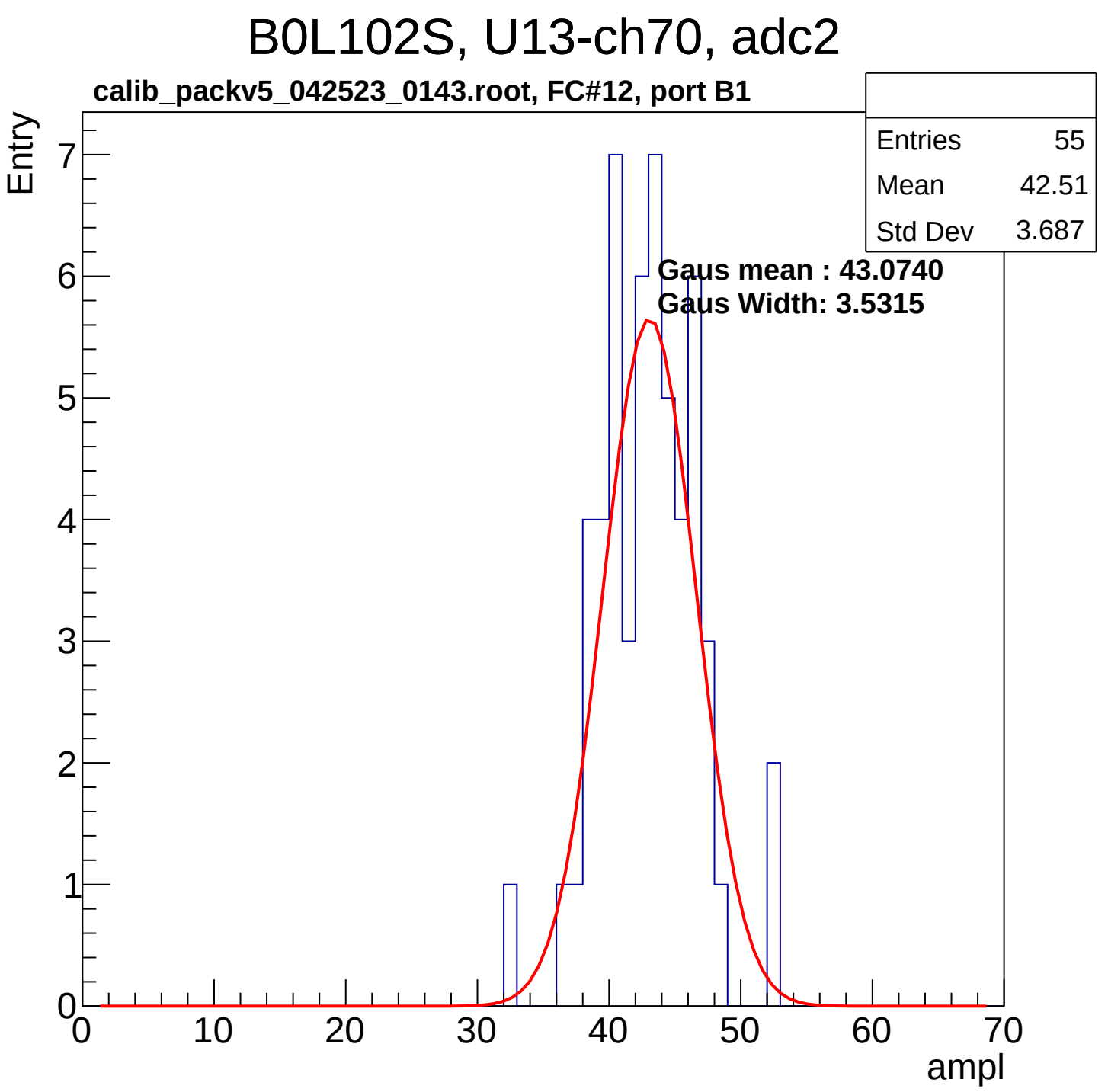
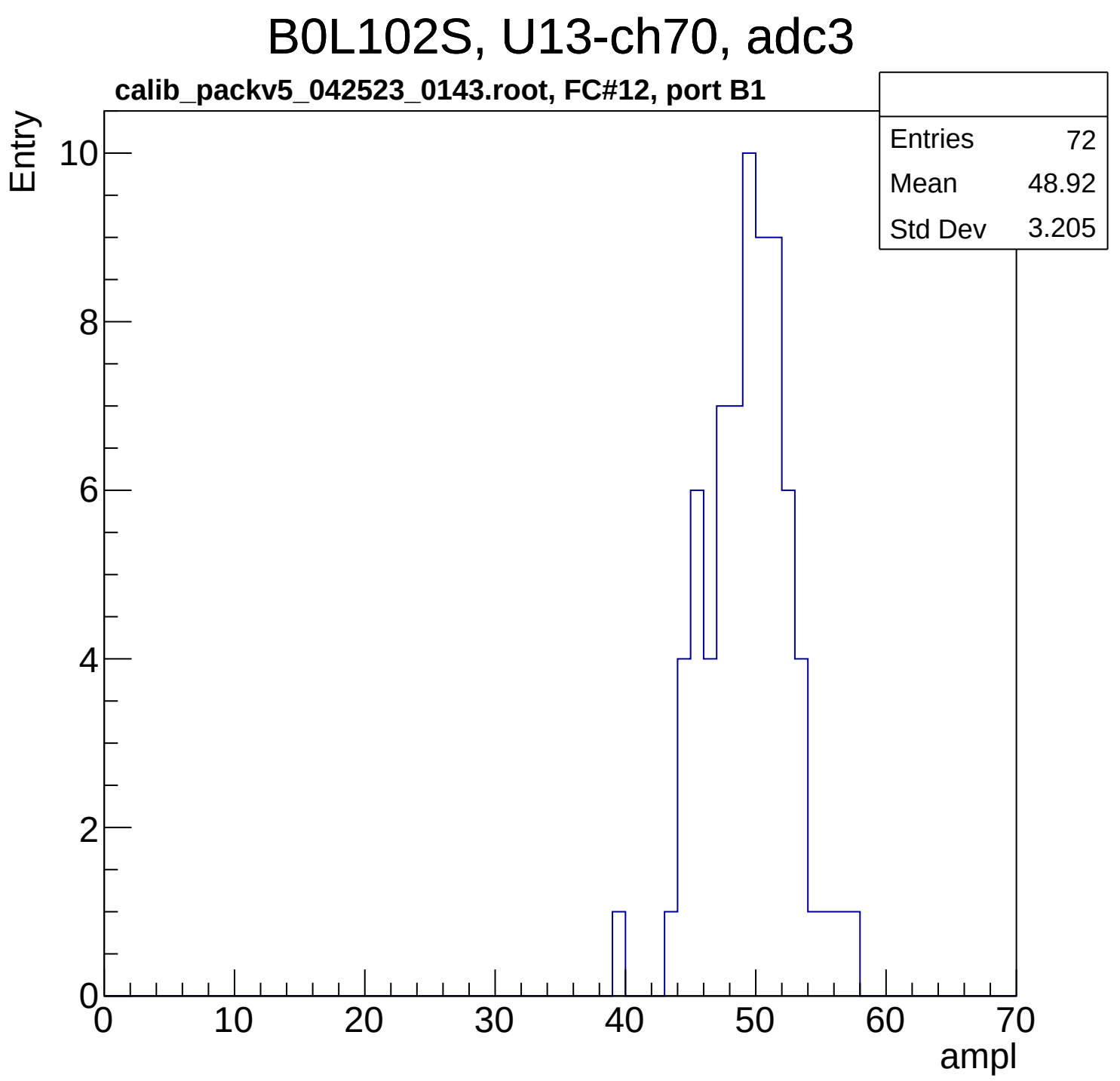
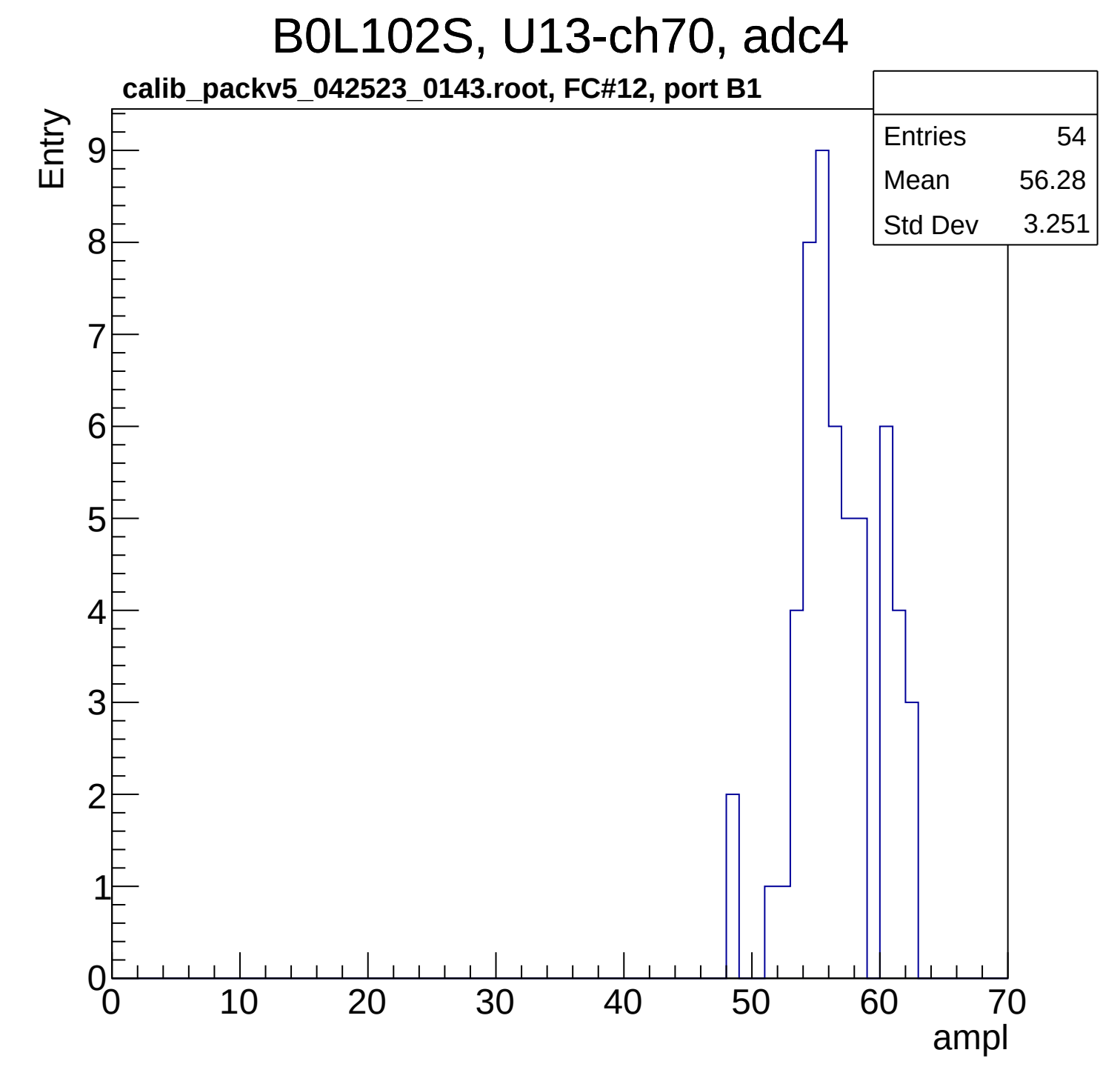
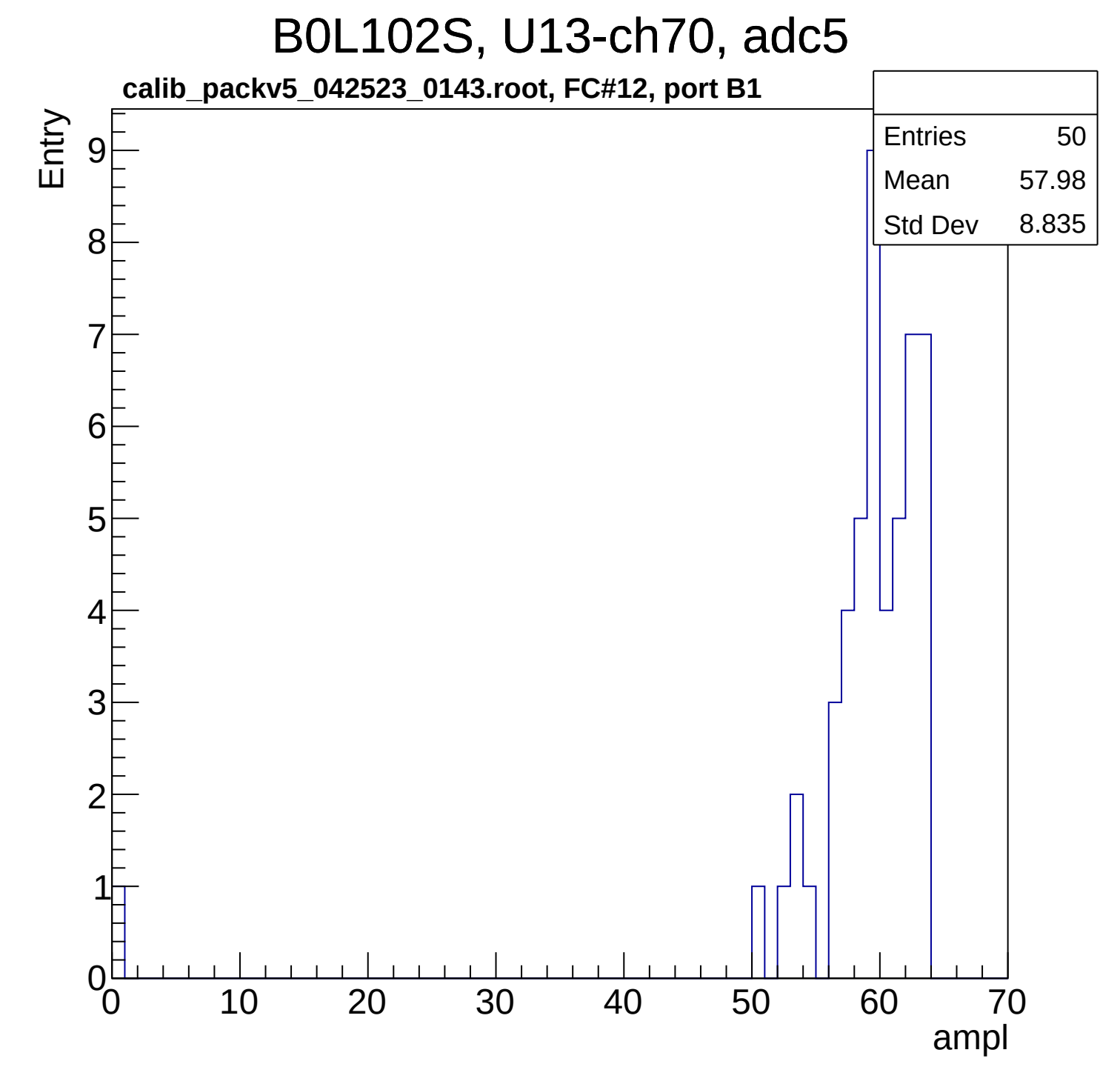
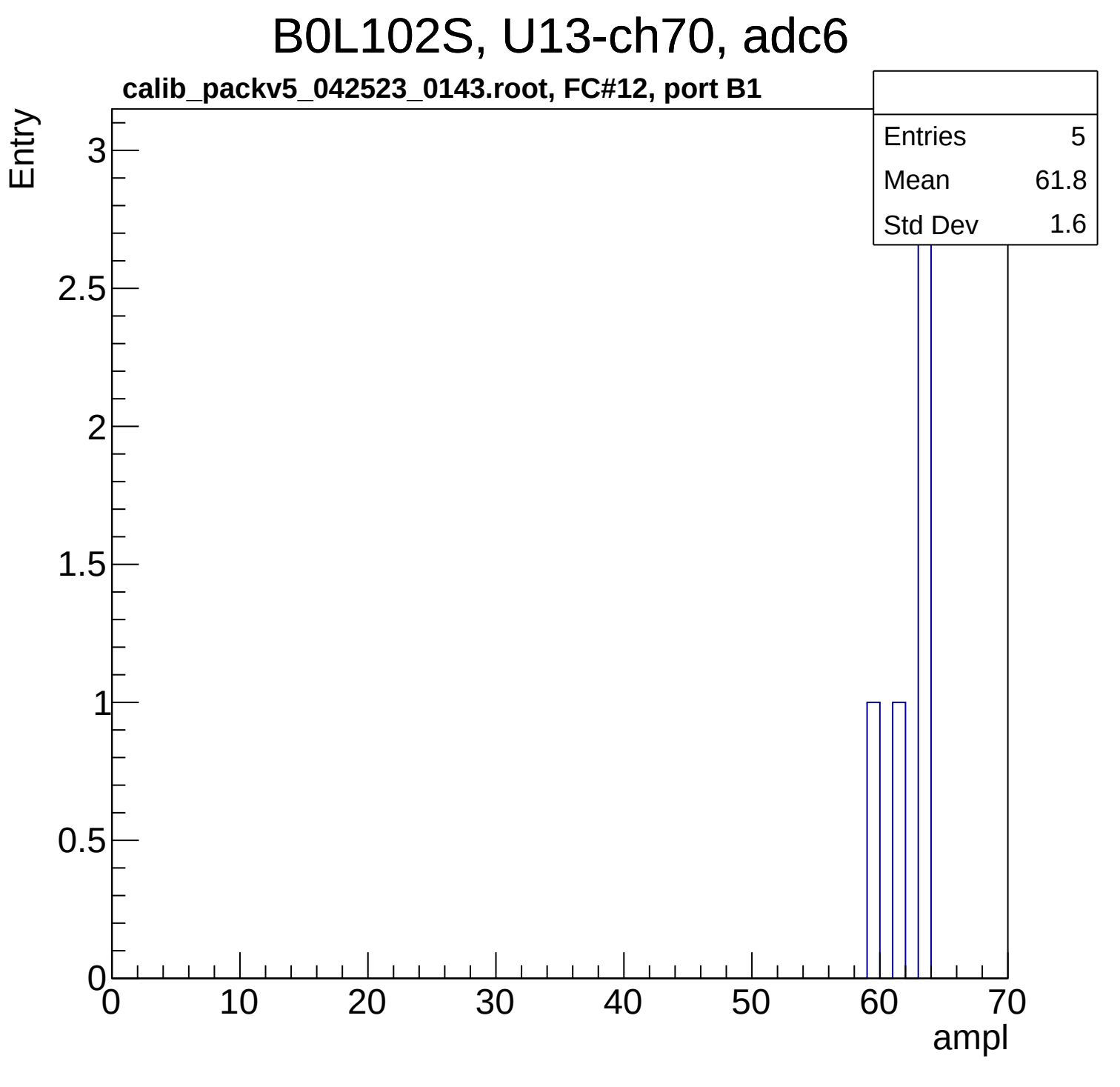
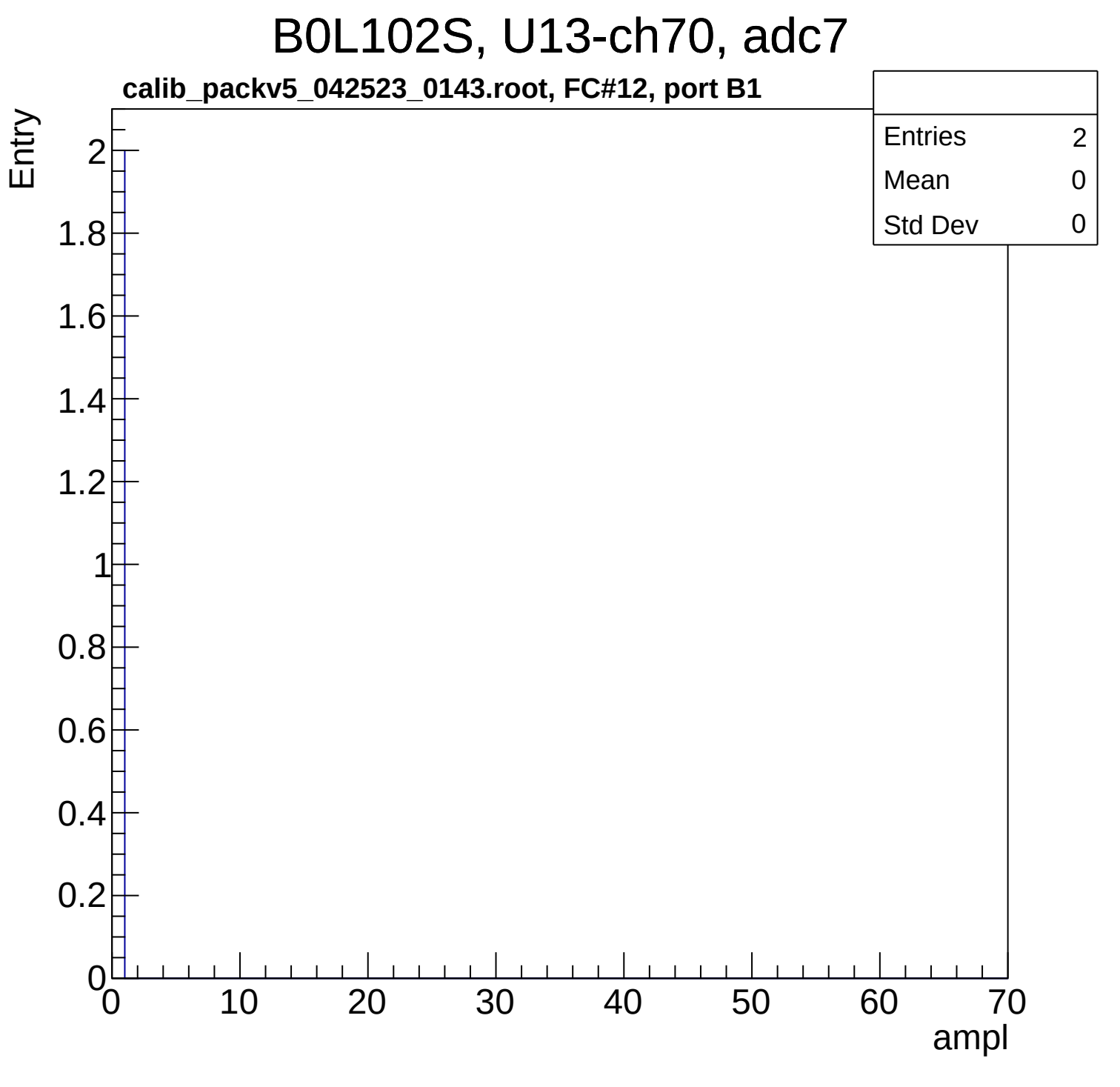

B0L102S, U13-ch70, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 42.51
Std Dev 3.687
Gaus mean : 43.0740
Gaus Width: 3.5315
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

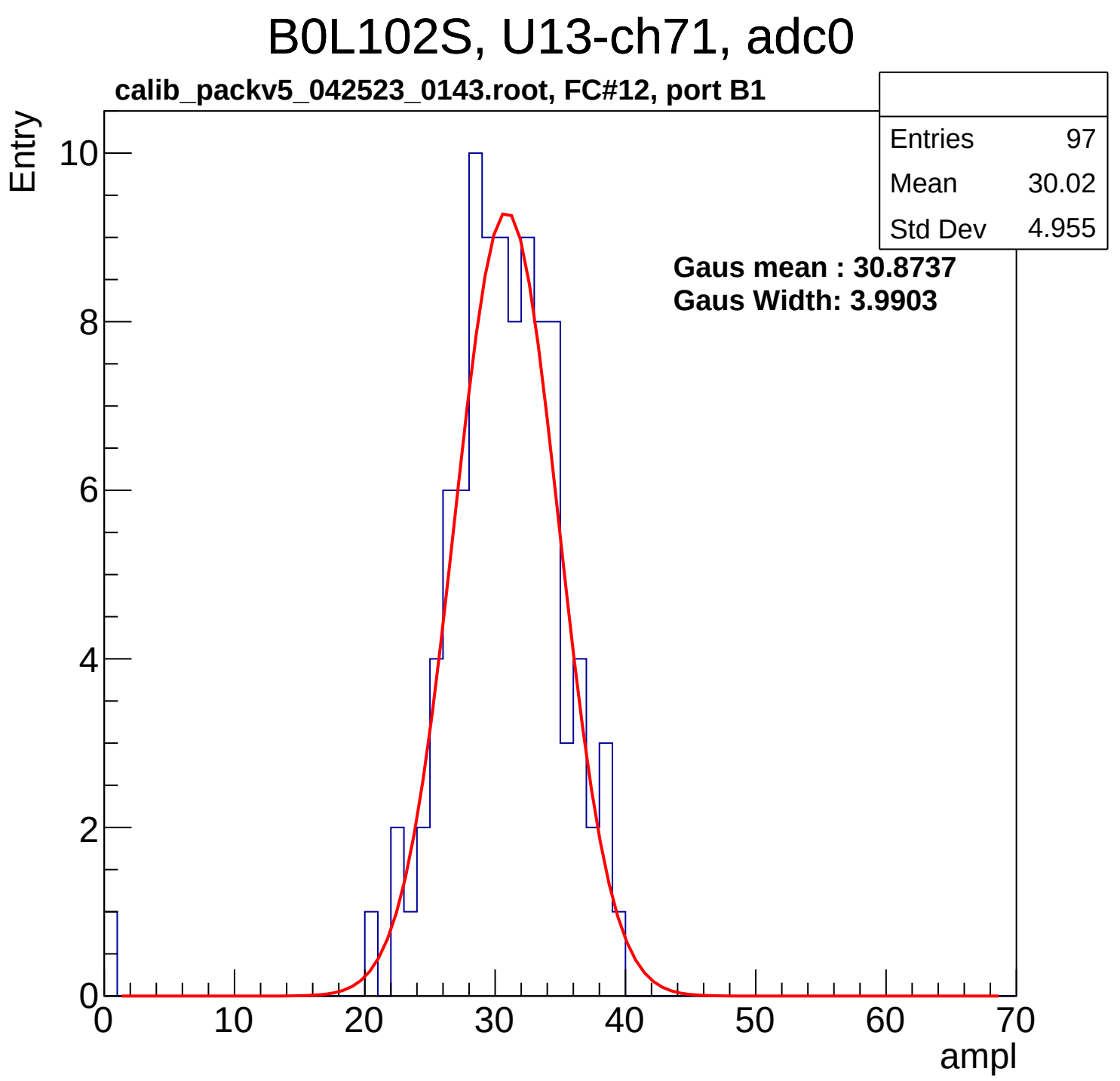

B0L102S, U13-ch70, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.92
Std Dev 3.205
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

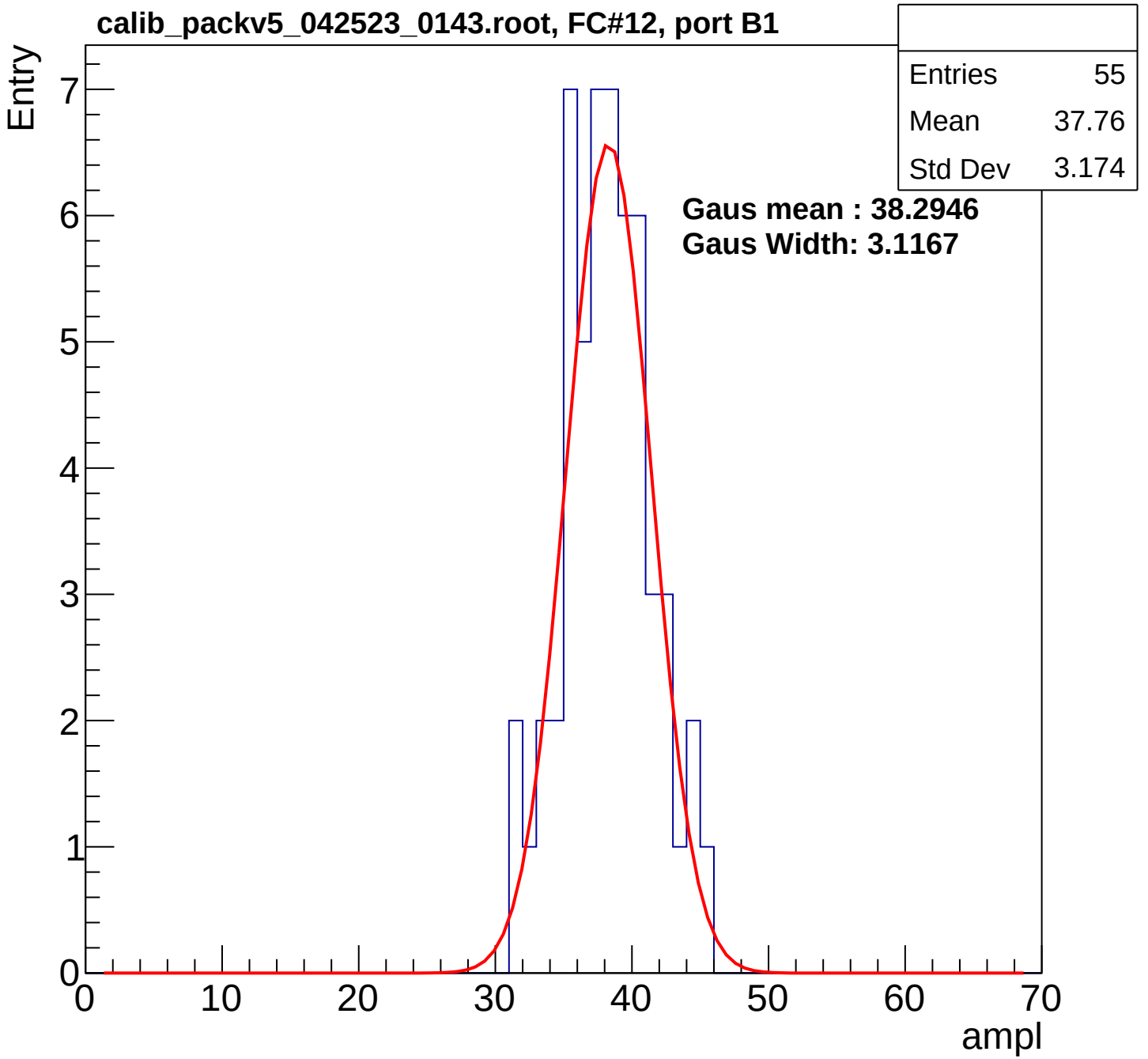

B0L102S, U13-ch70, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.28
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

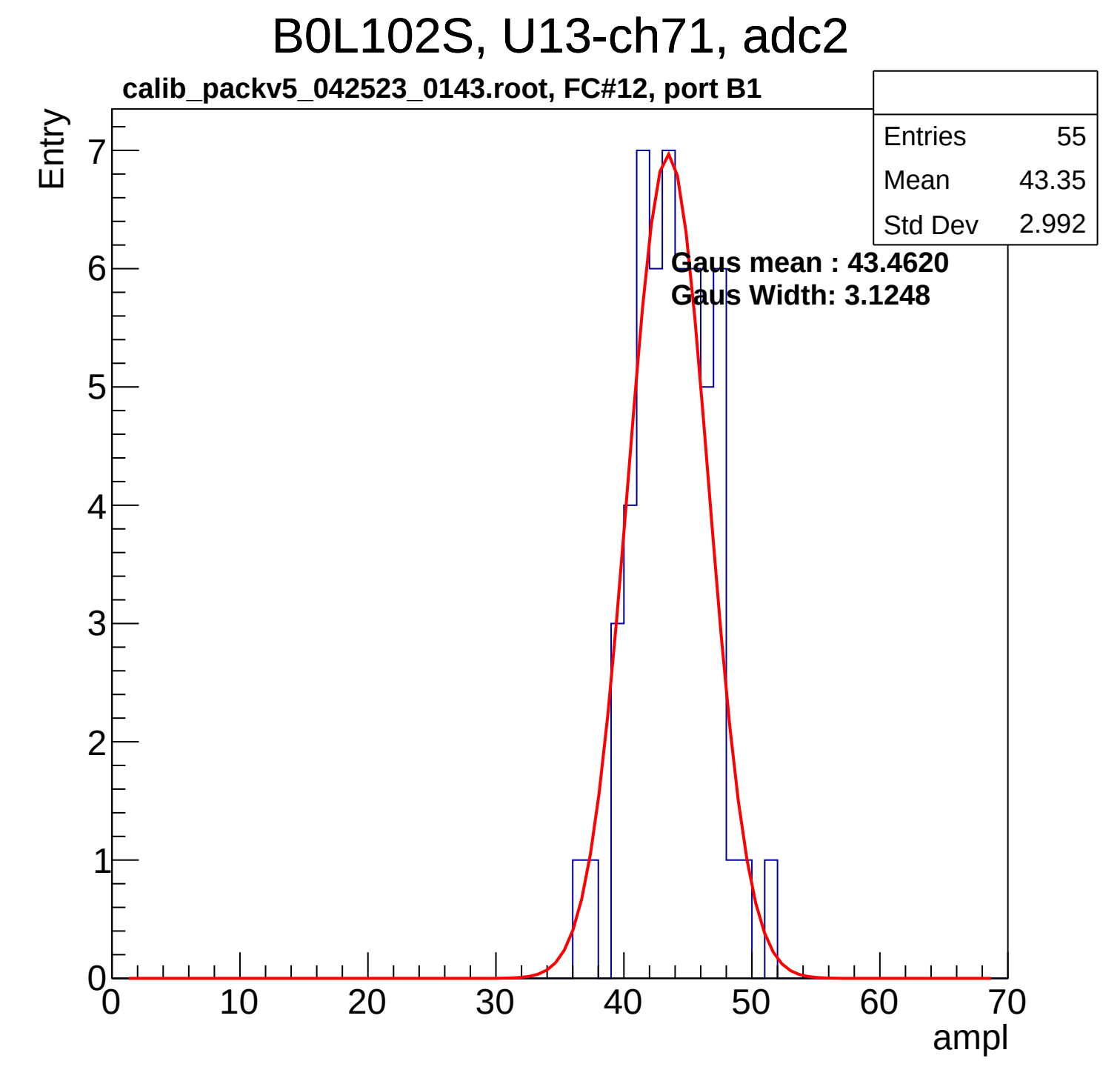

B0L102S, U13-ch70, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 57.98
Std Dev 8.835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

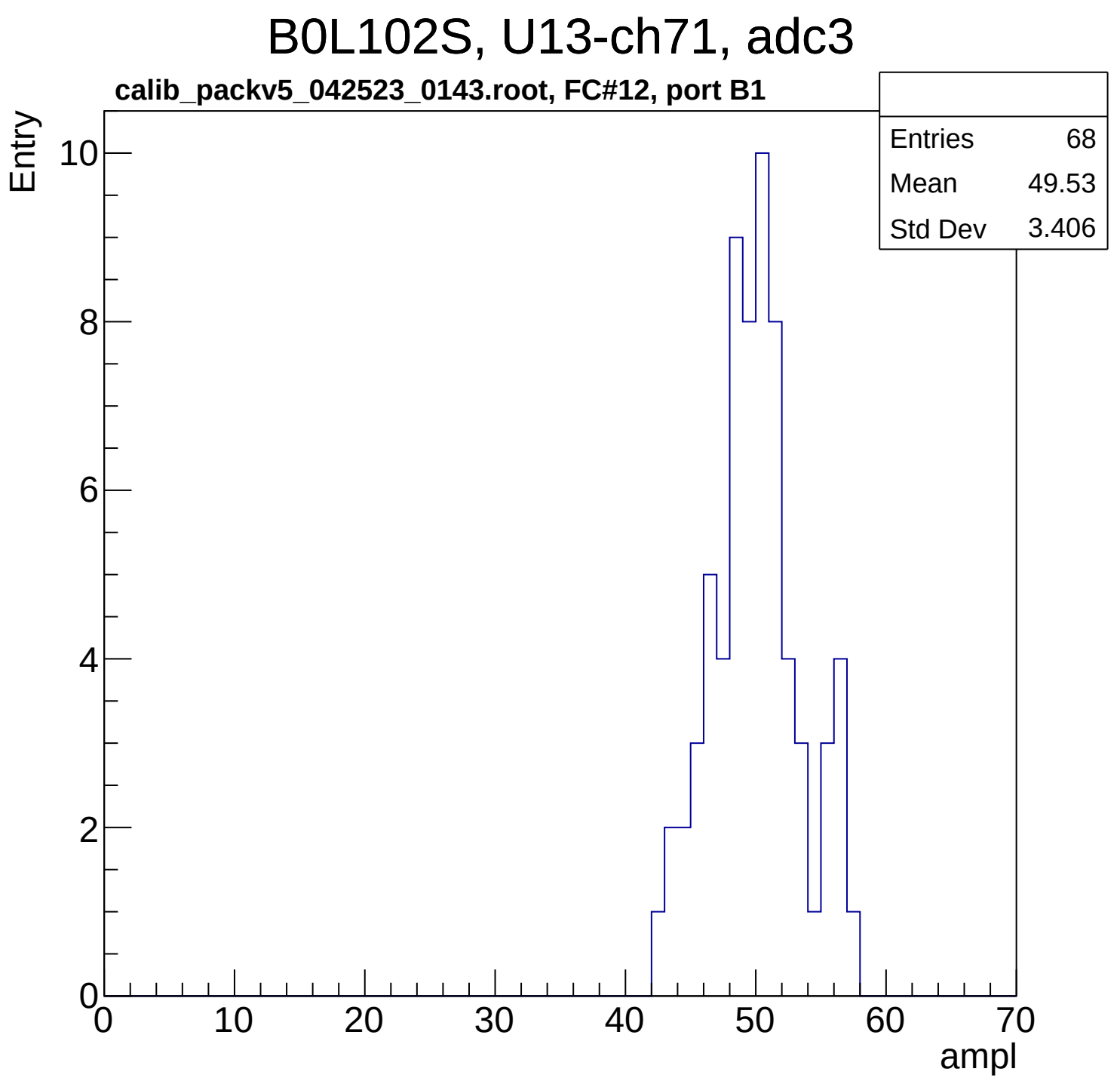

B0L102S, U13-ch70, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 1.6
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

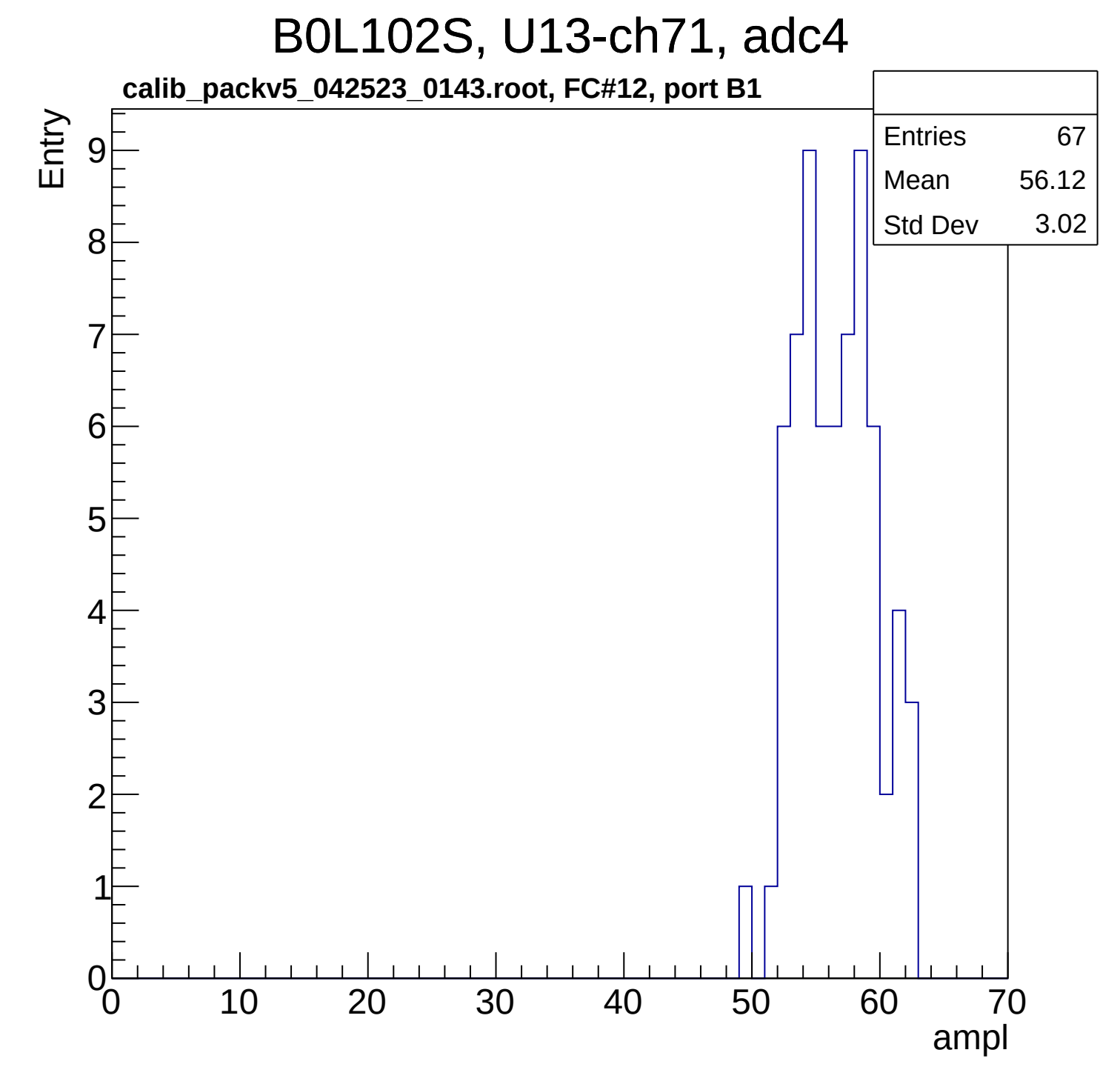

B0L102S, U13-ch70, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

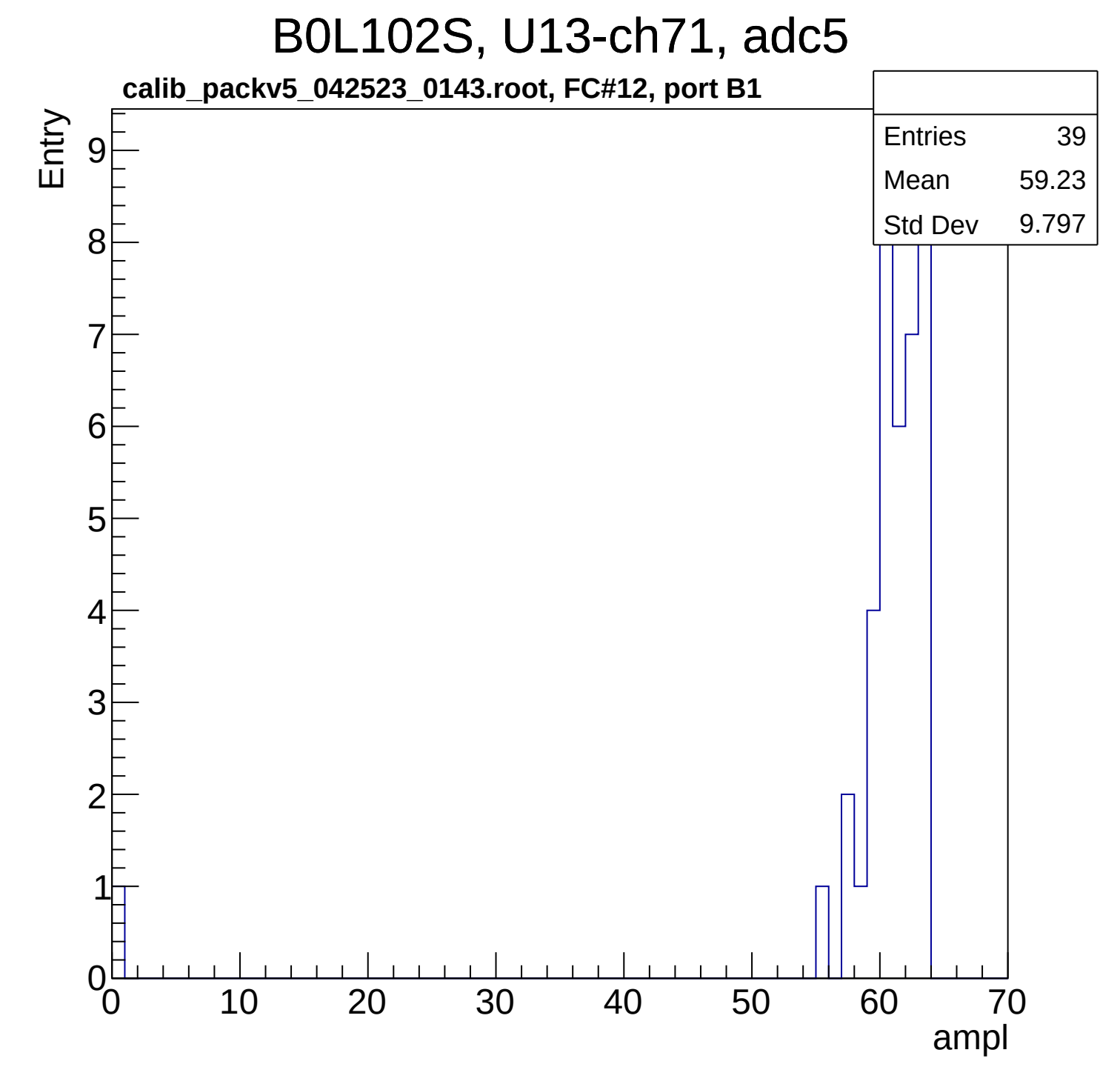

B0L102S, U13-ch71, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 97
Mean 30.02
Std Dev 4.955
Gaus mean : 30.8737
Gaus Width: 3.9903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

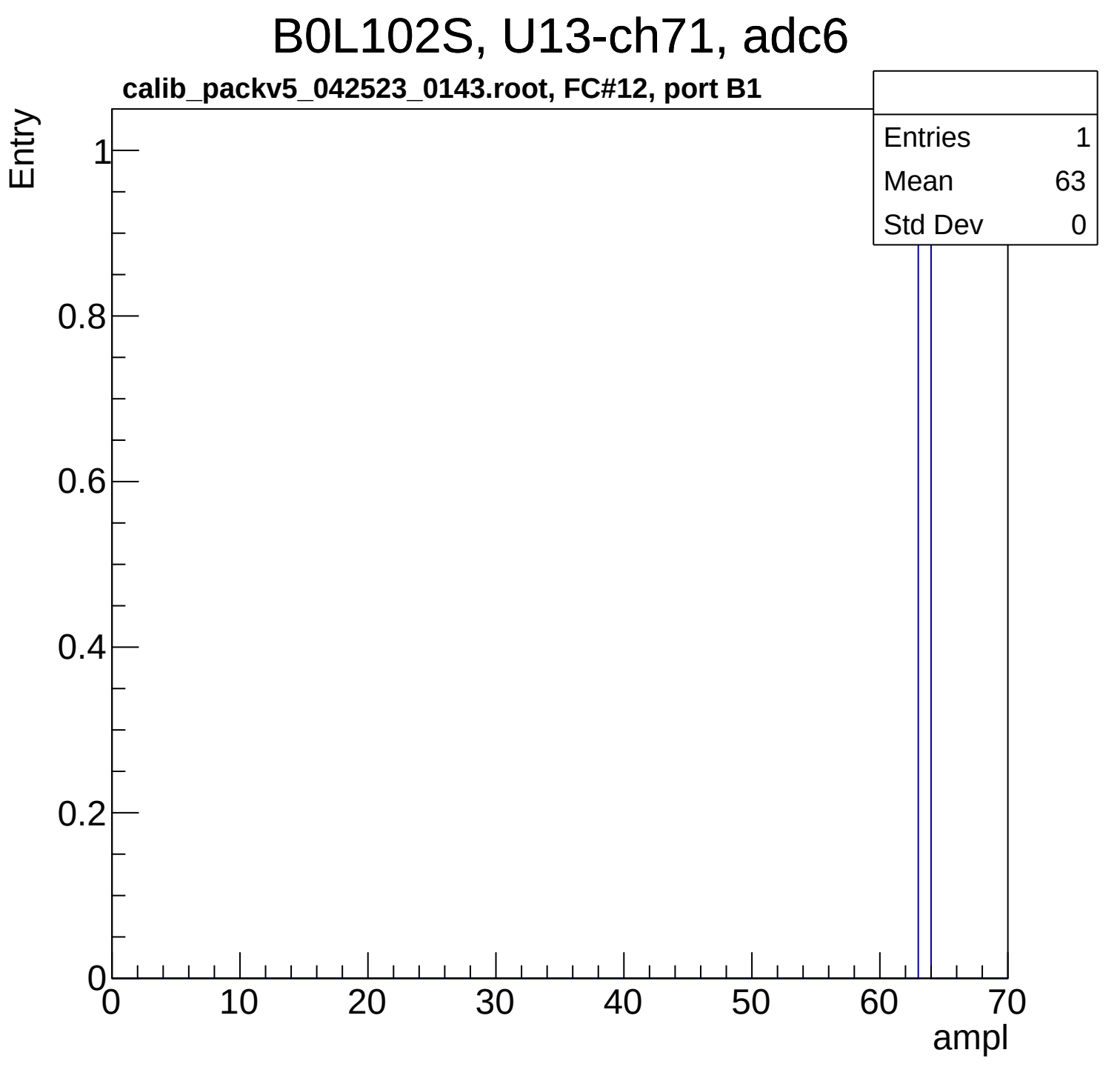

B0L102S, U13-ch71, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 37.76
Std Dev 3.174
Gaus mean : 38.2946
Gaus Width: 3.1167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

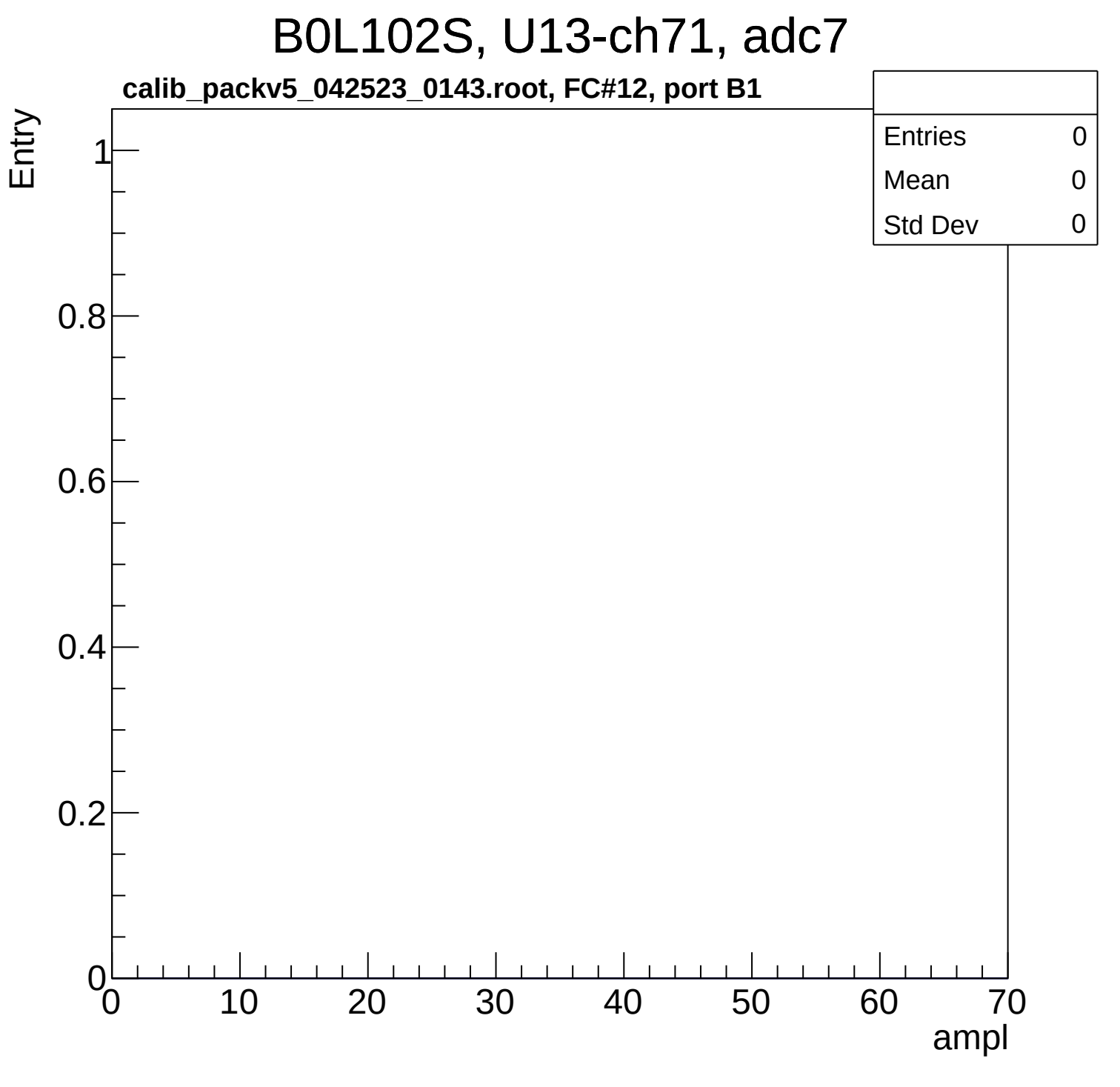

B0L102S, U13-ch71, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 43.35
Std Dev 2.992
Gaus mean : 43.4620
Gaus Width: 3.1248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

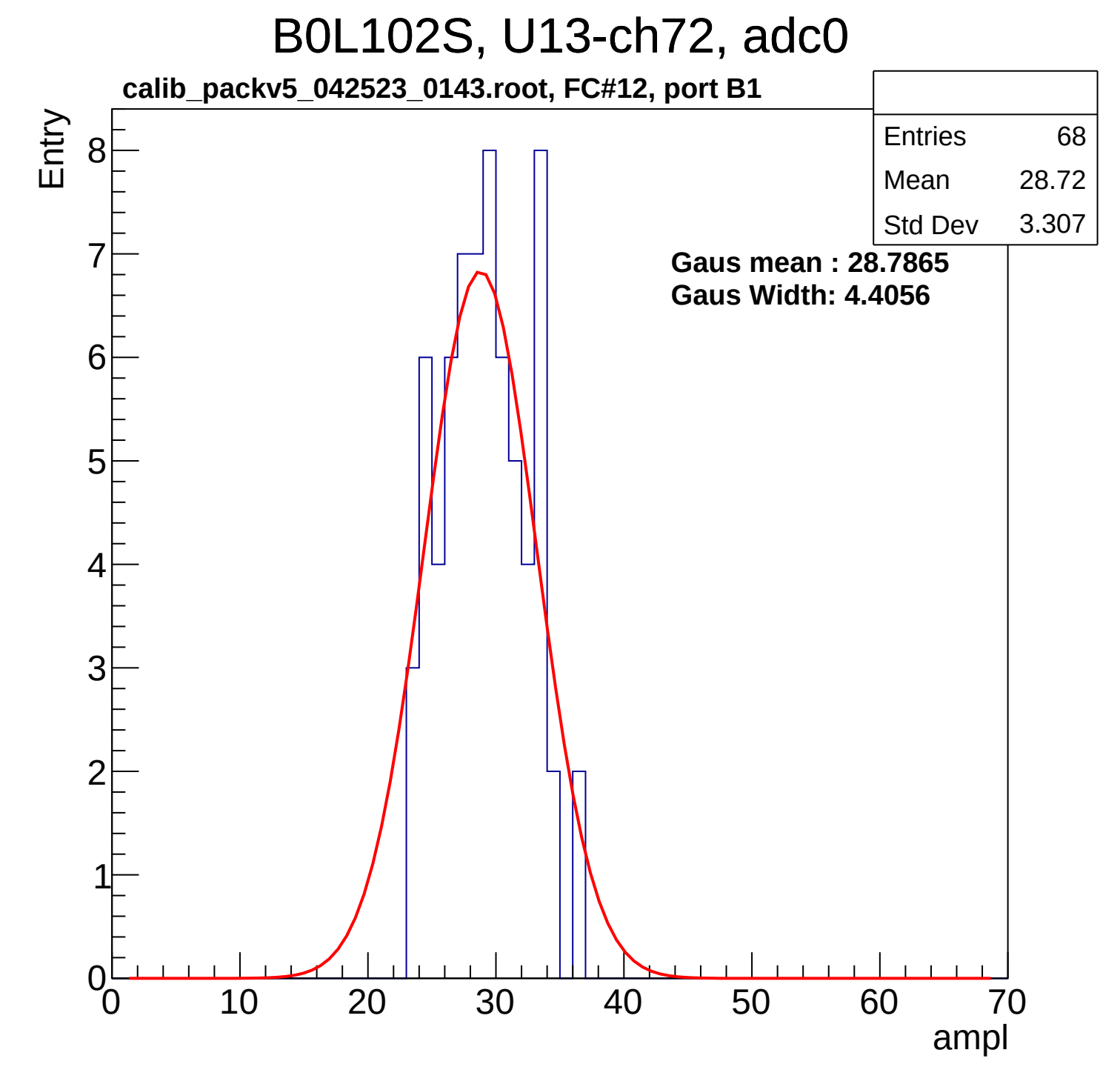

B0L102S, U13-ch71, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.53
Std Dev 3.406
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

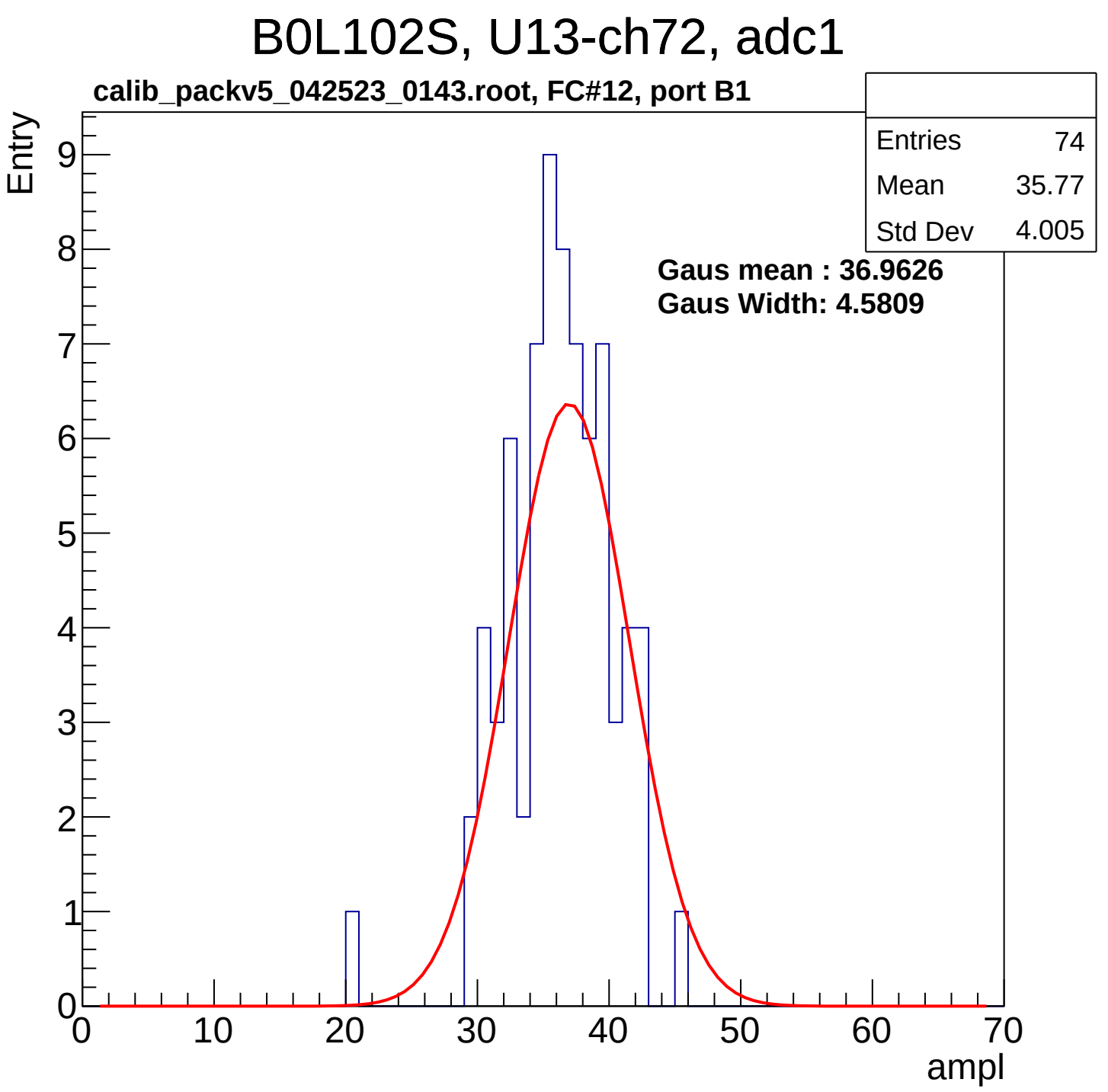

B0L102S, U13-ch71, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 56.12
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

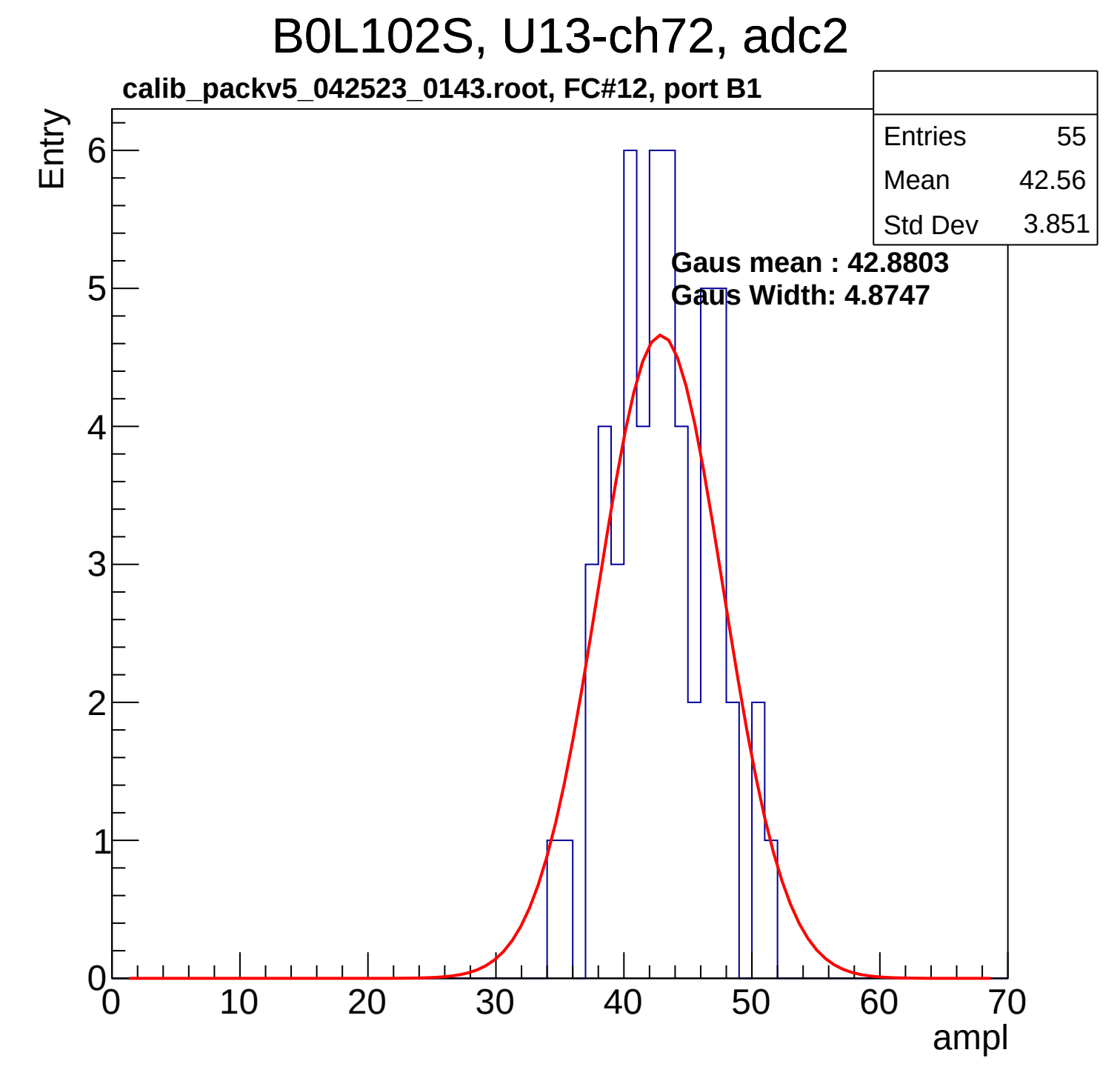

B0L102S, U13-ch71, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 59.23
Std Dev 9.797
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

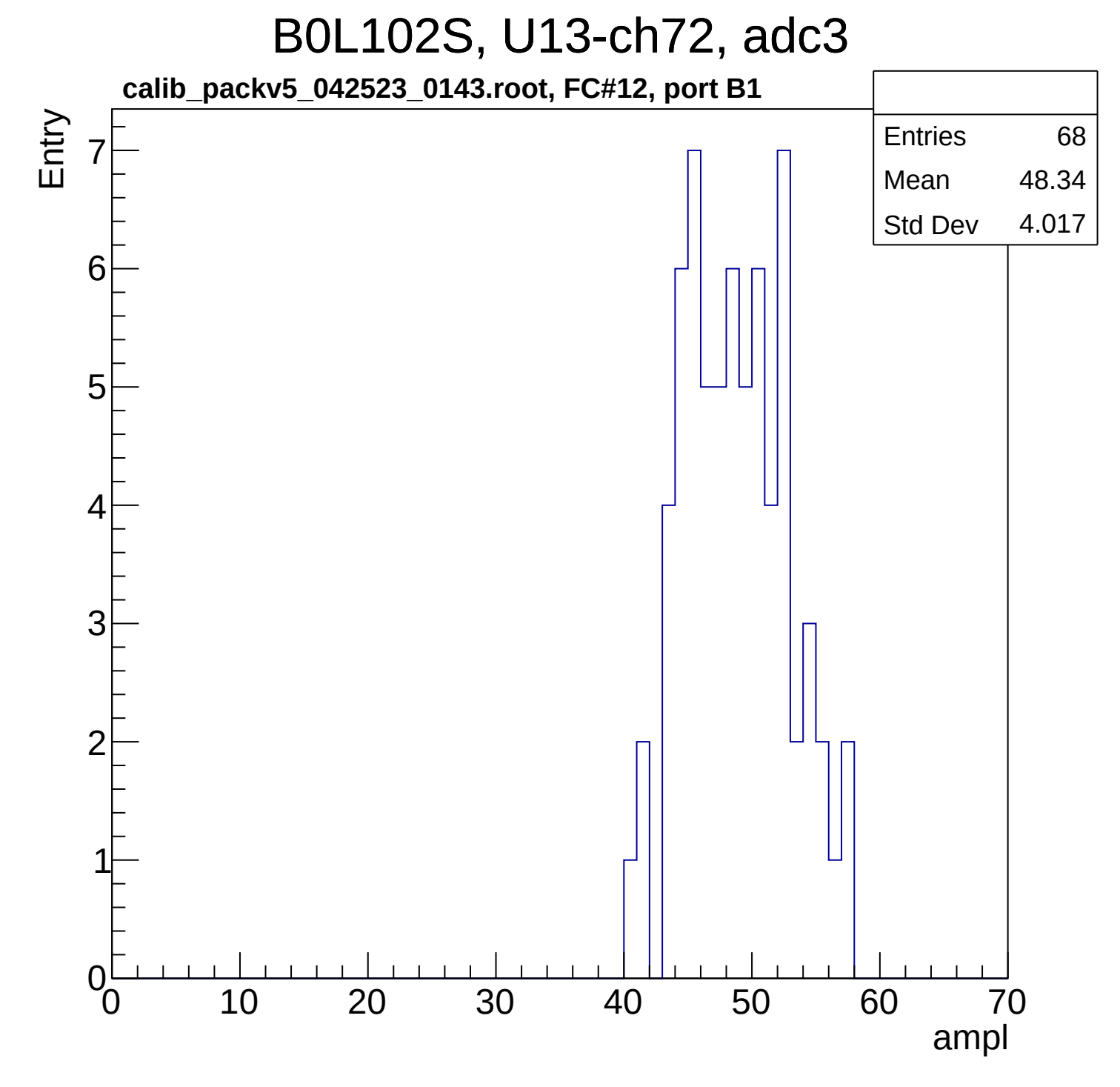

B0L102S, U13-ch71, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

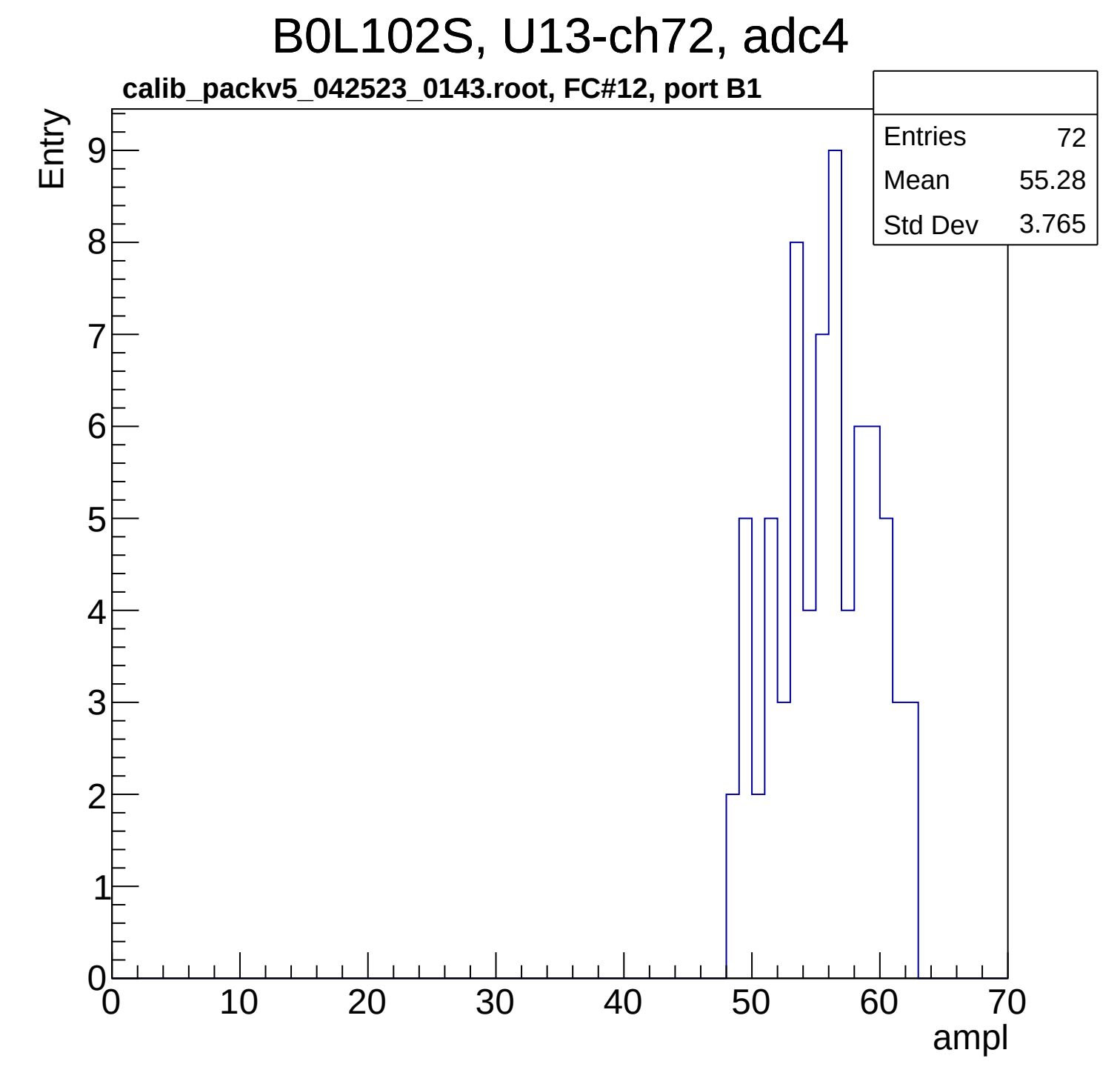

B0L102S, U13-ch71, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch72, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.72
Std Dev 3.307
Gaus mean : 28.7865
Gaus Width: 4.4056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch72, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 35.77
Std Dev 4.005
Gaus mean : 36.9626
Gaus Width: 4.5809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch72, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 42.56
Std Dev 3.851
Gaus mean : 42.8803
Gaus Width: 4.8747
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch72, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.34
Std Dev 4.017
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch72, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 55.28
Std Dev 3.765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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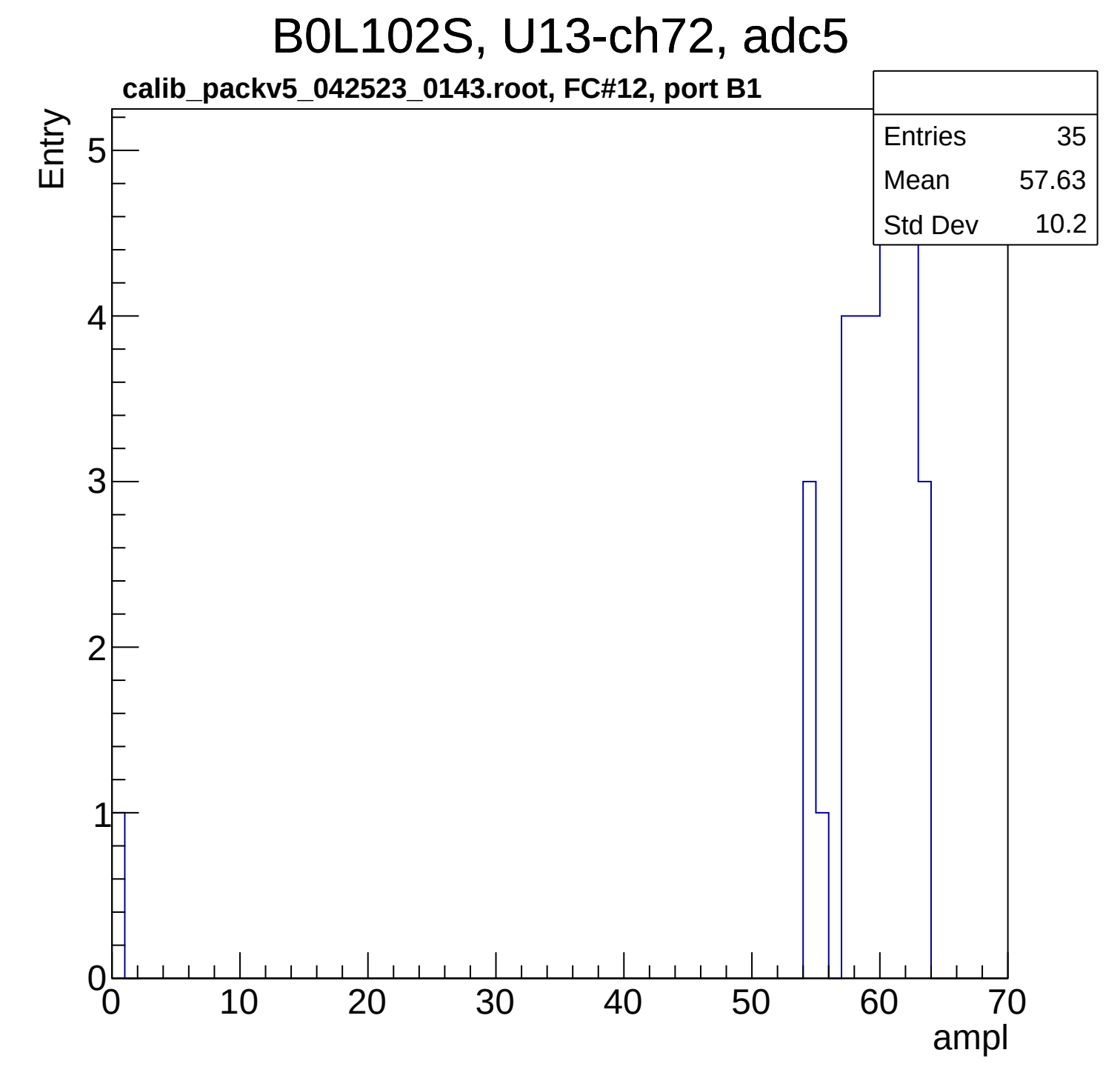
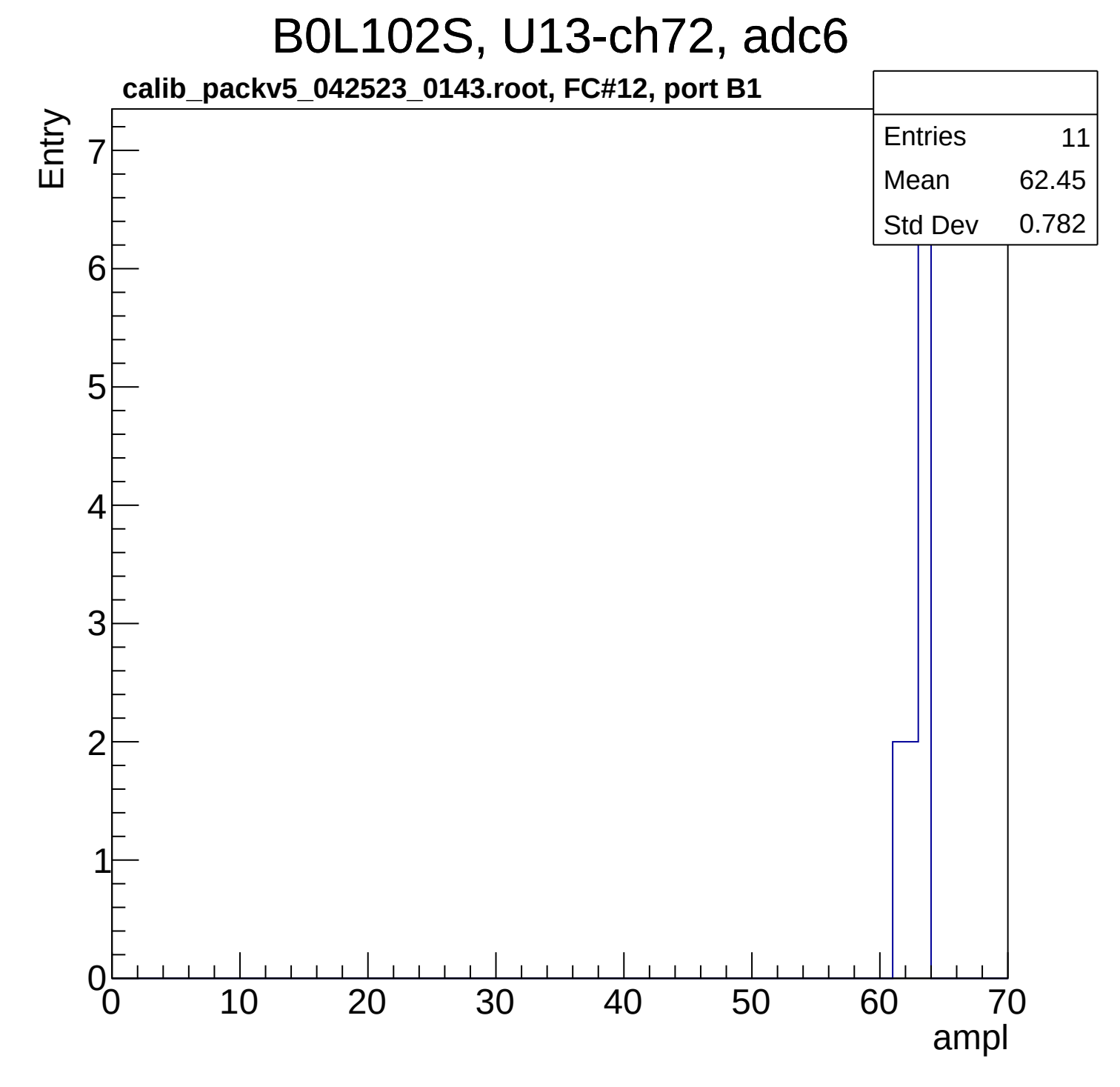
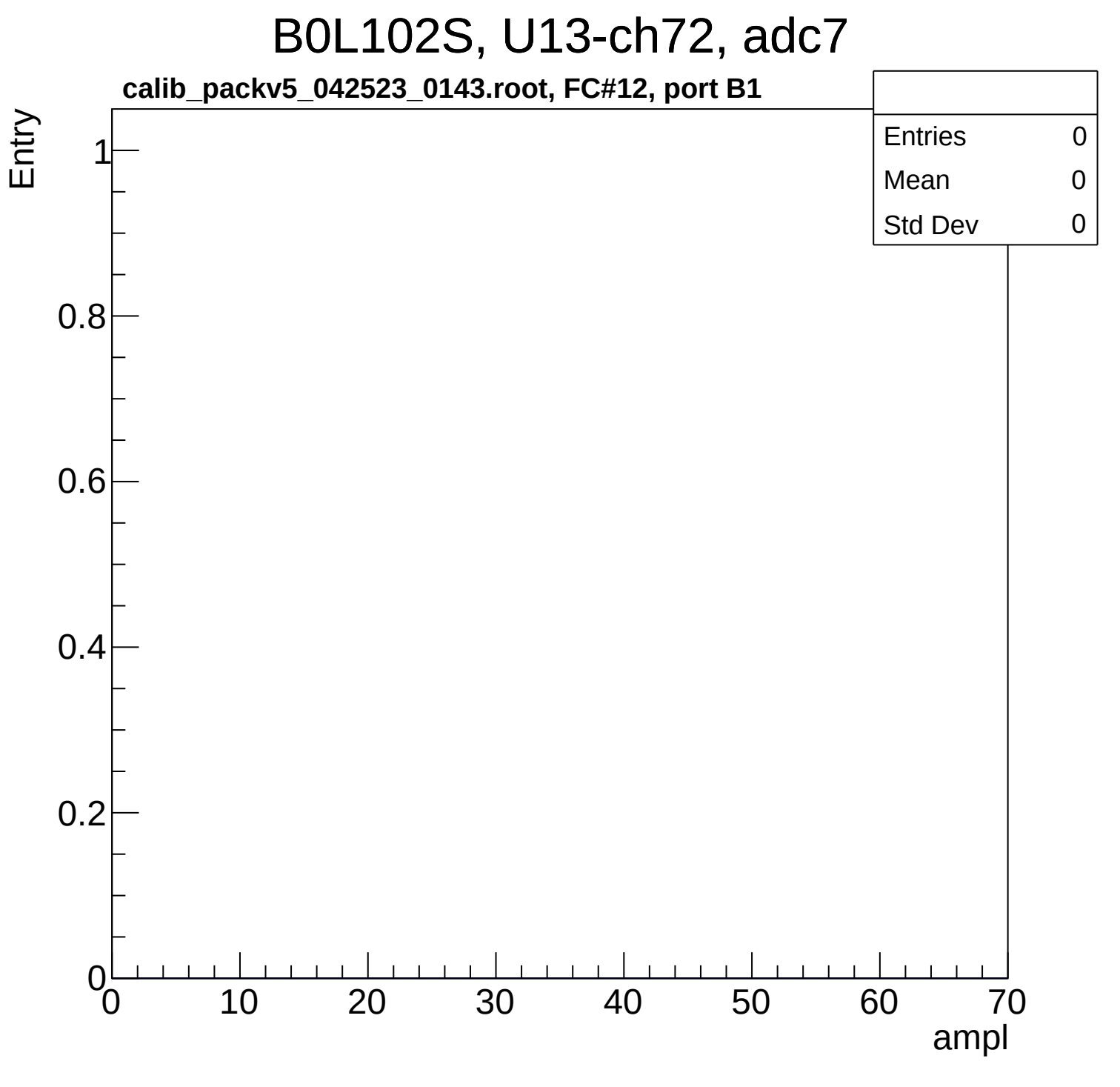
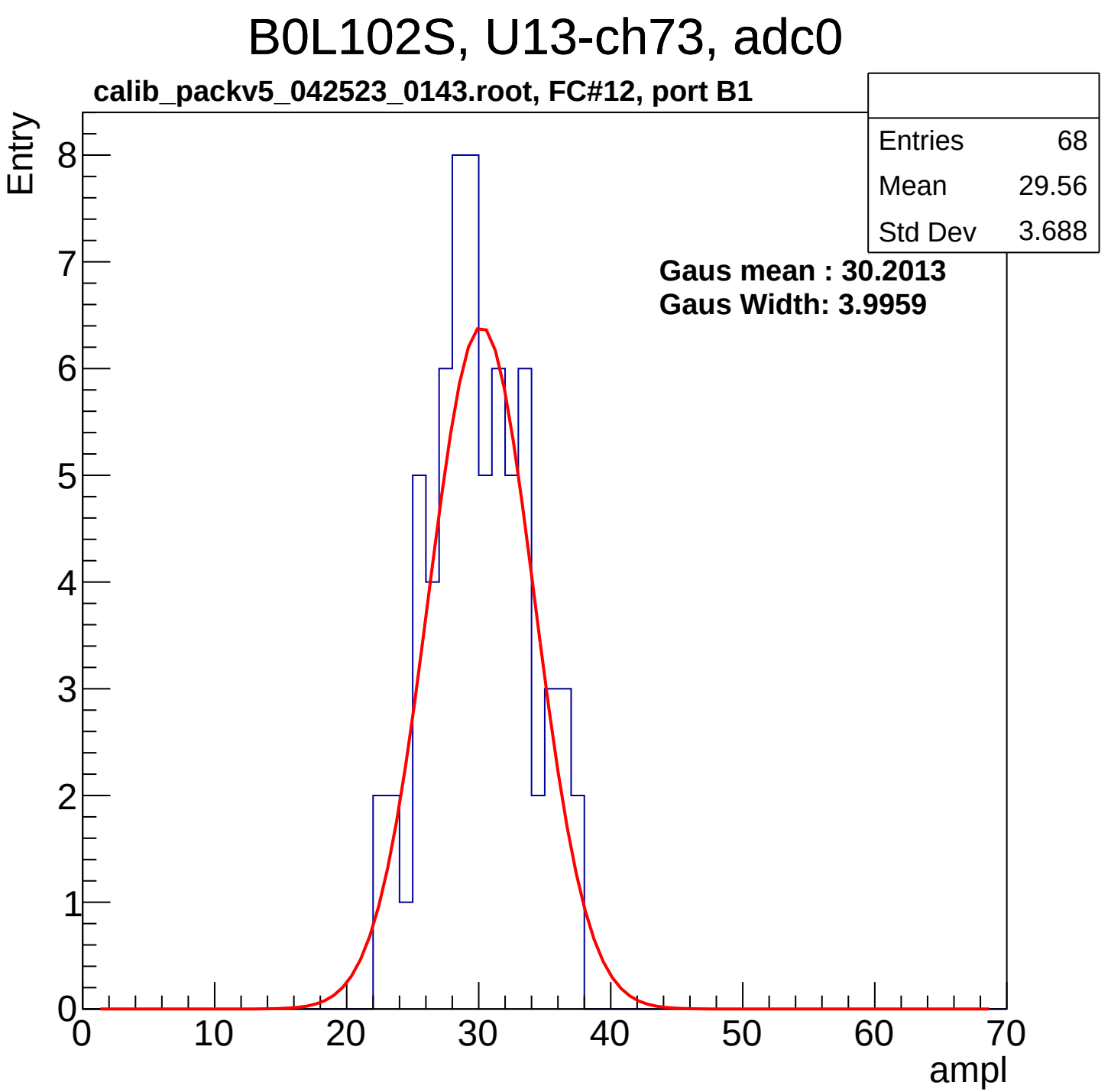
40
50
60
70


B0L102S, U13-ch72, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 57.63
Std Dev 10.2
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch72, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 62.45
Std Dev 0.782
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch72, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch73, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.56
Std Dev 3.688
Gaus mean : 30.2013
Gaus Width: 3.9959
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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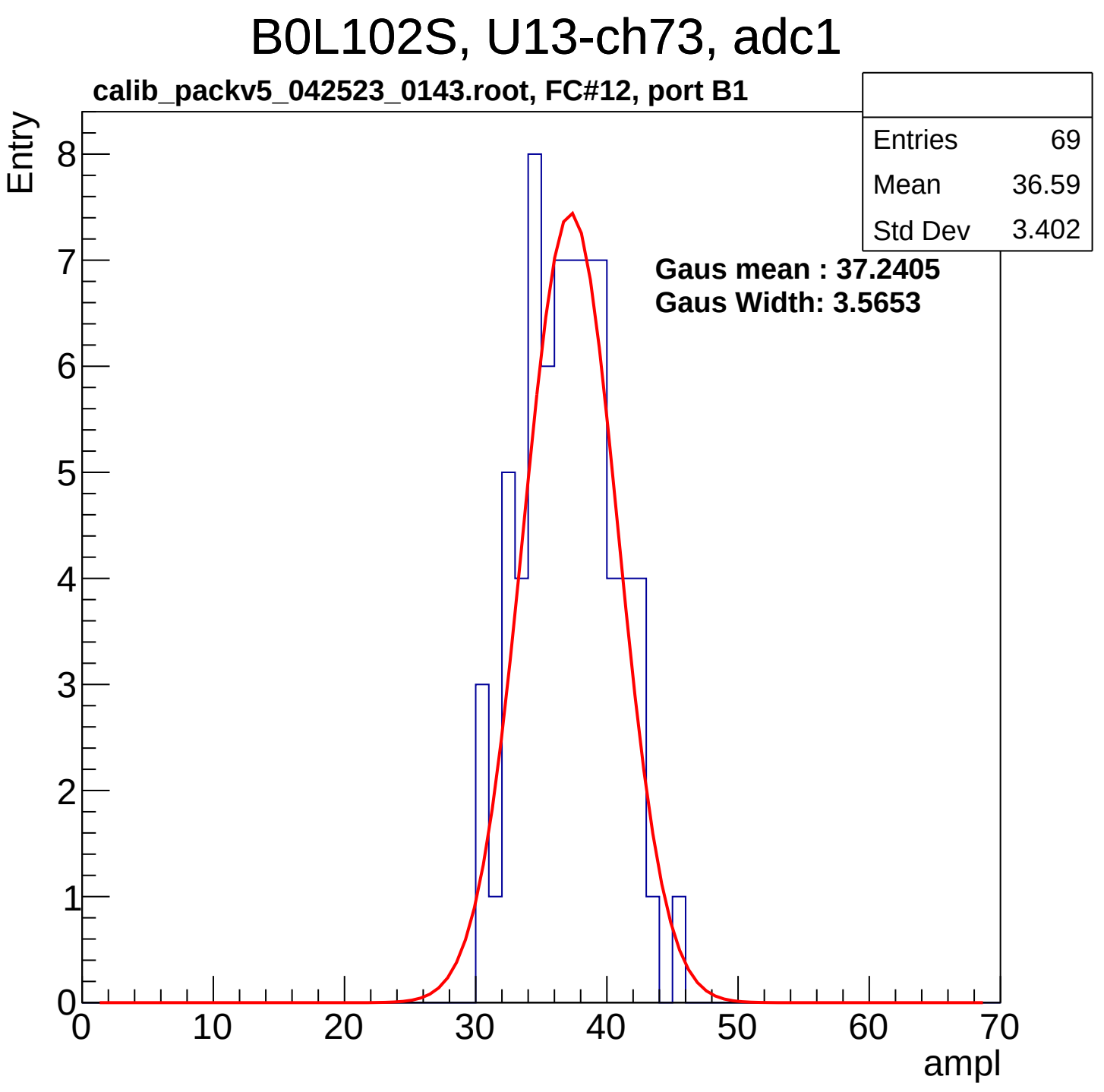
B0L102S, U13-ch73, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 36.59
Std Dev 3.402
Gaus mean : 37.2405
Gaus Width: 3.5653
Entry
ampl
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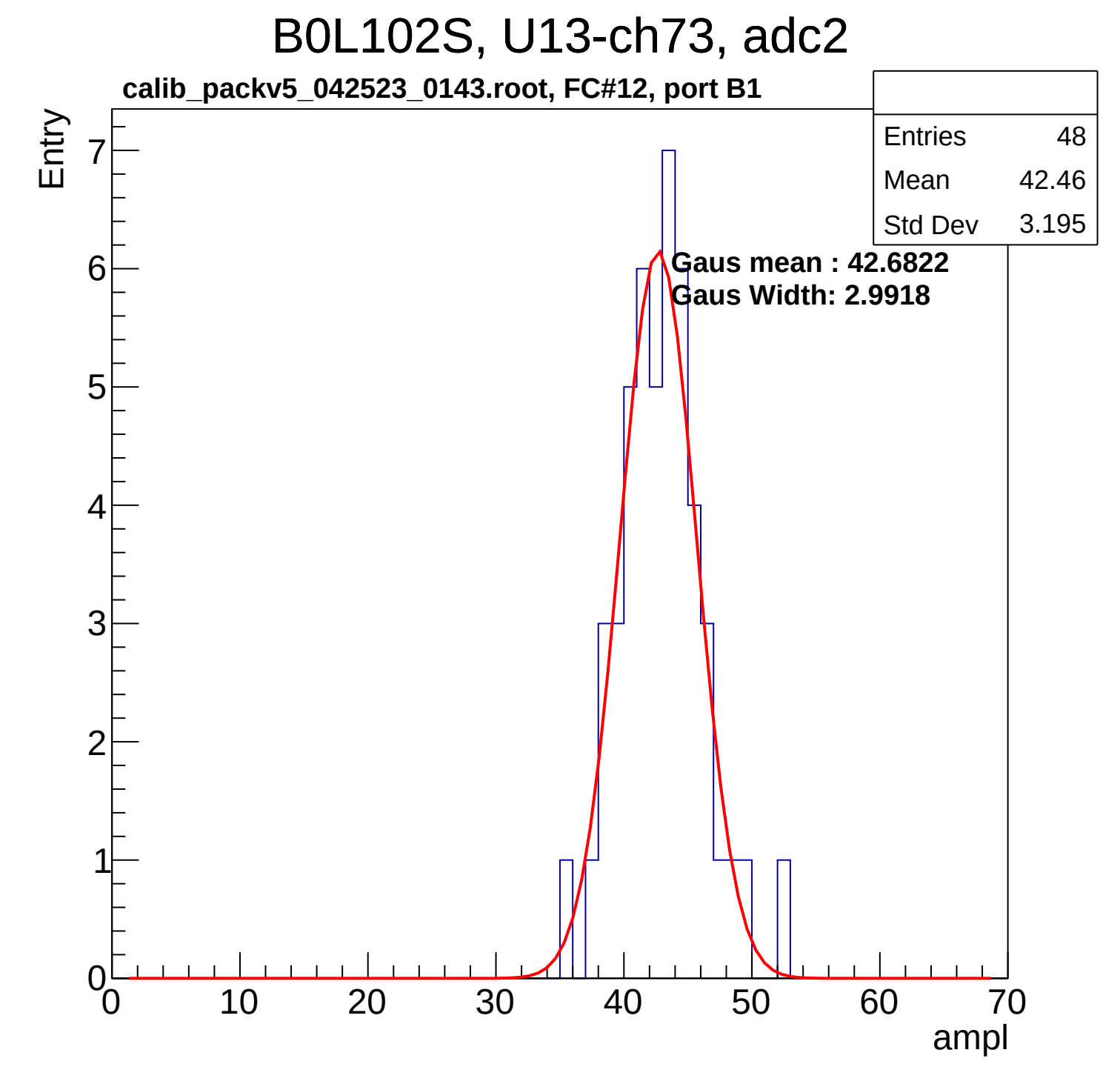
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch73, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 42.46
Std Dev 3.195
Gaus mean : 42.6822
Gaus Width: 2.9918
Entry
ampl
0
1
2
3
4
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6
7
0
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20
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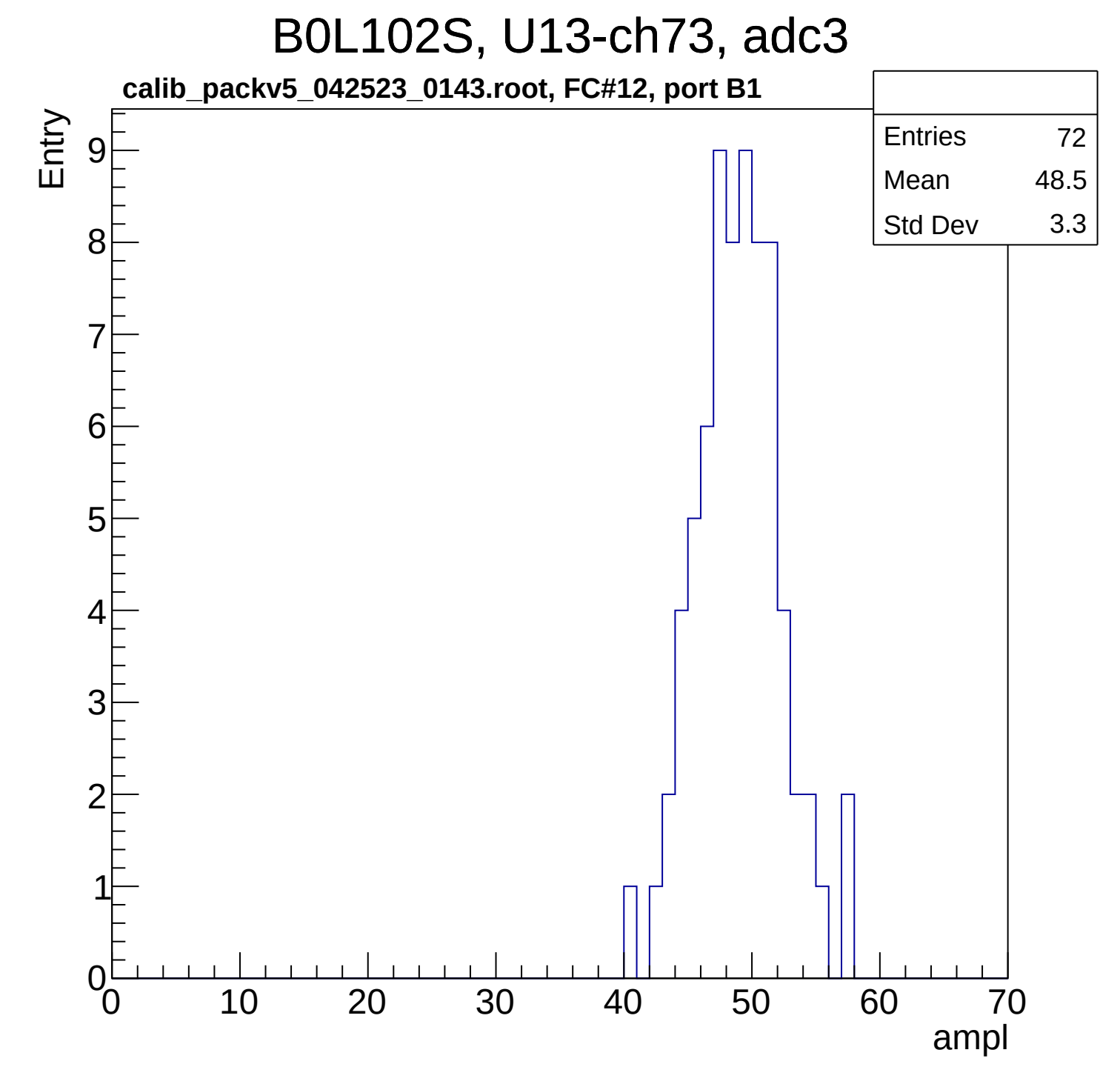
60
70


B0L102S, U13-ch73, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.5
Std Dev 3.3
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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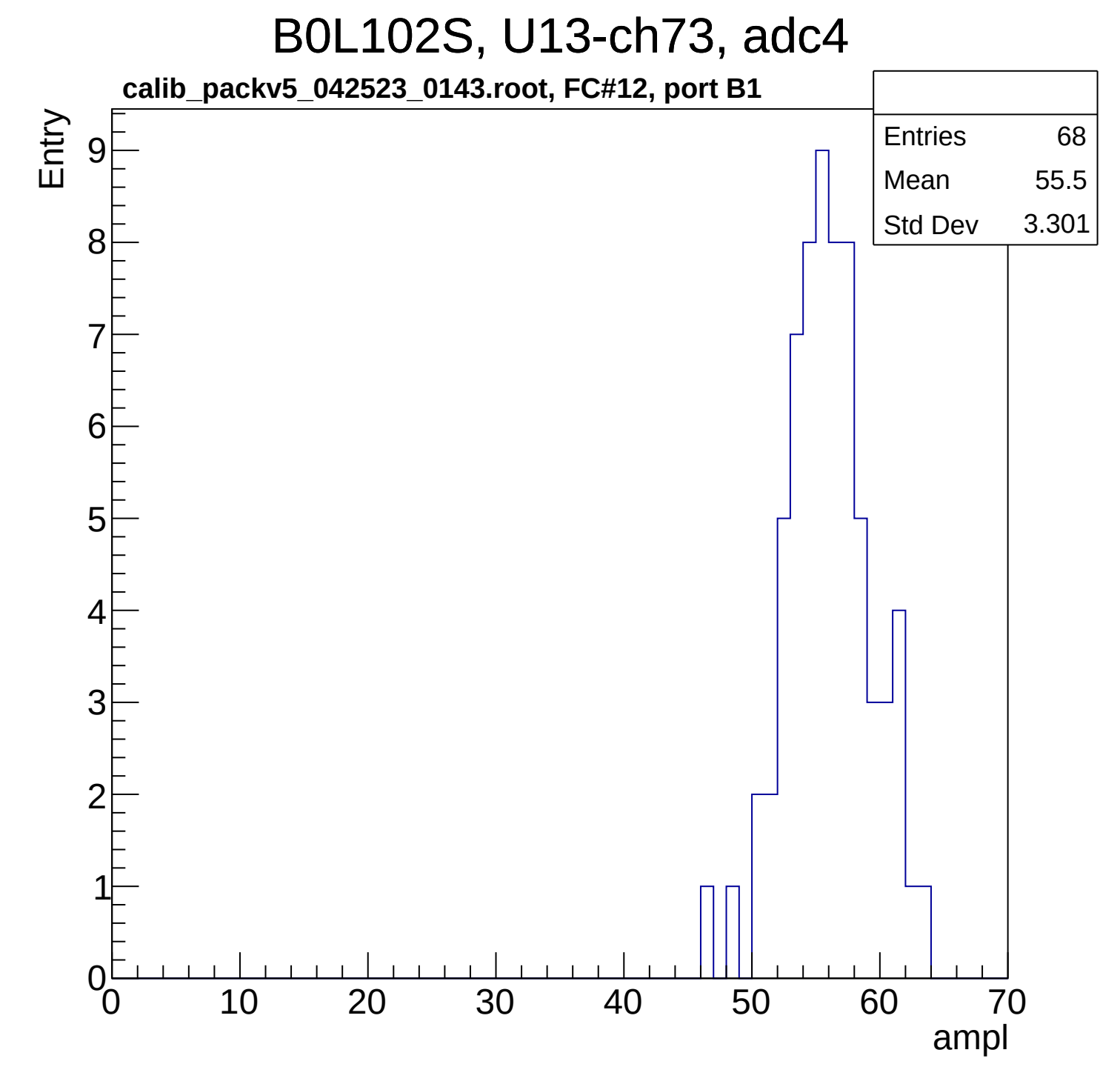
B0L102S, U13-ch73, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55.5
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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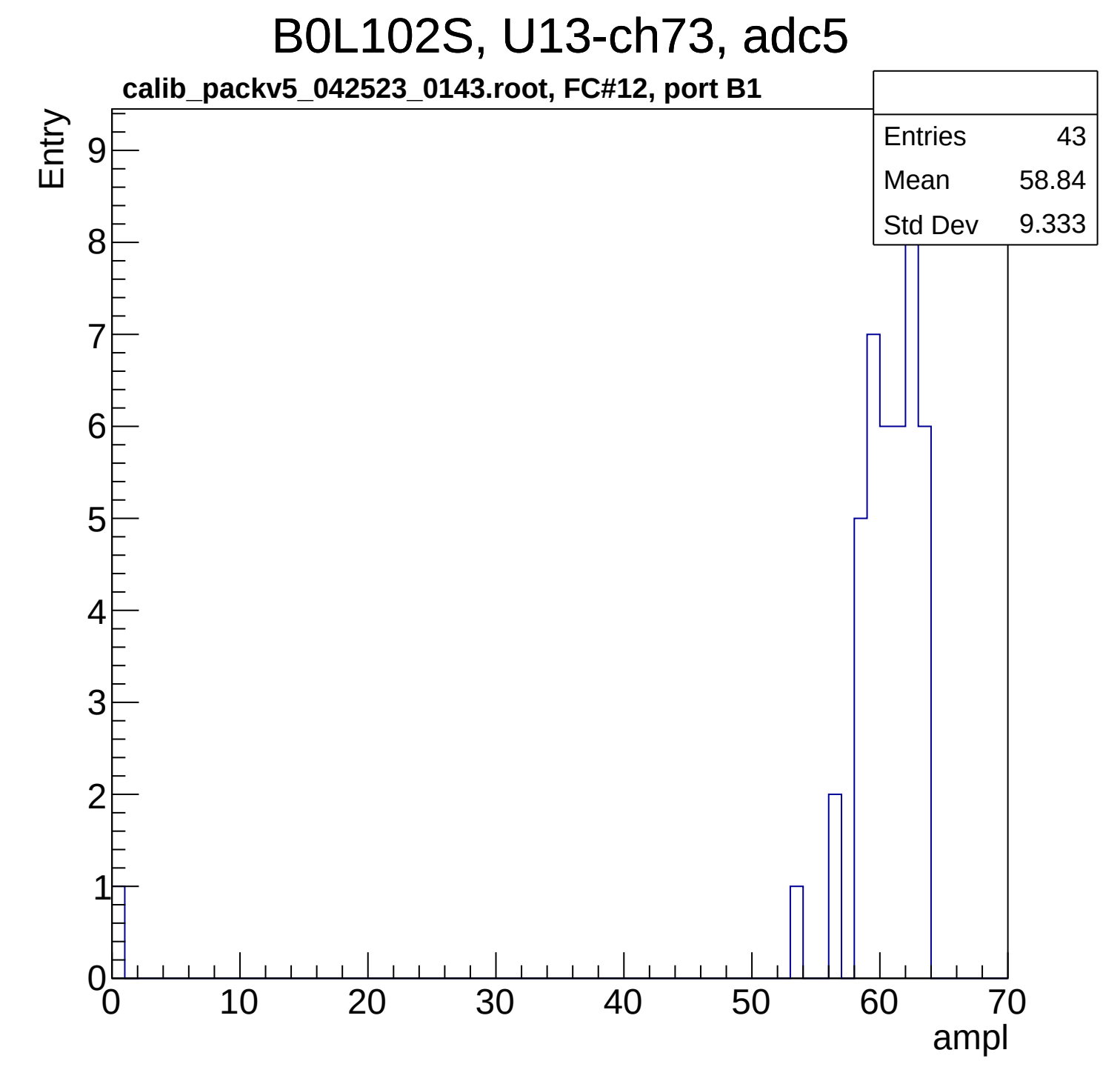
B0L102S, U13-ch73, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.84
Std Dev 9.333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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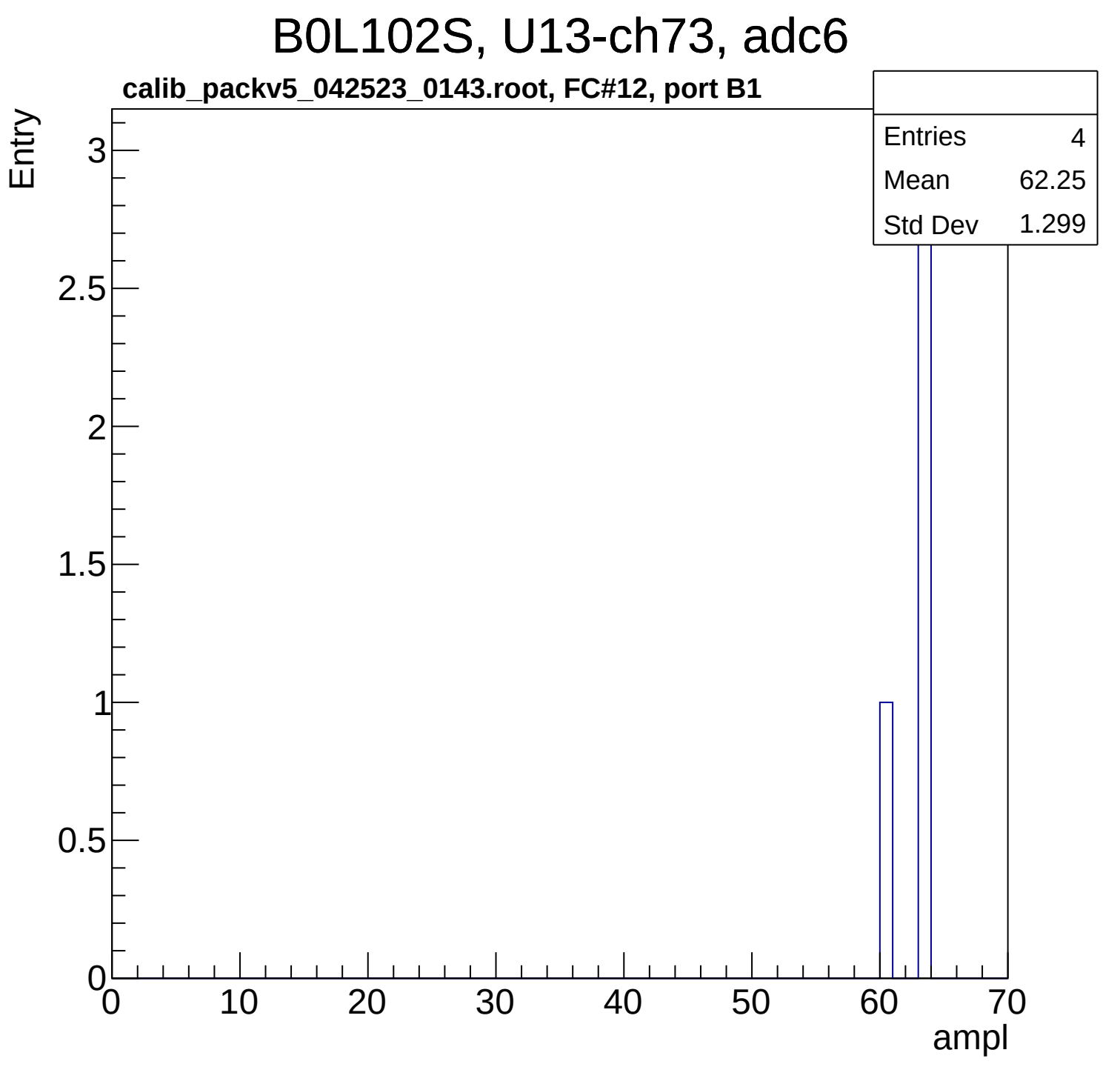
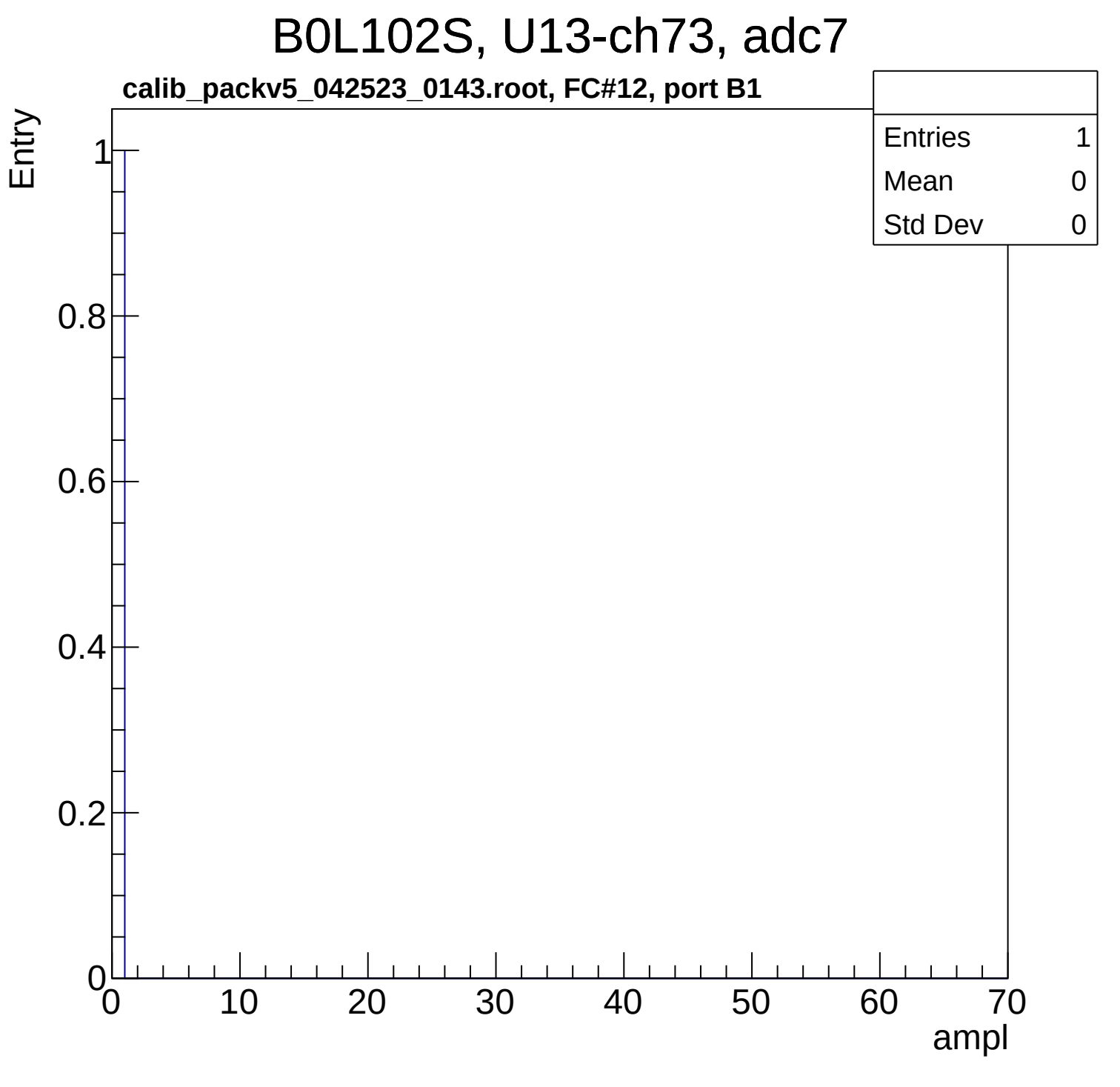
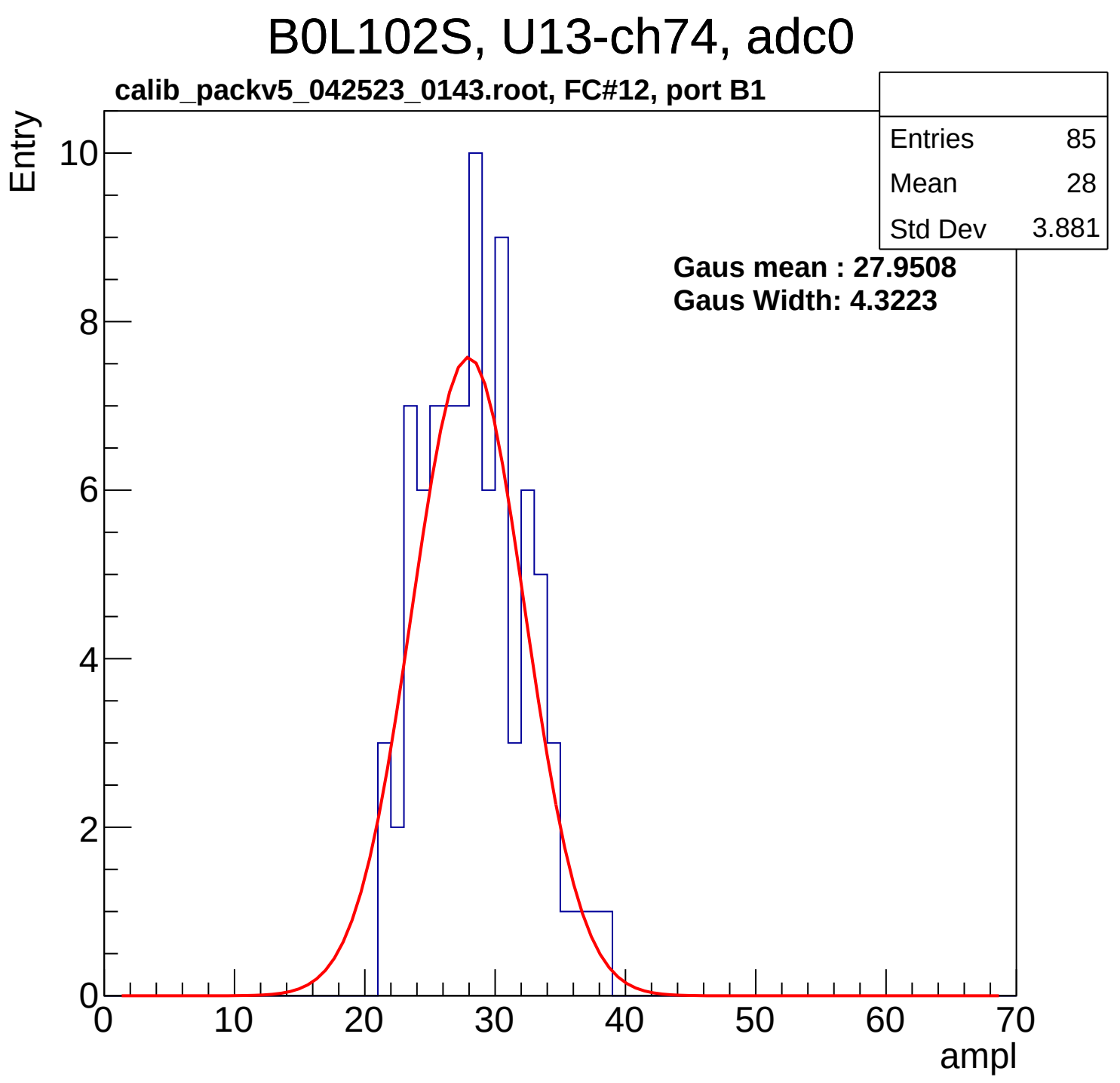
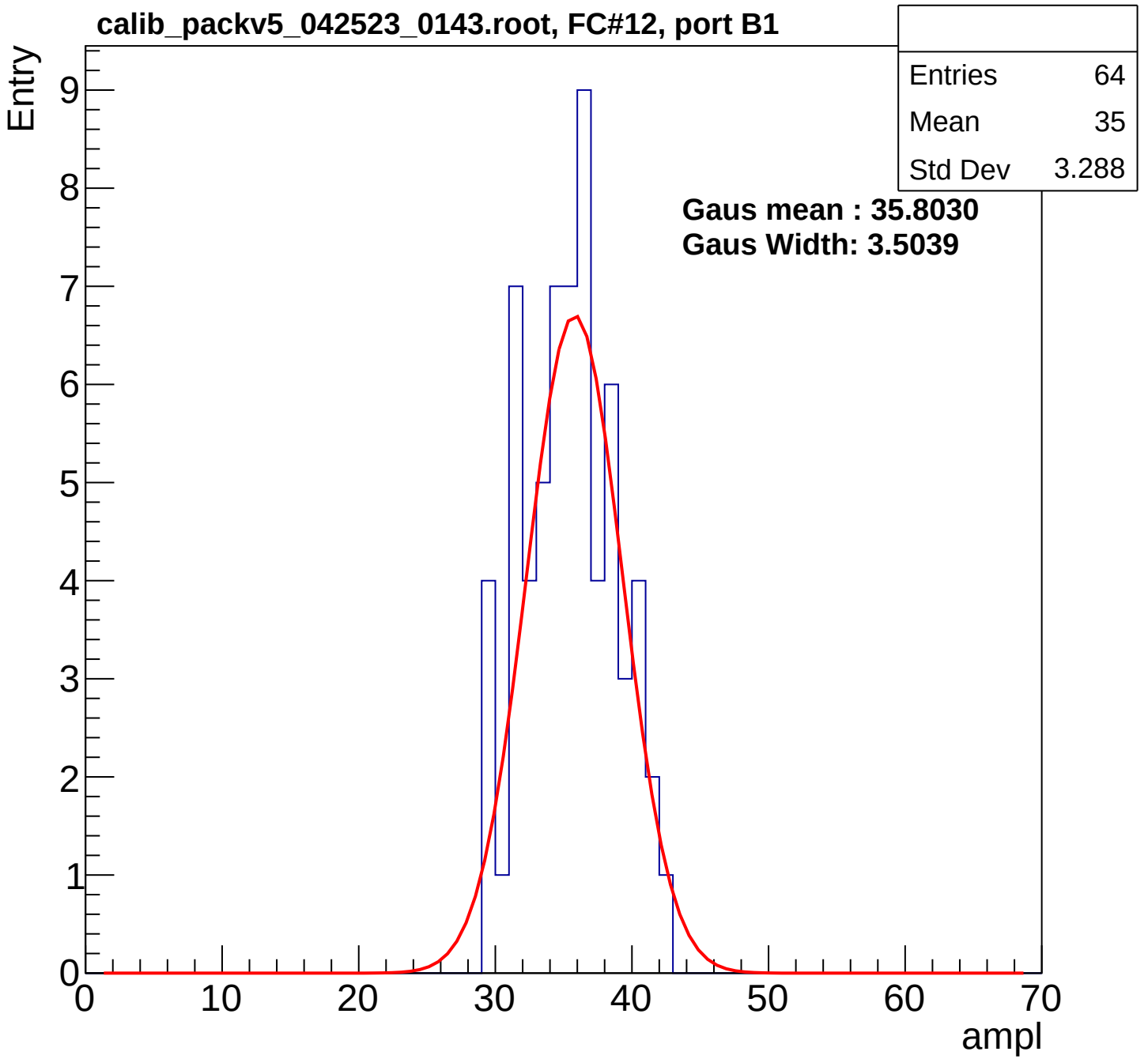

B0L102S, U13-ch73, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch73, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch74, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 28
Std Dev 3.881
Gaus mean : 27.9508
Gaus Width: 4.3223
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch74, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 35
Std Dev 3.288
Gaus mean : 35.8030
Gaus Width: 3.5039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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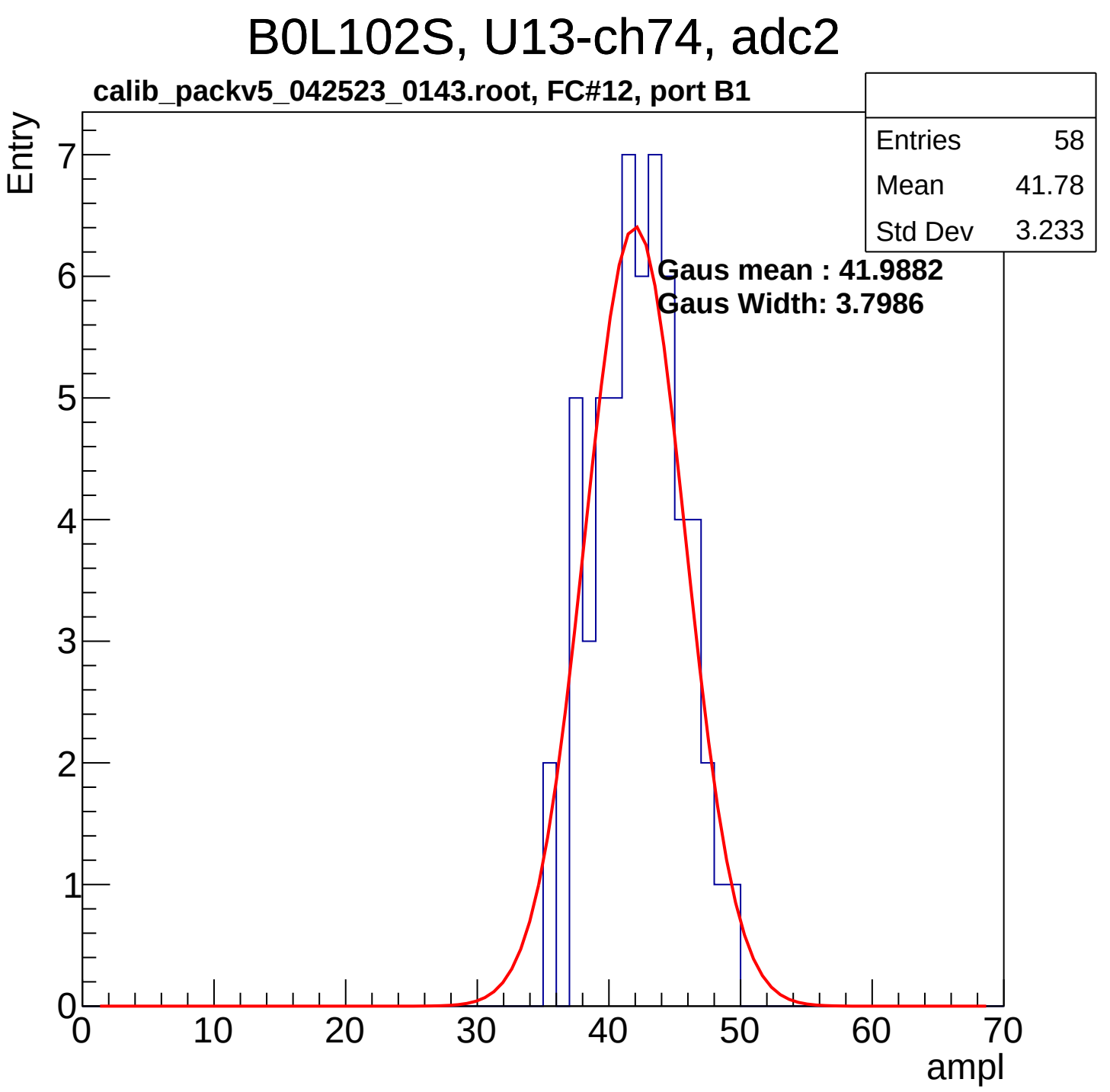
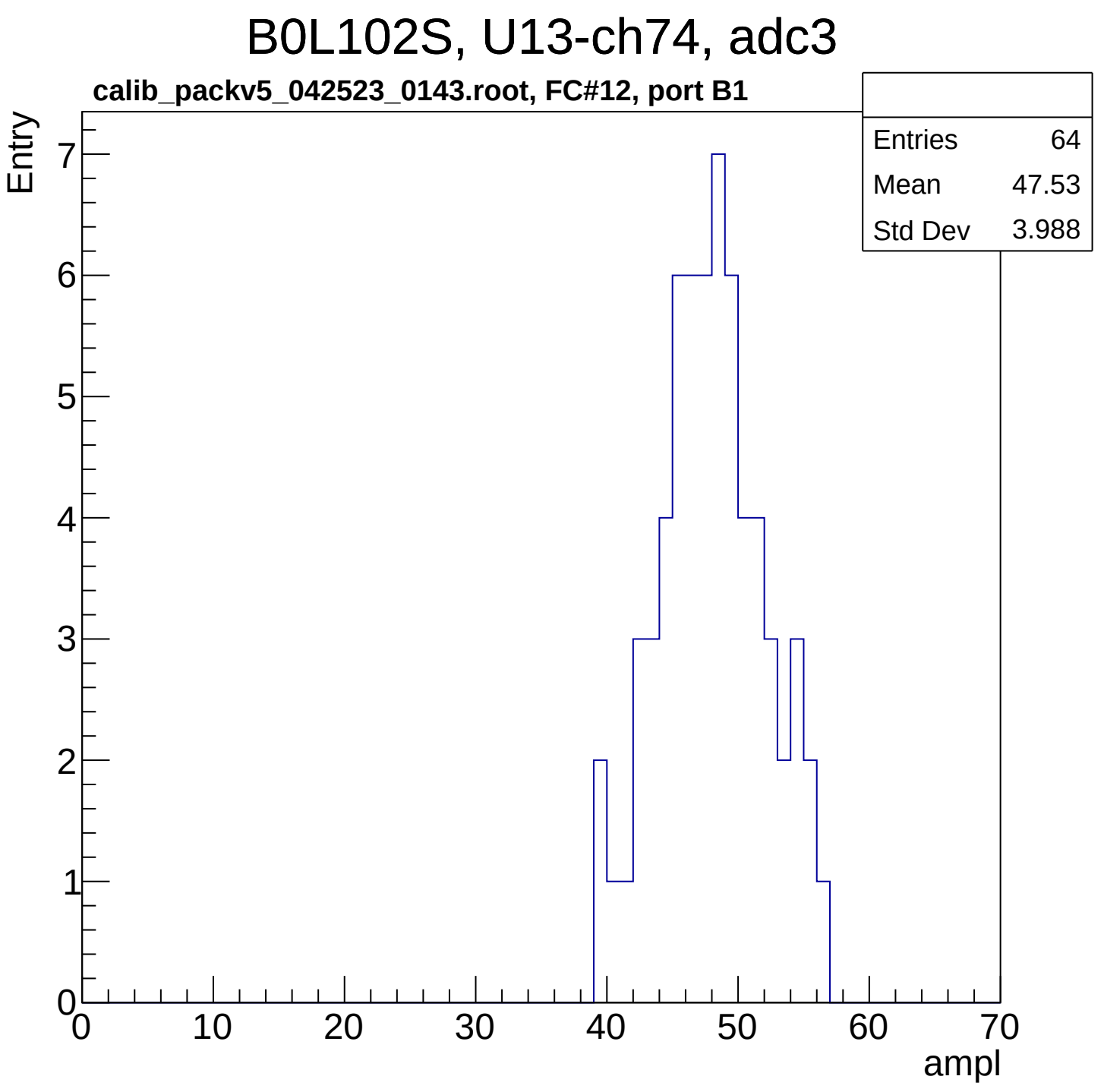
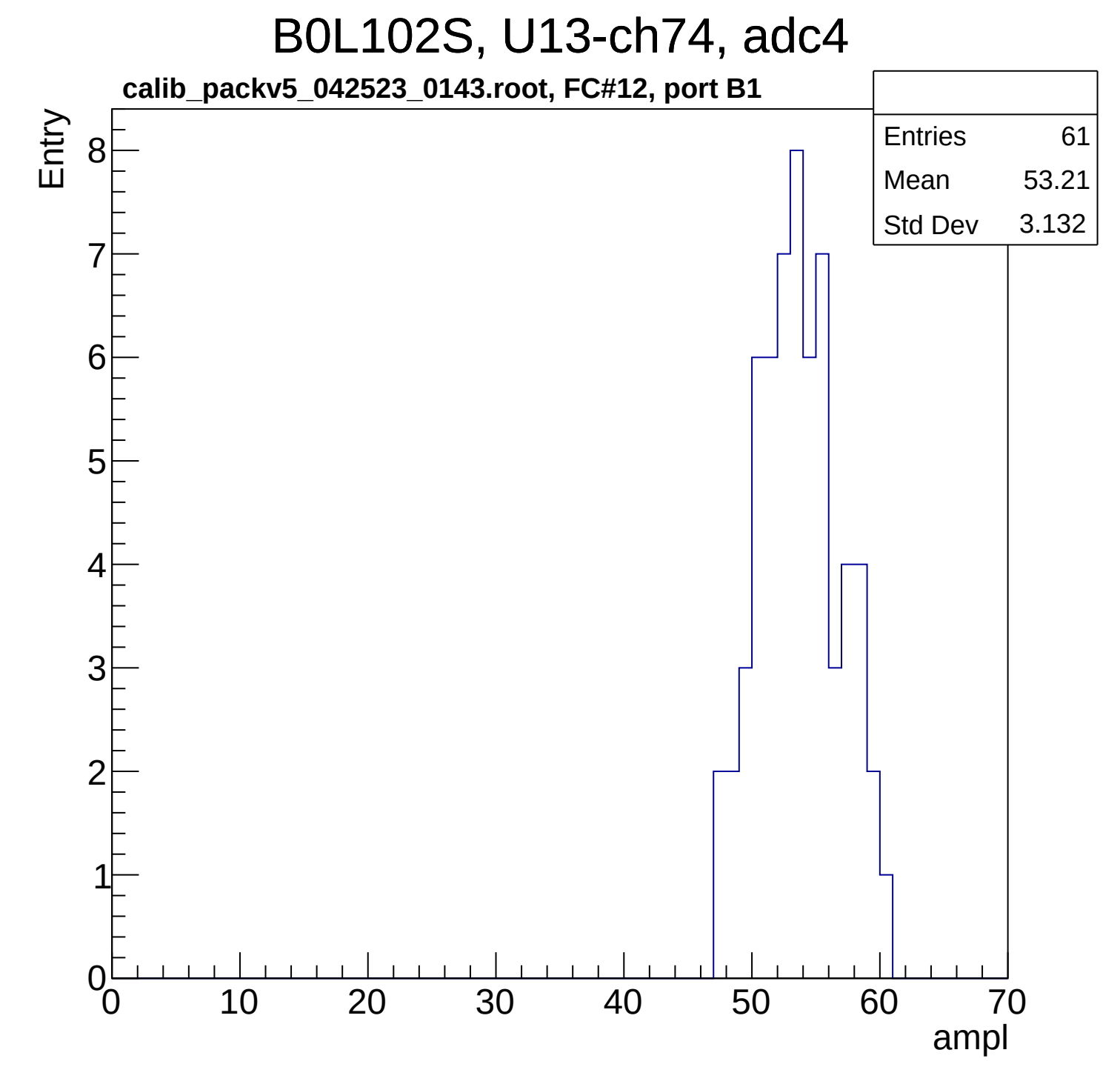
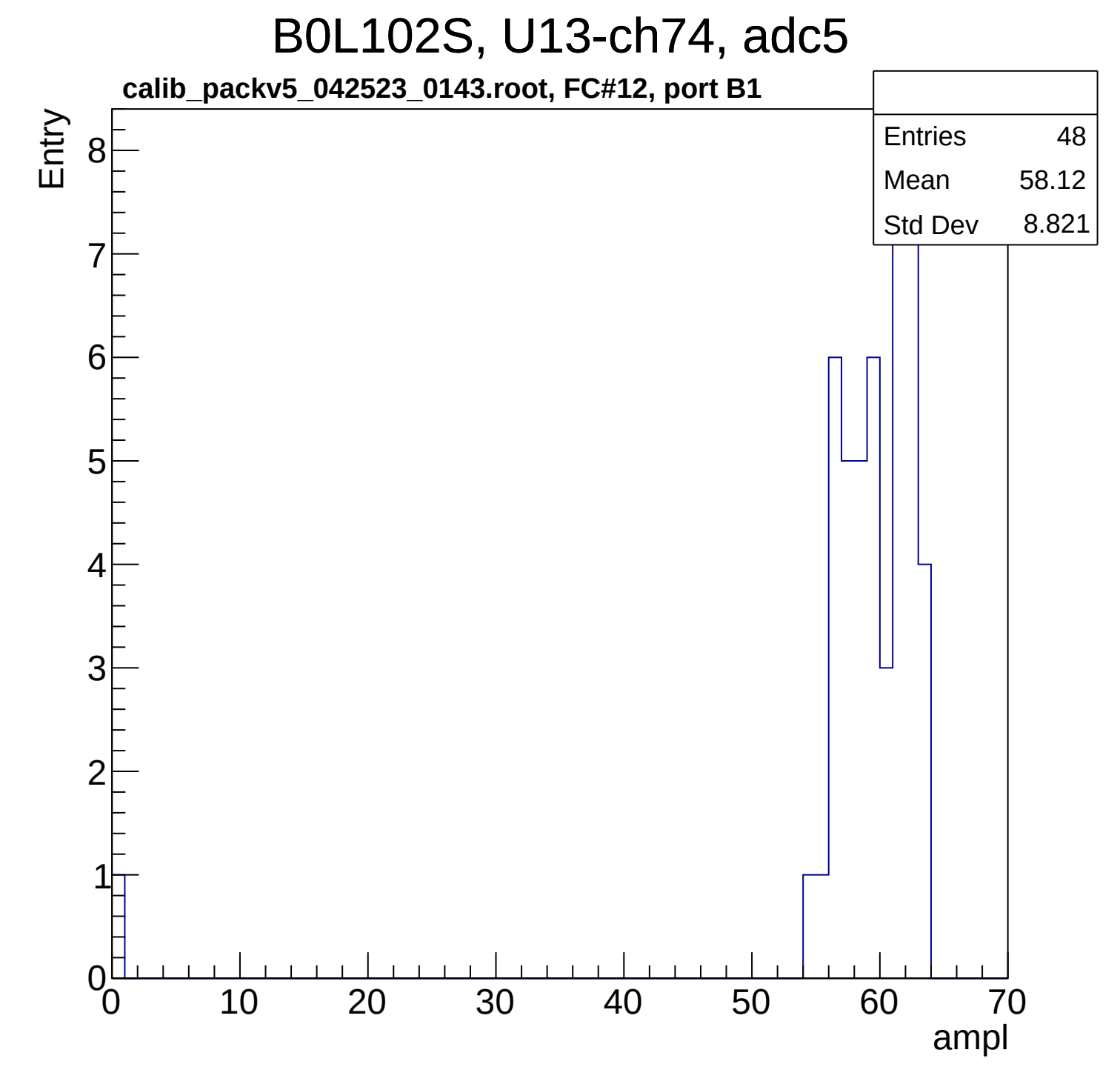
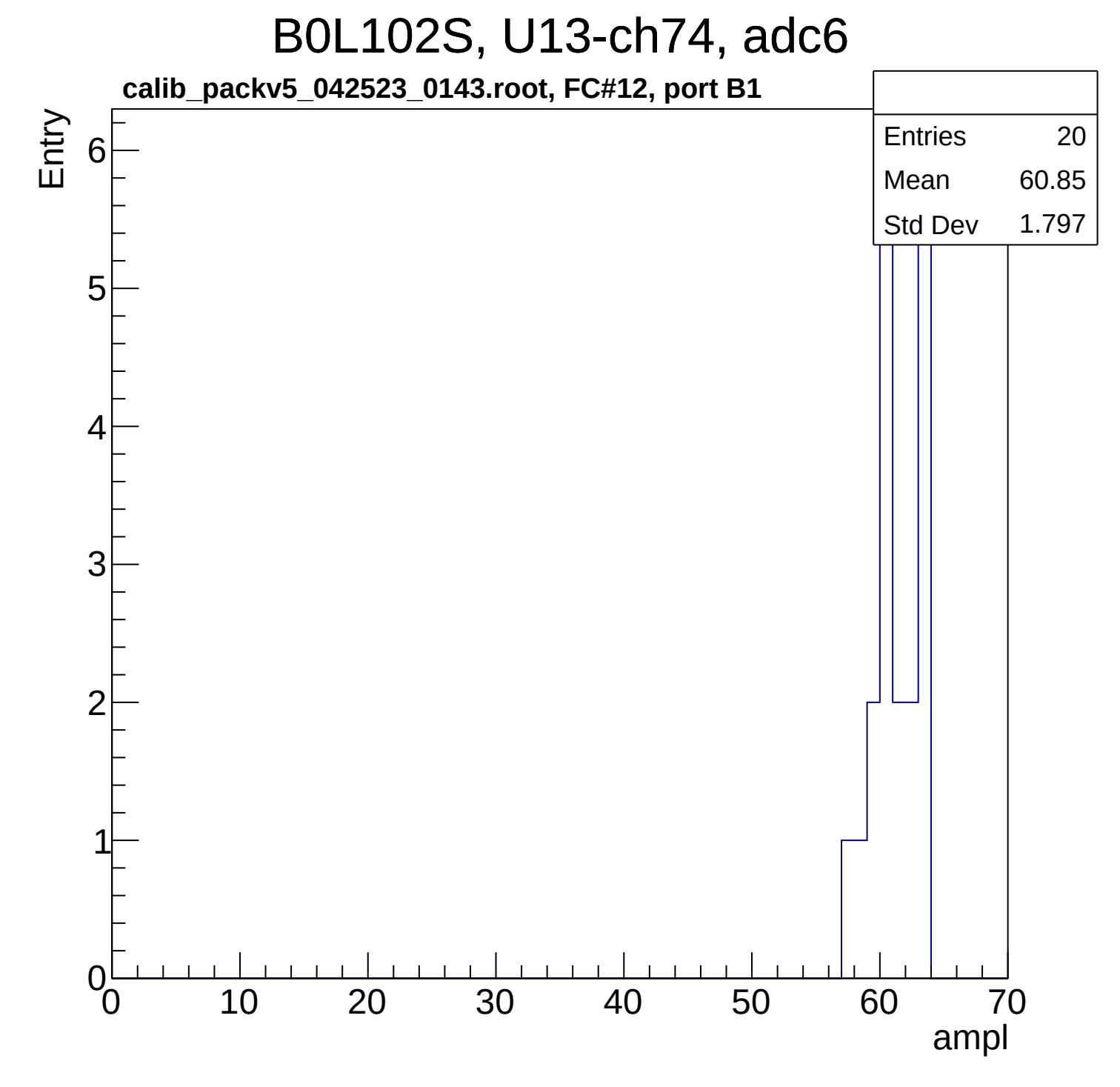
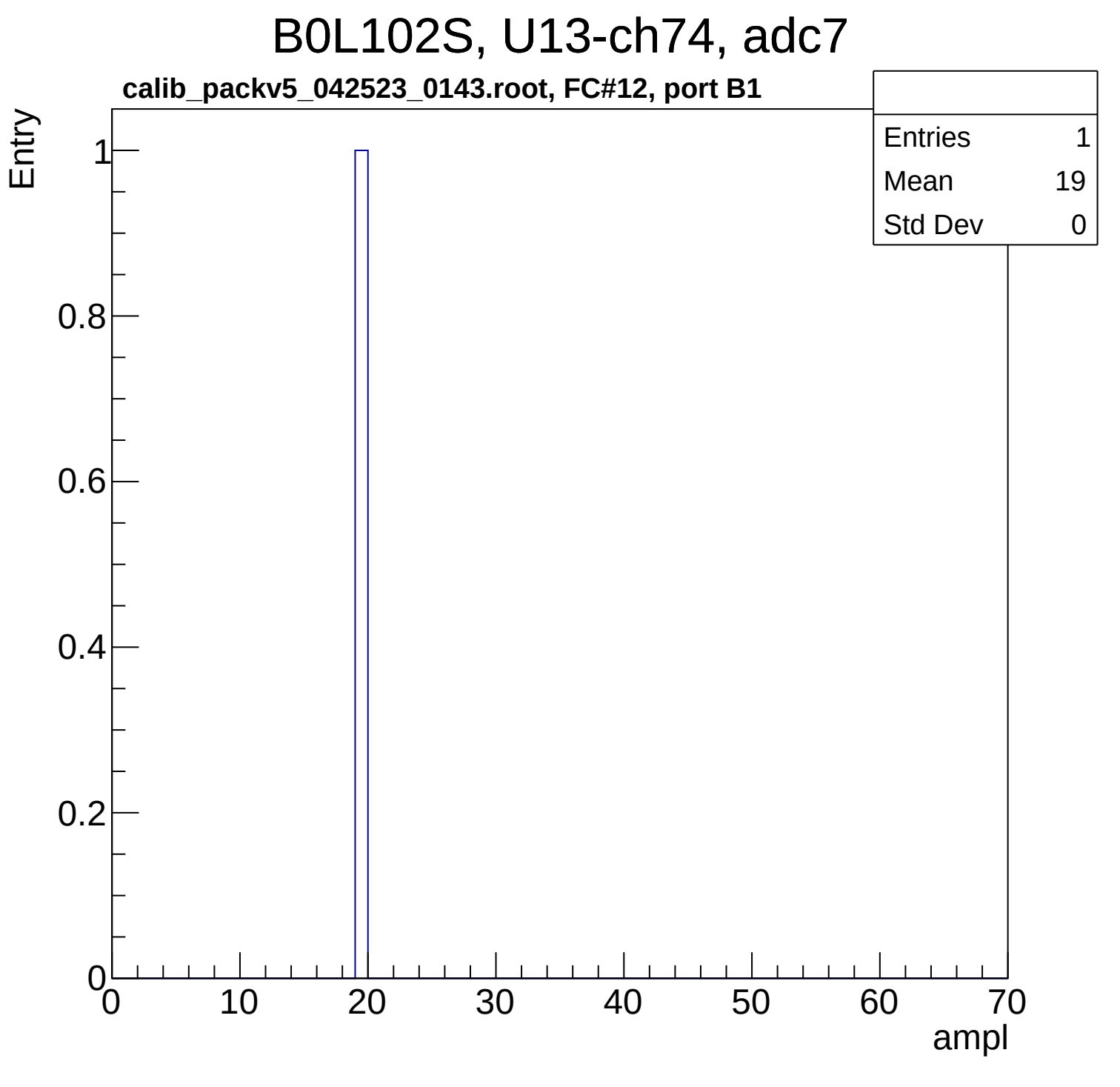

B0L102S, U13-ch74, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 41.78
Std Dev 3.233
Gaus mean : 41.9882
Gaus Width: 3.7986
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

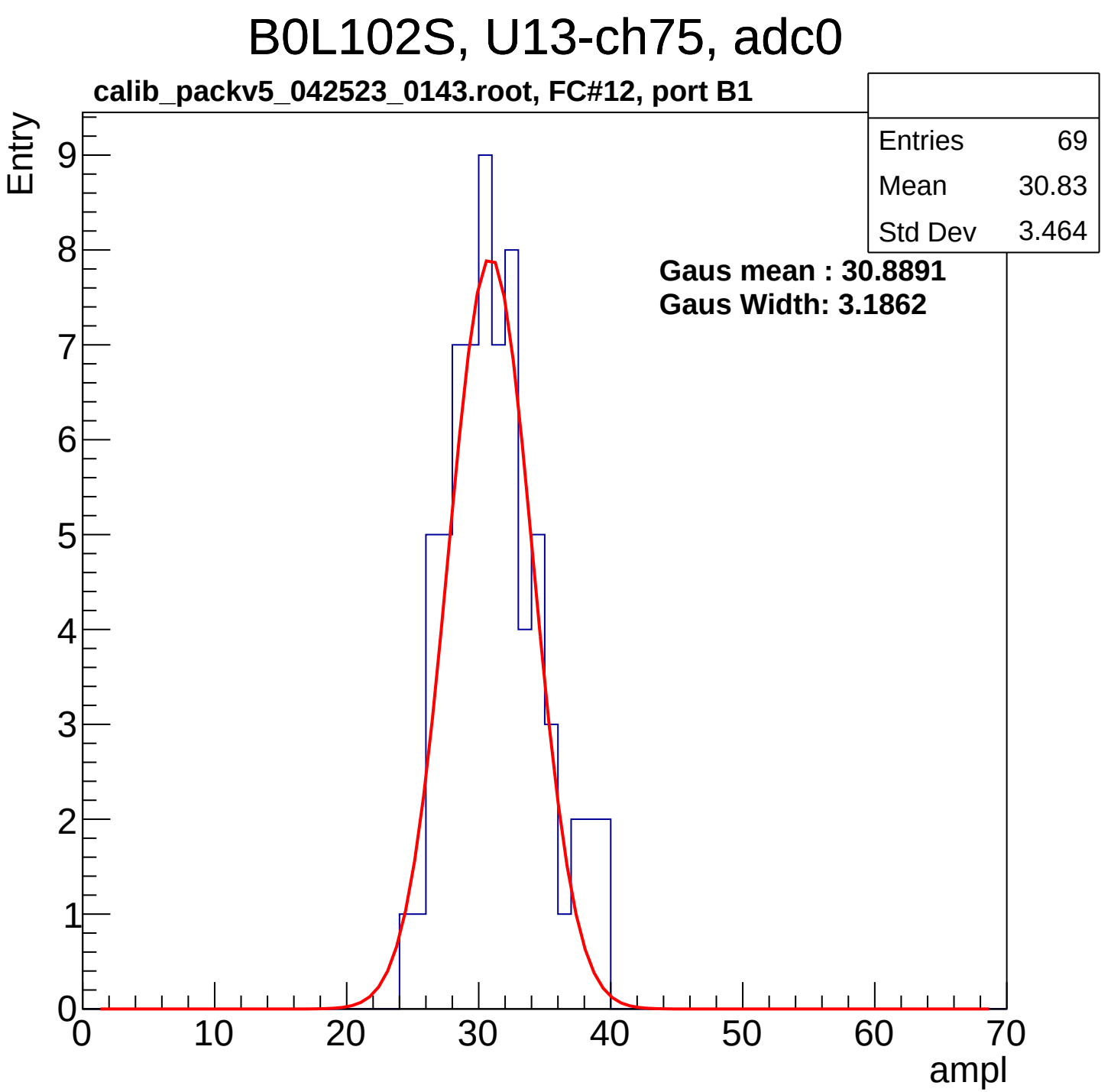

B0L102S, U13-ch74, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 47.53
Std Dev 3.988
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

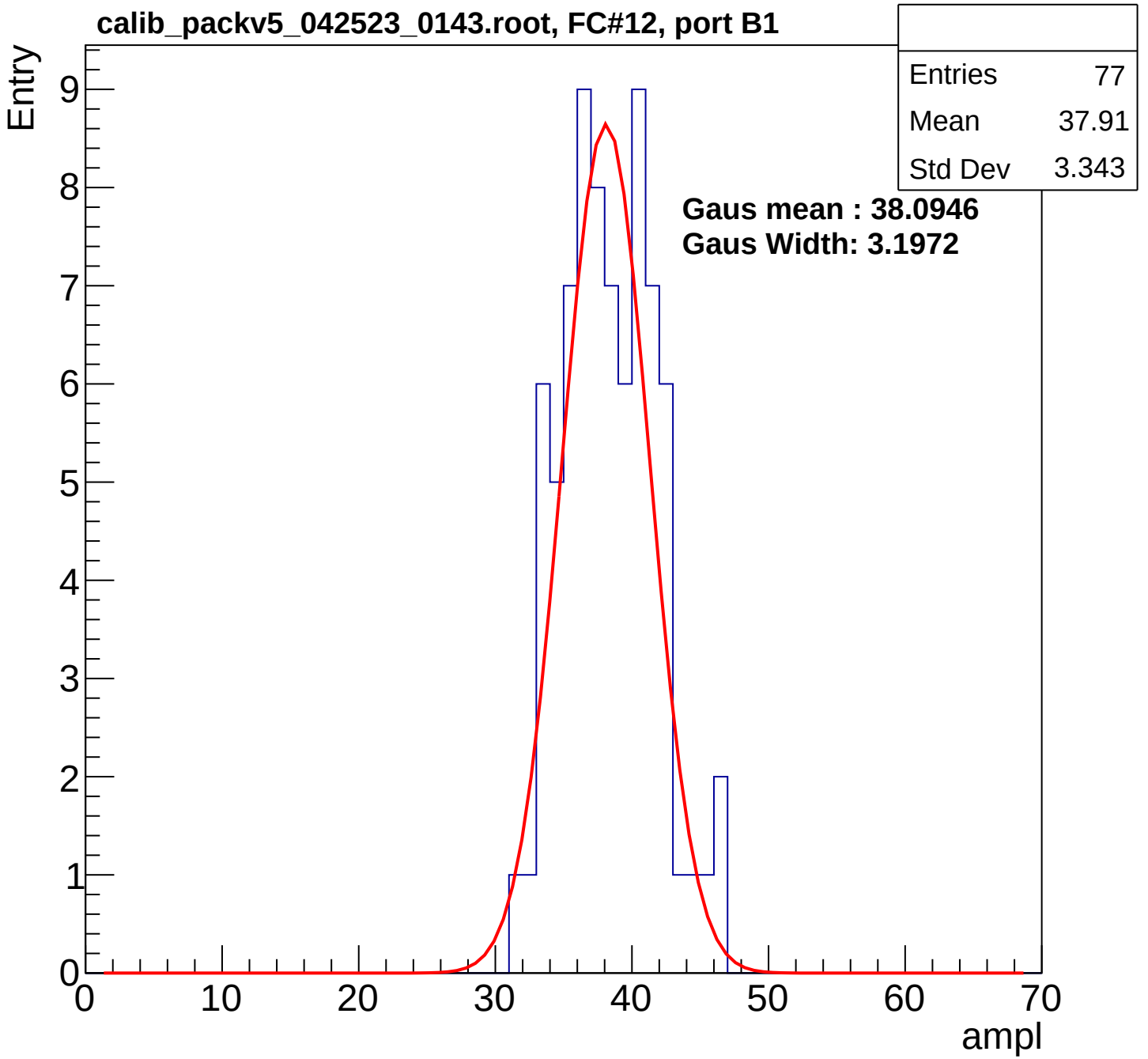

B0L102S, U13-ch74, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 53.21
Std Dev 3.132
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

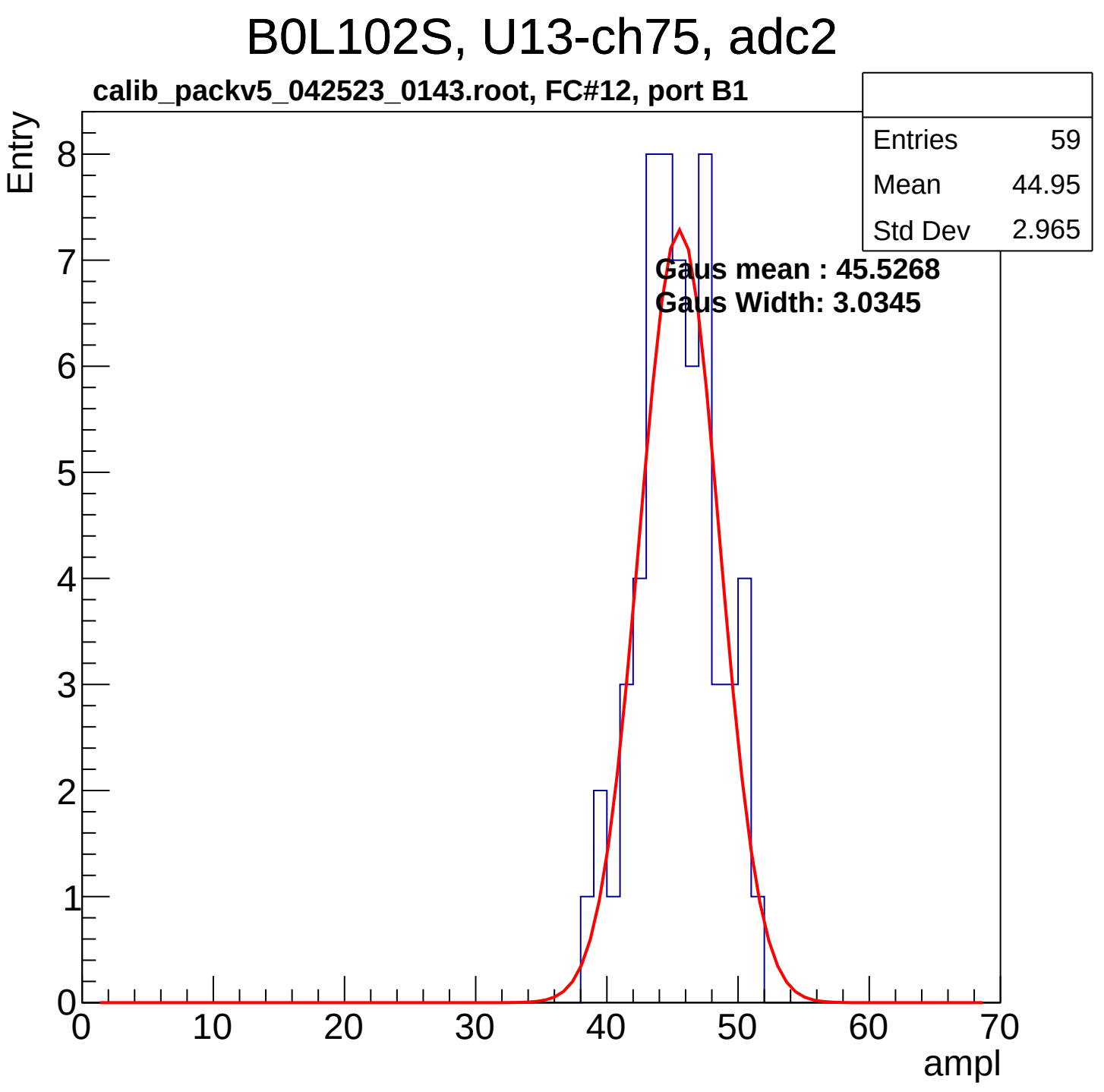

B0L102S, U13-ch74, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.12
Std Dev 8.821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

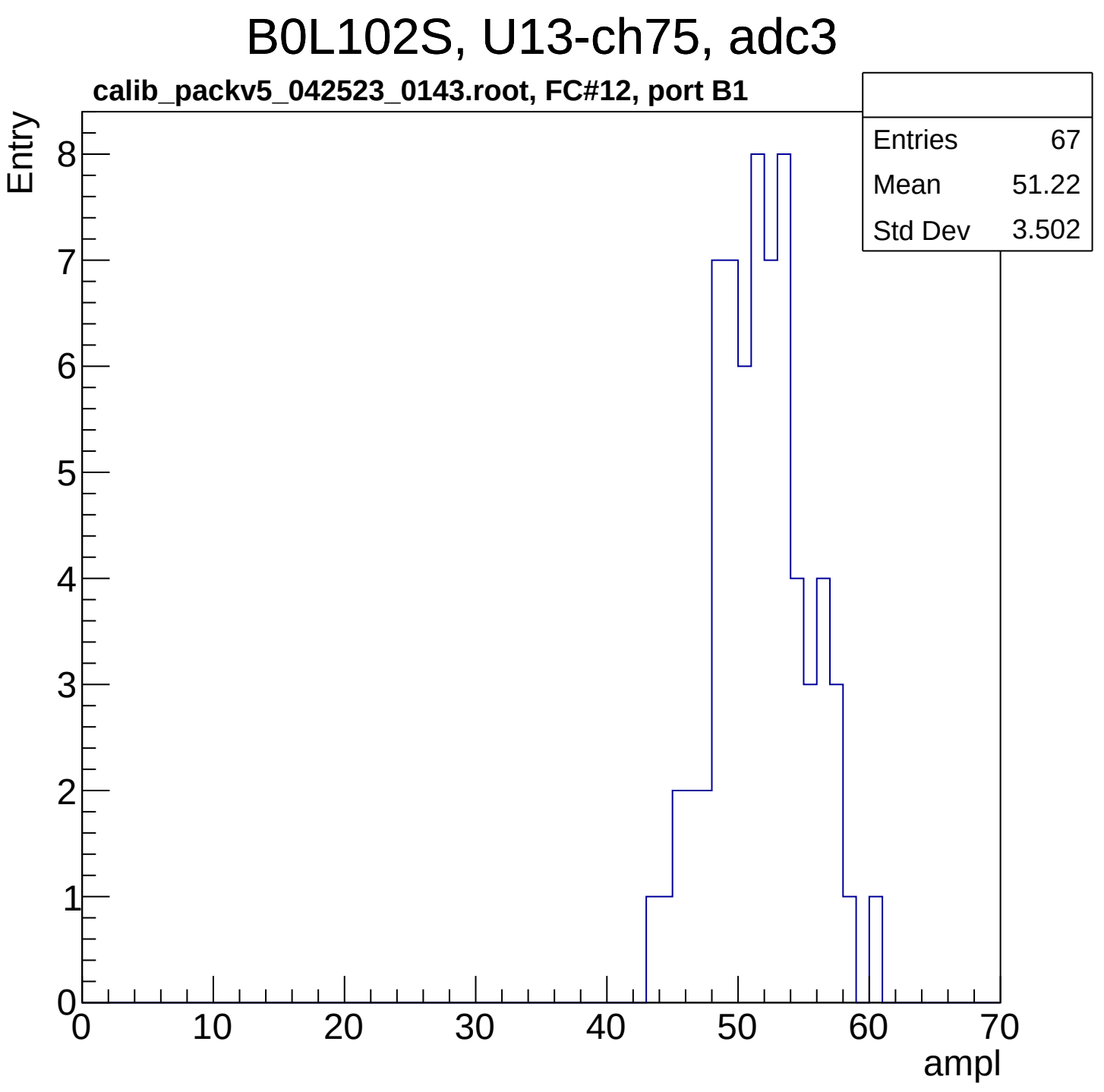

B0L102S, U13-ch74, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 60.85
Std Dev 1.797
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

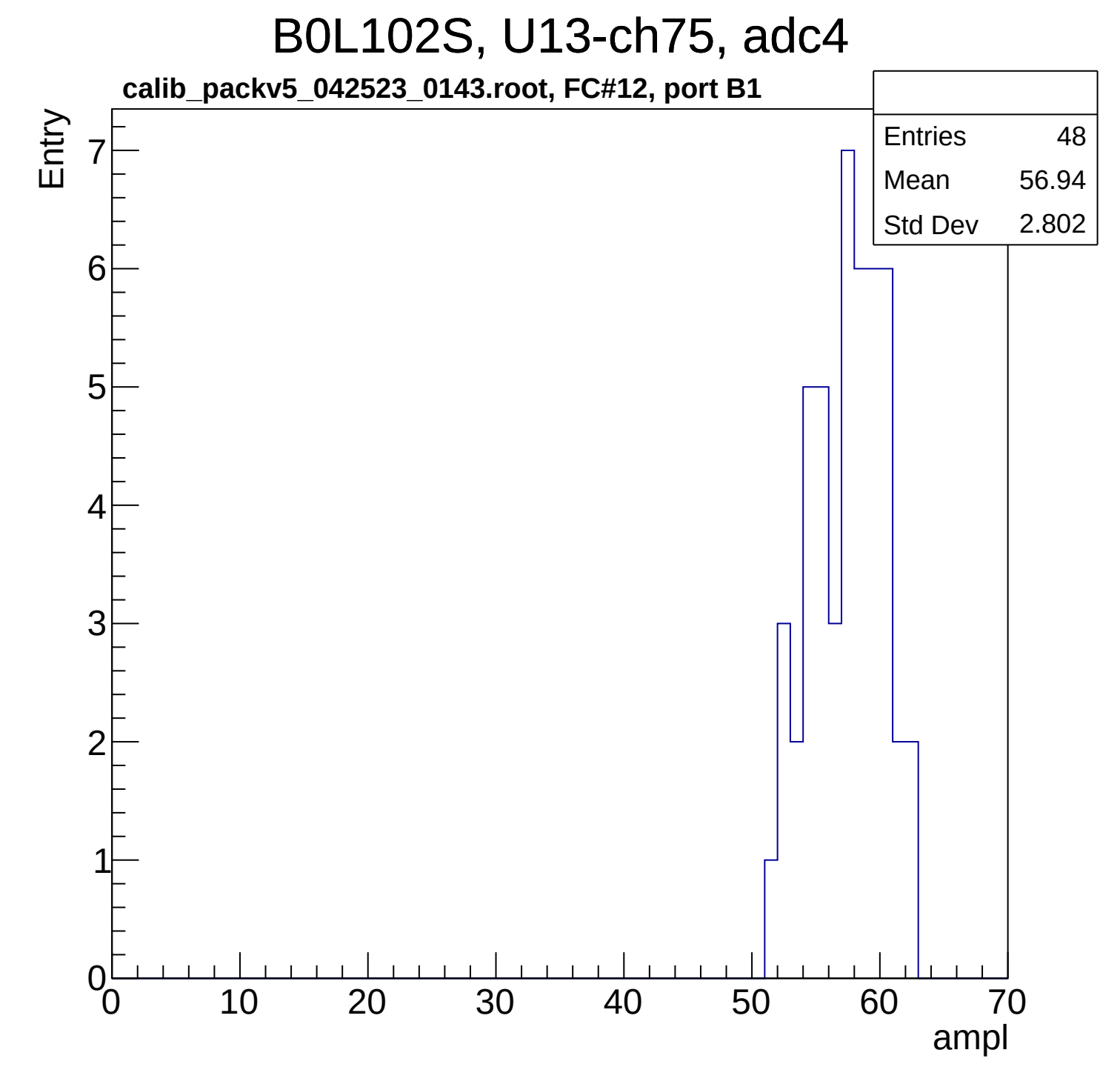

B0L102S, U13-ch74, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

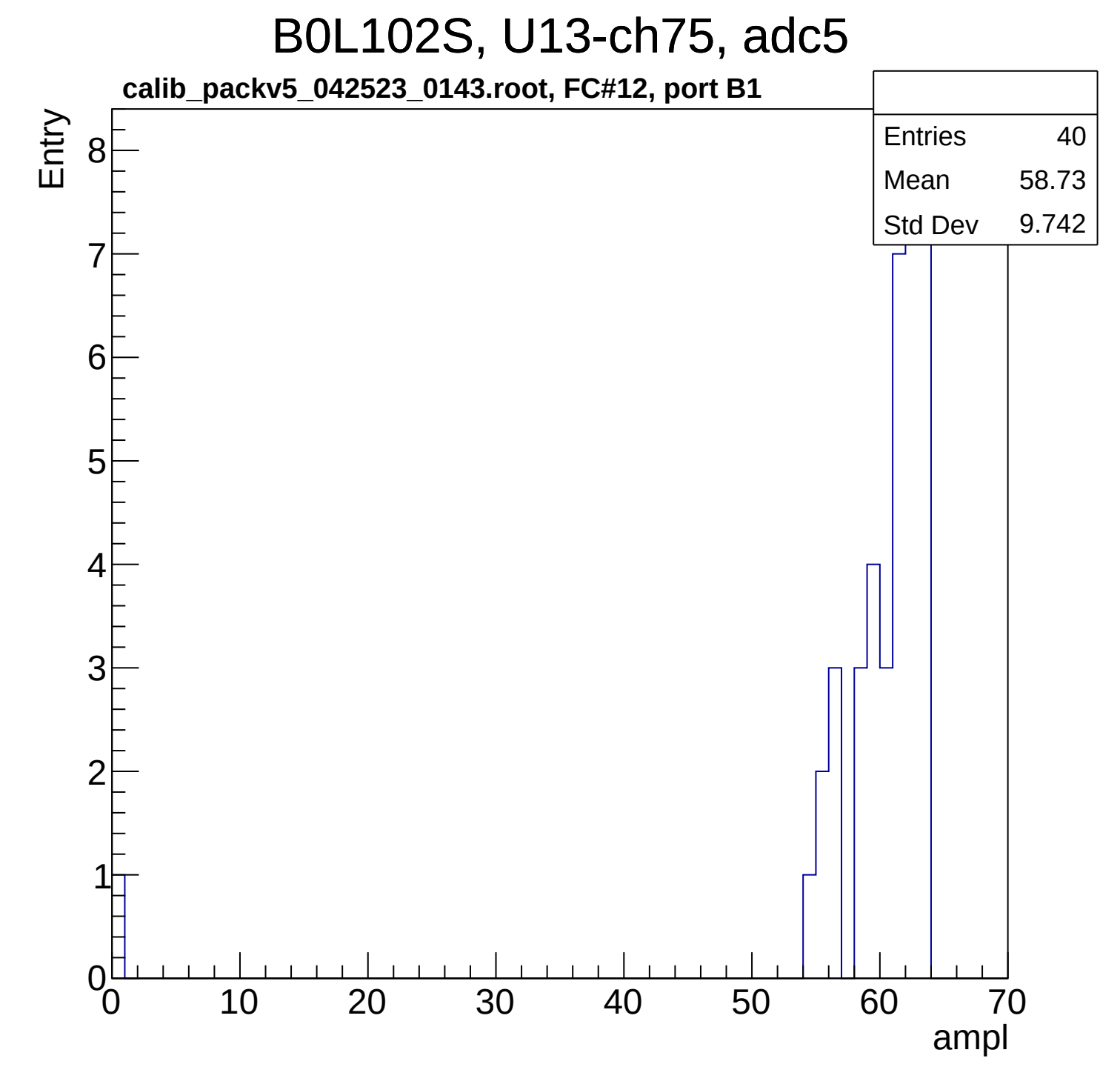

B0L102S, U13-ch75, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 30.83
Std Dev 3.464
Gaus mean : 30.8891
Gaus Width: 3.1862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 37.91
Std Dev 3.343
Gaus mean : 38.0946
Gaus Width: 3.1972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 44.95
Std Dev 2.965
Gaus mean : 45.5268
Gaus Width: 3.0345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 51.22
Std Dev 3.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 56.94
Std Dev 2.802
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.73
Std Dev 9.742
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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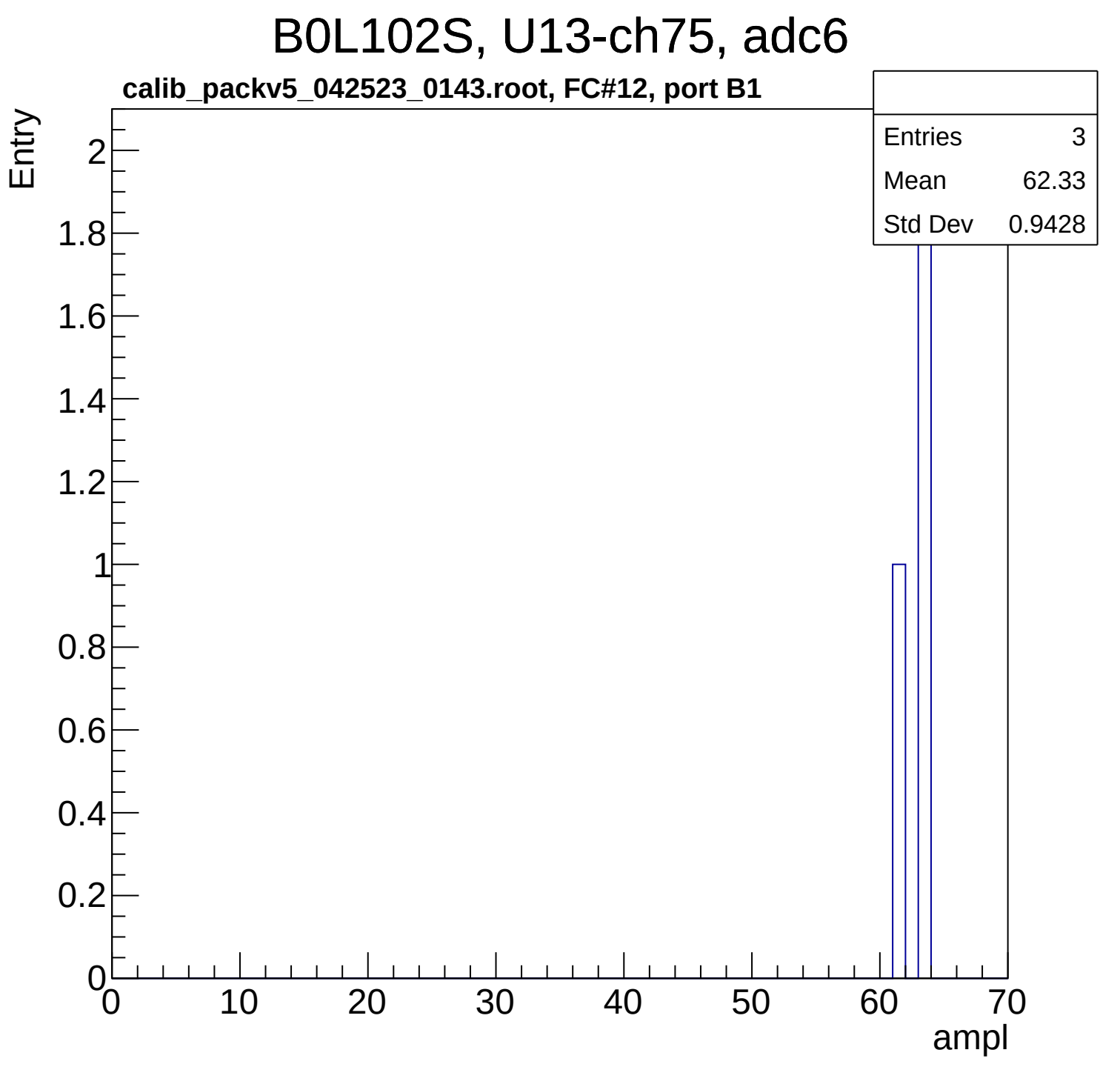
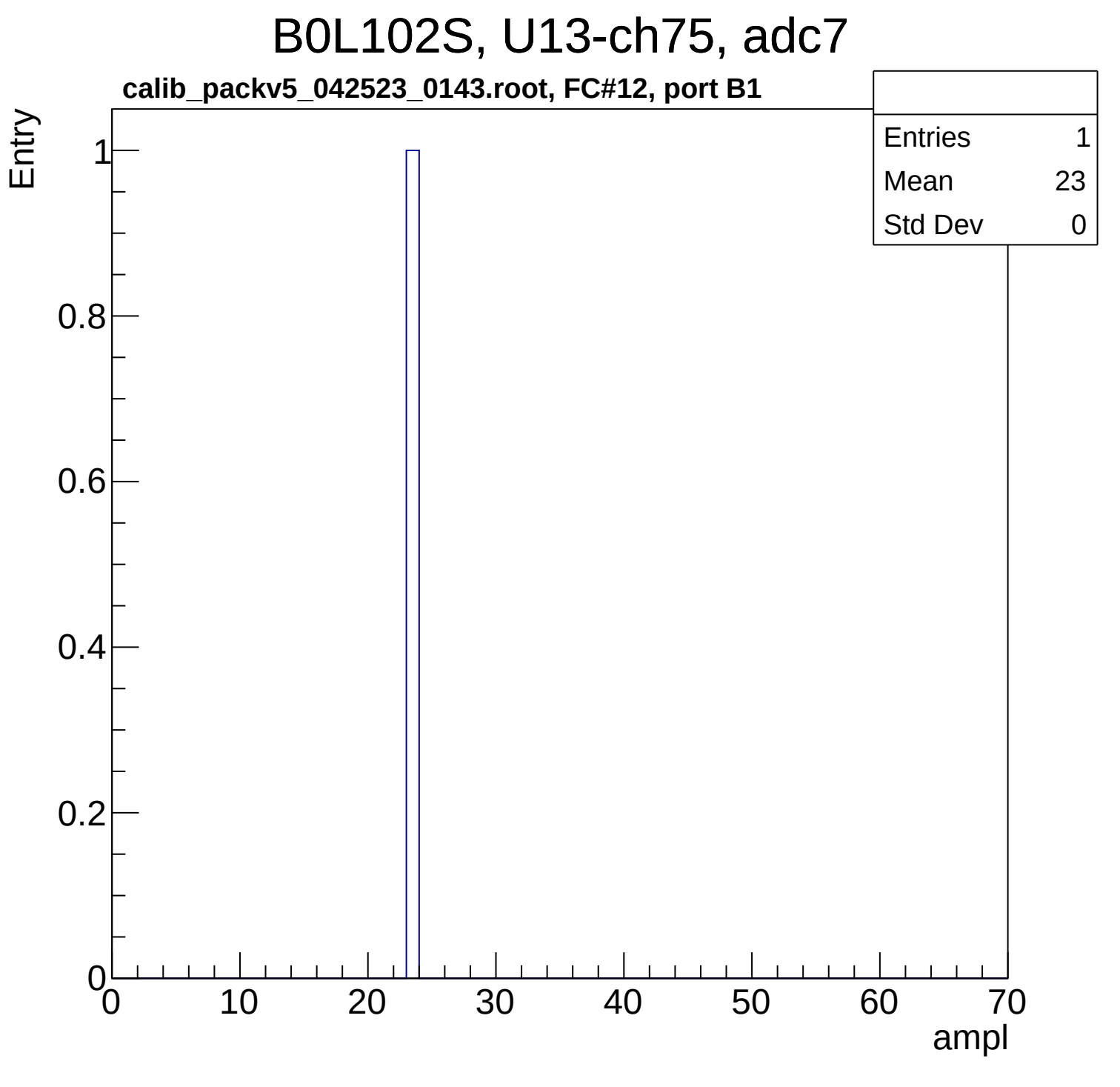
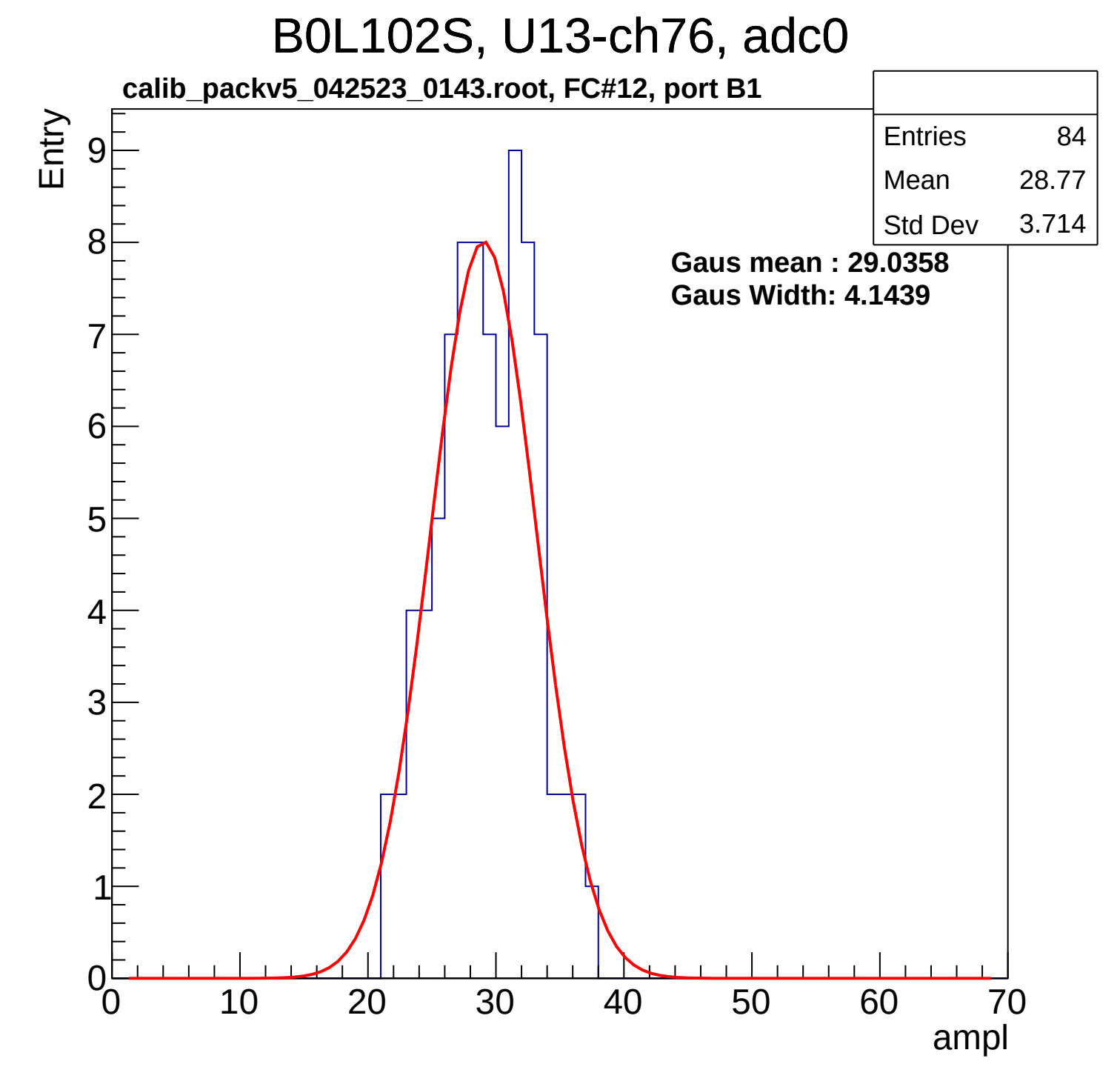
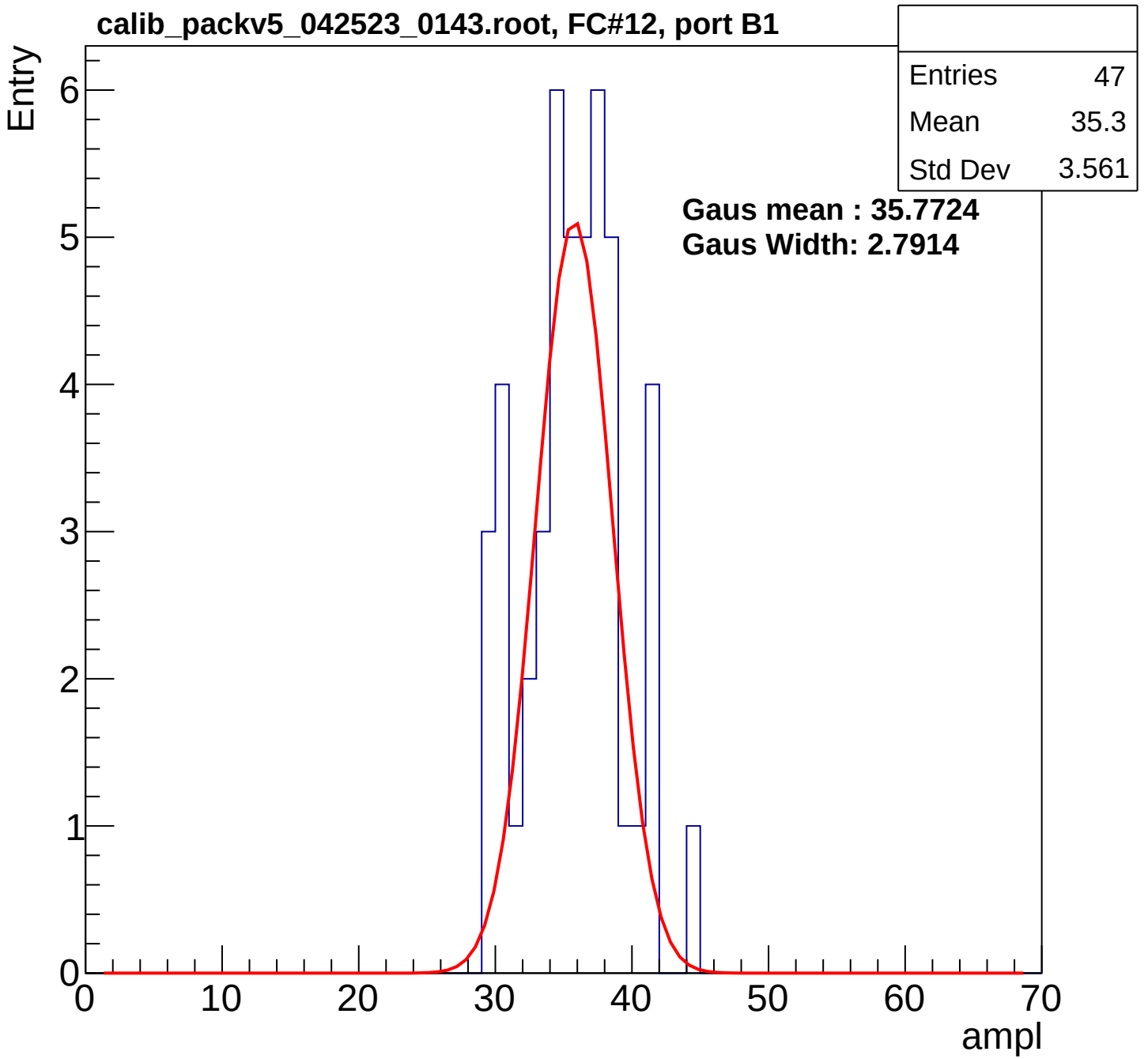
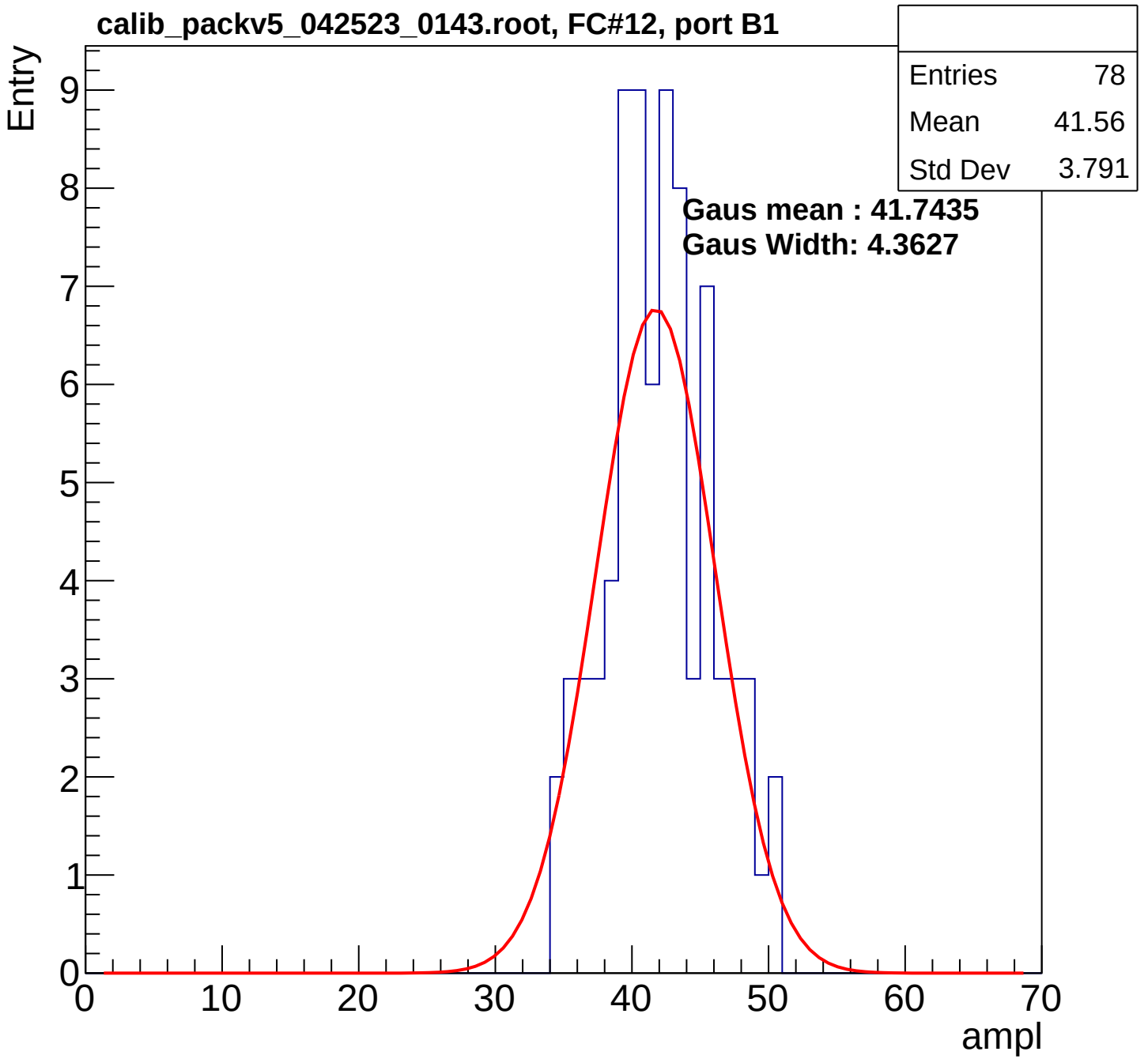

B0L102S, U13-ch75, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch75, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch76, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 28.77
Std Dev 3.714
Gaus mean : 29.0358
Gaus Width: 4.1439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch76, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 35.3
Std Dev 3.561
Gaus mean : 35.7724
Gaus Width: 2.7914
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch76, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 41.56
Std Dev 3.791
Gaus mean : 41.7435
Gaus Width: 4.3627
Entry
ampl
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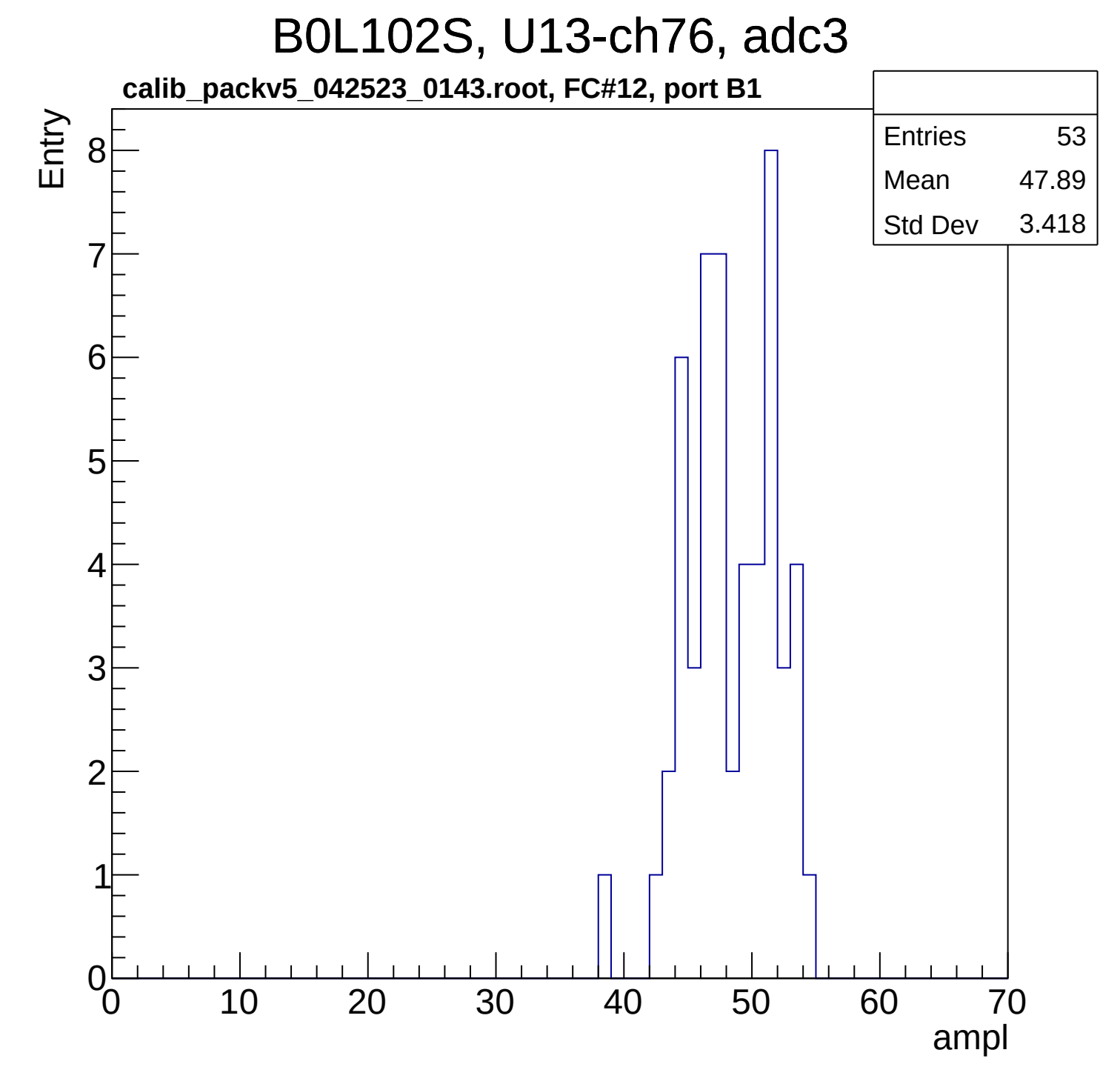
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch76, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 47.89
Std Dev 3.418
Entry
ampl
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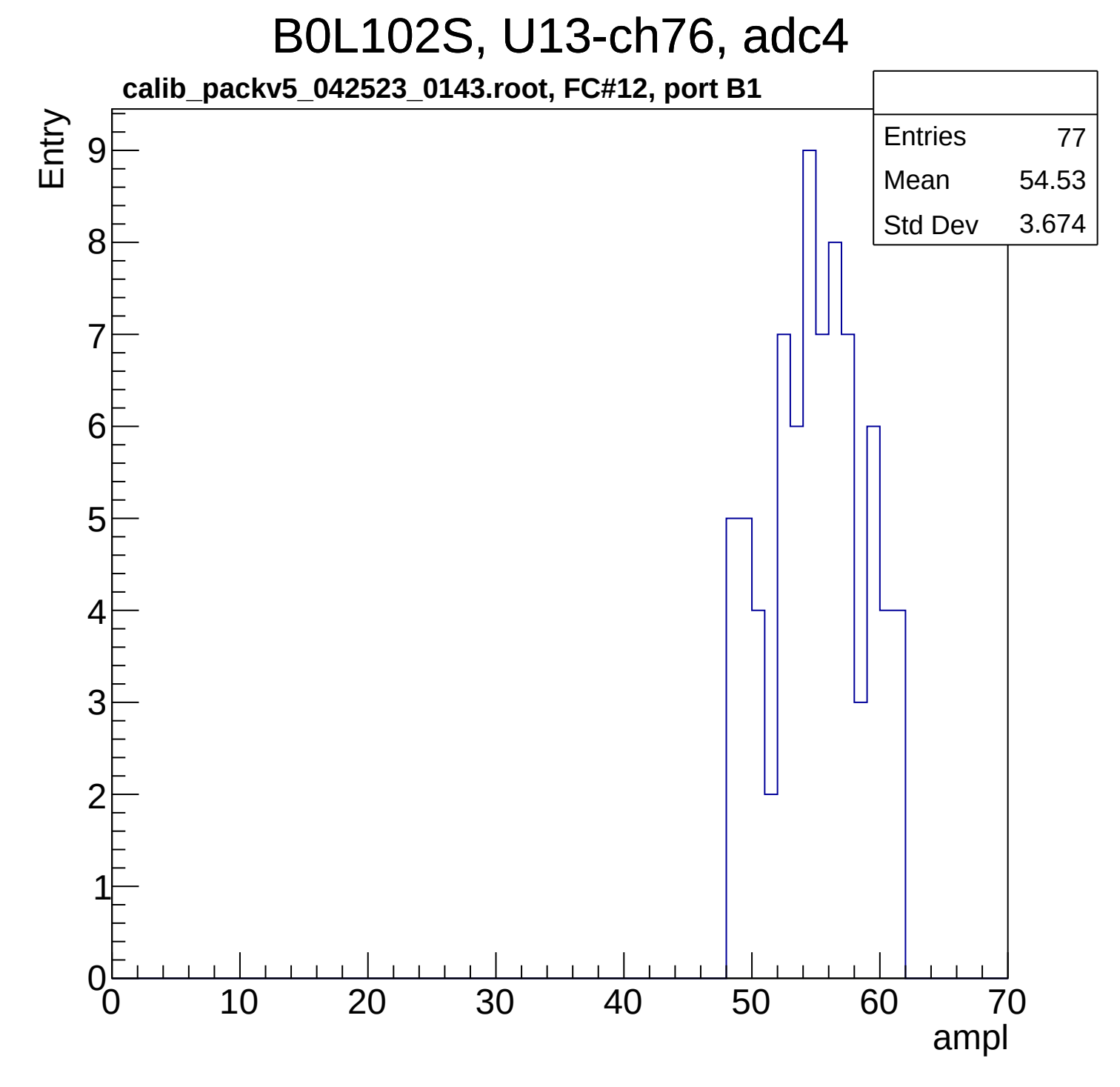
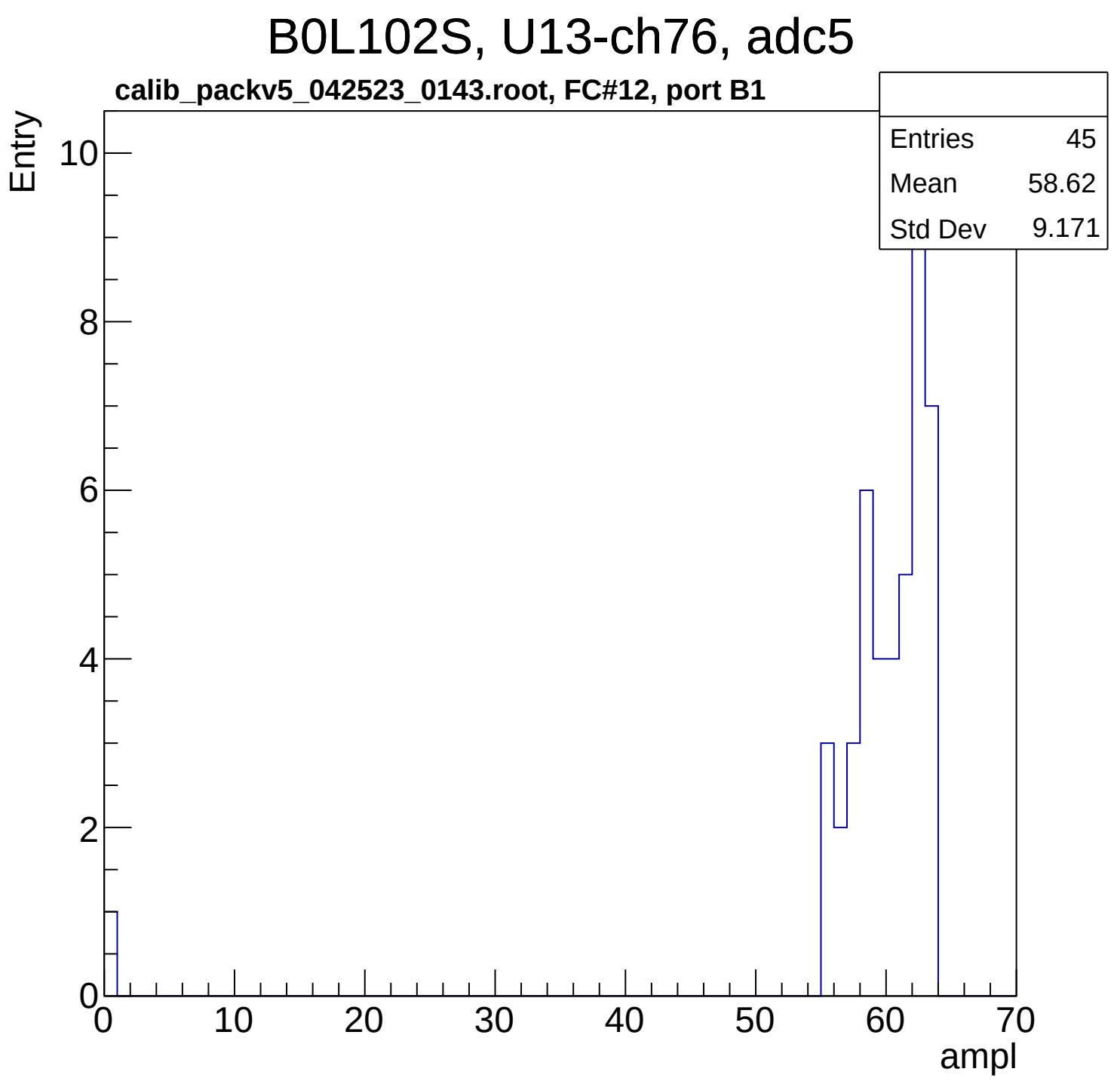
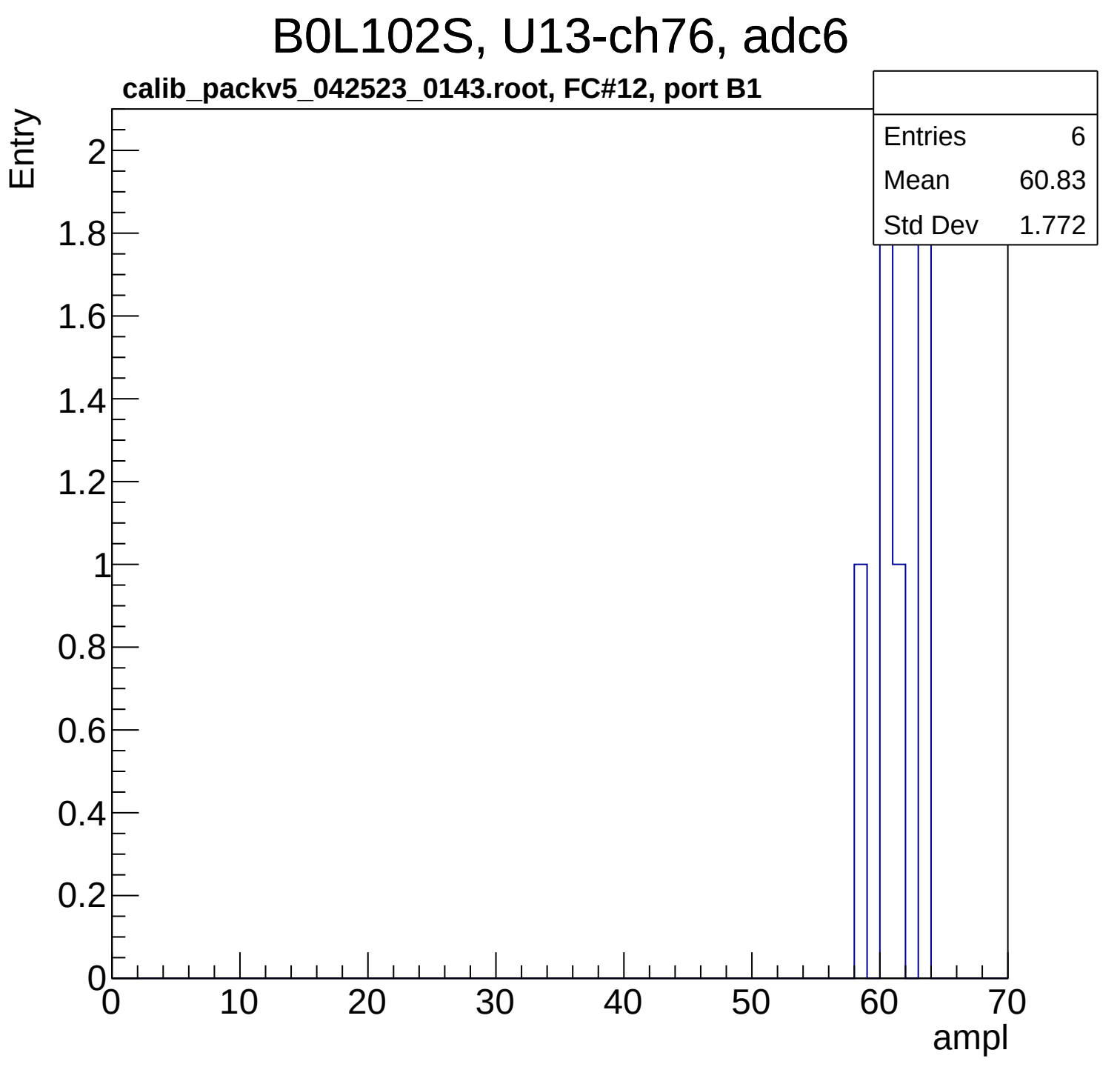
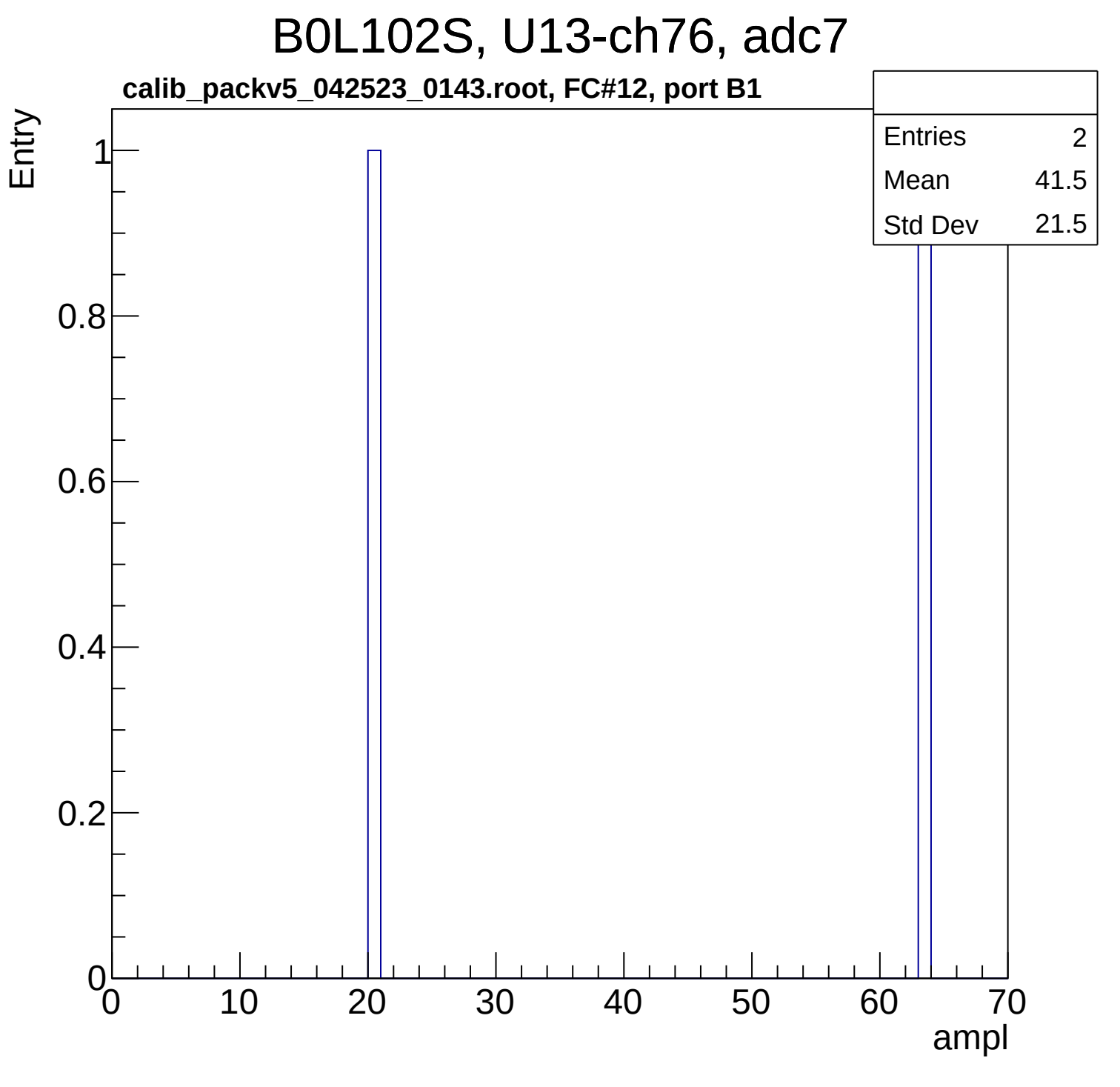
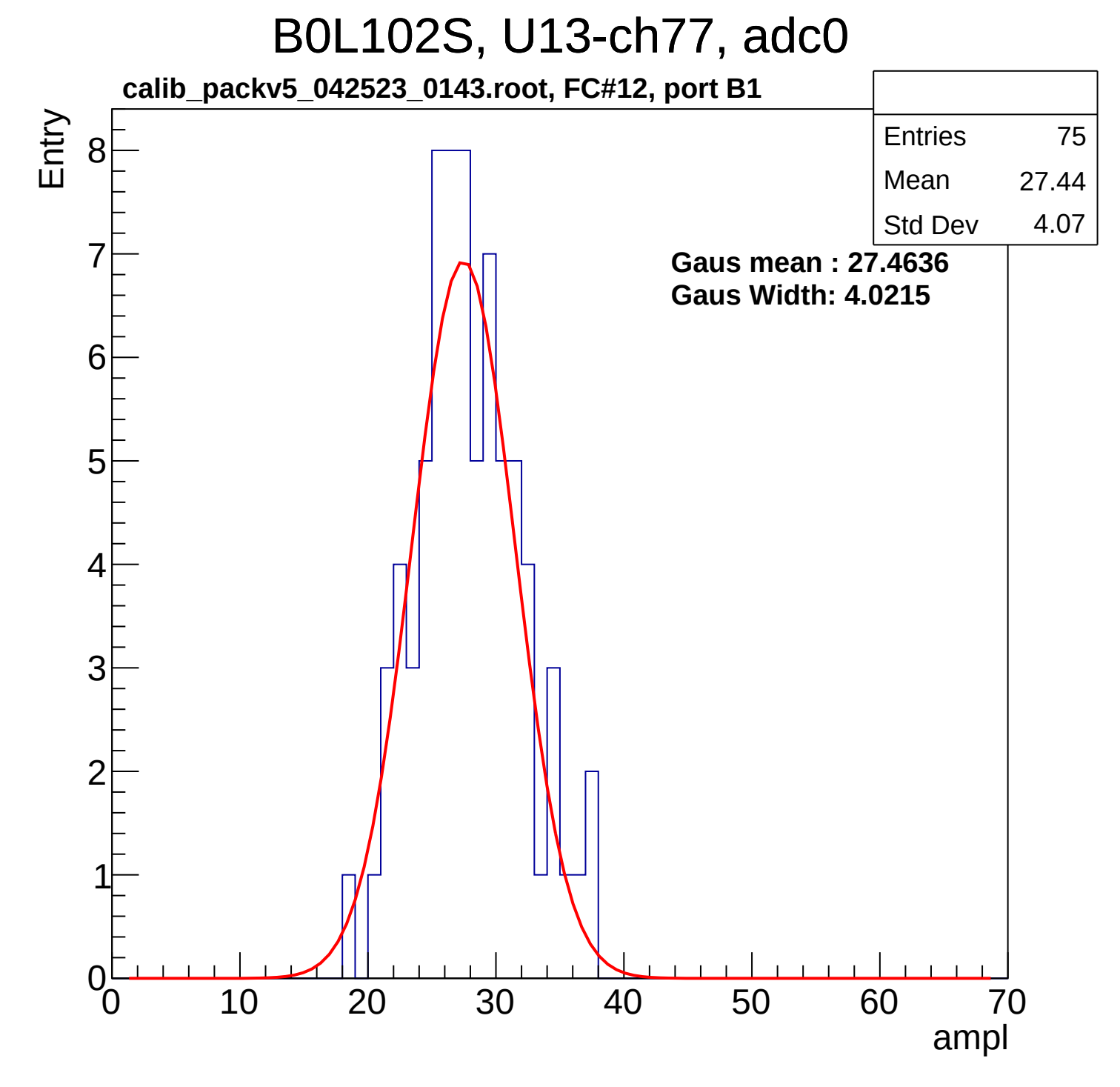
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

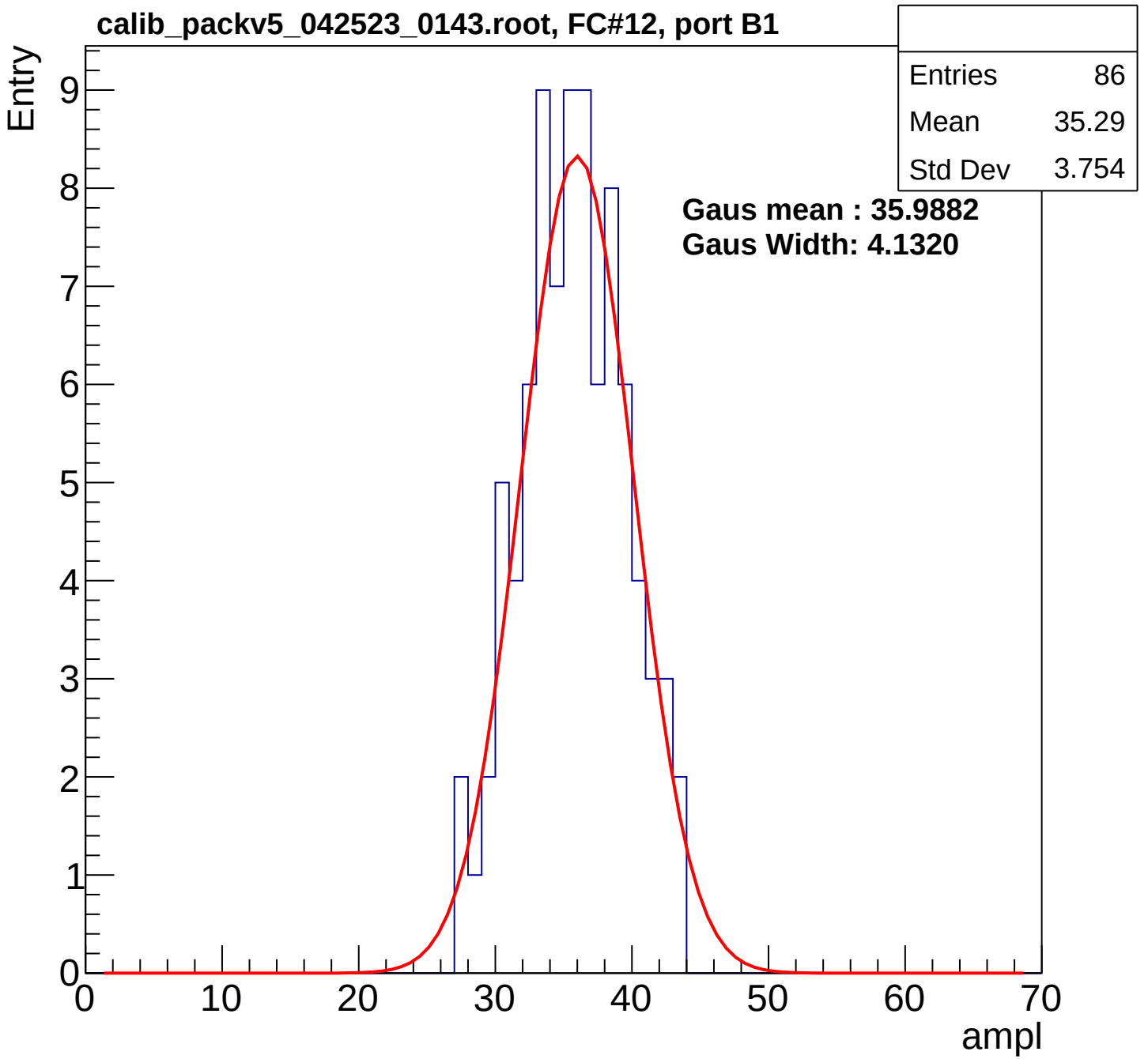

B0L102S, U13-ch76, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 54.53
Std Dev 3.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch76, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.62
Std Dev 9.171
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch76, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 60.83
Std Dev 1.772
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch76, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 41.5
Std Dev 21.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch77, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.44
Std Dev 4.07
Gaus mean : 27.4636
Gaus Width: 4.0215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch77, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 35.29
Std Dev 3.754
Gaus mean : 35.9882
Gaus Width: 4.1320
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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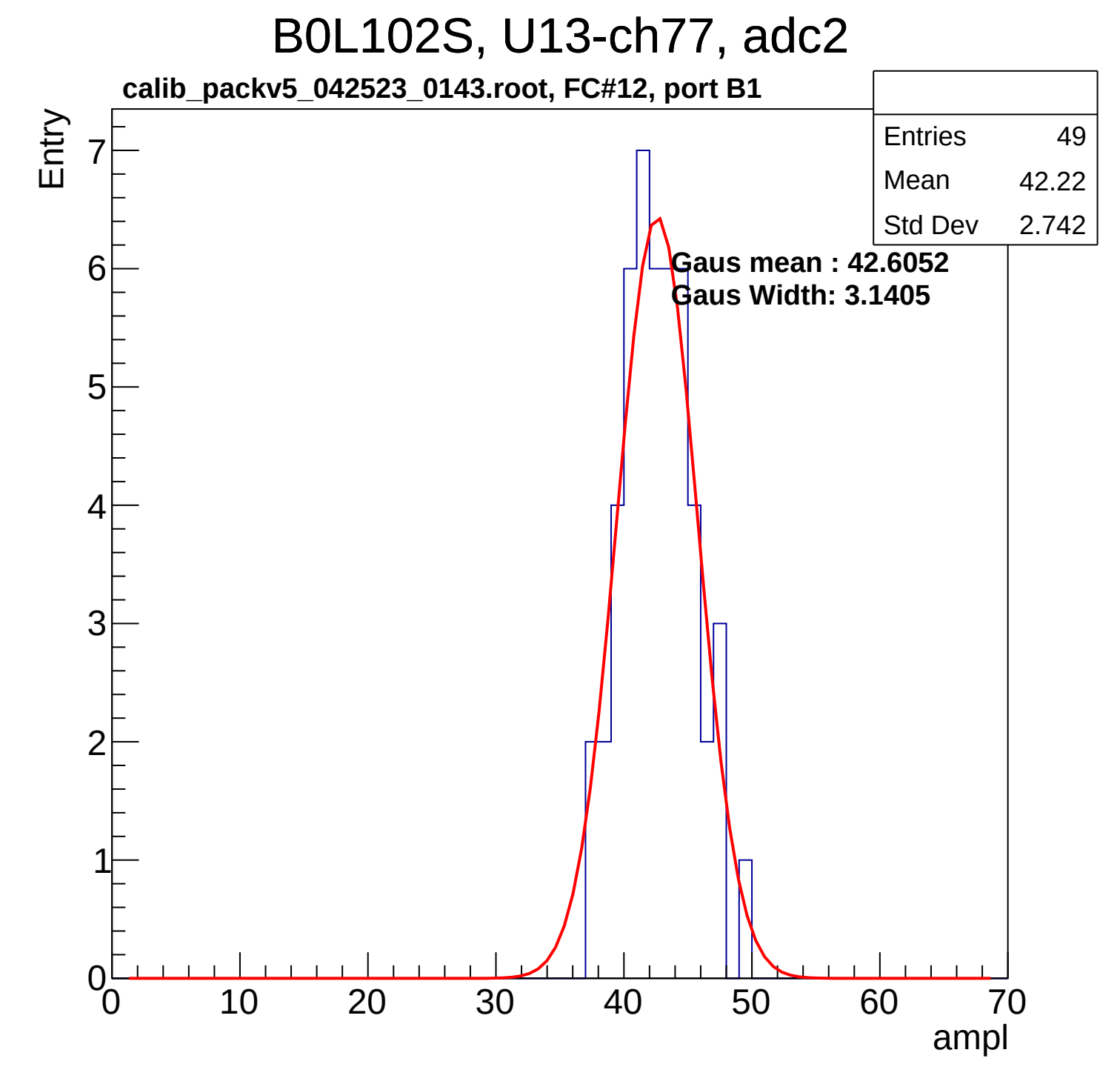
B0L102S, U13-ch77, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 42.22
Std Dev 2.742
Gaus mean : 42.6052
Gaus Width: 3.1405
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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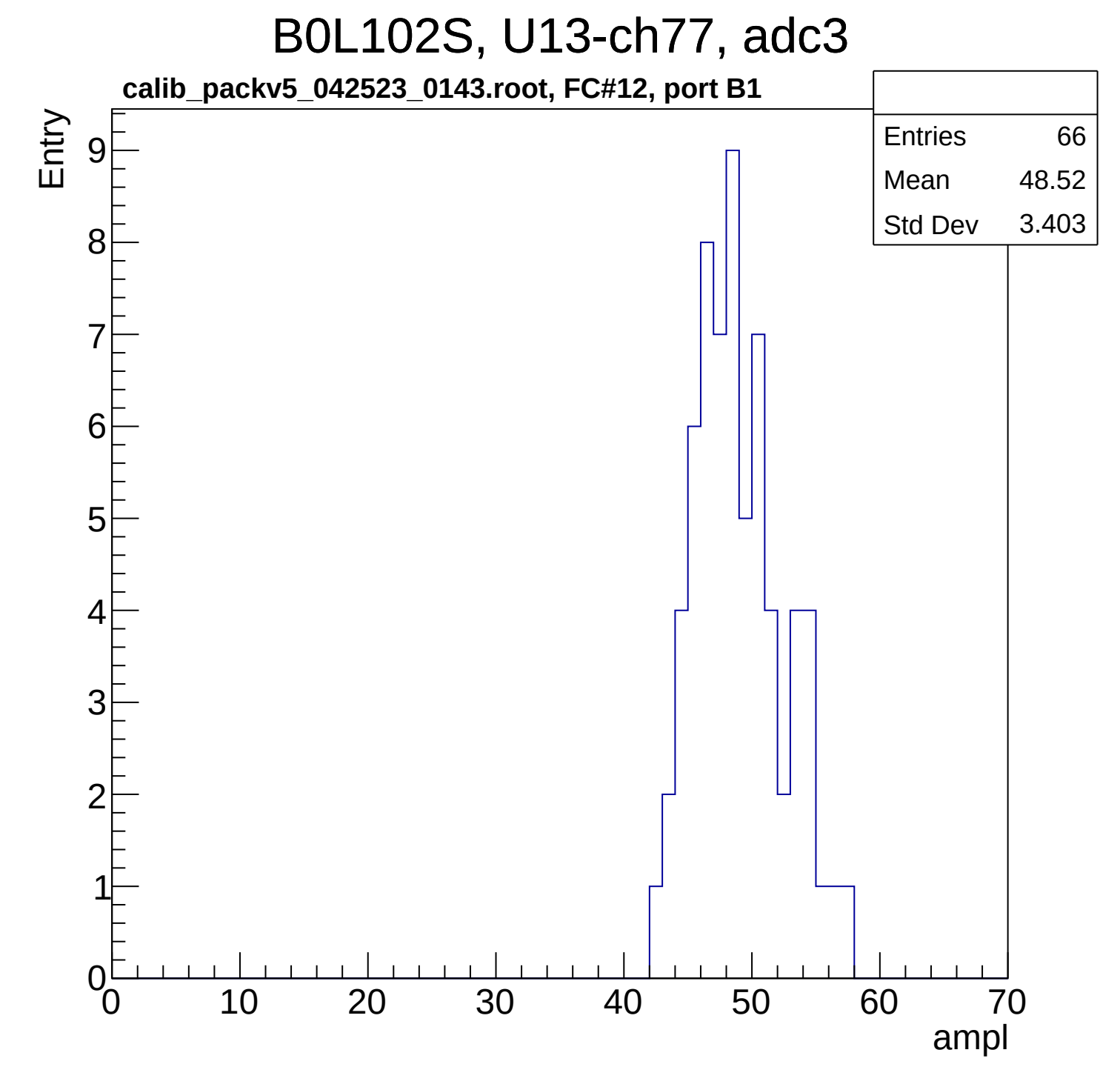
B0L102S, U13-ch77, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 48.52
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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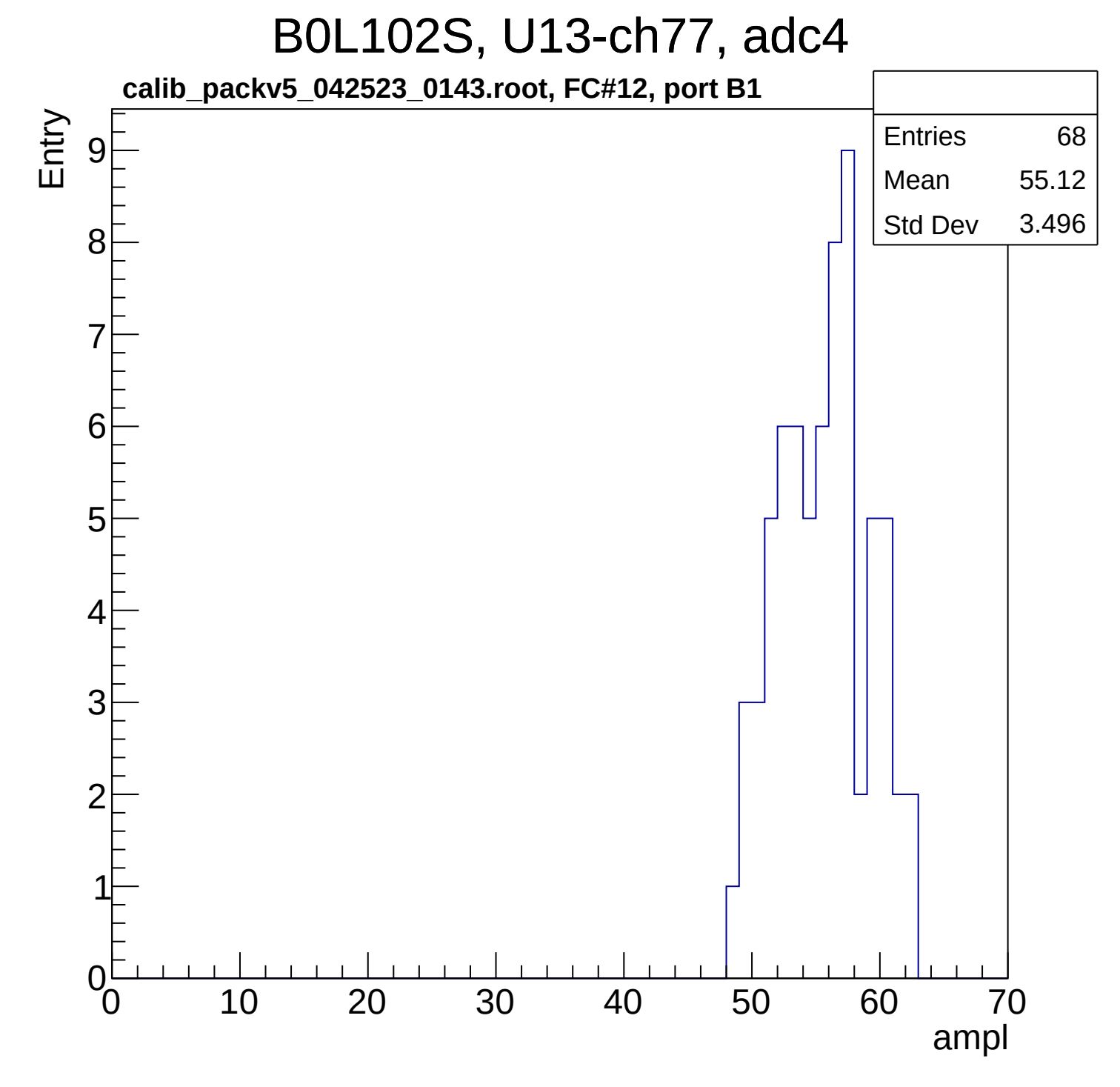
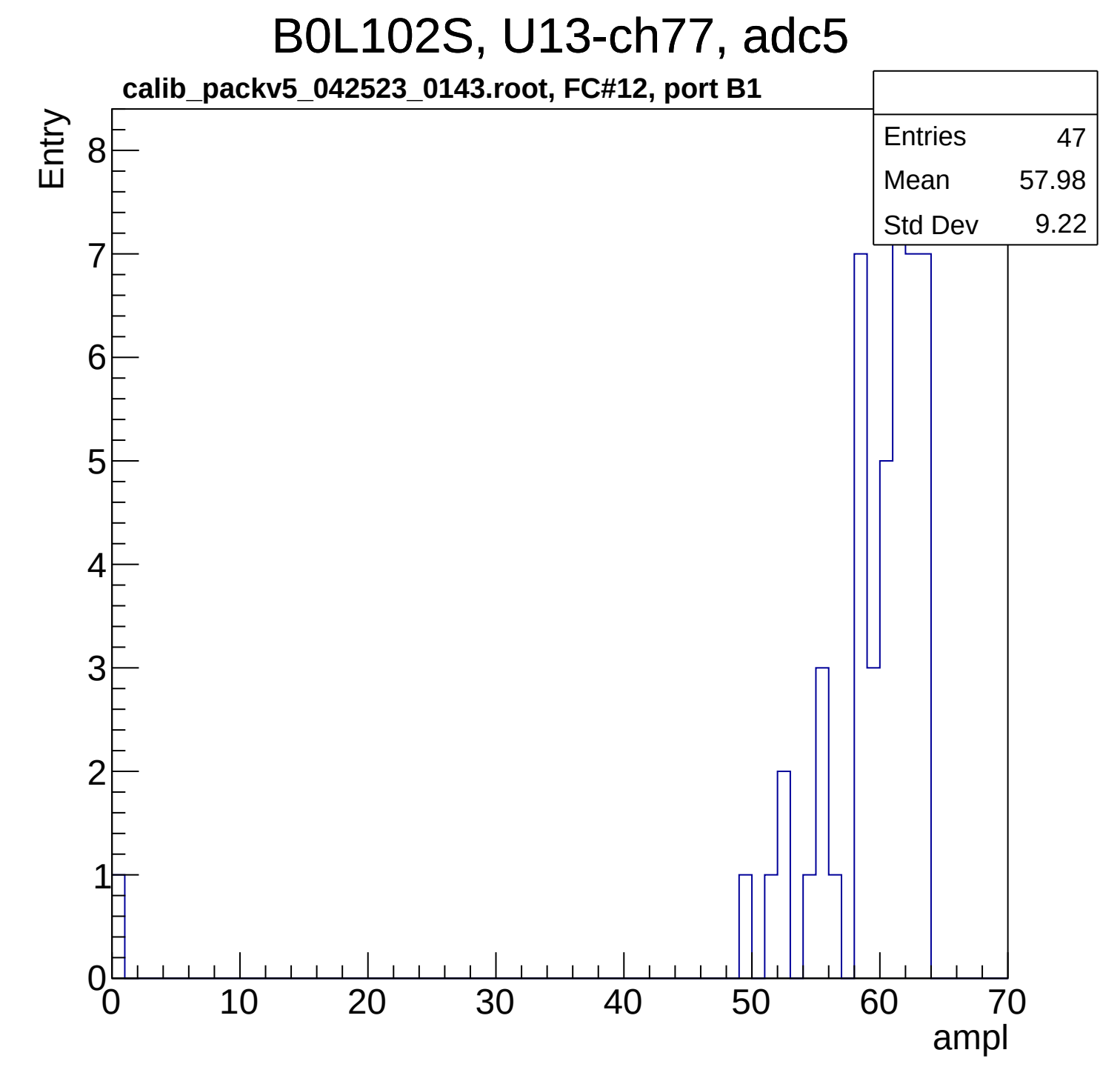
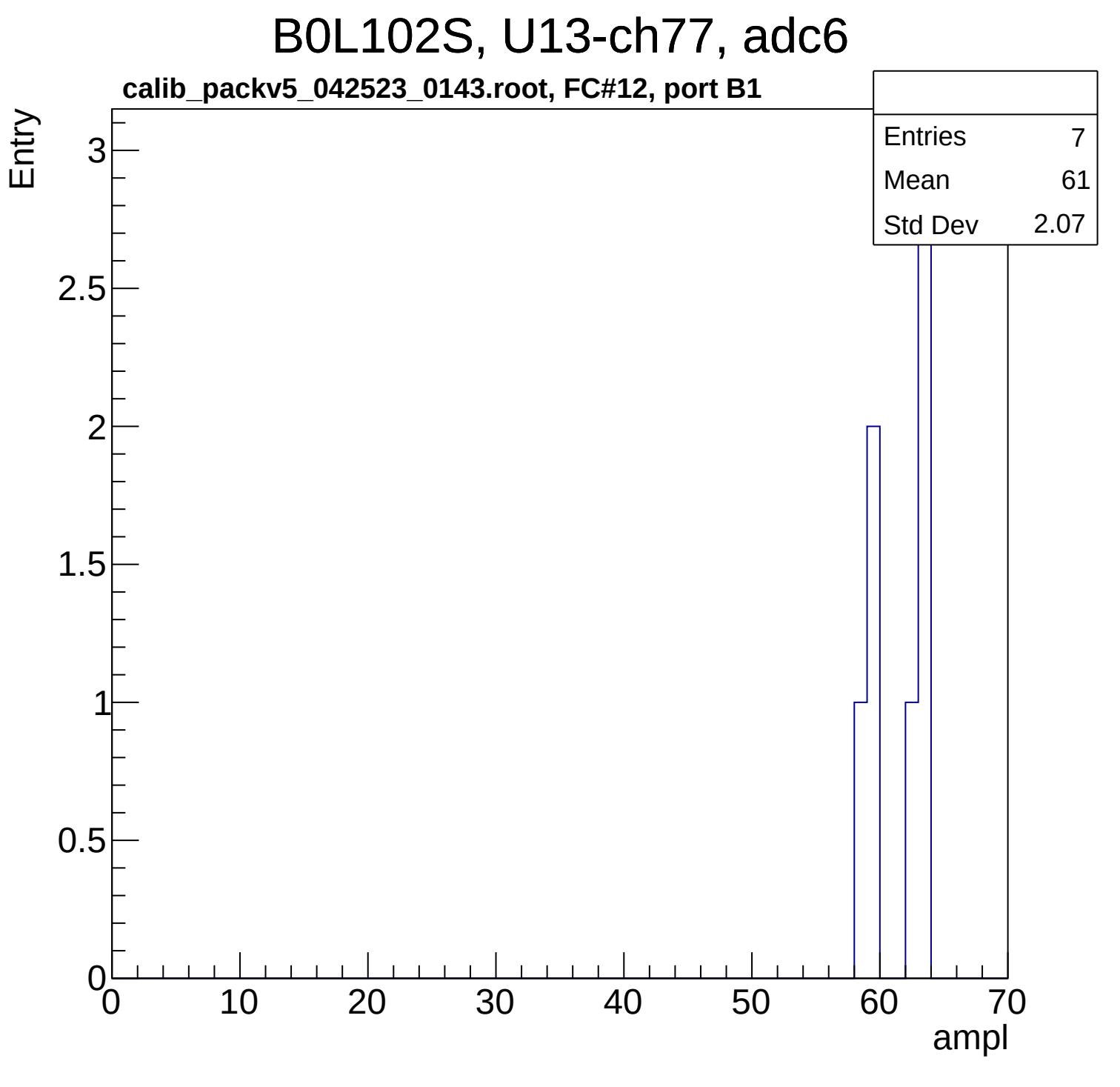
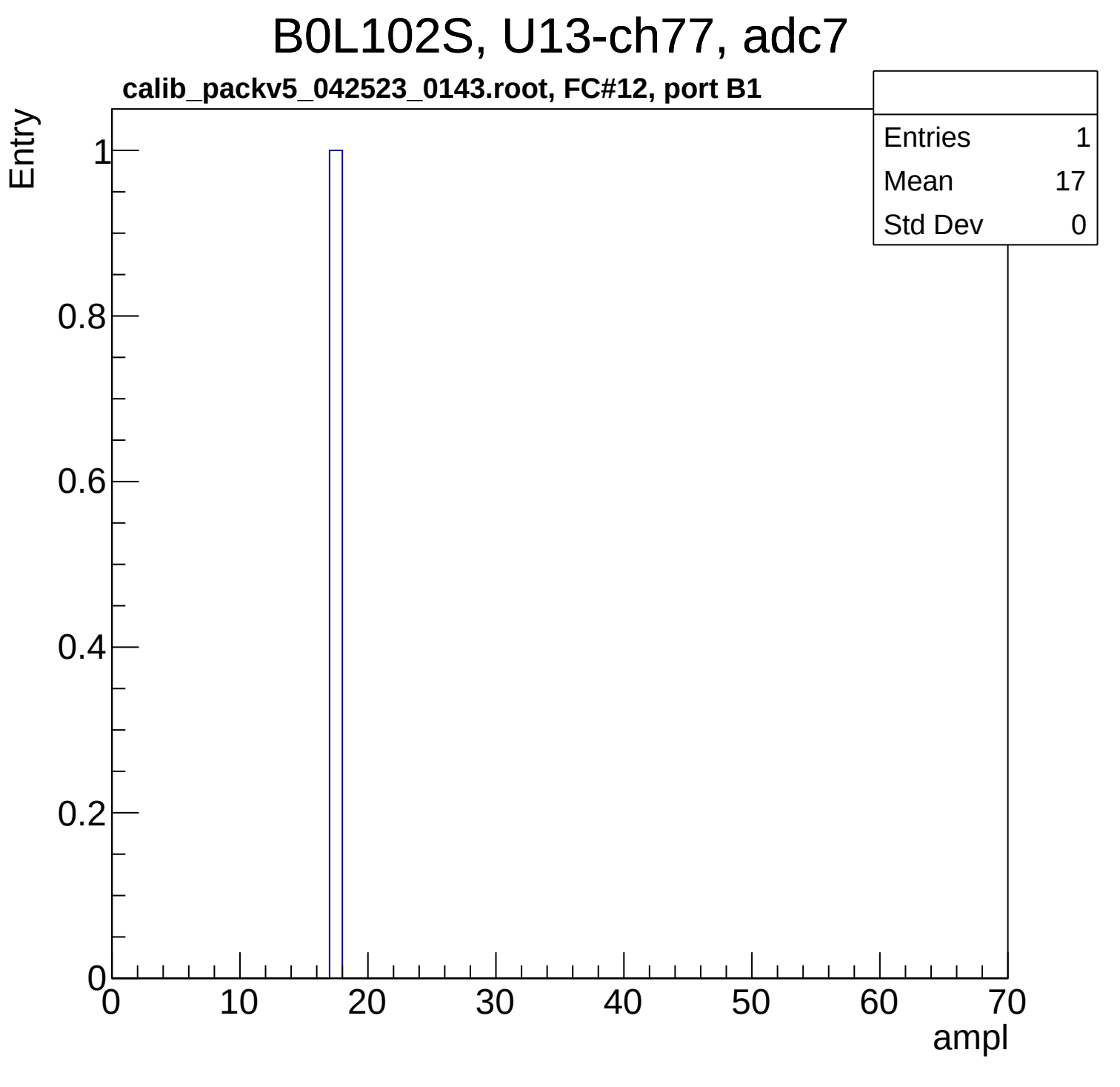
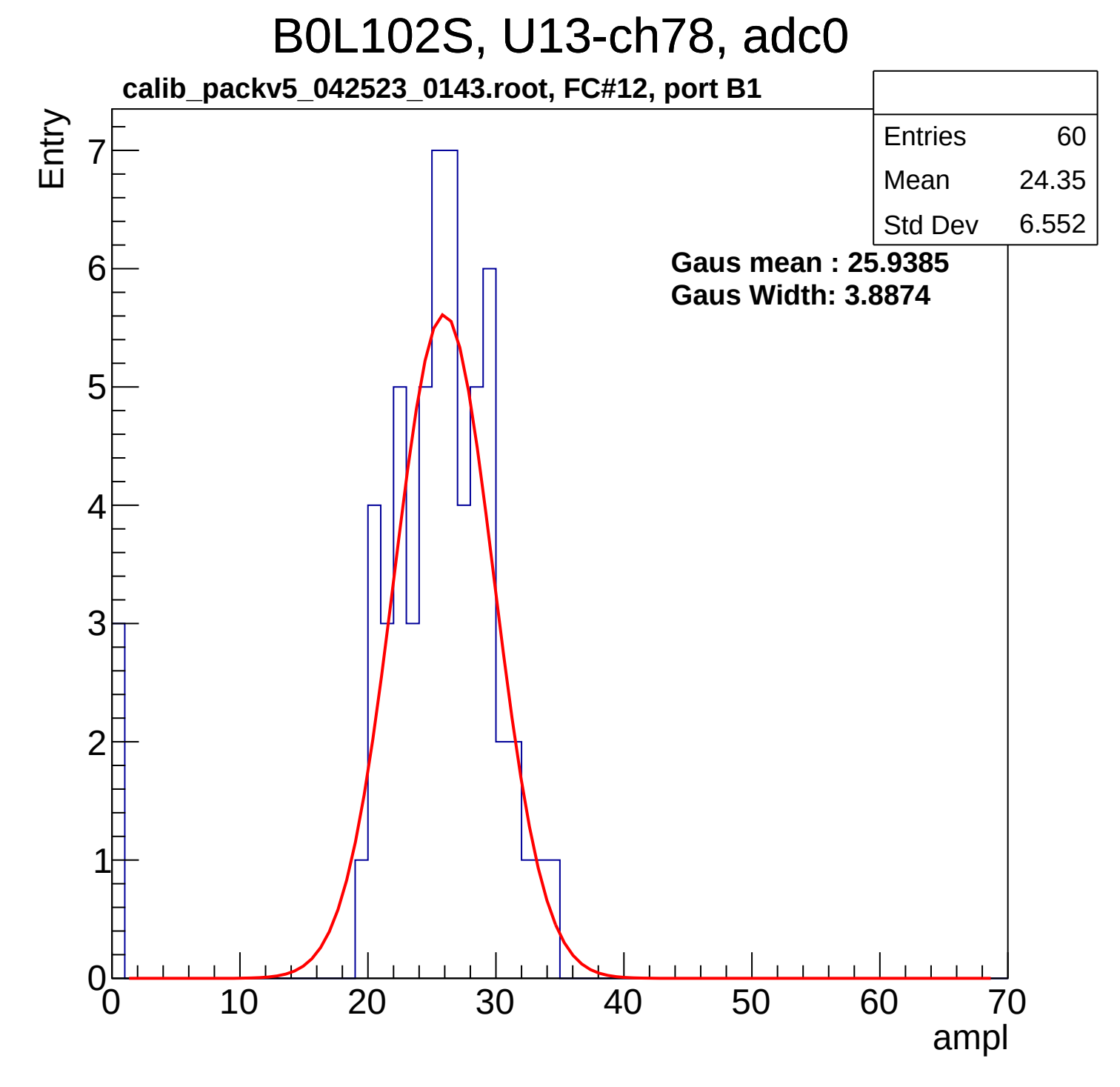
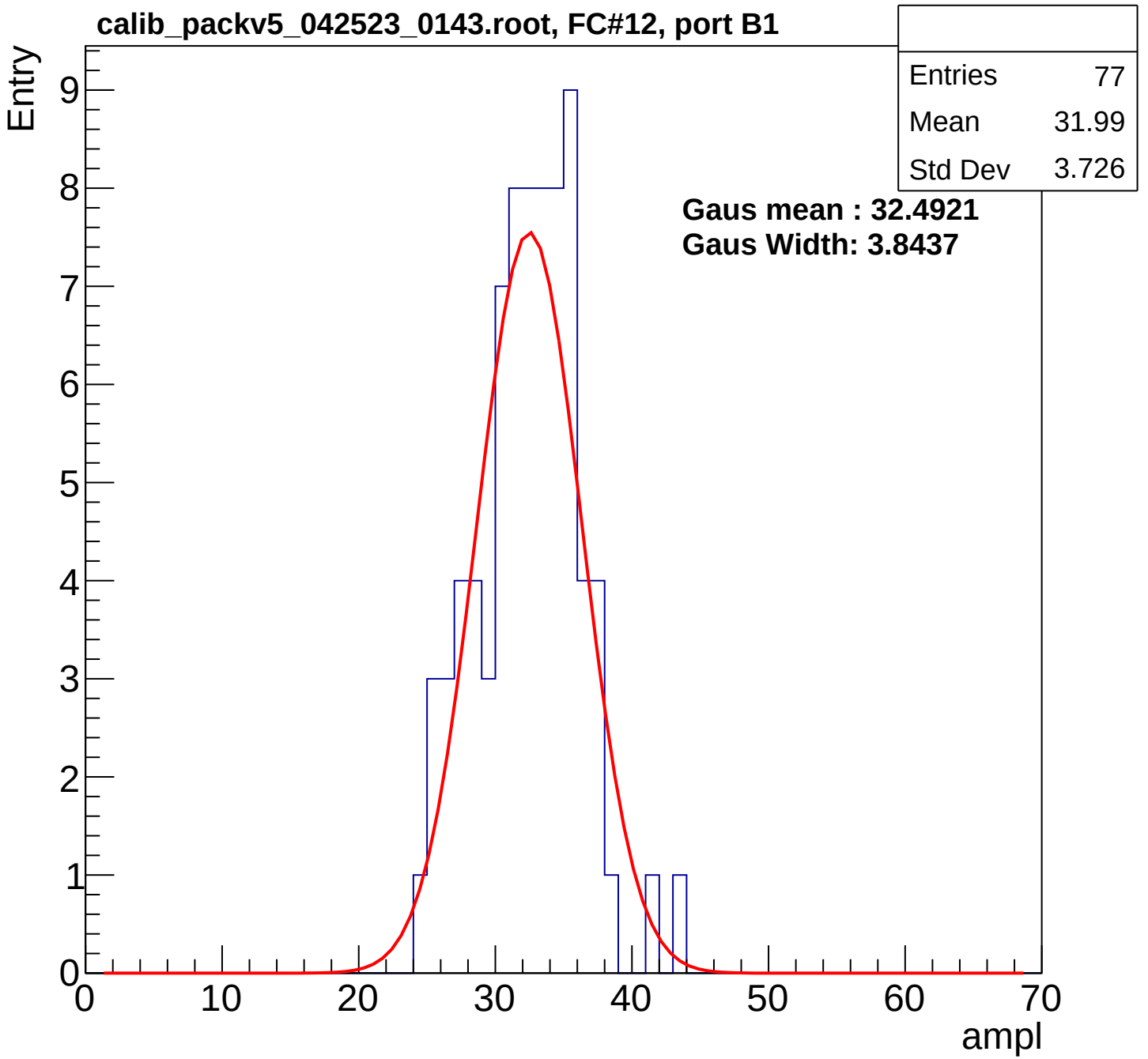

B0L102S, U13-ch77, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55.12
Std Dev 3.496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

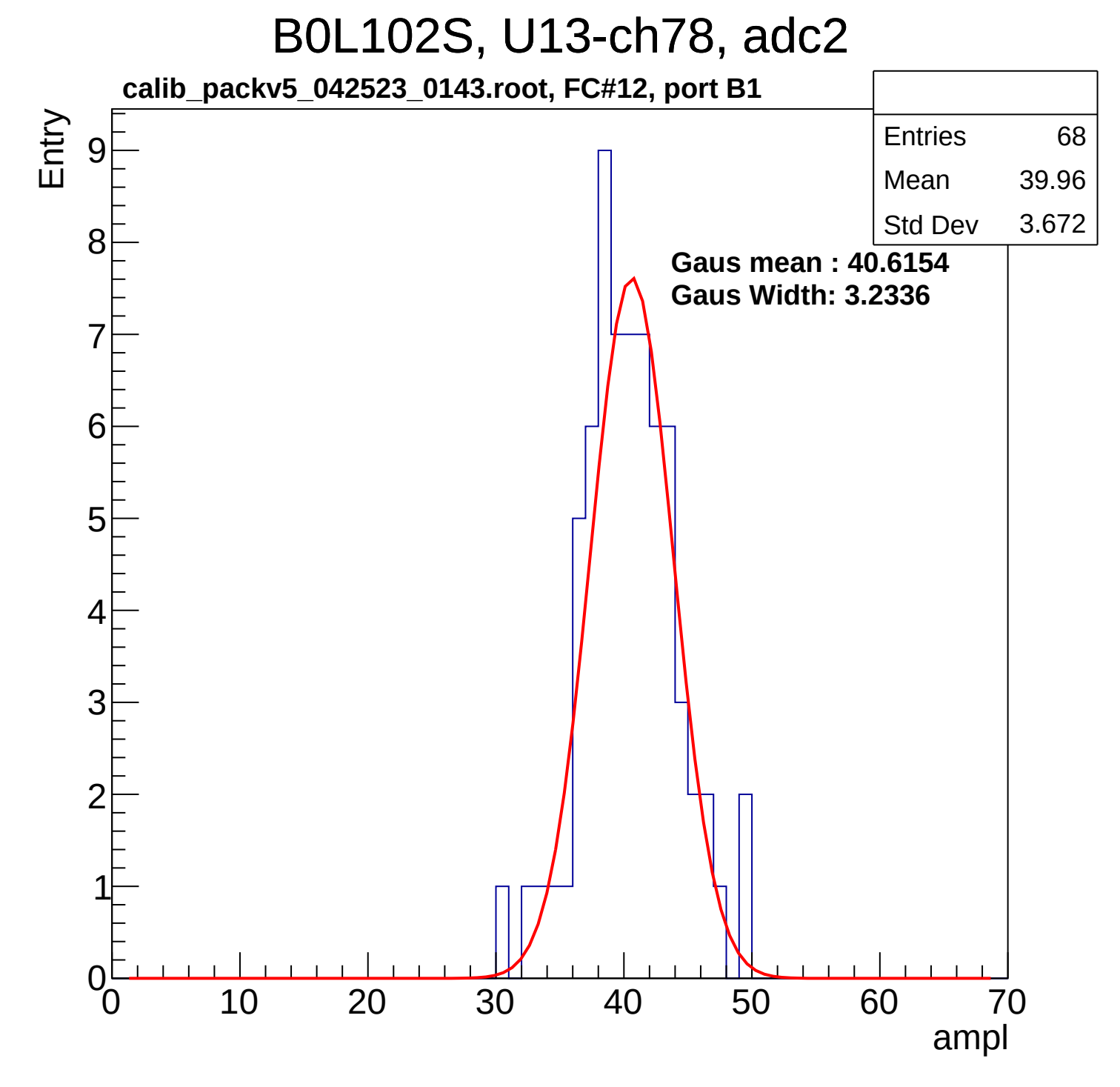

B0L102S, U13-ch77, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 57.98
Std Dev 9.22
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

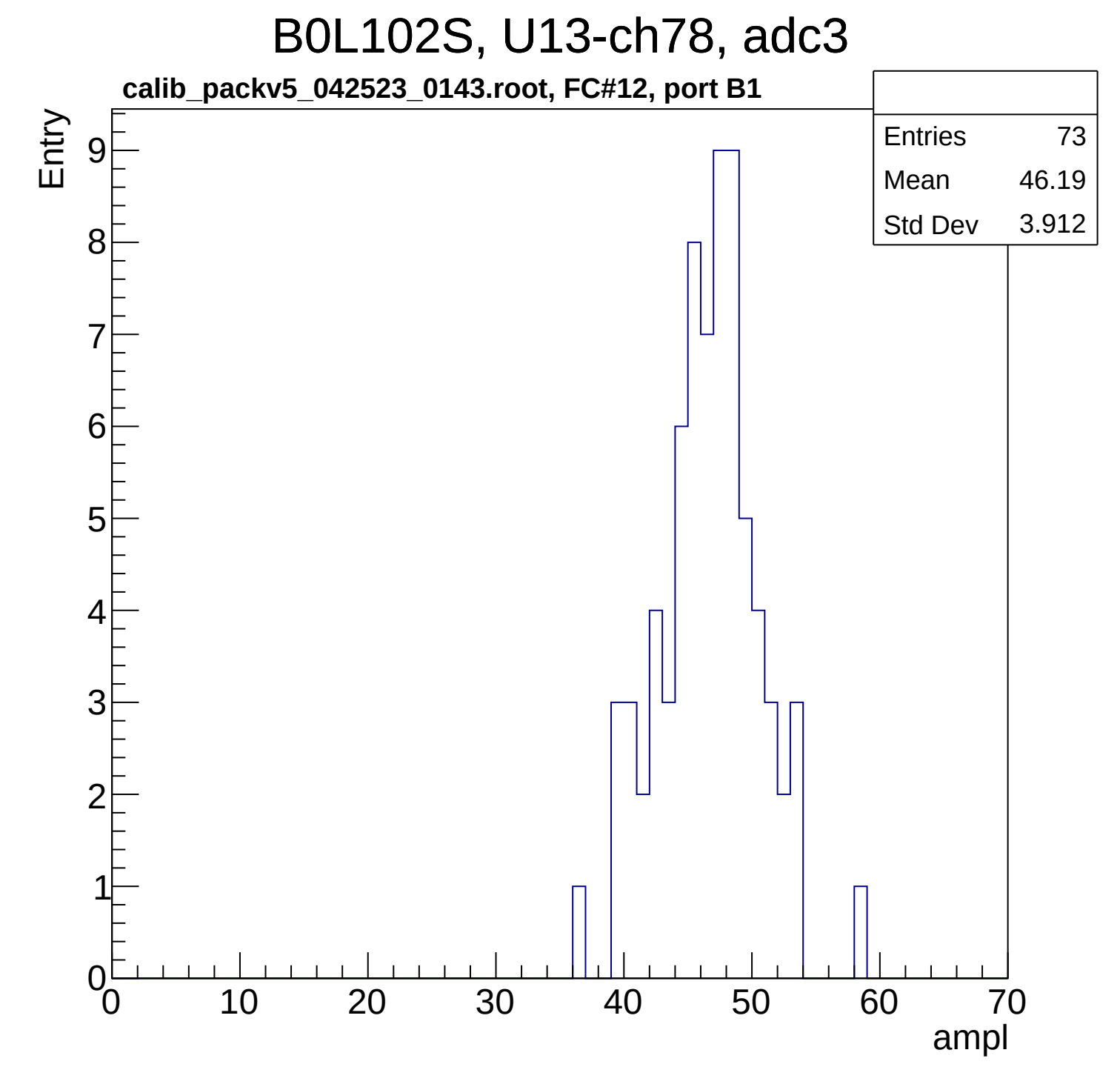

B0L102S, U13-ch77, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61
Std Dev 2.07
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

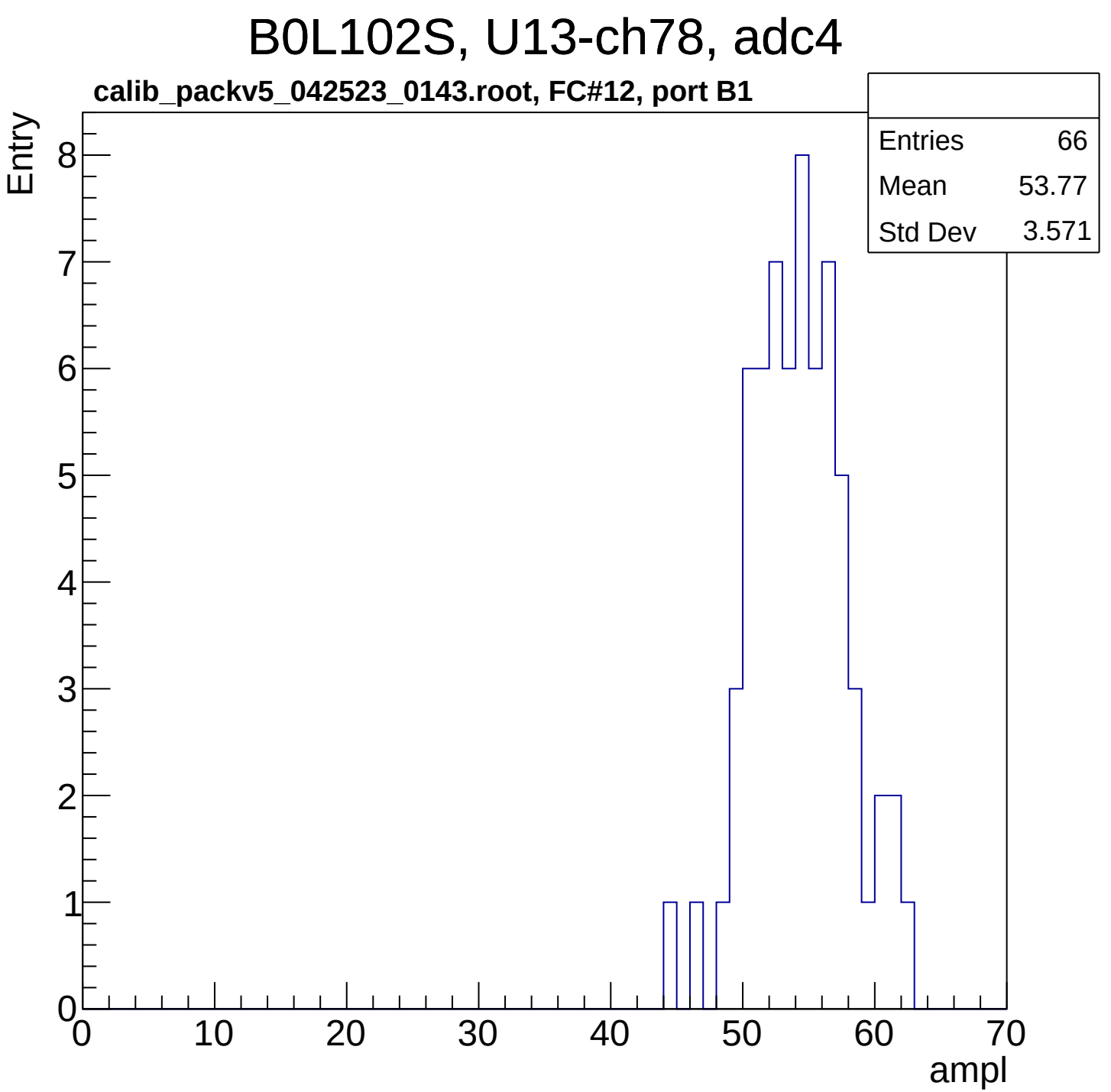

B0L102S, U13-ch77, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 17
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch78, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 24.35
Std Dev 6.552
Gaus mean : 25.9385
Gaus Width: 3.8874
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch78, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 31.99
Std Dev 3.726
Gaus mean : 32.4921
Gaus Width: 3.8437
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch78, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 39.96
Std Dev 3.672
Gaus mean : 40.6154
Gaus Width: 3.2336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch78, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 46.19
Std Dev 3.912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch78, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 53.77
Std Dev 3.571
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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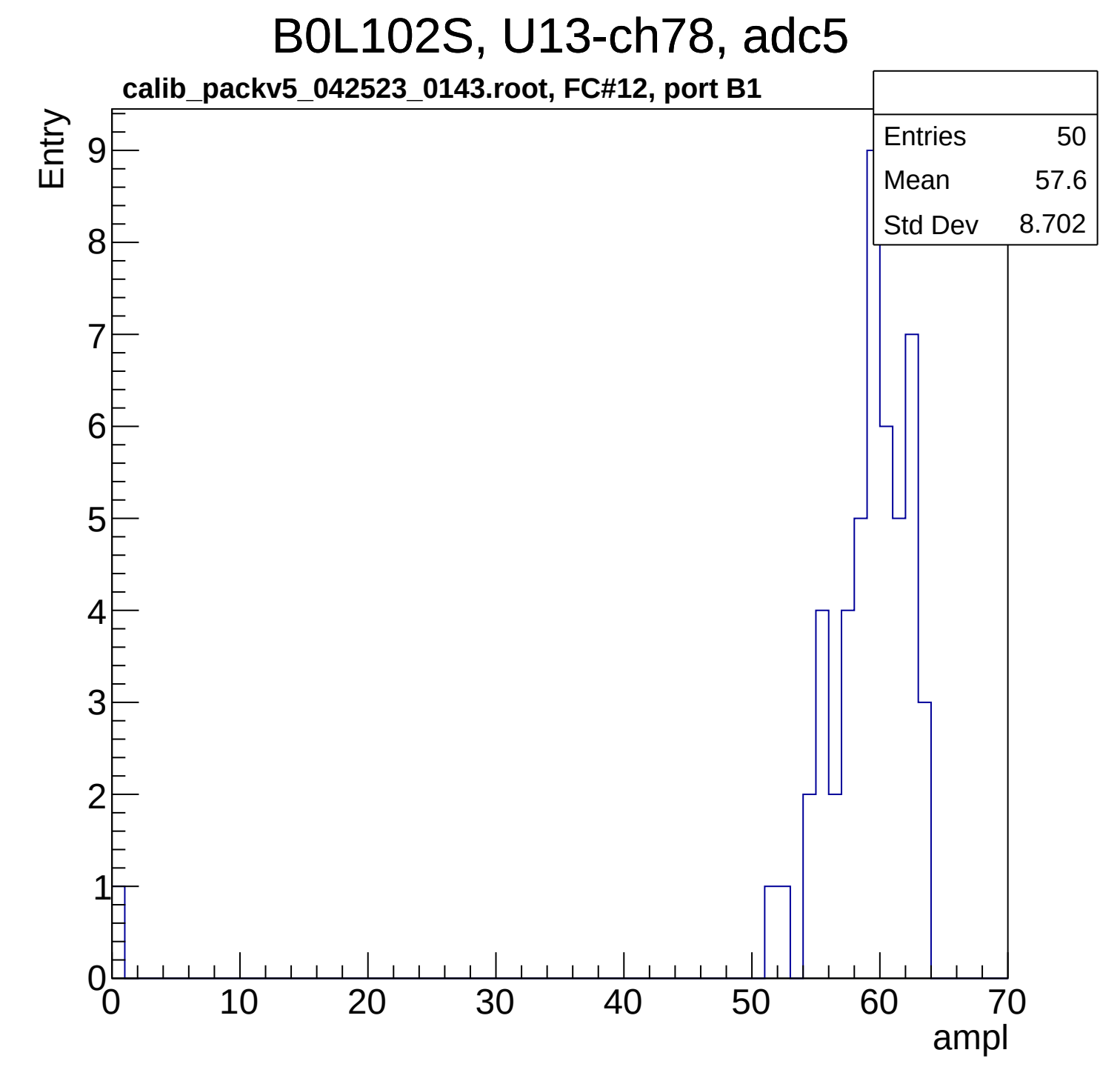
B0L102S, U13-ch78, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 57.6
Std Dev 8.702
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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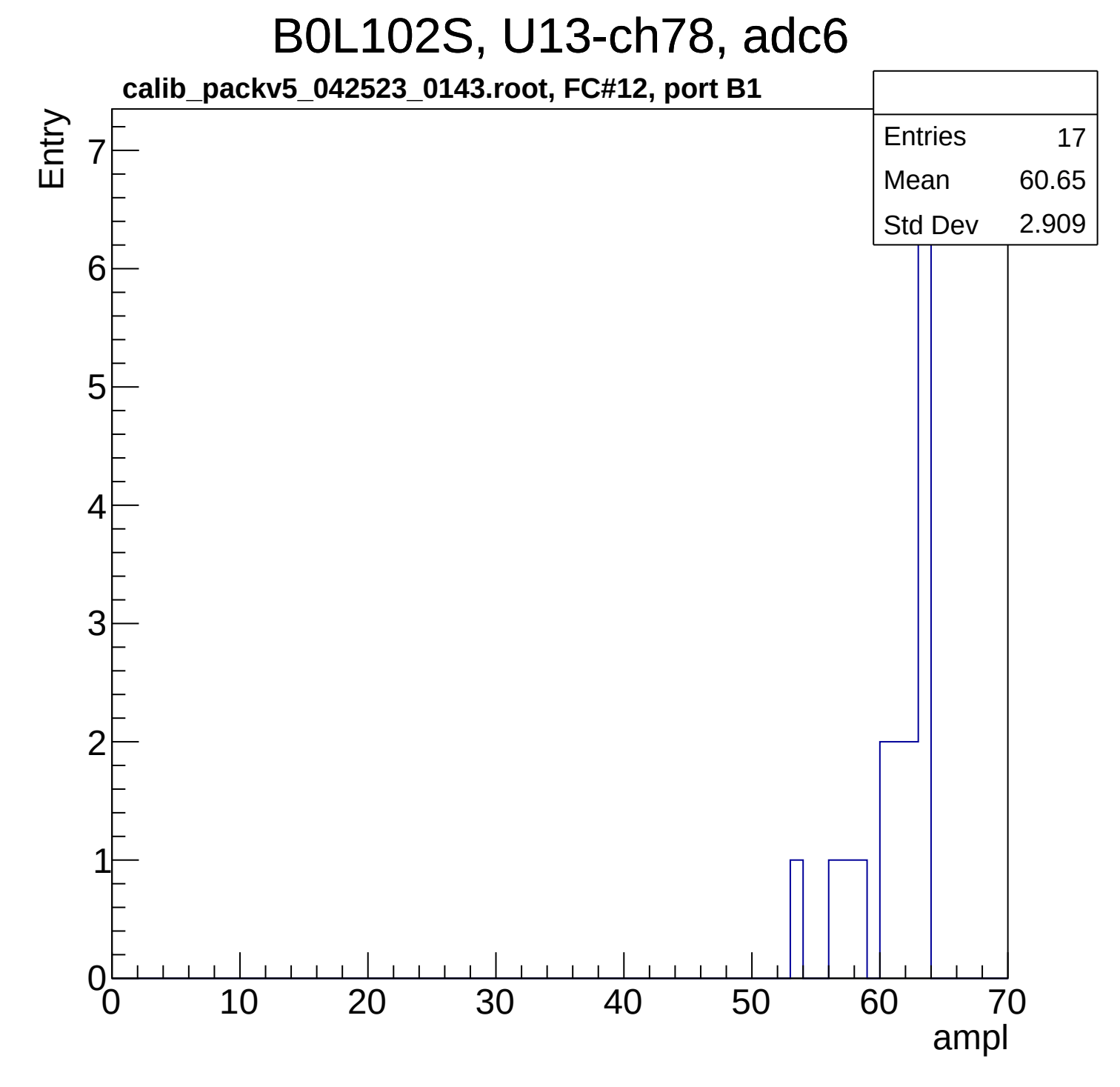
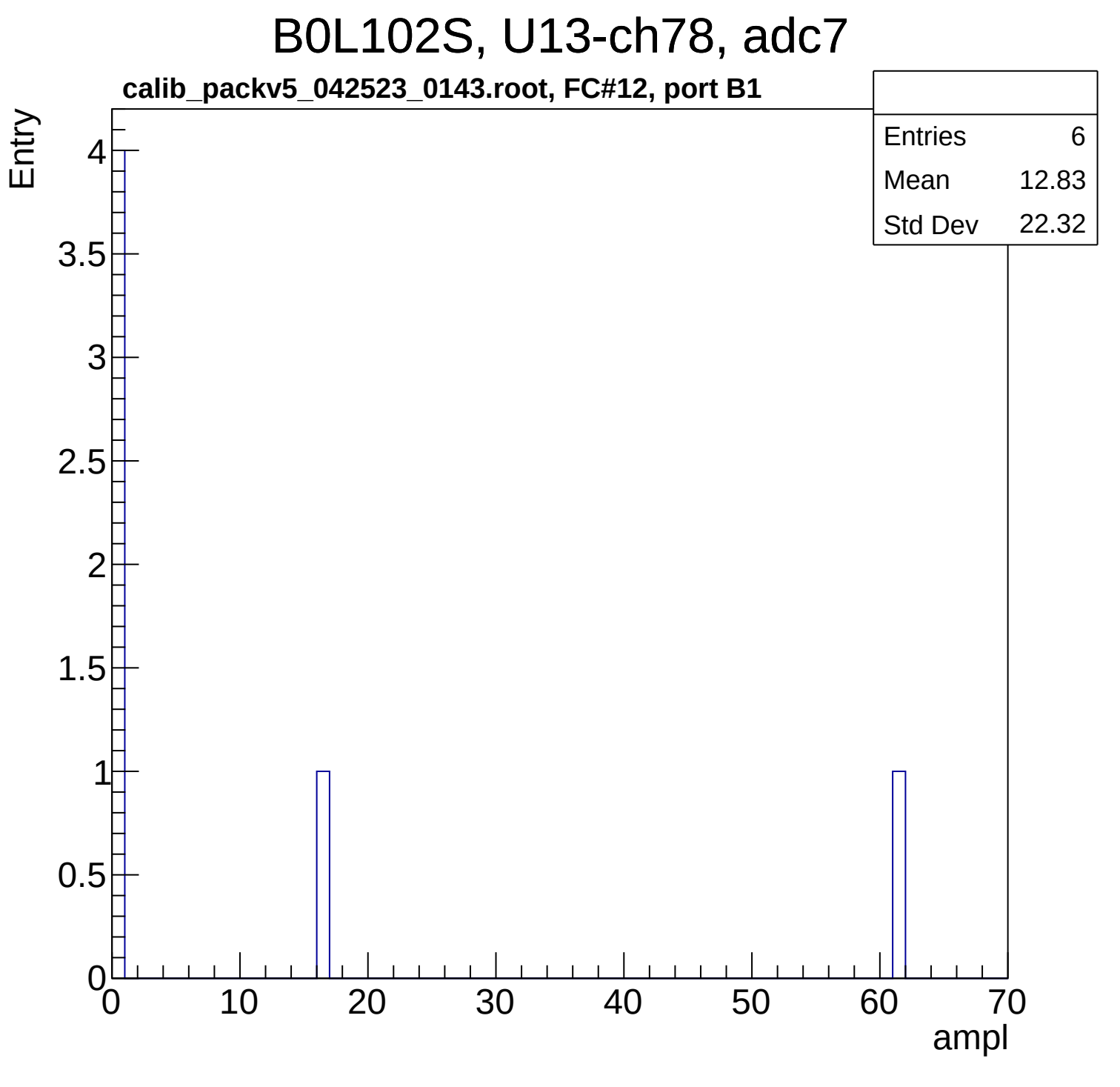

B0L102S, U13-ch78, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 60.65
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch78, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 12.83
Std Dev 22.32
Entry
ampl
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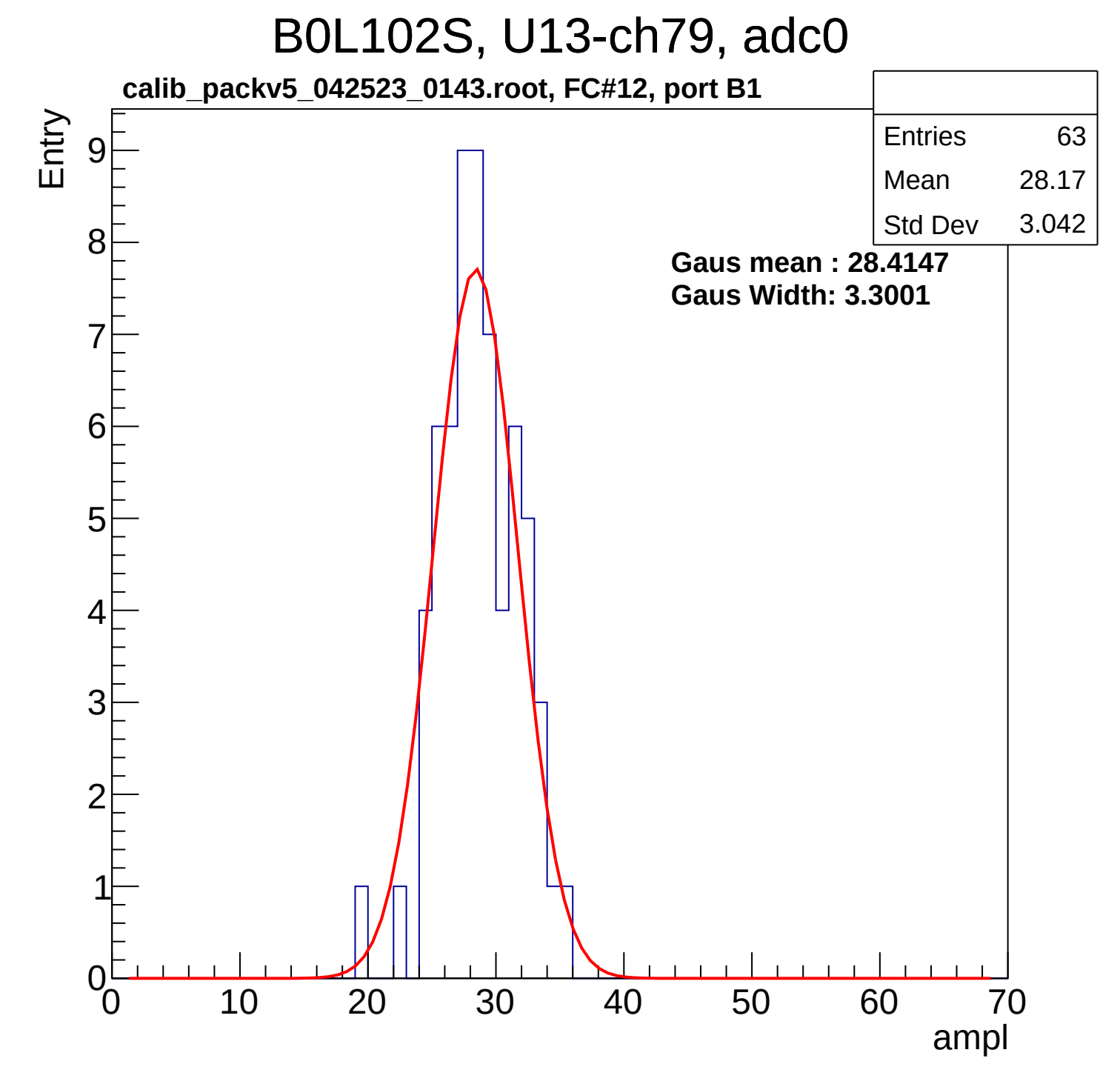
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch79, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.17
Std Dev 3.042
Gaus mean : 28.4147
Gaus Width: 3.3001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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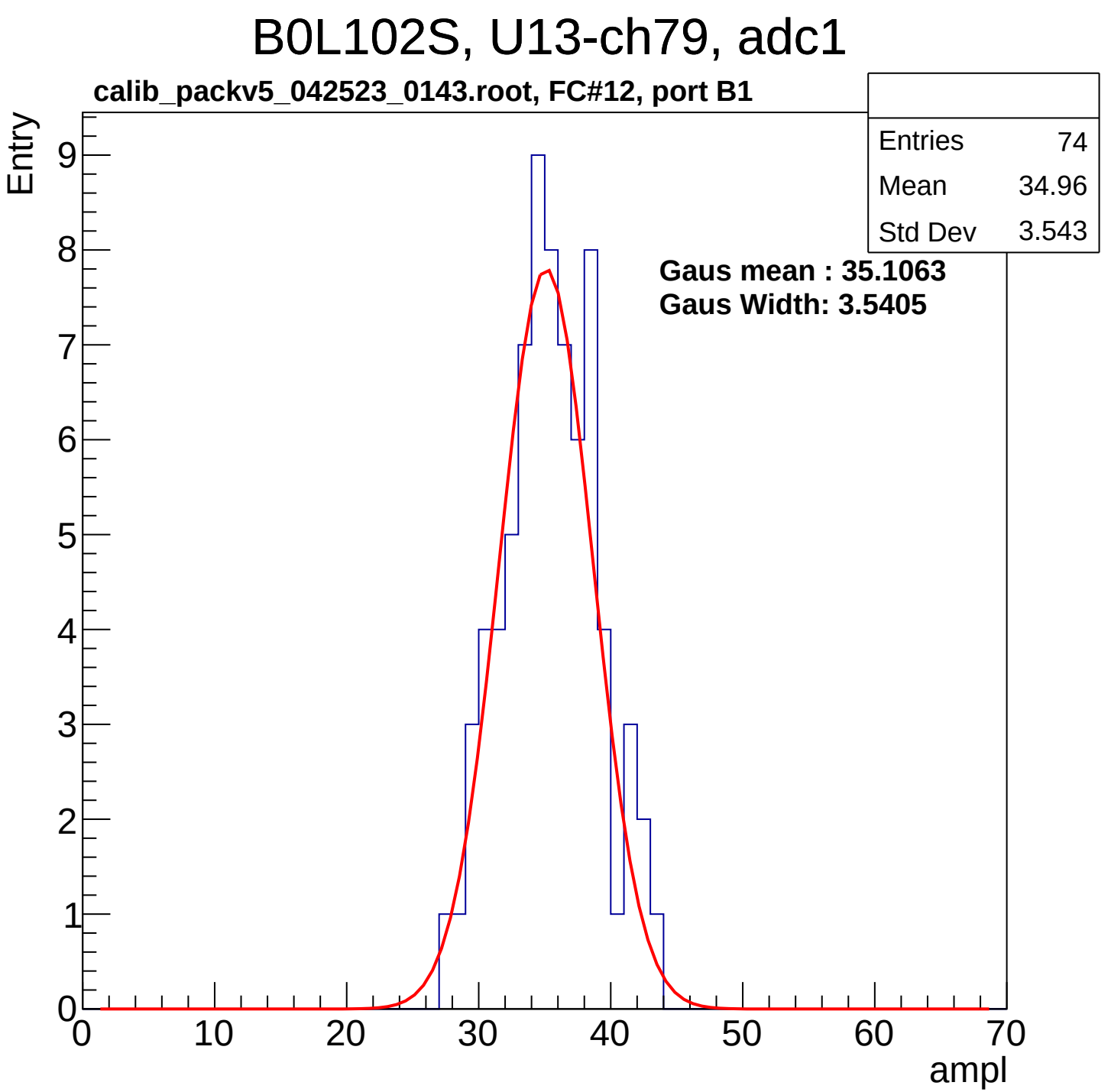
B0L102S, U13-ch79, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 34.96
Std Dev 3.543
Gaus mean : 35.1063
Gaus Width: 3.5405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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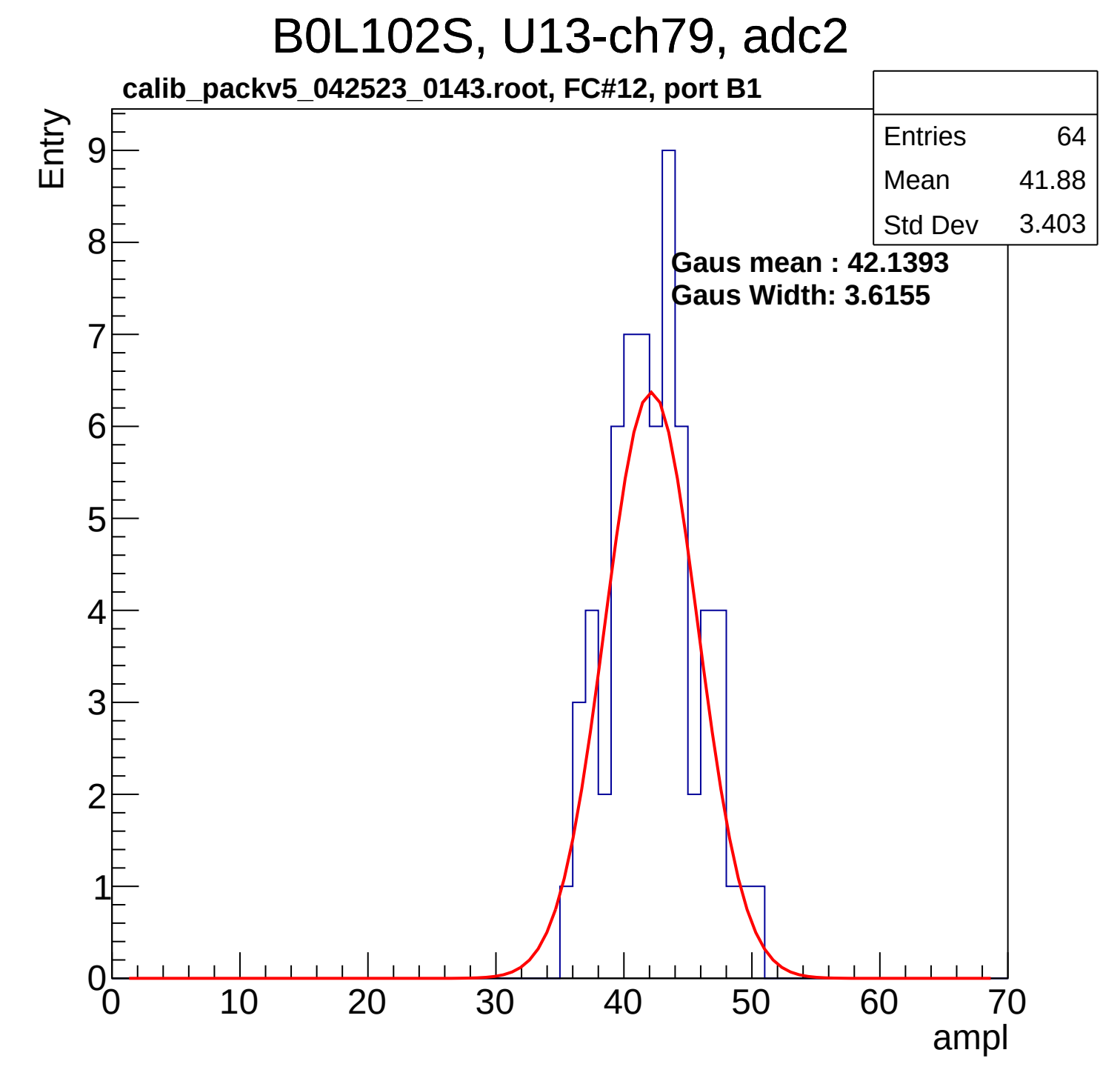
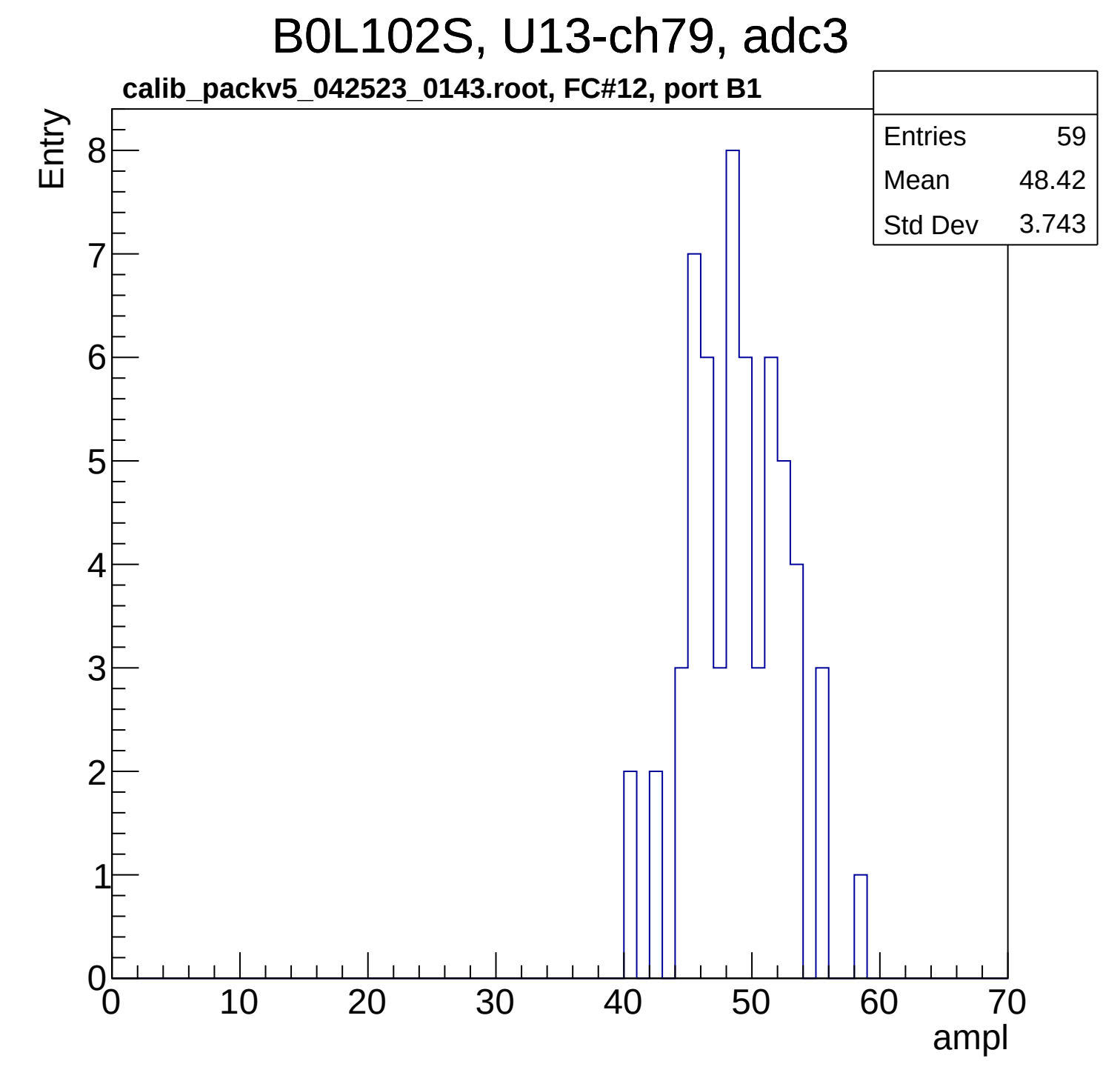

B0L102S, U13-ch79, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 41.88
Std Dev 3.403
Gaus mean : 42.1393
Gaus Width: 3.6155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch79, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.42
Std Dev 3.743
Entry
ampl
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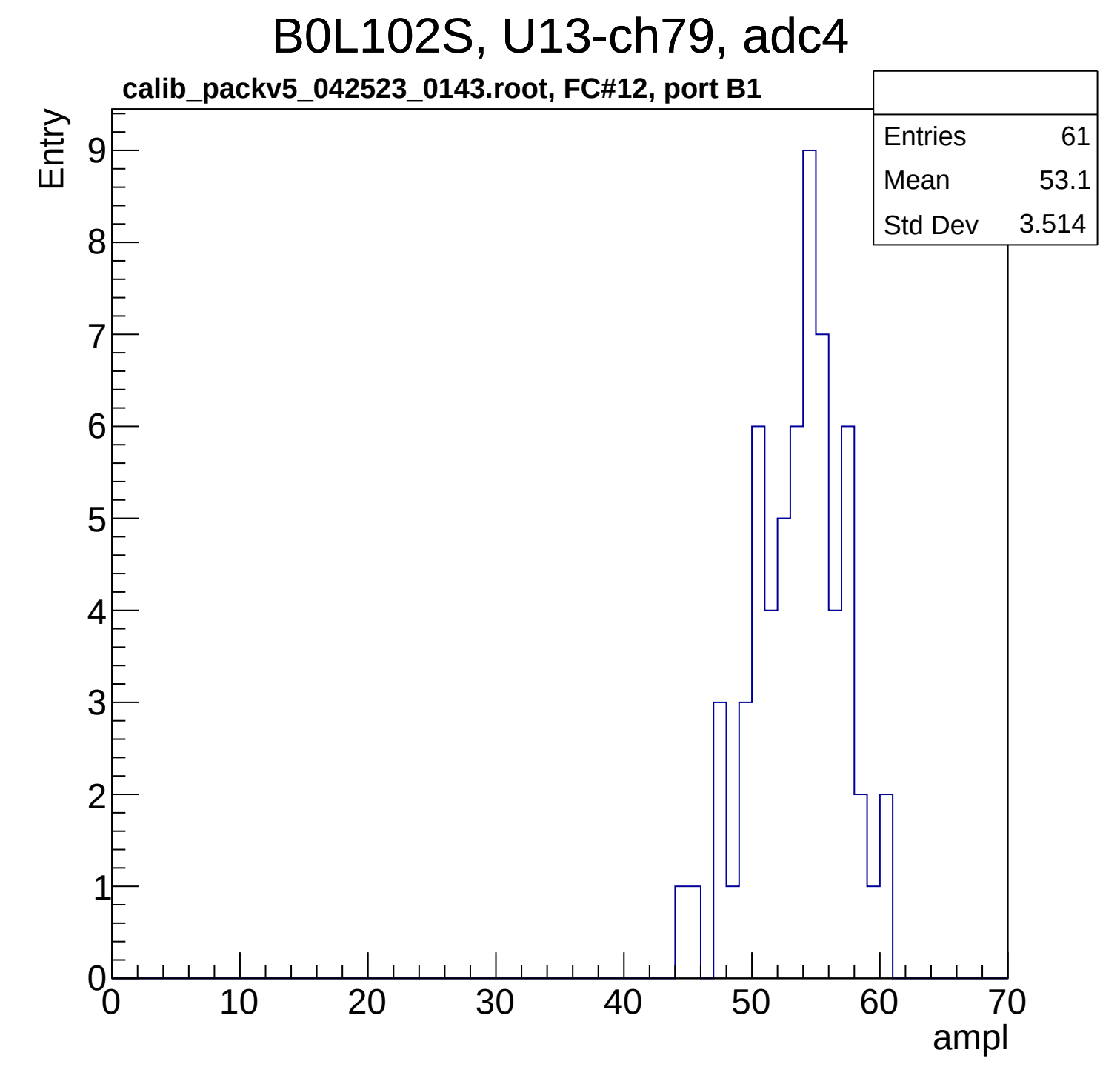
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch79, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 53.1
Std Dev 3.514
Entry
ampl
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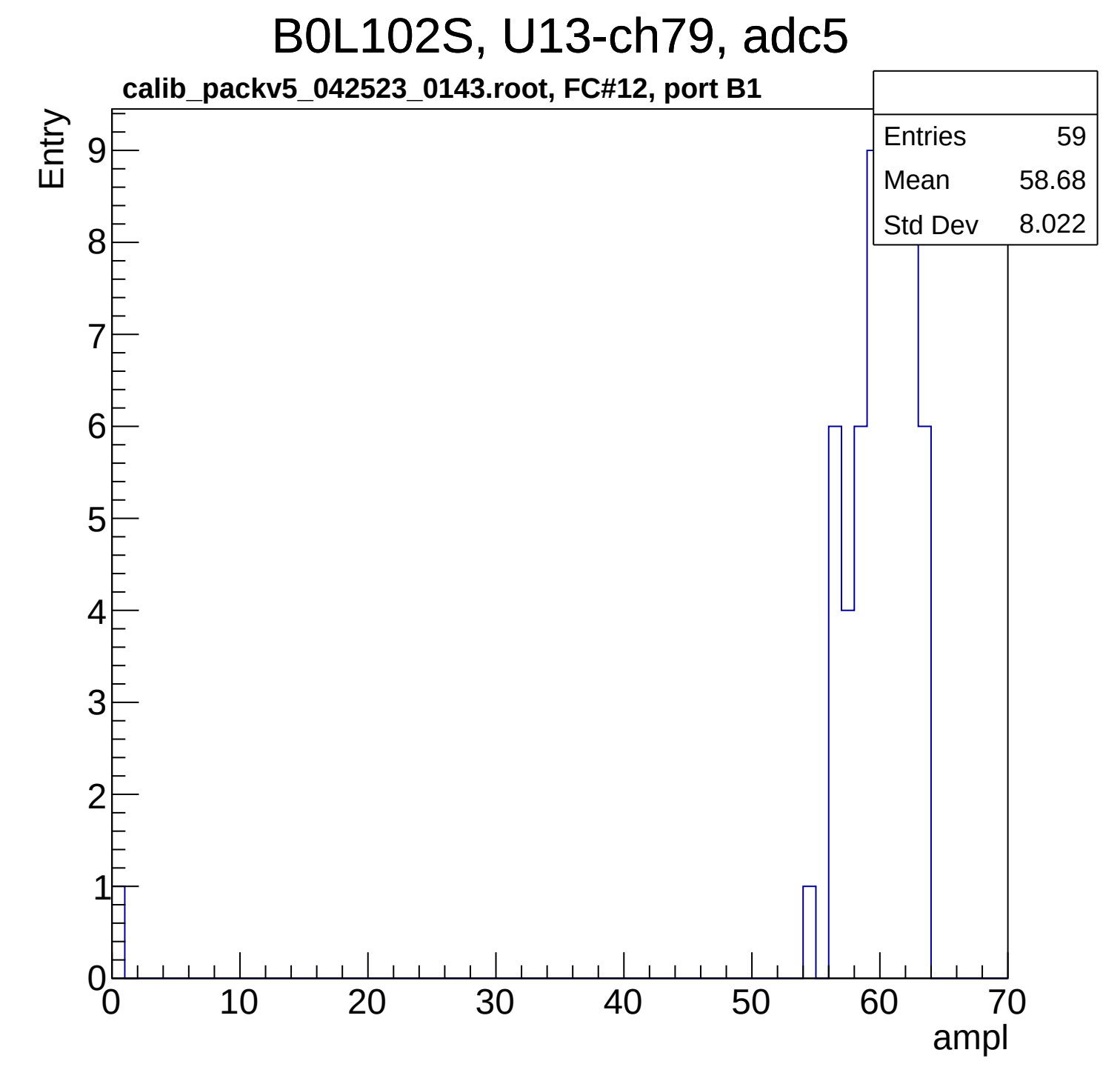
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch79, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.68
Std Dev 8.022
Entry
ampl
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3
4
5
6
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0
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60
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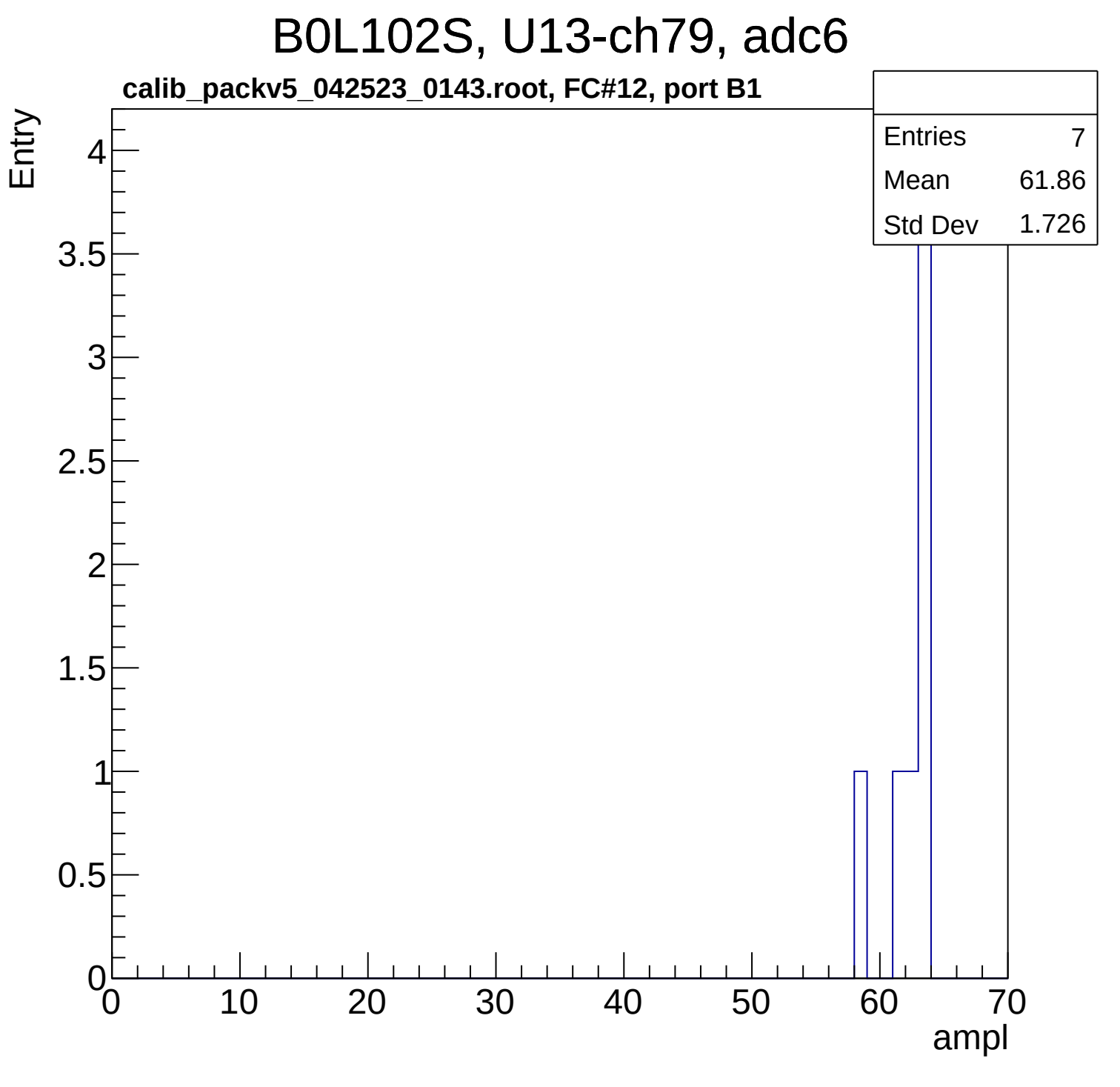
B0L102S, U13-ch79, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.86
Std Dev 1.726
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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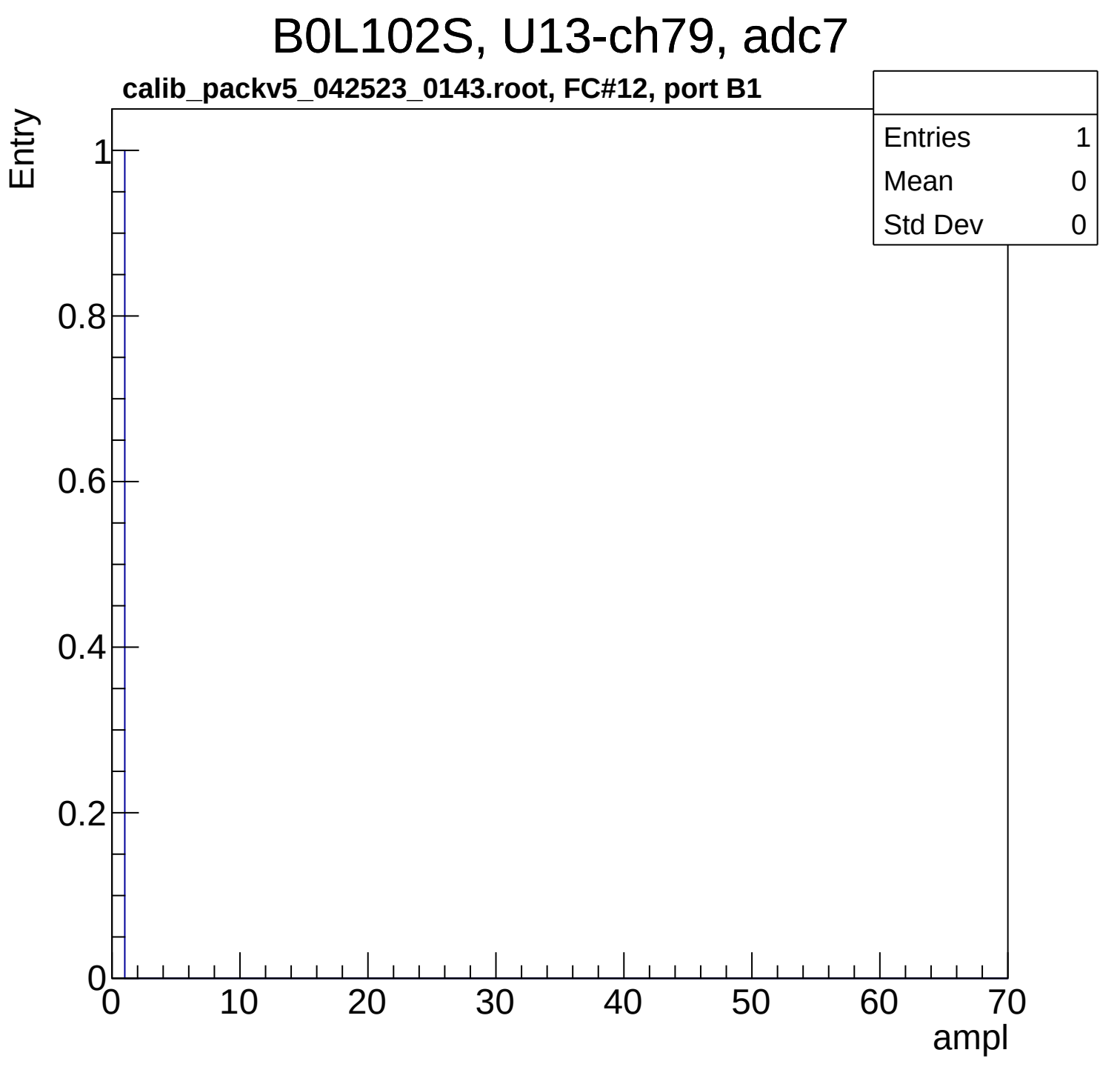
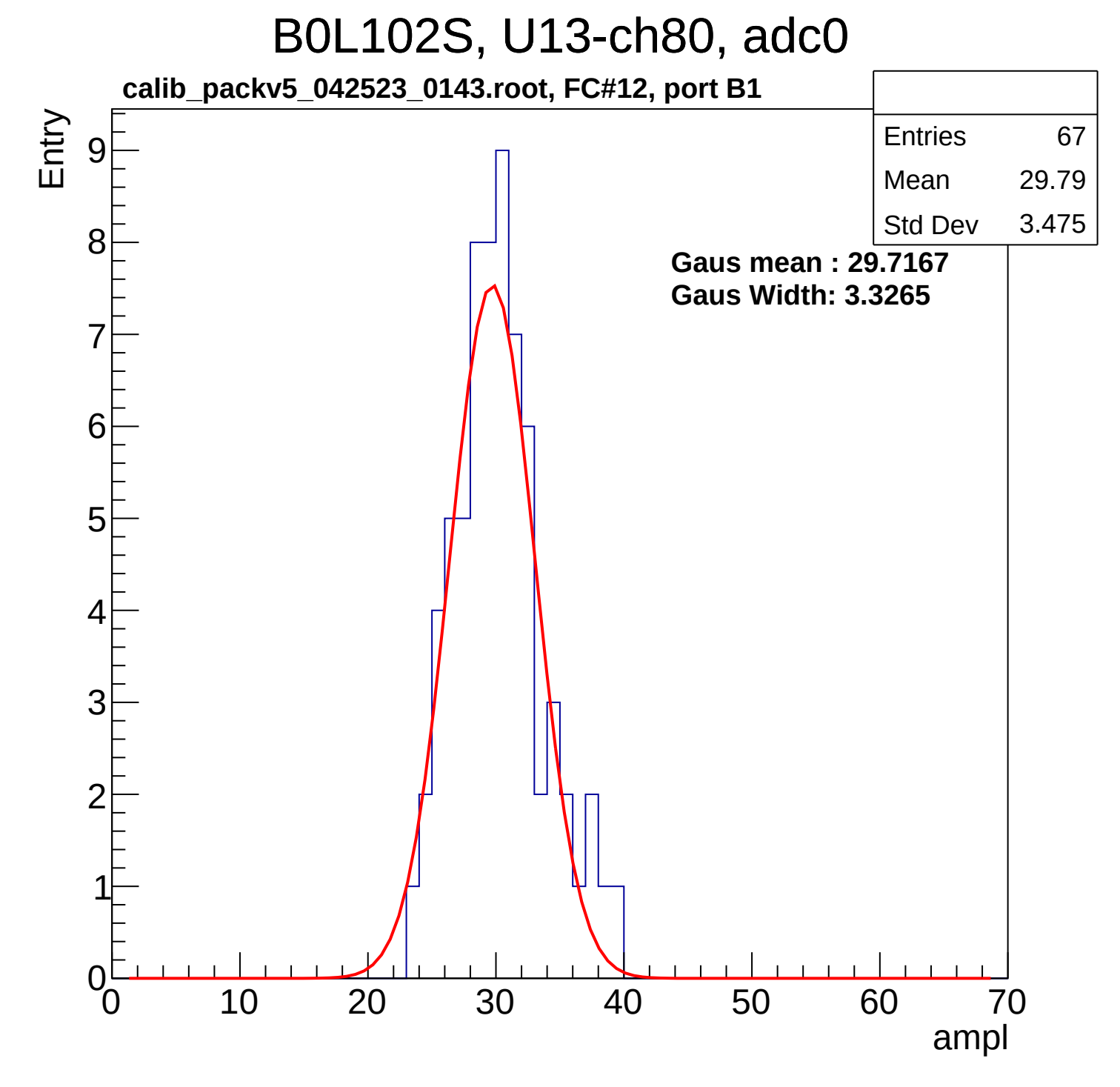
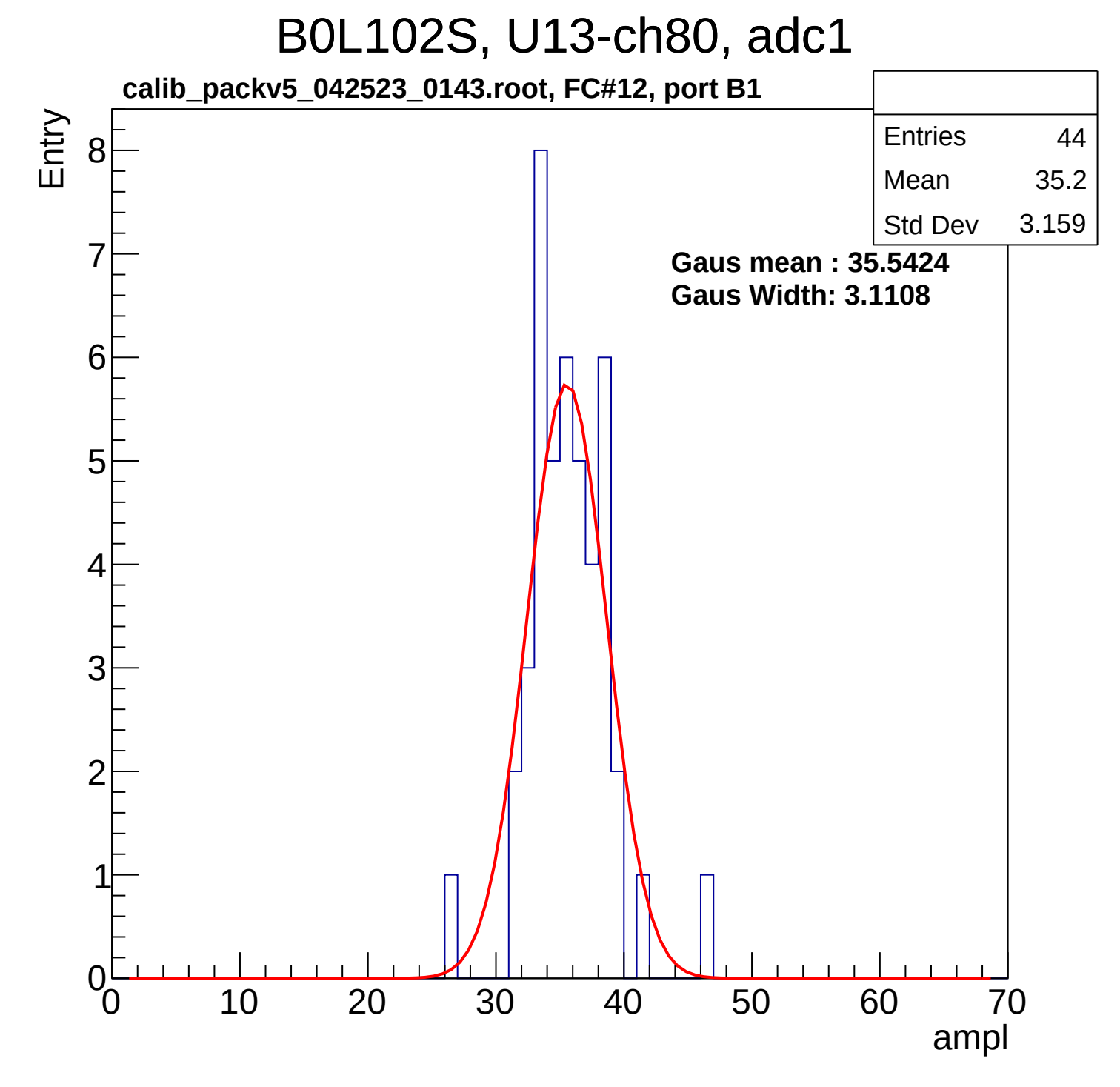
40
50
60
70


B0L102S, U13-ch79, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch80, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.79
Std Dev 3.475
Gaus mean : 29.7167
Gaus Width: 3.3265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch80, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 35.2
Std Dev 3.159
Gaus mean : 35.5424
Gaus Width: 3.1108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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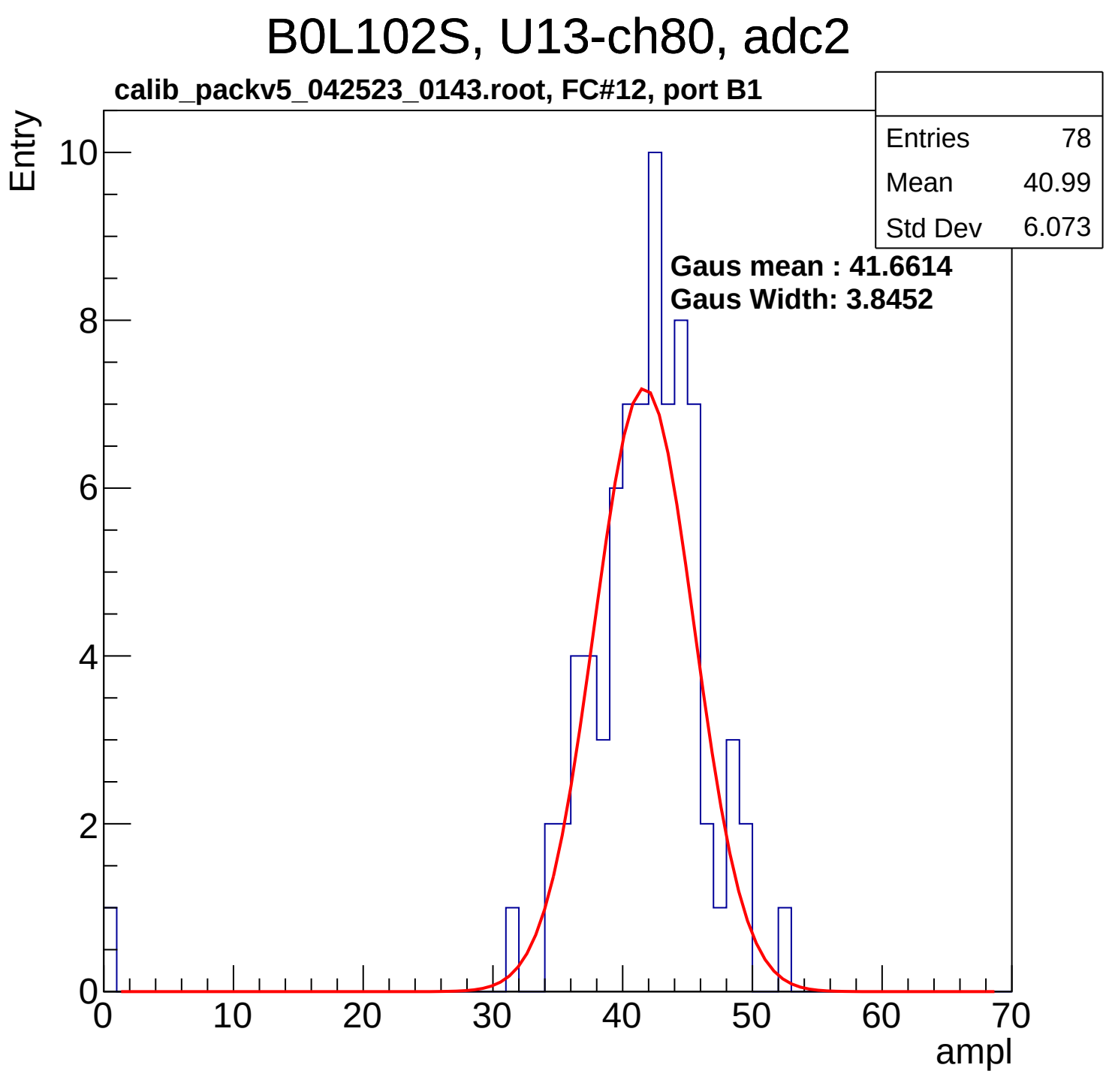
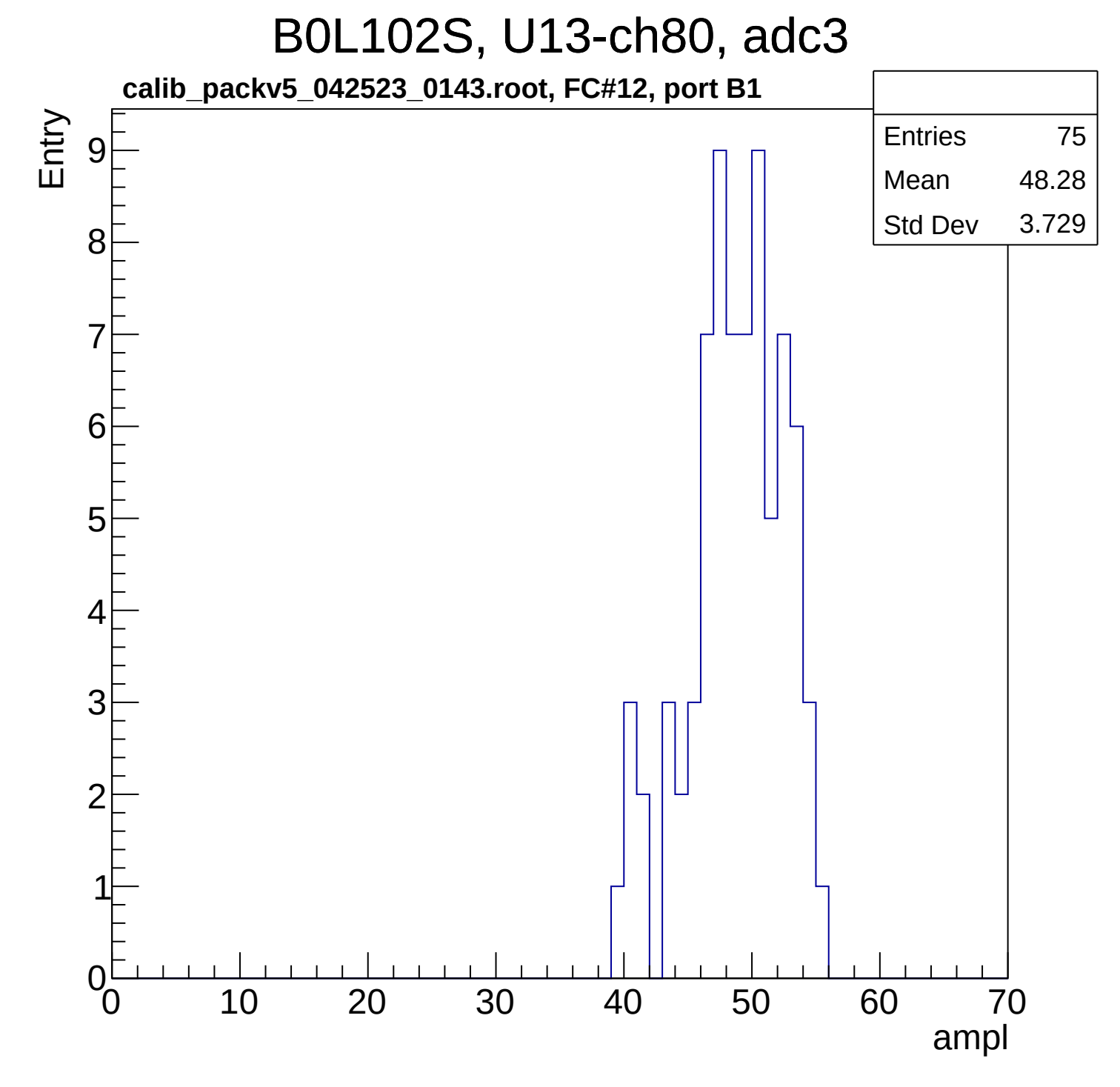

B0L102S, U13-ch80, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 40.99
Std Dev 6.073
Gaus mean : 41.6614
Gaus Width: 3.8452
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch80, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 48.28
Std Dev 3.729
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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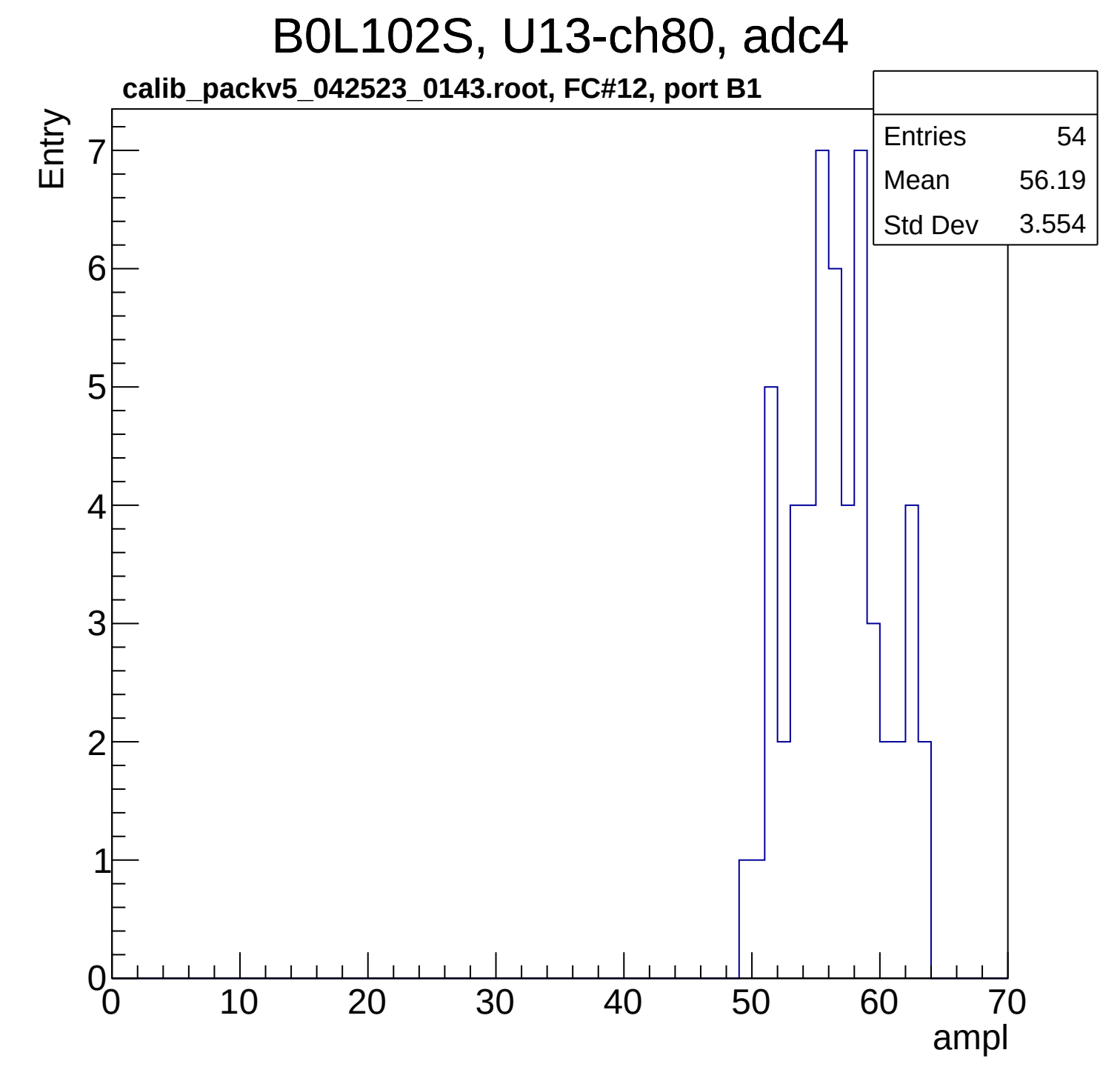
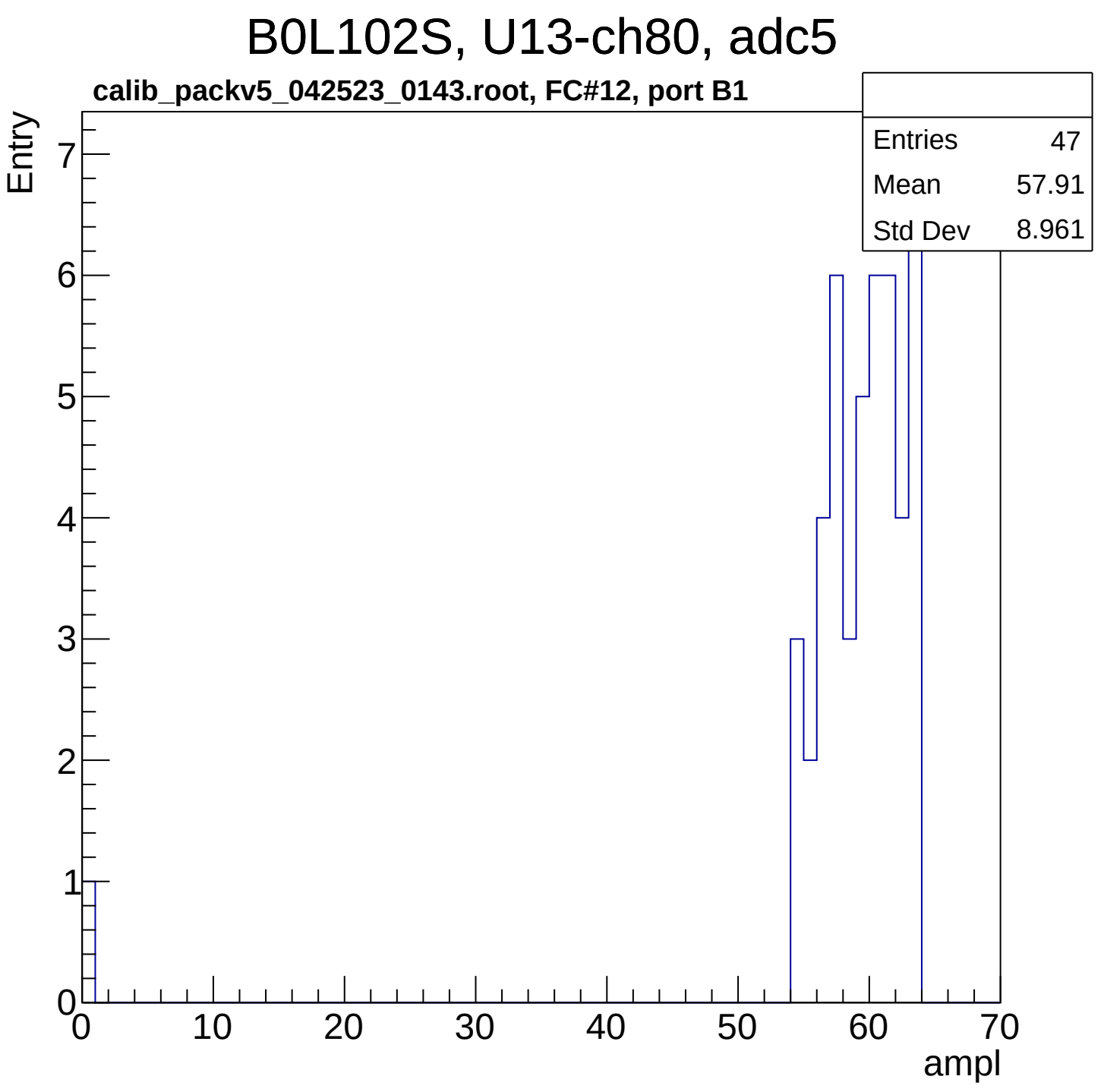

B0L102S, U13-ch80, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.19
Std Dev 3.554
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch80, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 57.91
Std Dev 8.961
Entry
ampl
0
1
2
3
4
5
6
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0
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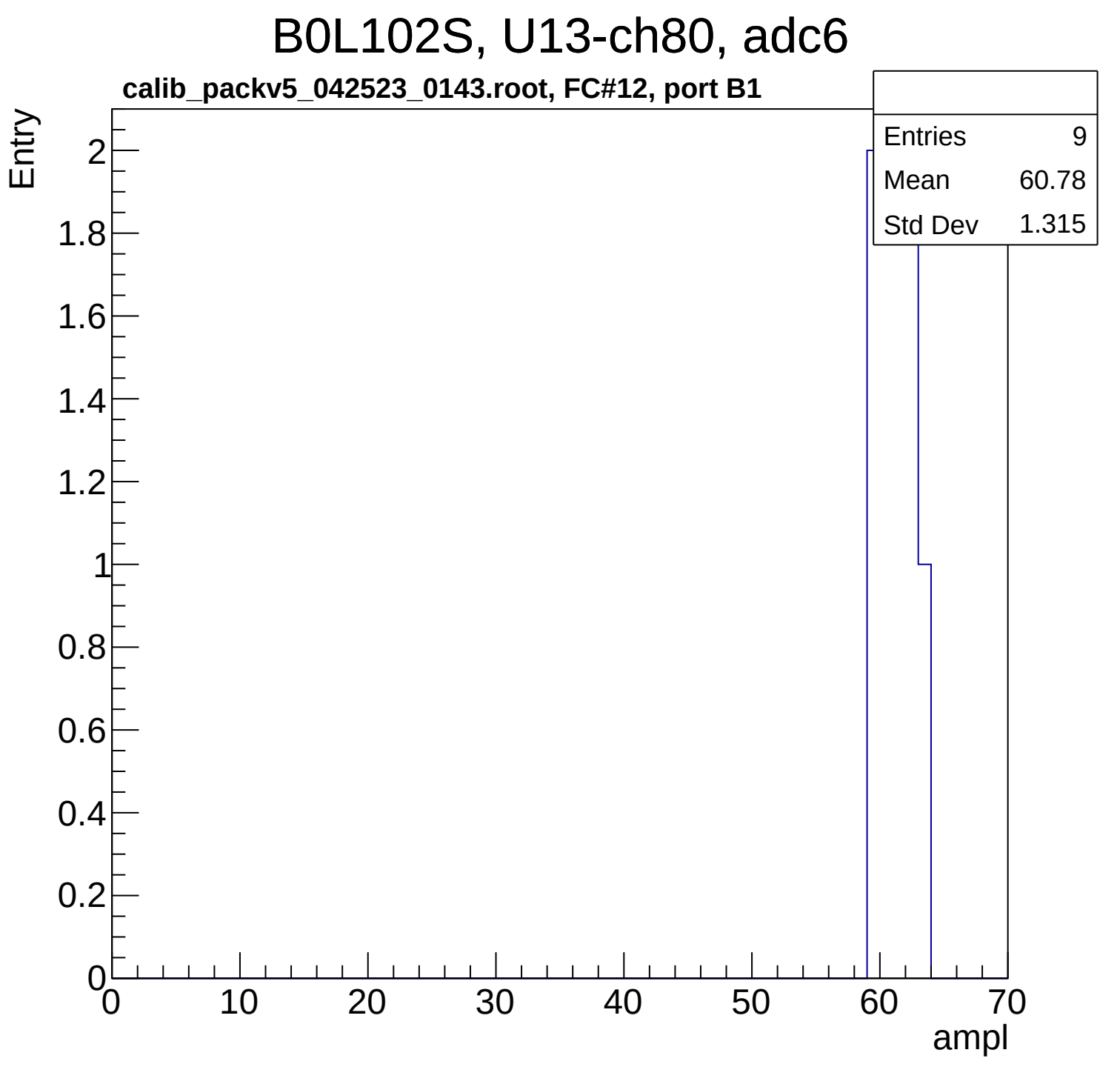
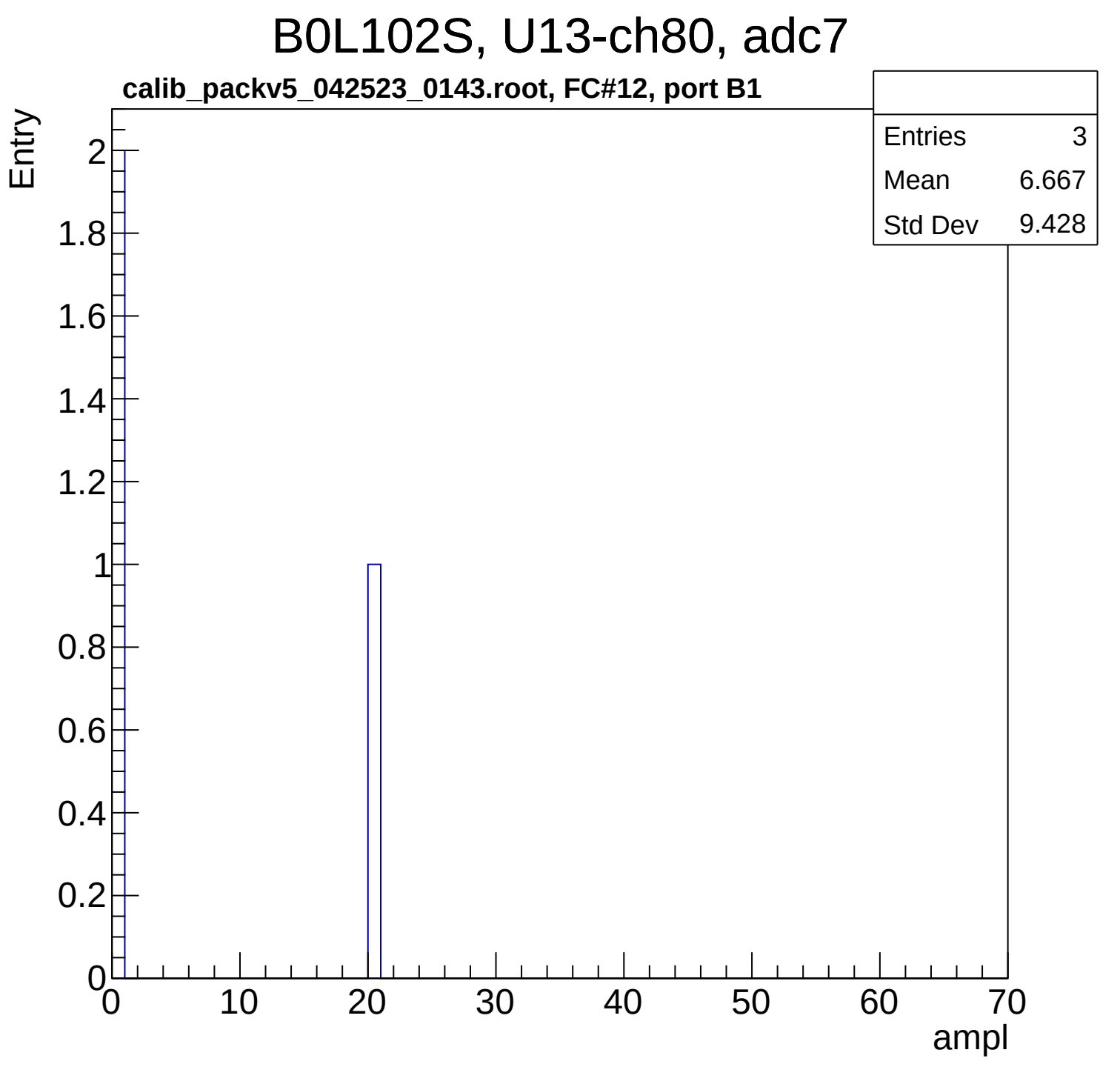

B0L102S, U13-ch80, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 60.78
Std Dev 1.315
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch80, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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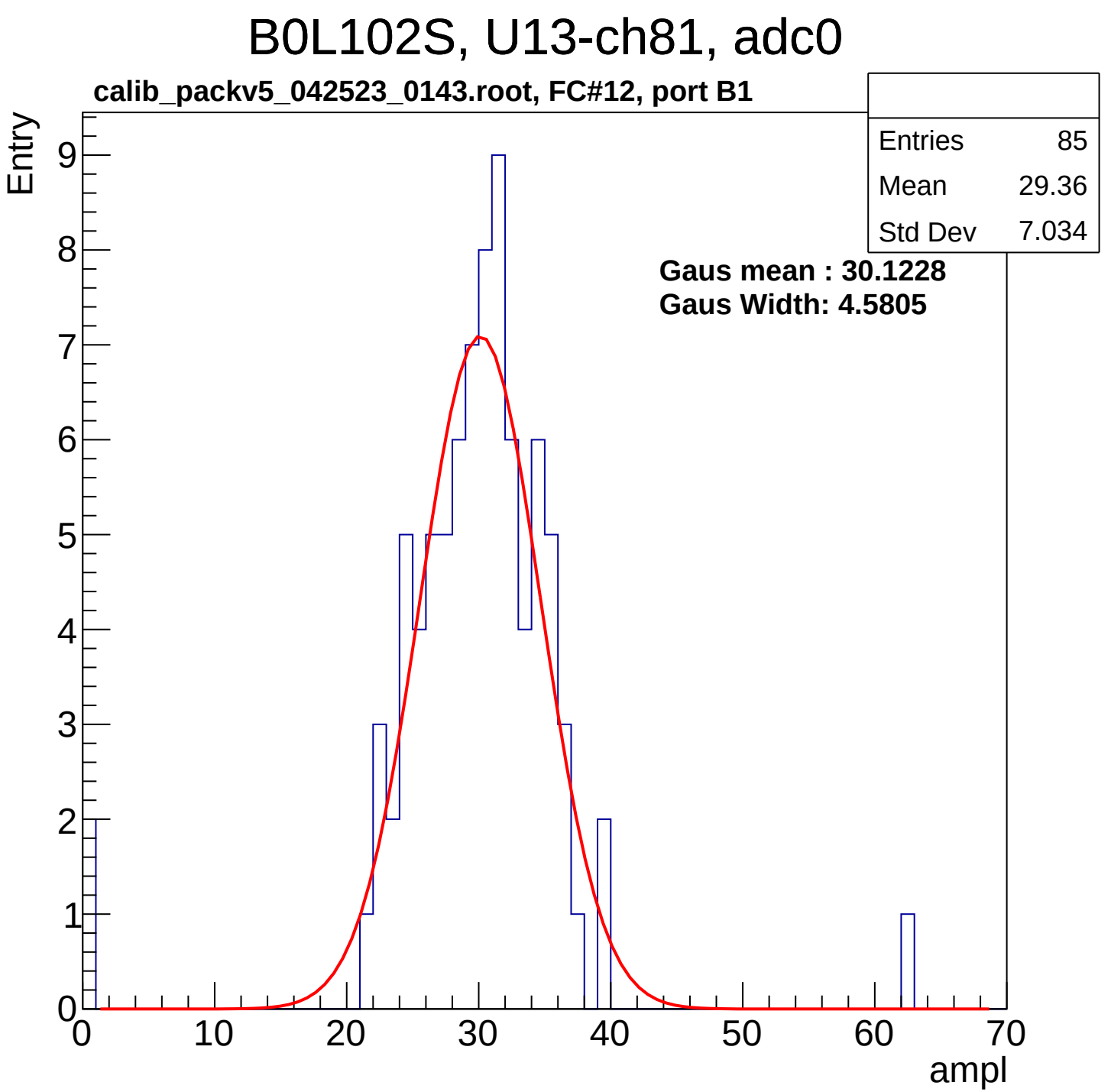
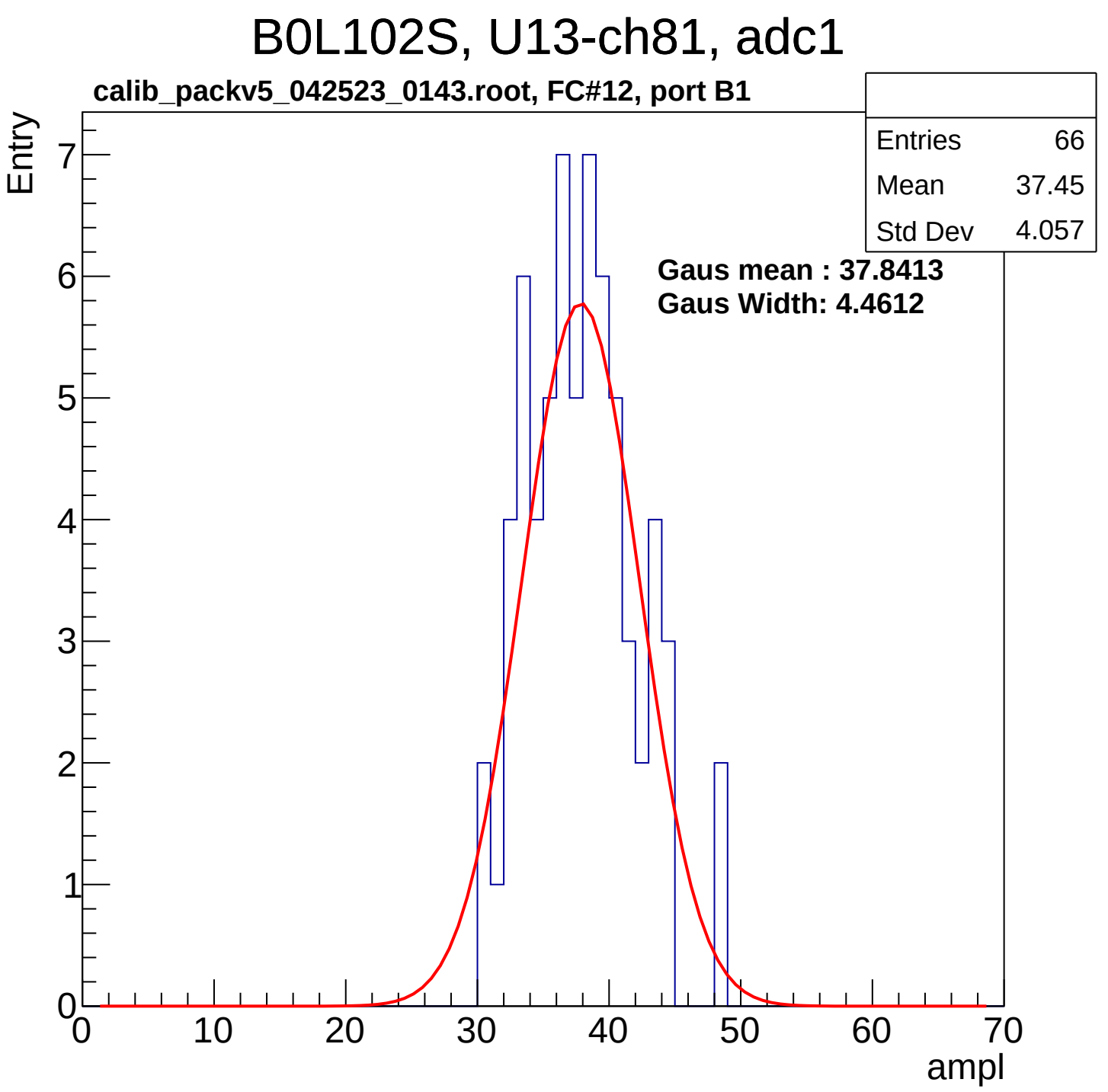

B0L102S, U13-ch81, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 29.36
Std Dev 7.034
Gaus mean : 30.1228
Gaus Width: 4.5805
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch81, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.45
Std Dev 4.057
Gaus mean : 37.8413
Gaus Width: 4.4612
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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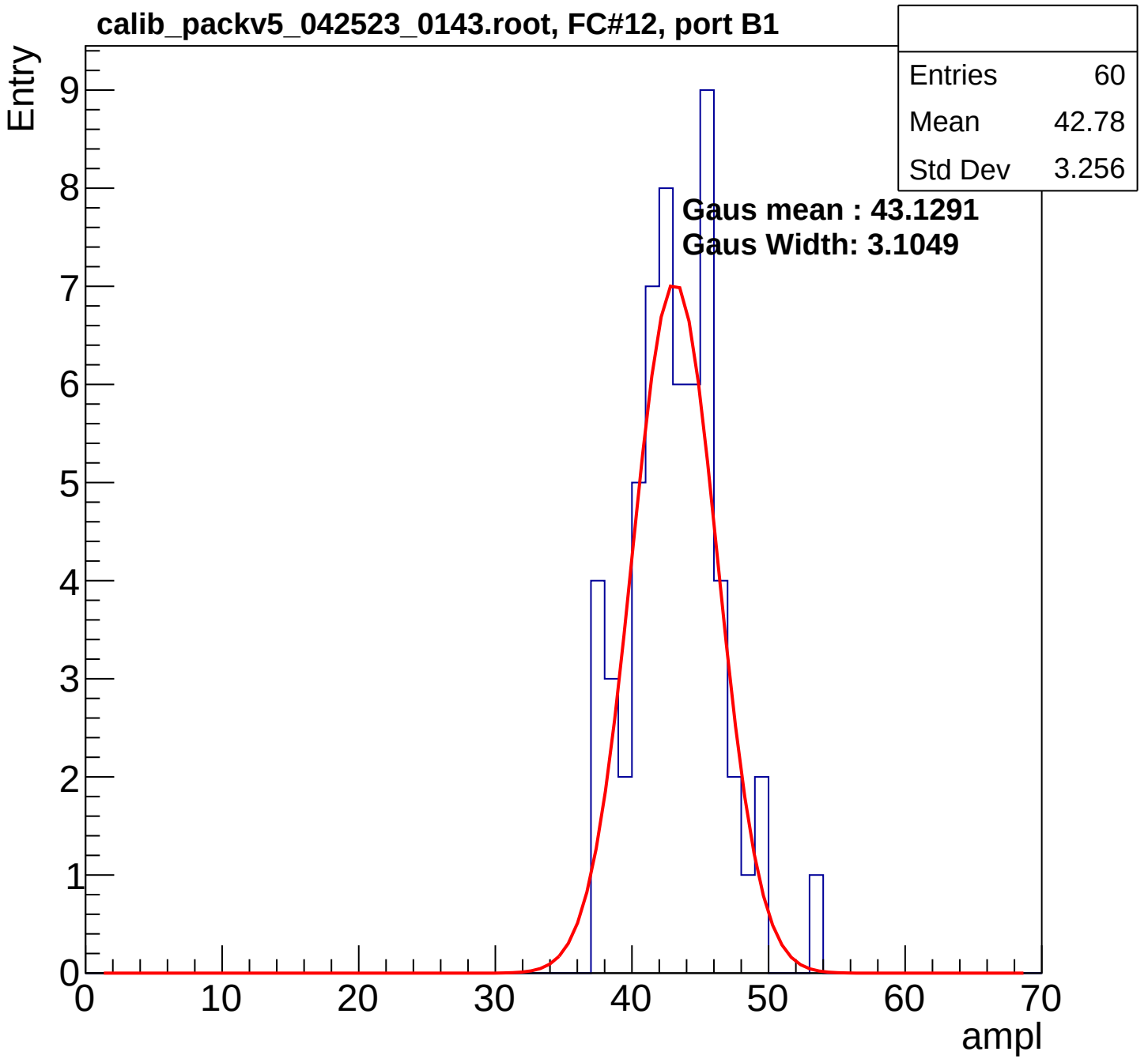
B0L102S, U13-ch81, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 42.78
Std Dev 3.256
Gaus mean : 43.1291
Gaus Width: 3.1049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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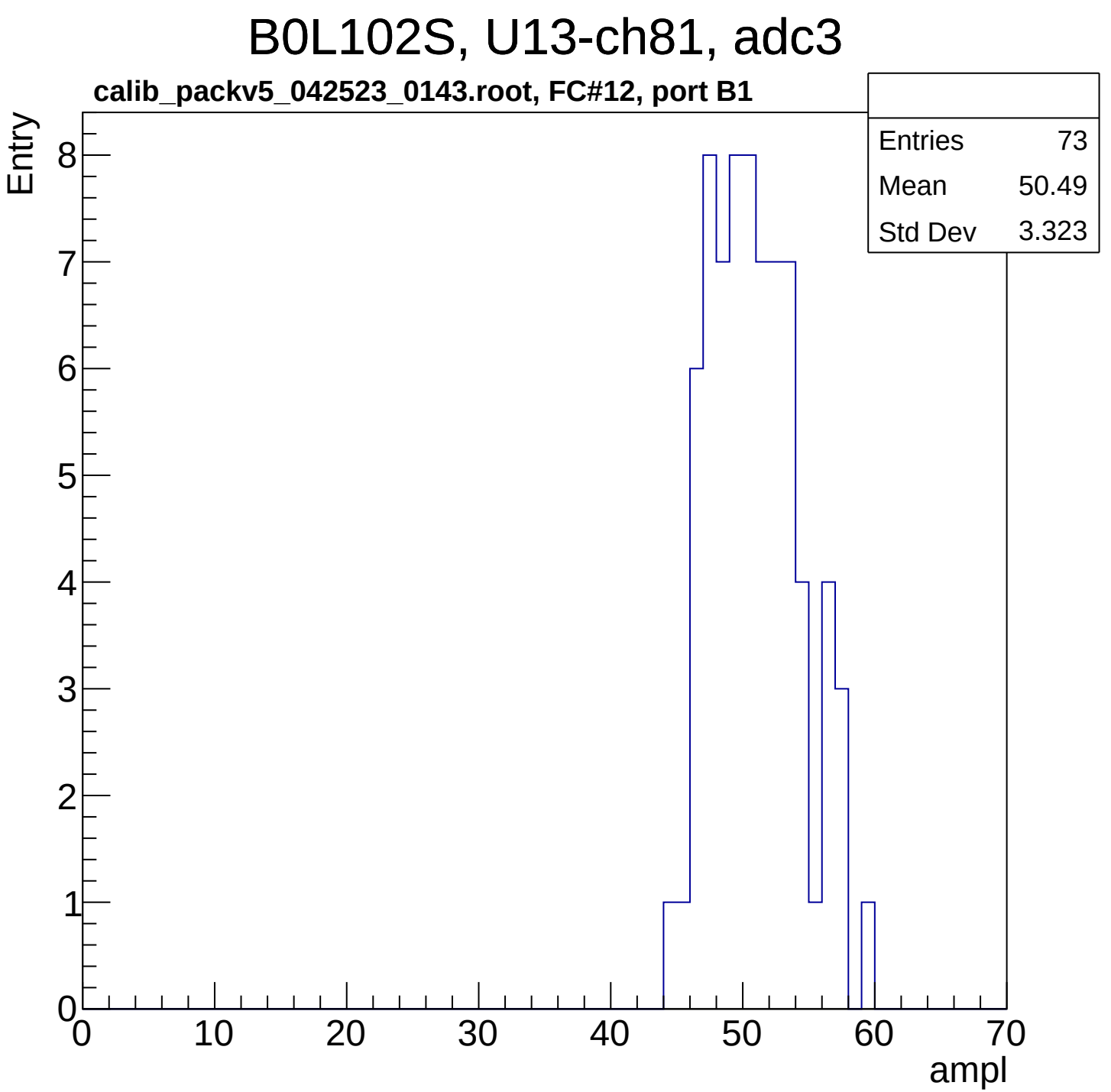
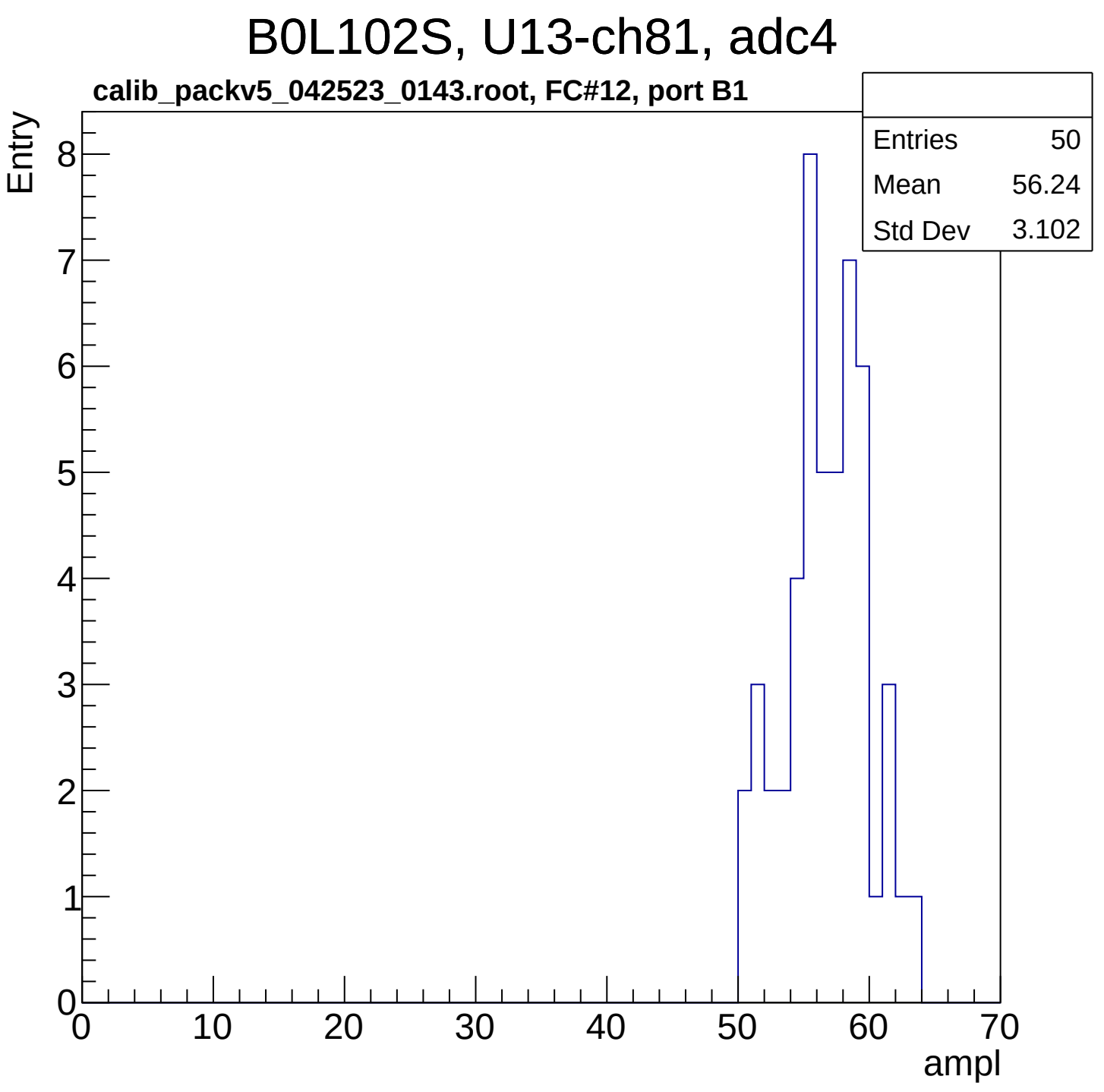

B0L102S, U13-ch81, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 50.49
Std Dev 3.323
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch81, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 56.24
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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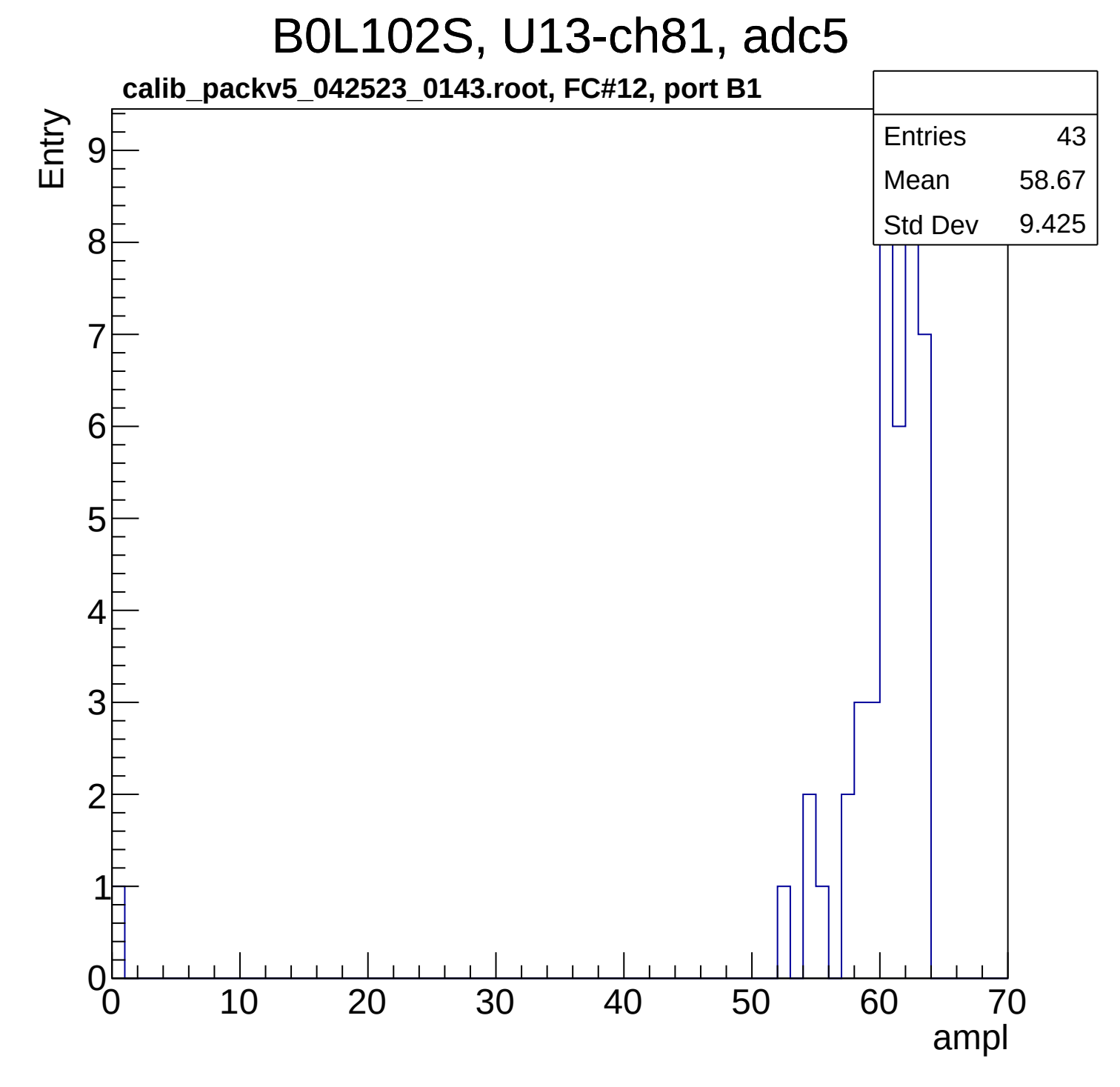
B0L102S, U13-ch81, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.67
Std Dev 9.425
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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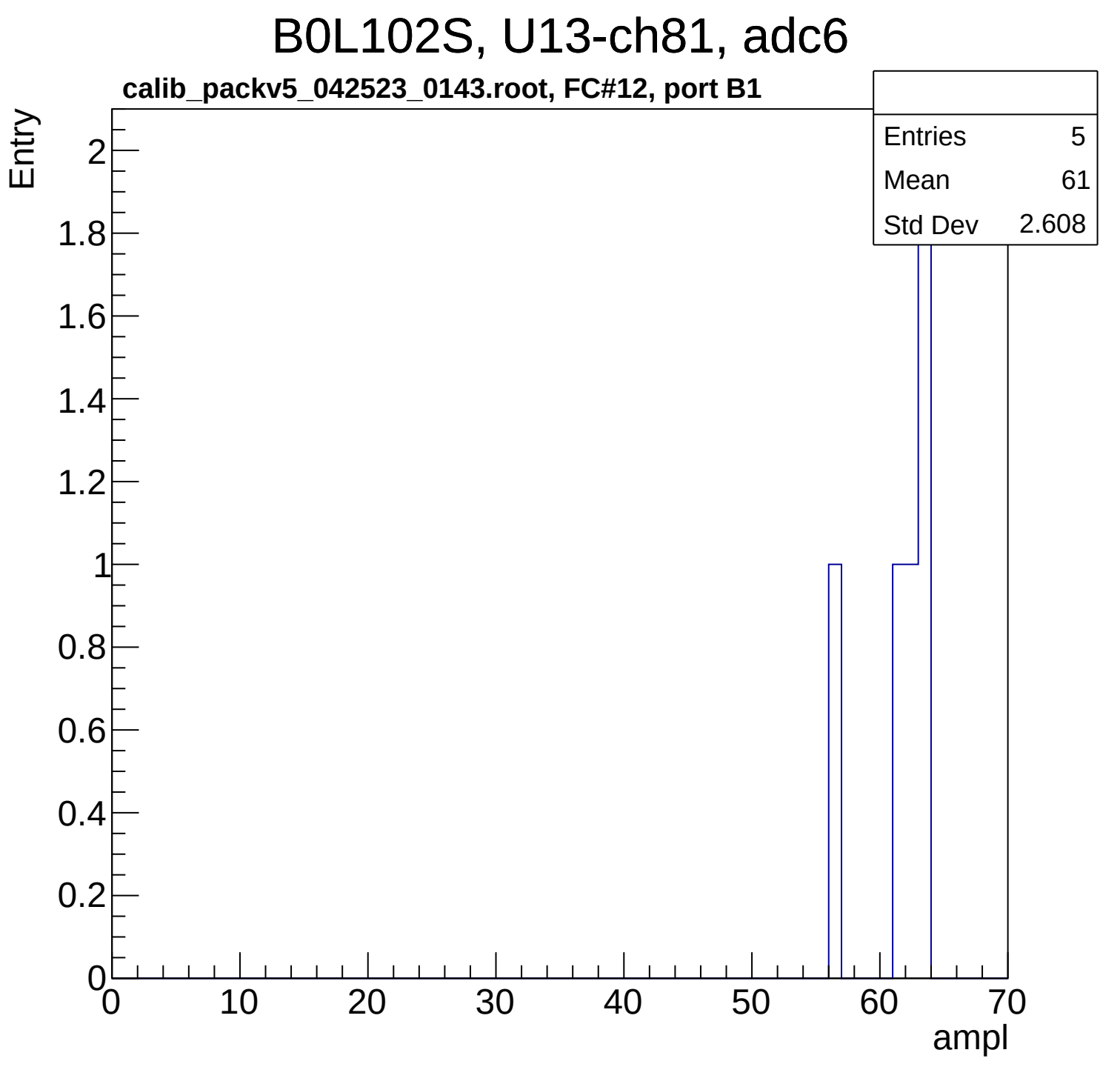
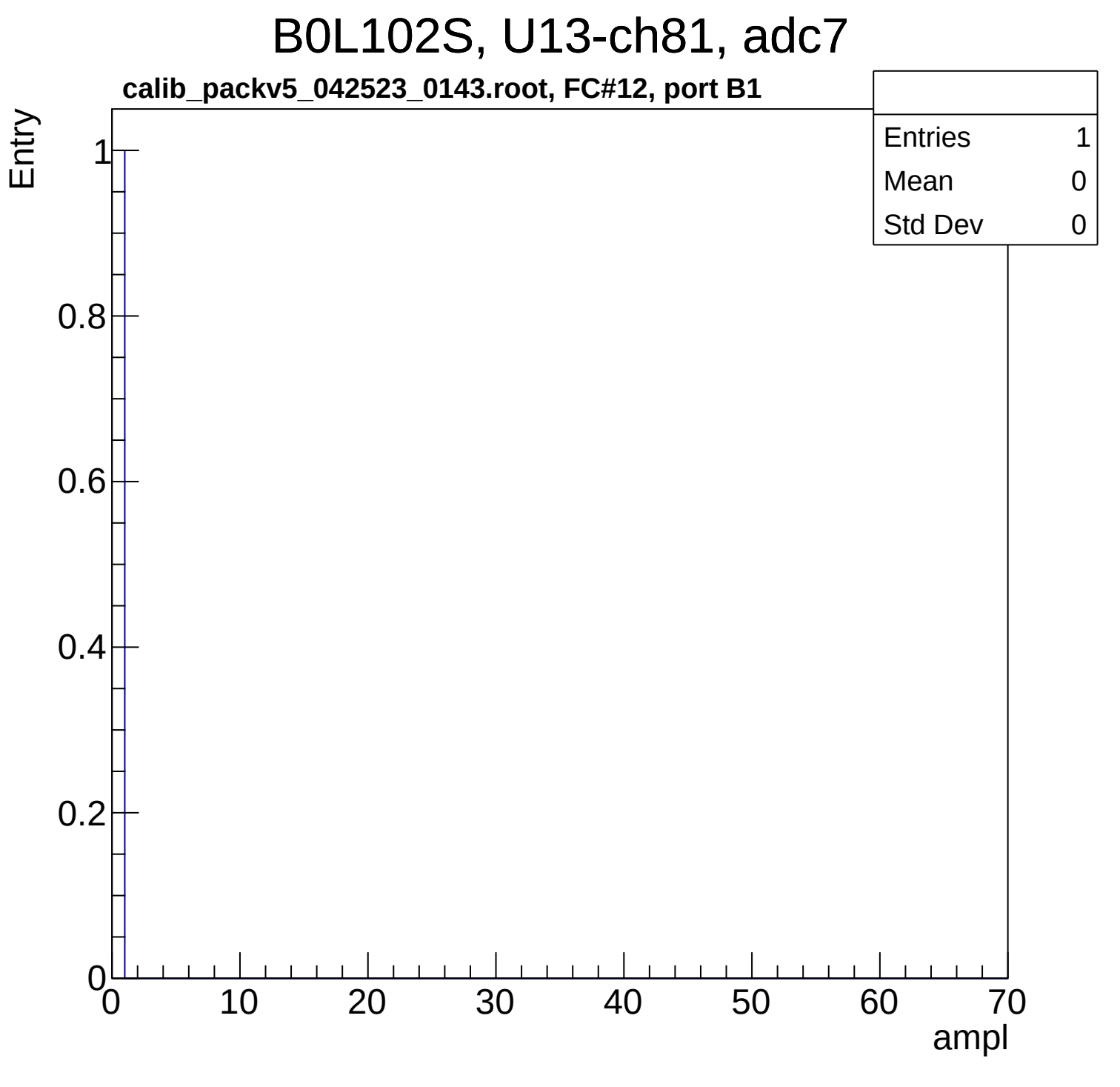

B0L102S, U13-ch81, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61
Std Dev 2.608
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch81, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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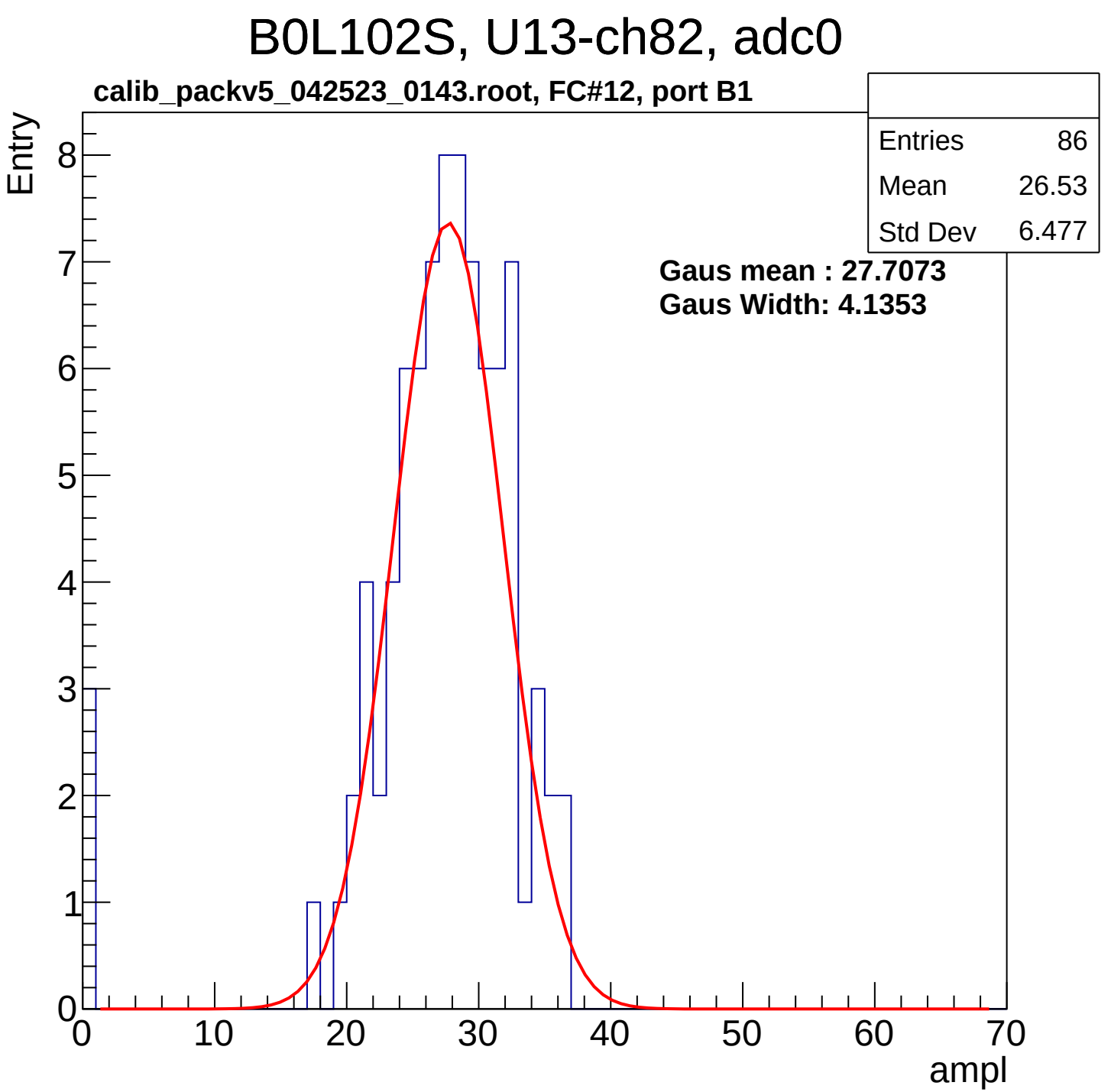
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch82, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 26.53
Std Dev 6.477
Gaus mean : 27.7073
Gaus Width: 4.1353
Entry
ampl
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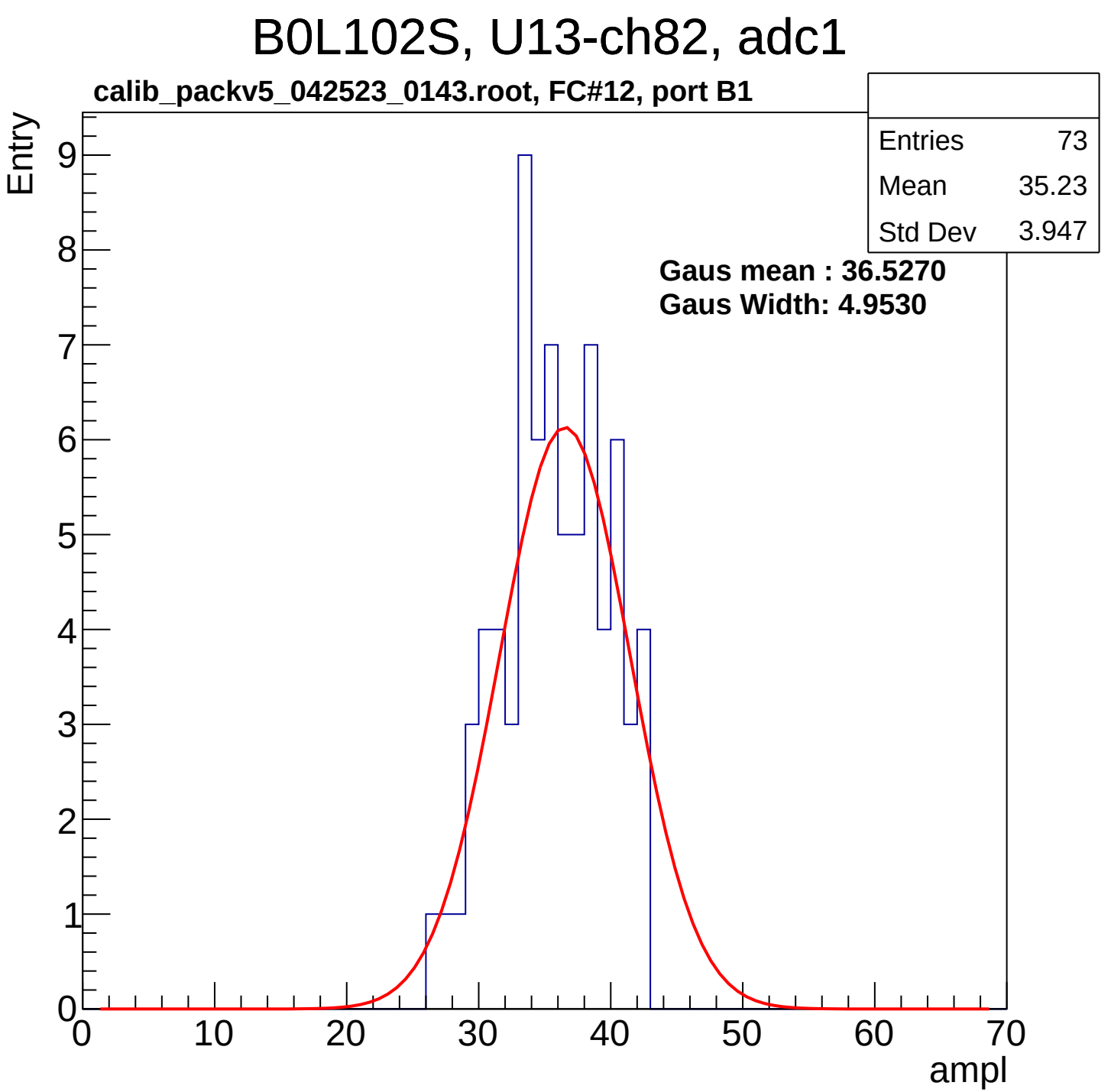
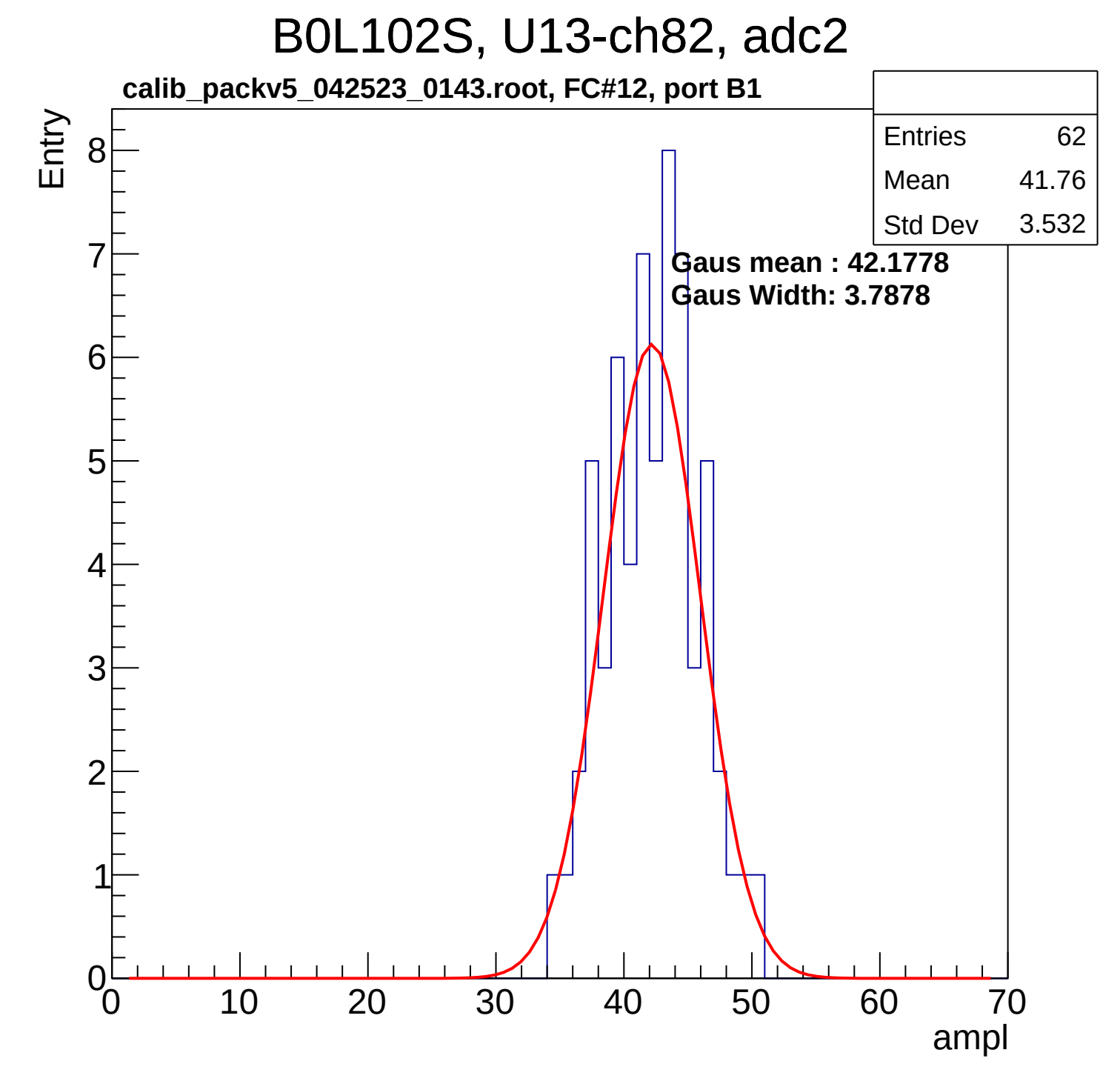
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch82, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 35.23
Std Dev 3.947
Gaus mean : 36.5270
Gaus Width: 4.9530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch82, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.76
Std Dev 3.532
Gaus mean : 42.1778
Gaus Width: 3.7878
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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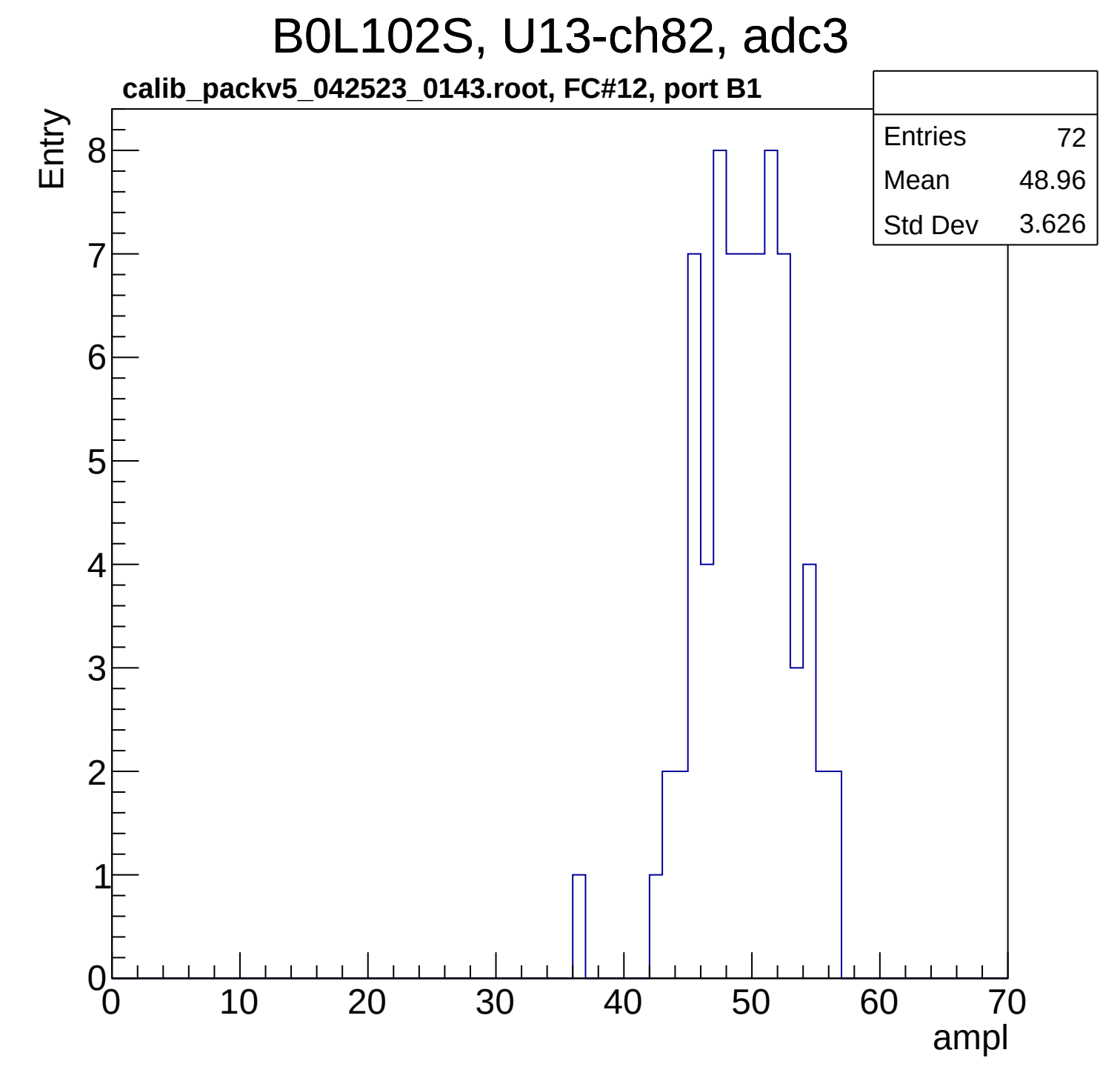
B0L102S, U13-ch82, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.96
Std Dev 3.626
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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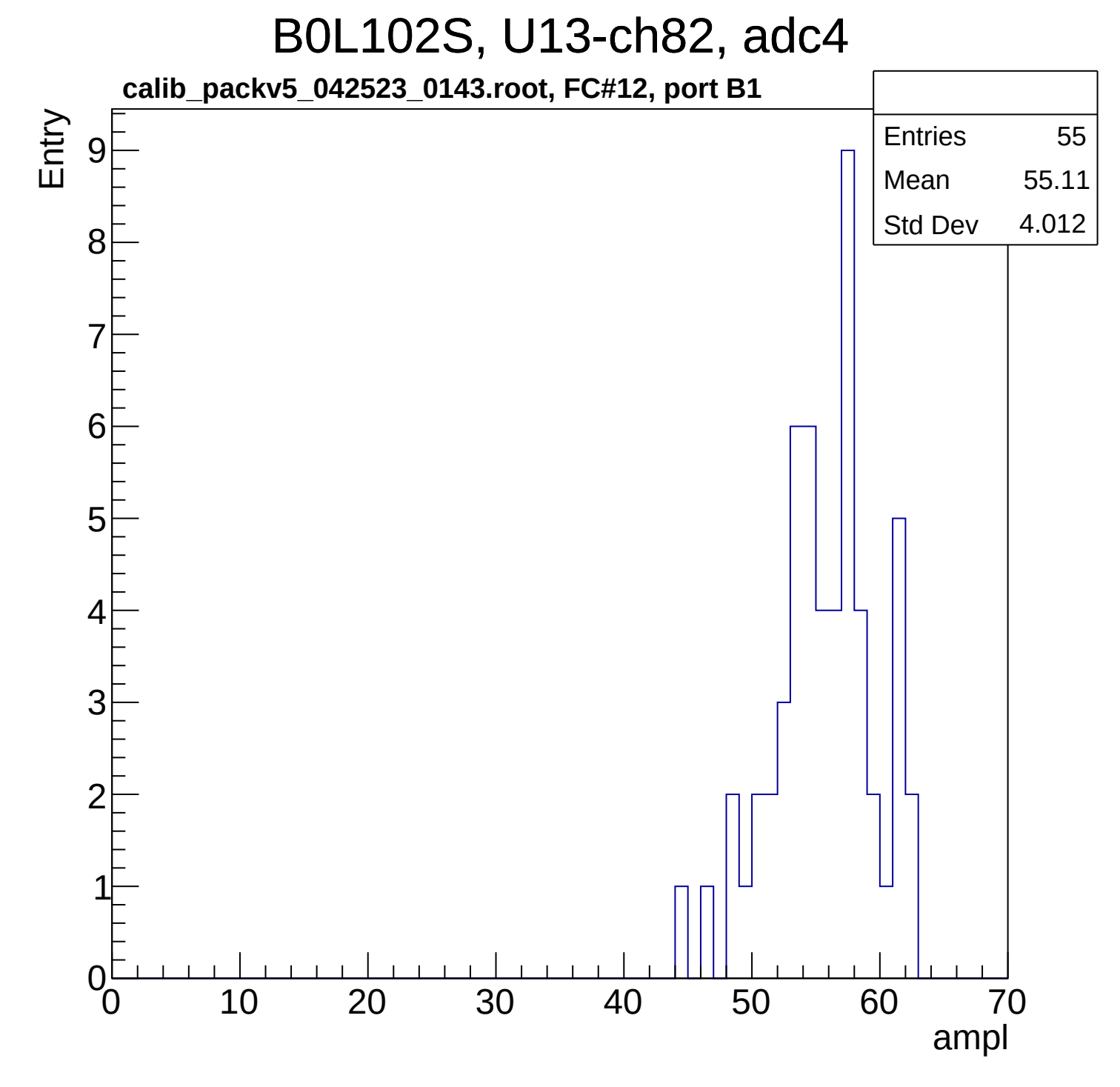
B0L102S, U13-ch82, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.11
Std Dev 4.012
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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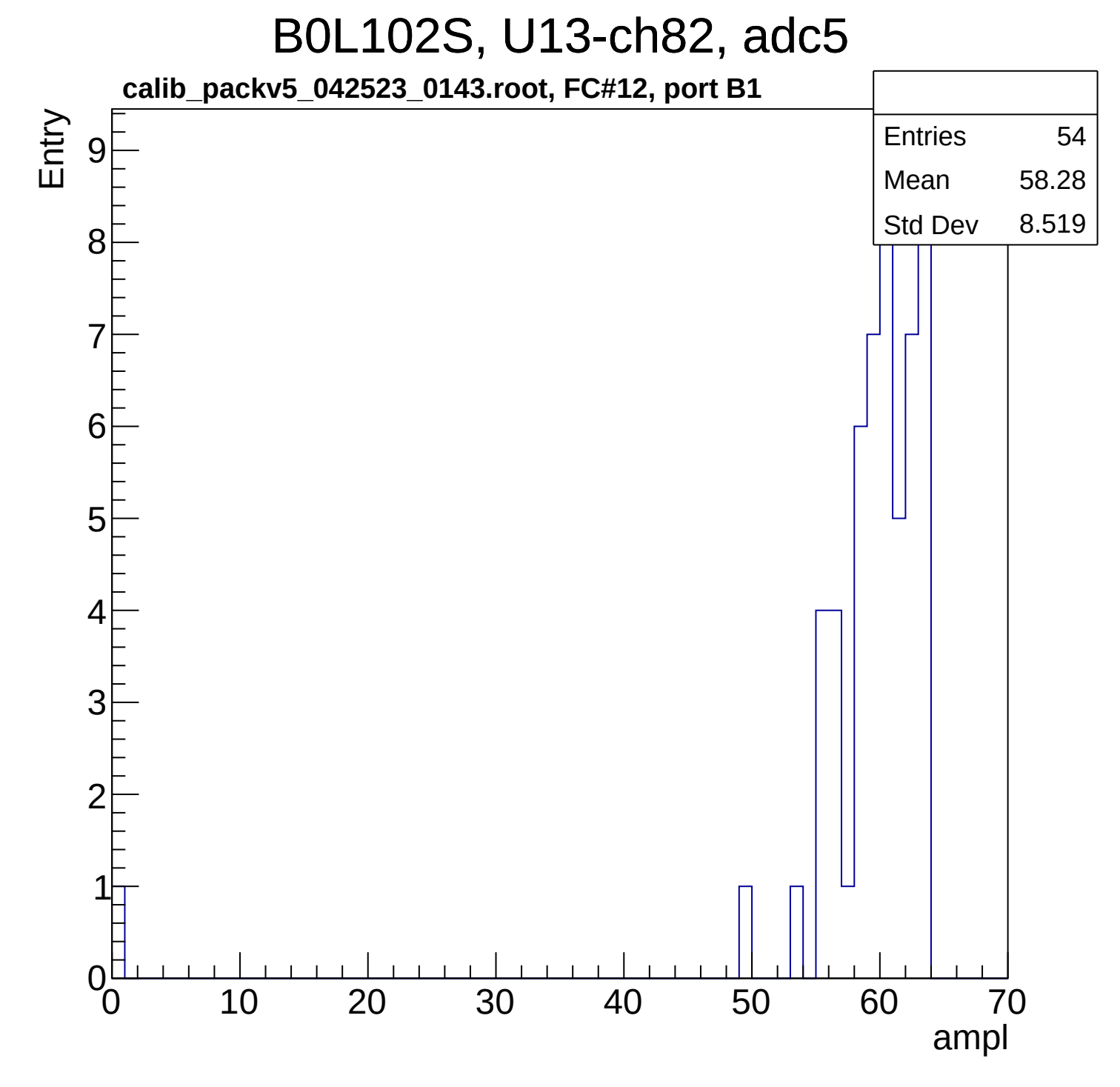
B0L102S, U13-ch82, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.28
Std Dev 8.519
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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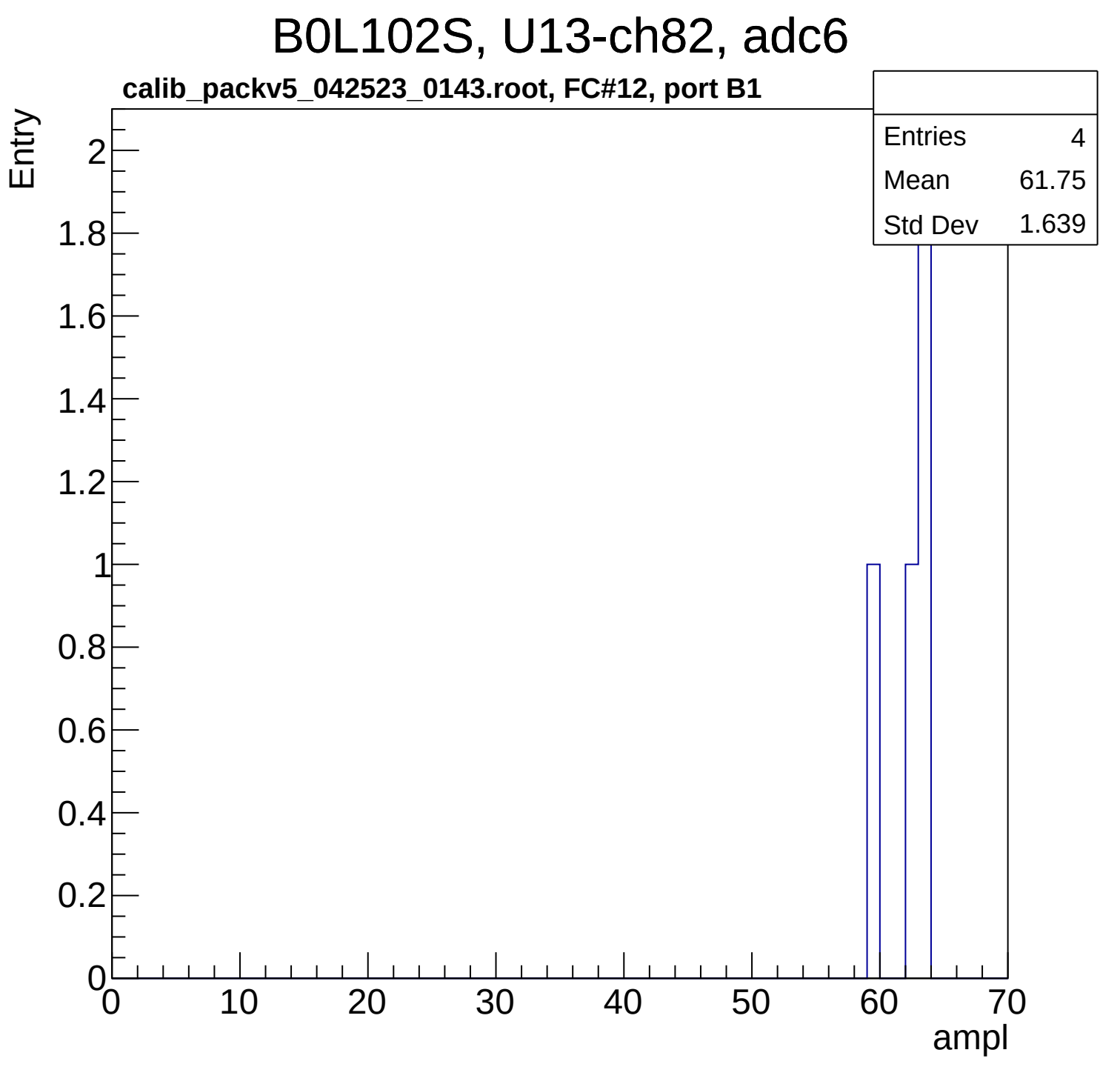
B0L102S, U13-ch82, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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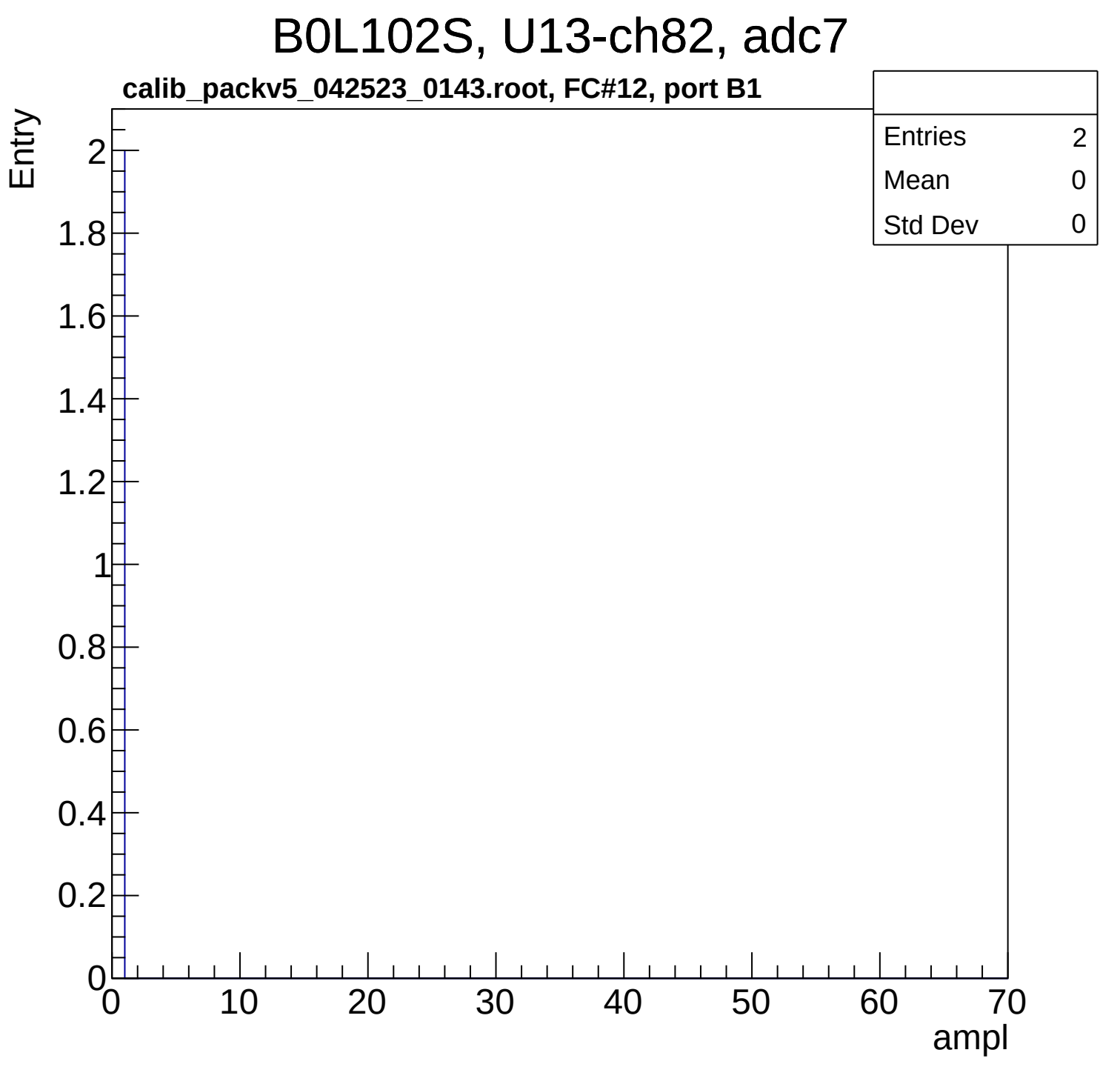
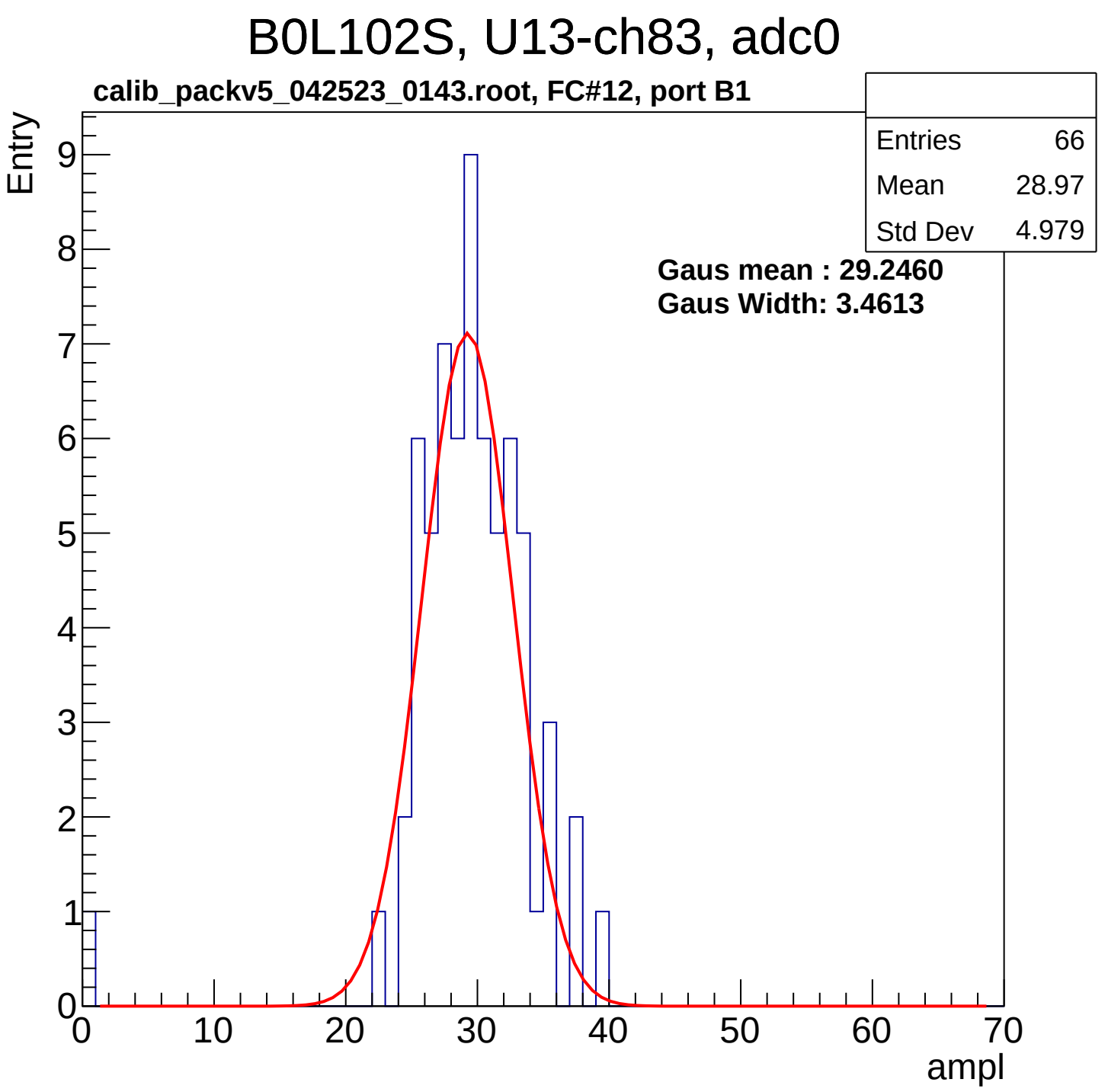

B0L102S, U13-ch82, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch83, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.97
Std Dev 4.979
Gaus mean : 29.2460
Gaus Width: 3.4613
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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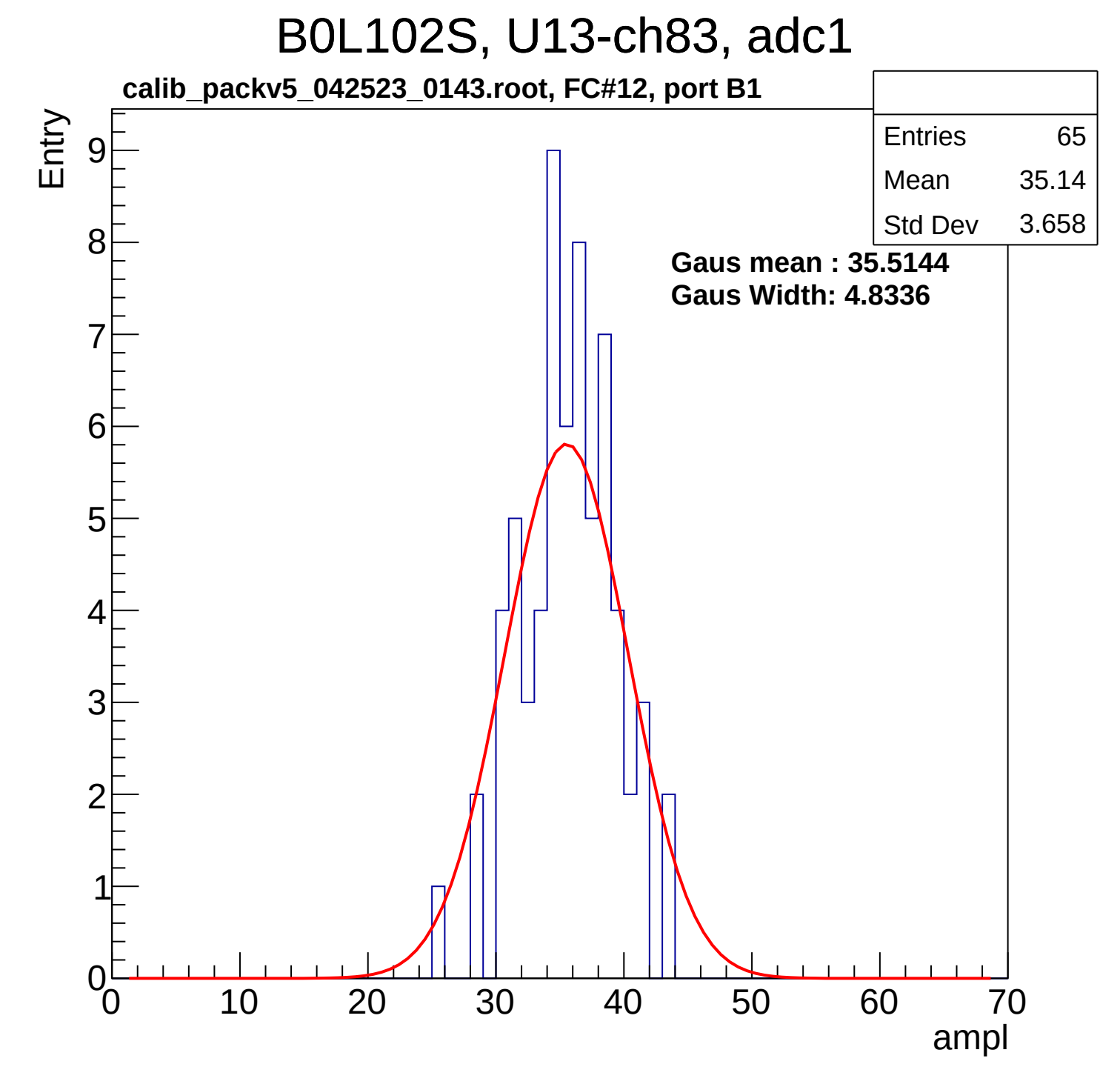
B0L102S, U13-ch83, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.14
Std Dev 3.658
Gaus mean : 35.5144
Gaus Width: 4.8336
Entry
ampl
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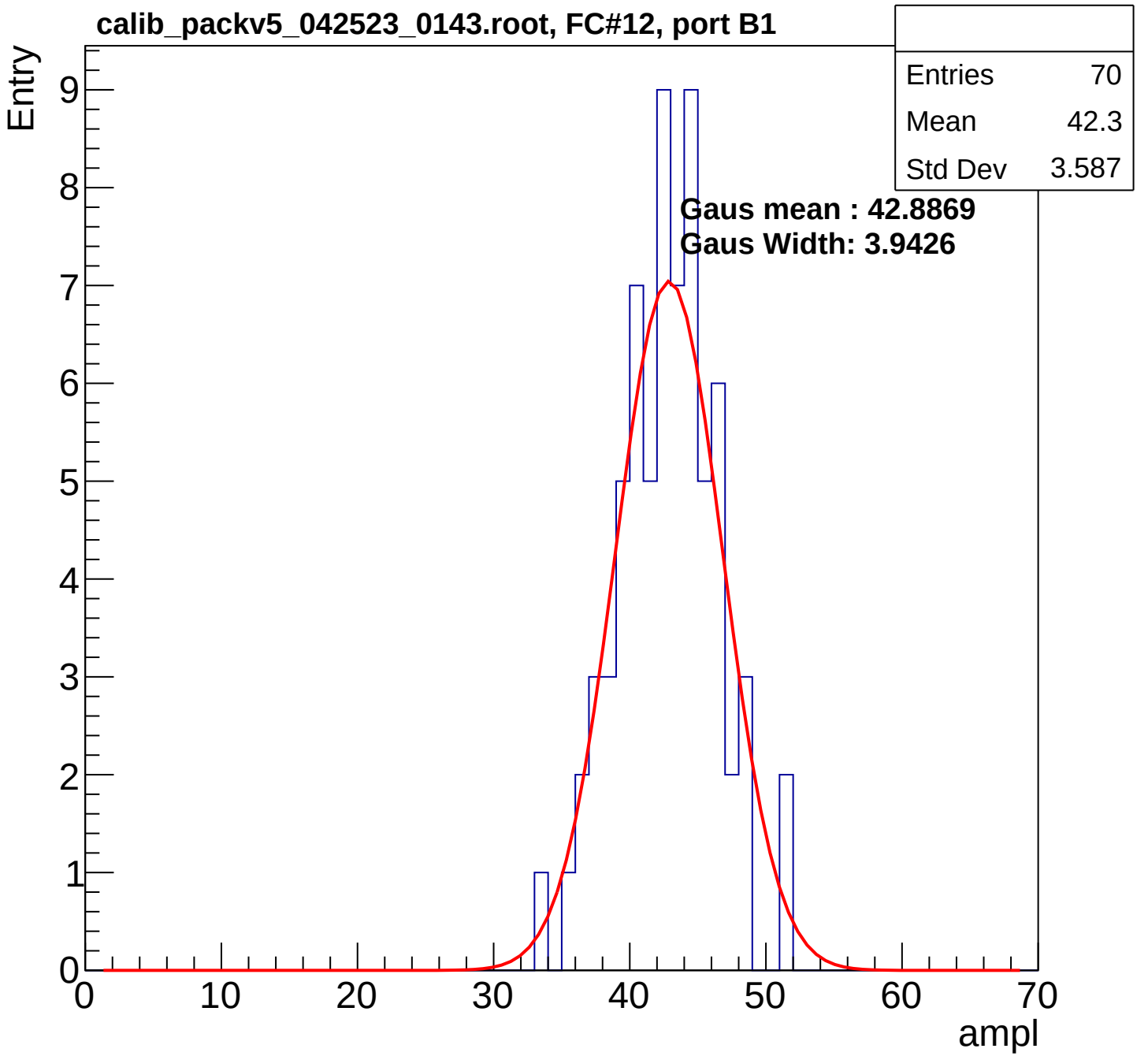
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch83, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 42.3
Std Dev 3.587
Gaus mean : 42.8869
Gaus Width: 3.9426
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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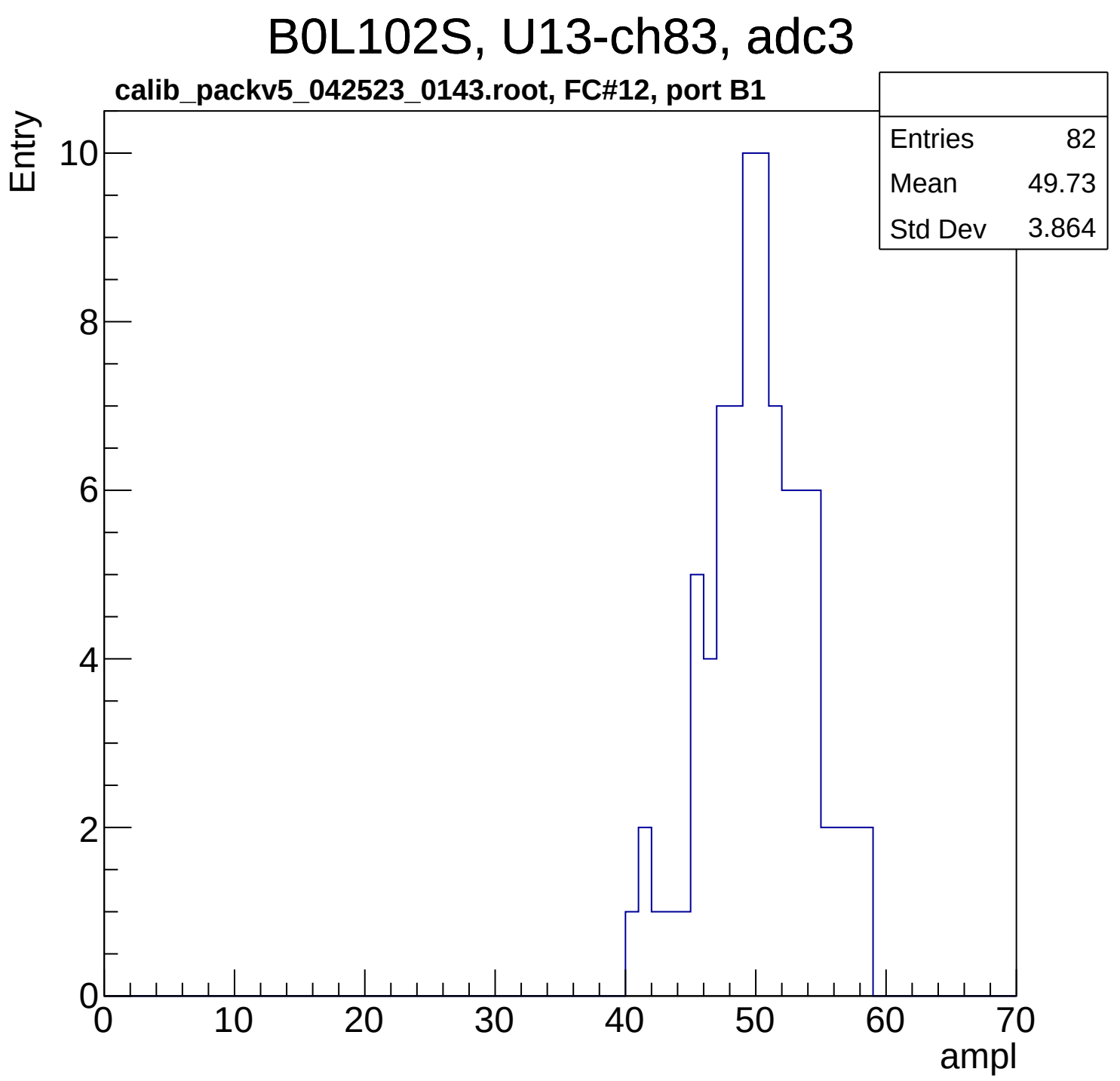
B0L102S, U13-ch83, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 49.73
Std Dev 3.864
Entry
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2
4
6
8
10
0
10
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30
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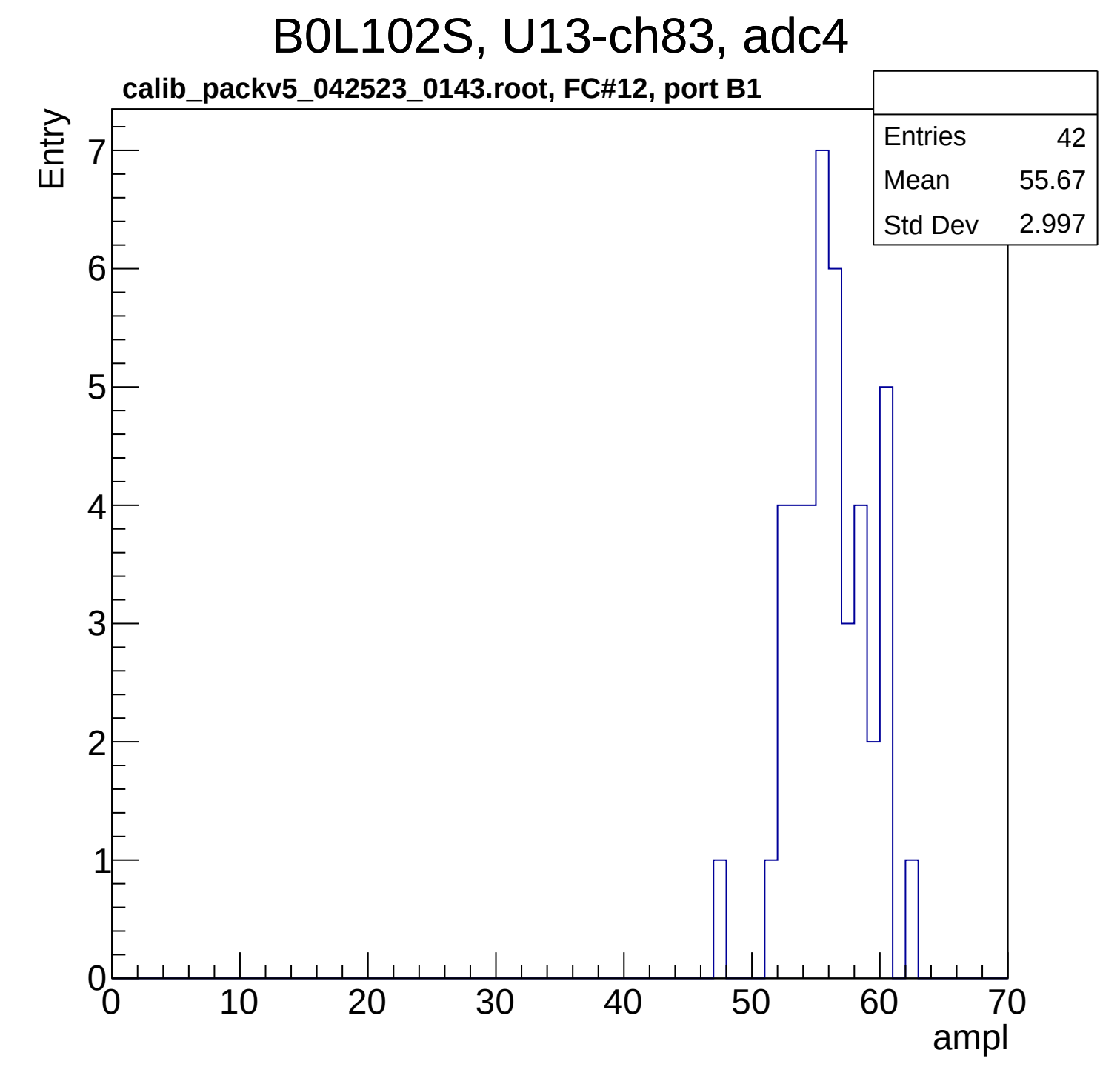
50
60
70
ampl


B0L102S, U13-ch83, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 55.67
Std Dev 2.997
Entry
ampl
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4
5
6
7
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20
30
40
50
60
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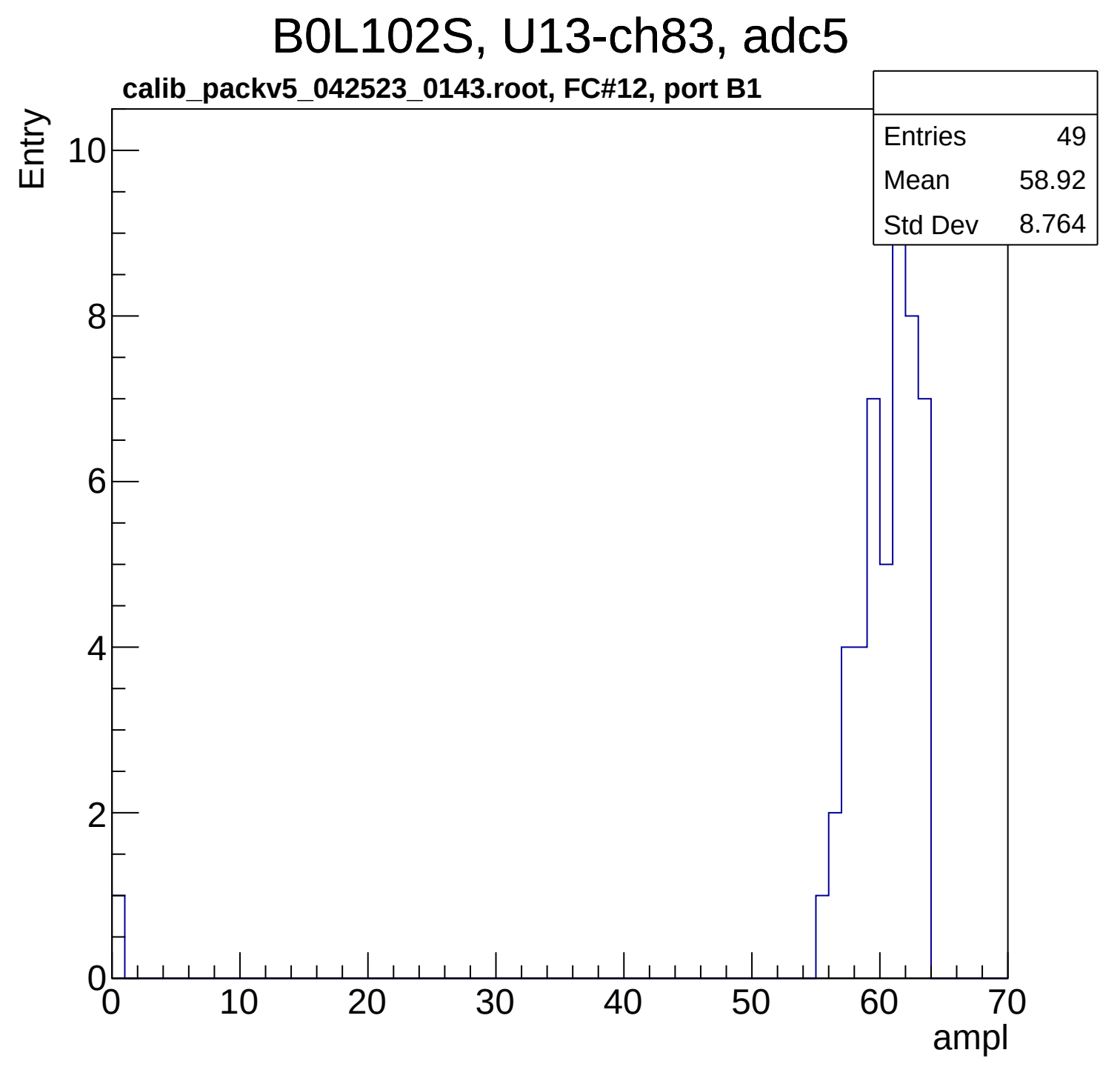
B0L102S, U13-ch83, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 58.92
Std Dev 8.764
Entry
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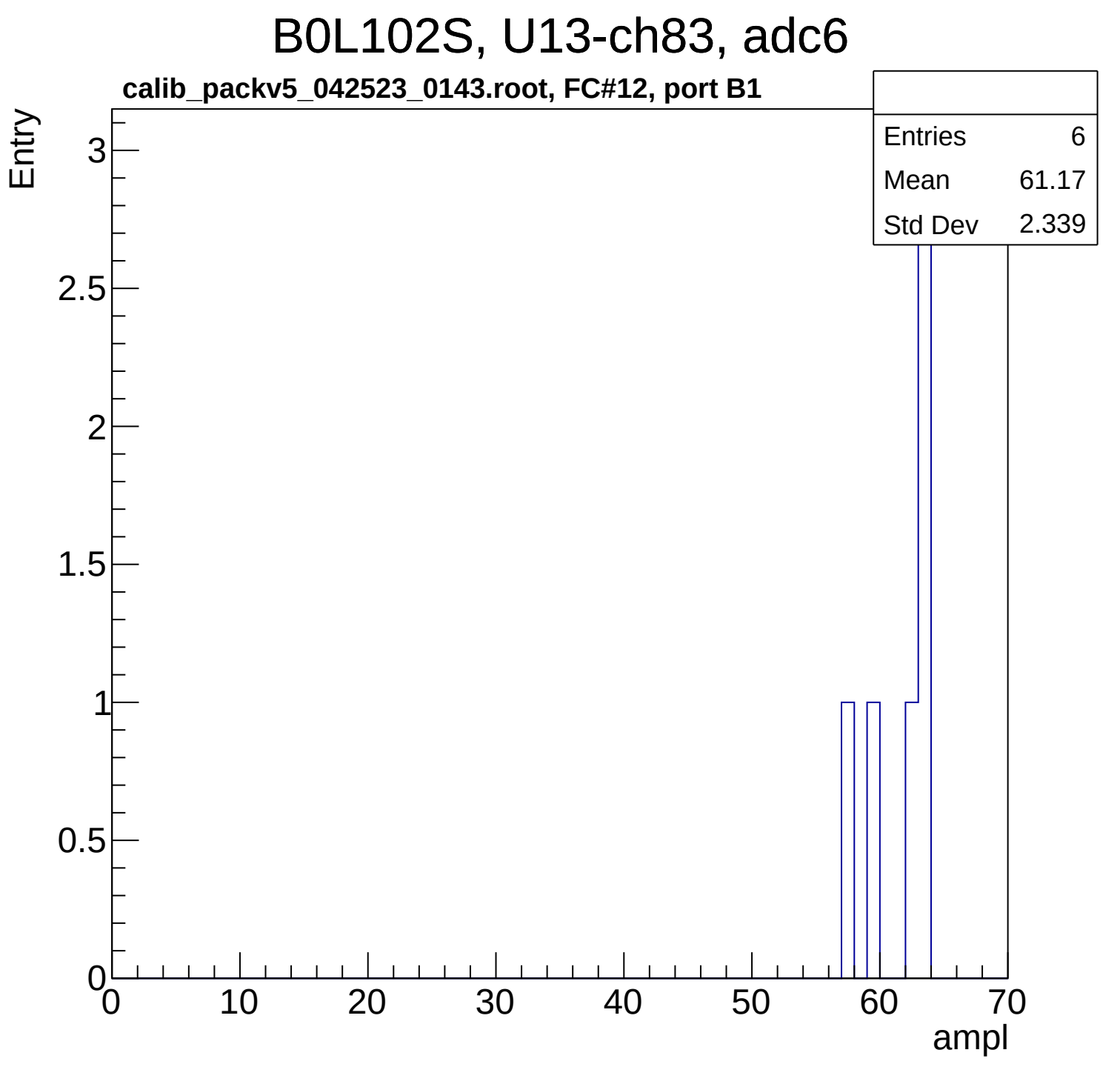
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch83, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.17
Std Dev 2.339
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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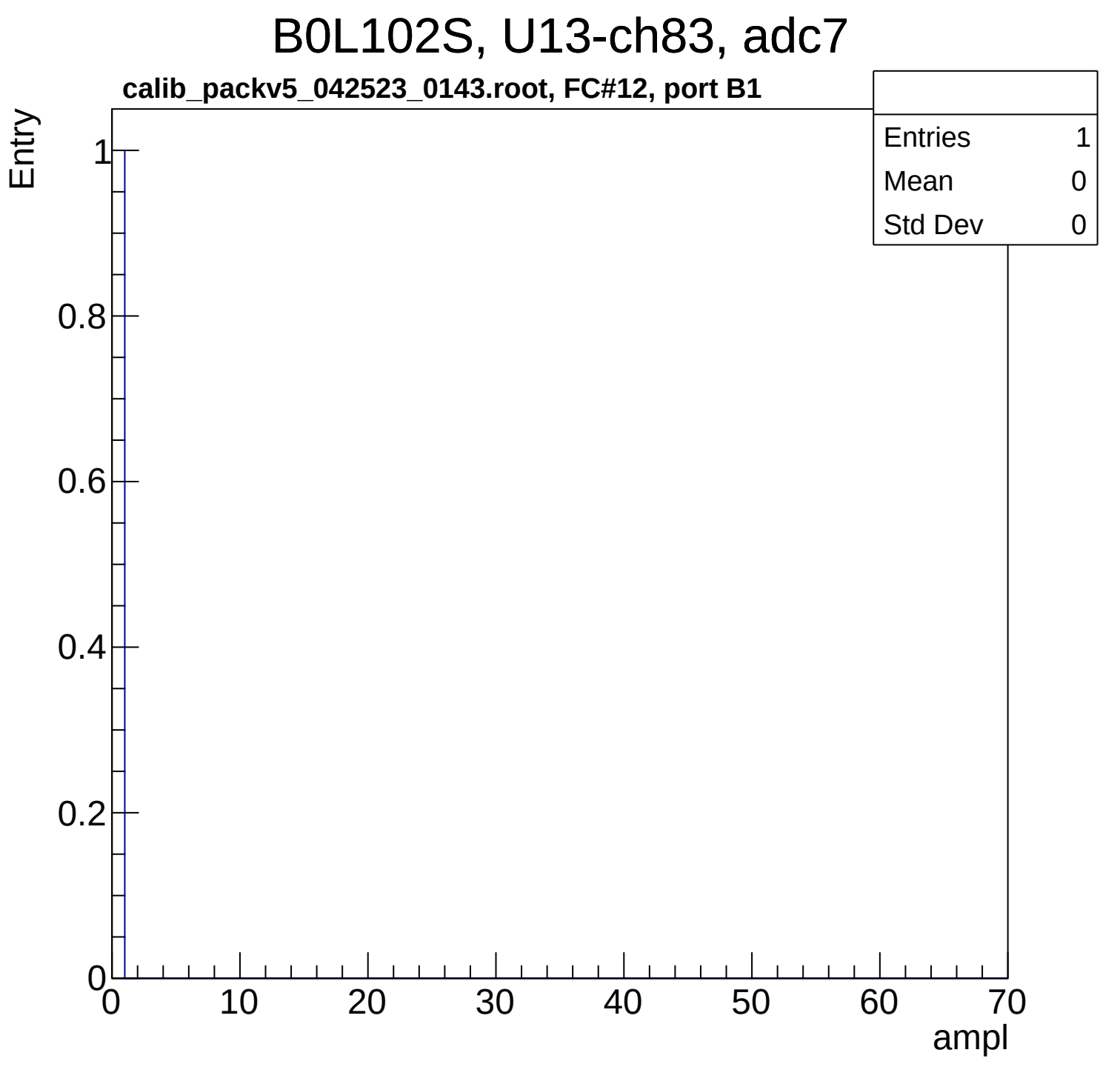
20
30
40
50
60
70


B0L102S, U13-ch83, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0
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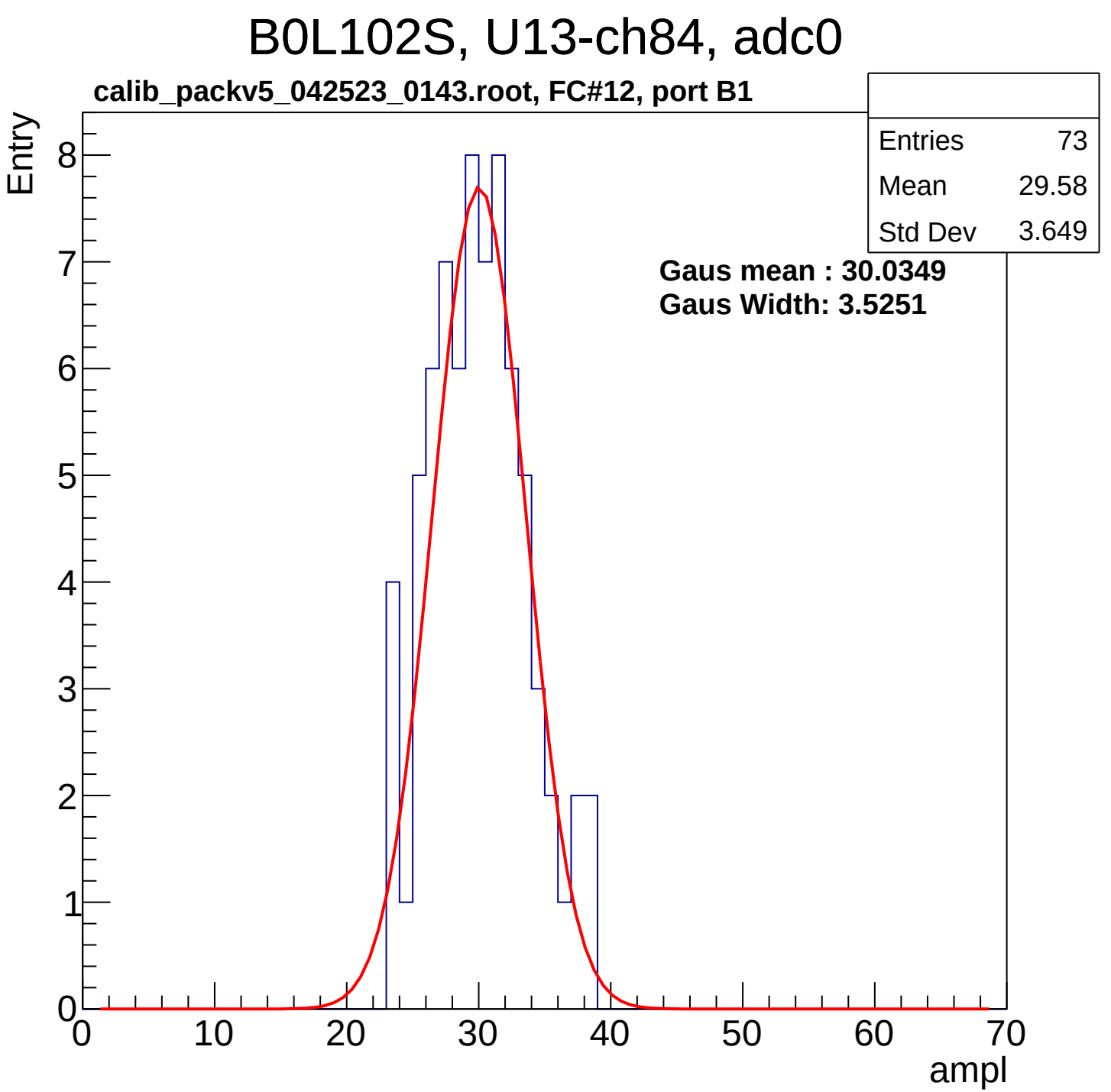
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch84, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.58
Std Dev 3.649
Gaus mean : 30.0349
Gaus Width: 3.5251
Entry
ampl
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1
2
3
4
5
6
7
8
0
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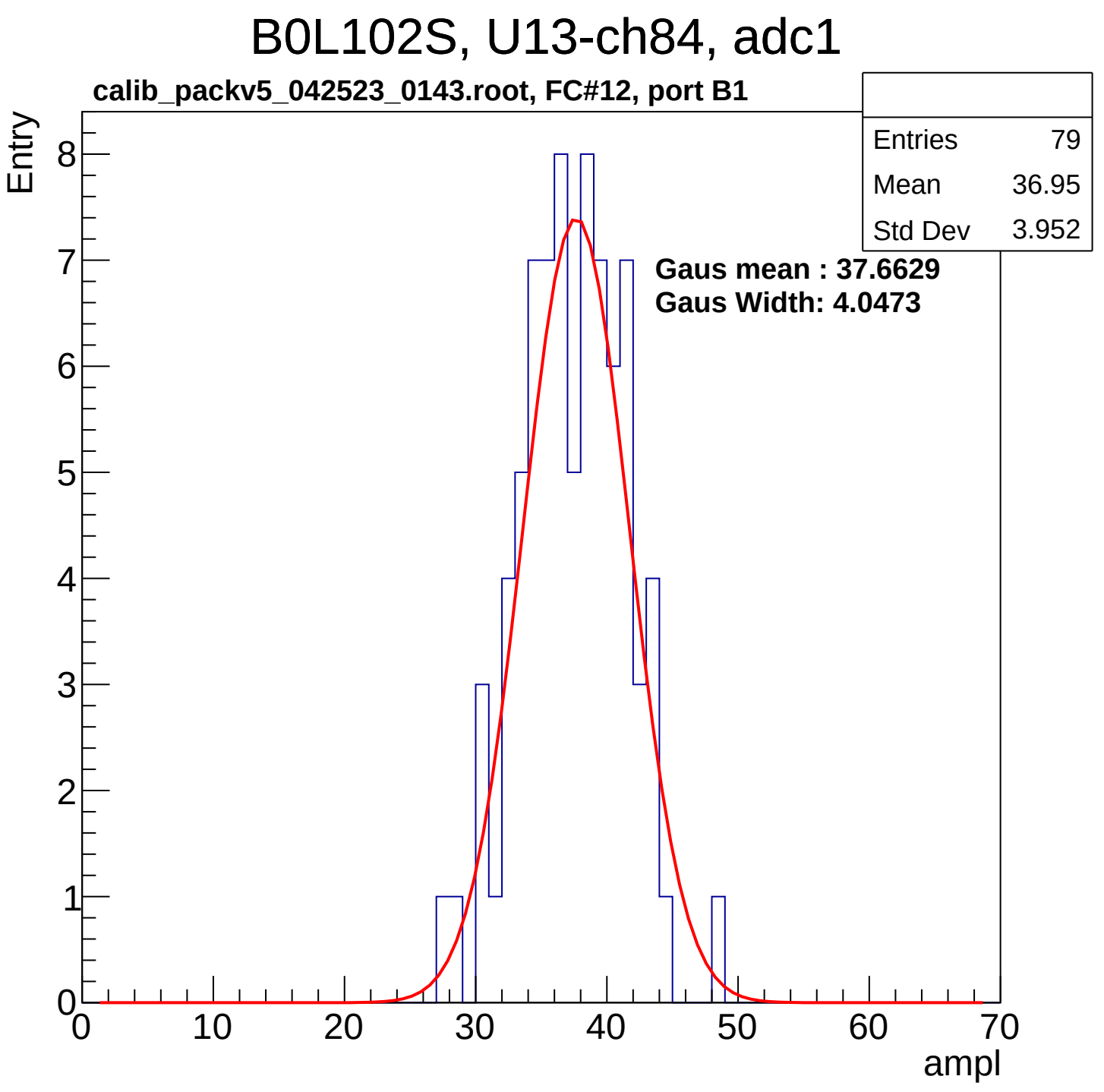
20
30
40
50
60
70


B0L102S, U13-ch84, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 36.95
Std Dev 3.952
Gaus mean : 37.6629
Gaus Width: 4.0473
Entry
ampl
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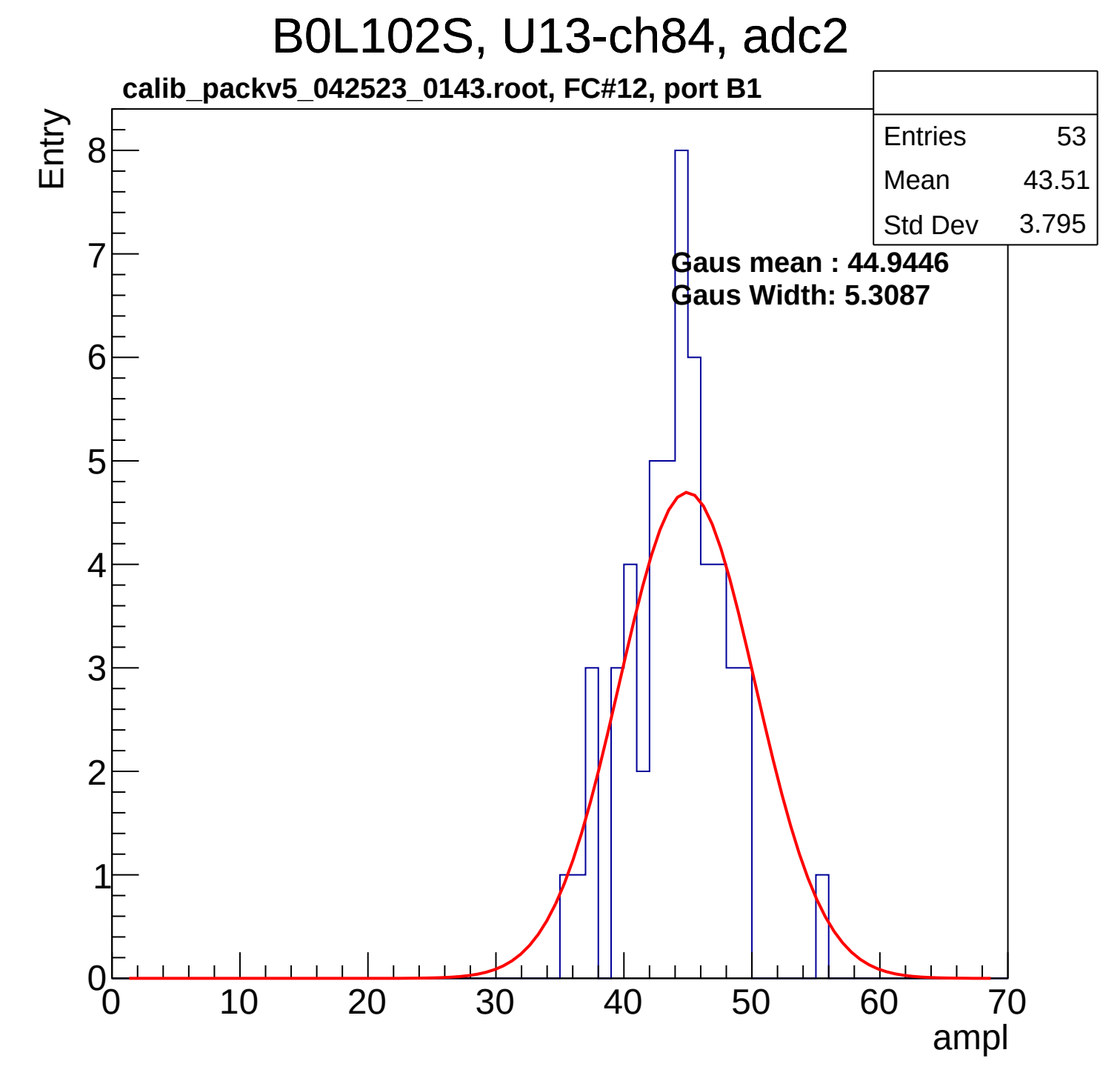
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch84, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 43.51
Std Dev 3.795
Gaus mean : 44.9446
Gaus Width: 5.3087
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
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50
60
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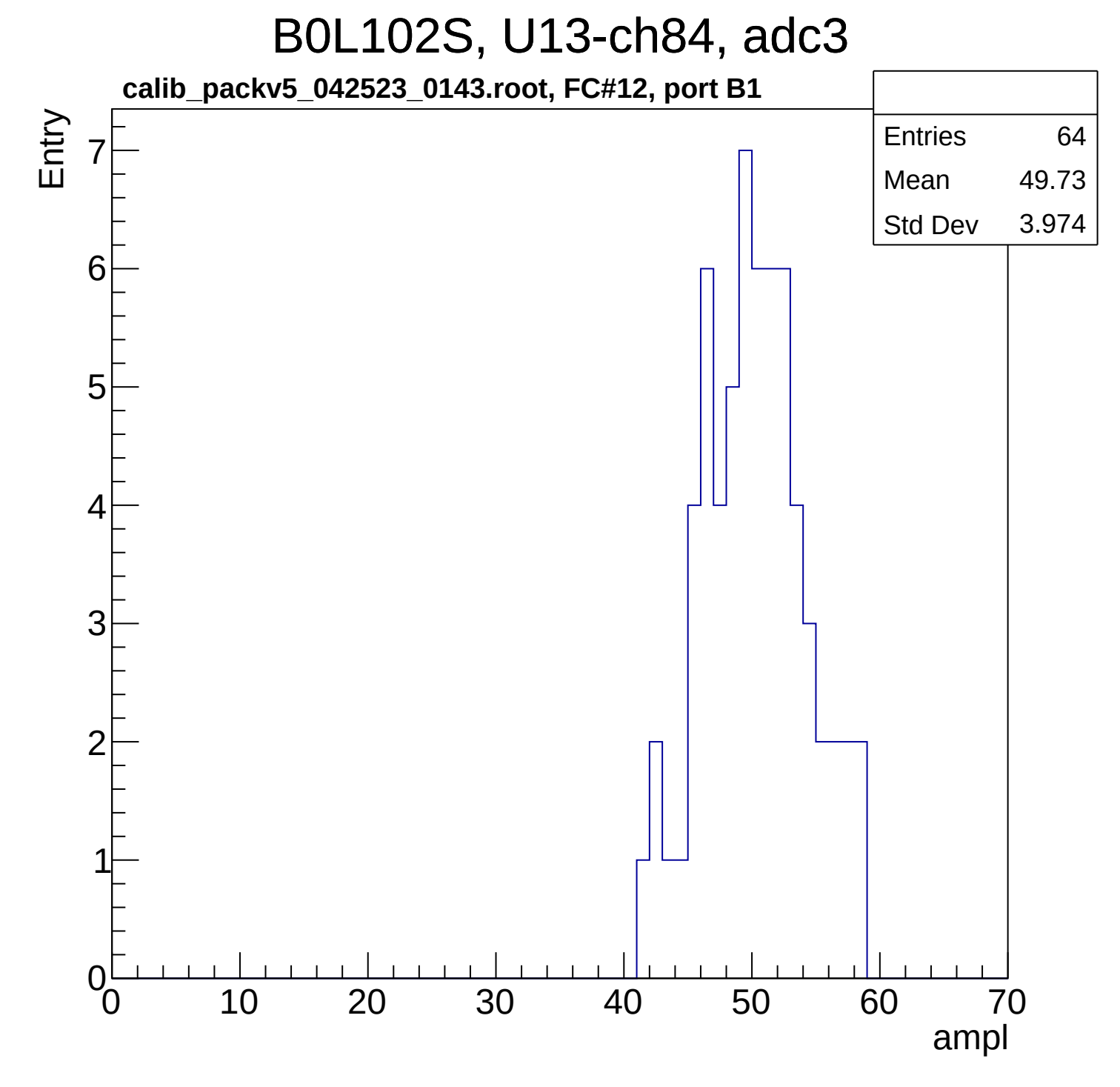
B0L102S, U13-ch84, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.73
Std Dev 3.974
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
50
60
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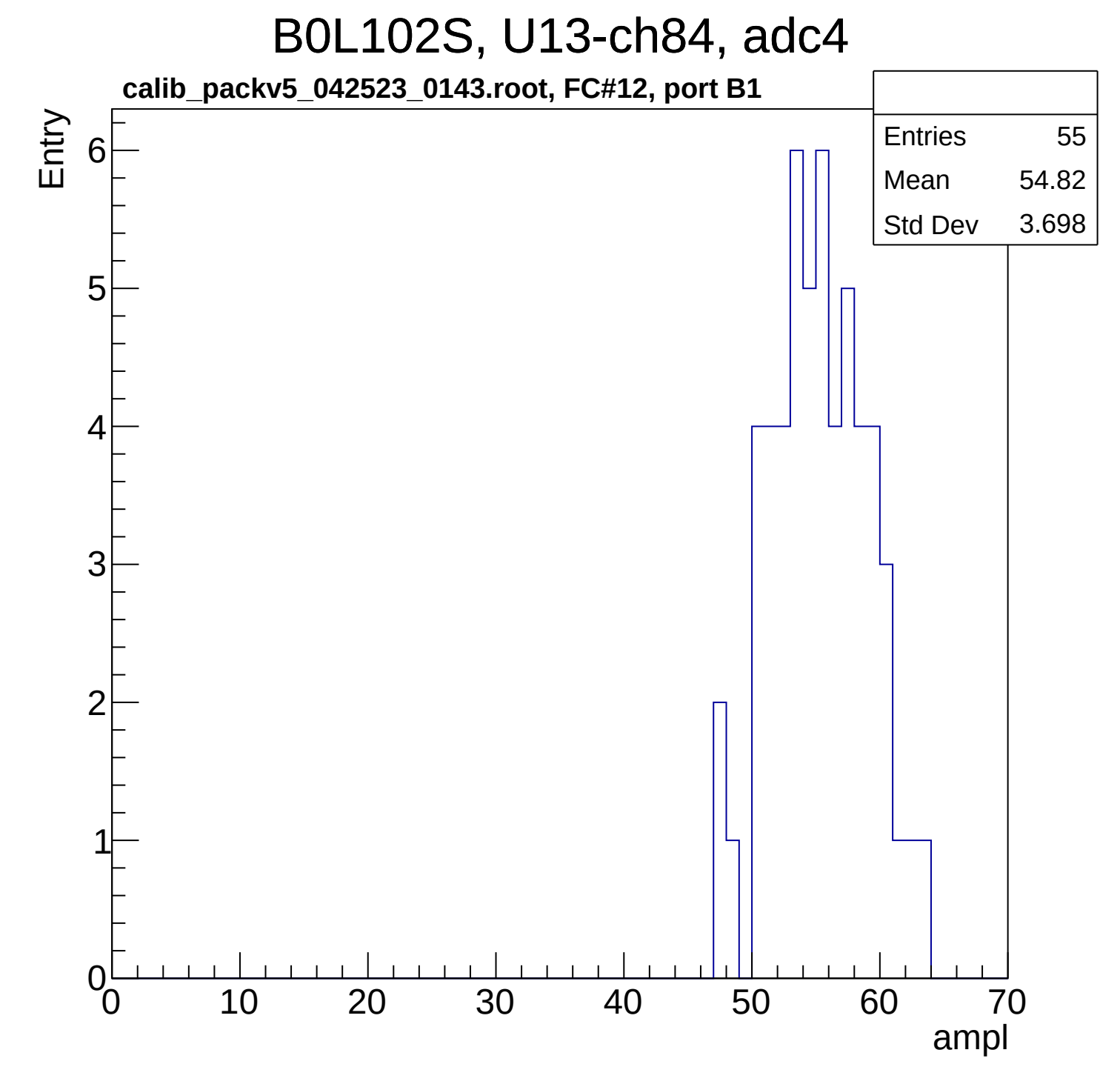
B0L102S, U13-ch84, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.82
Std Dev 3.698
Entry
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2
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4
5
6
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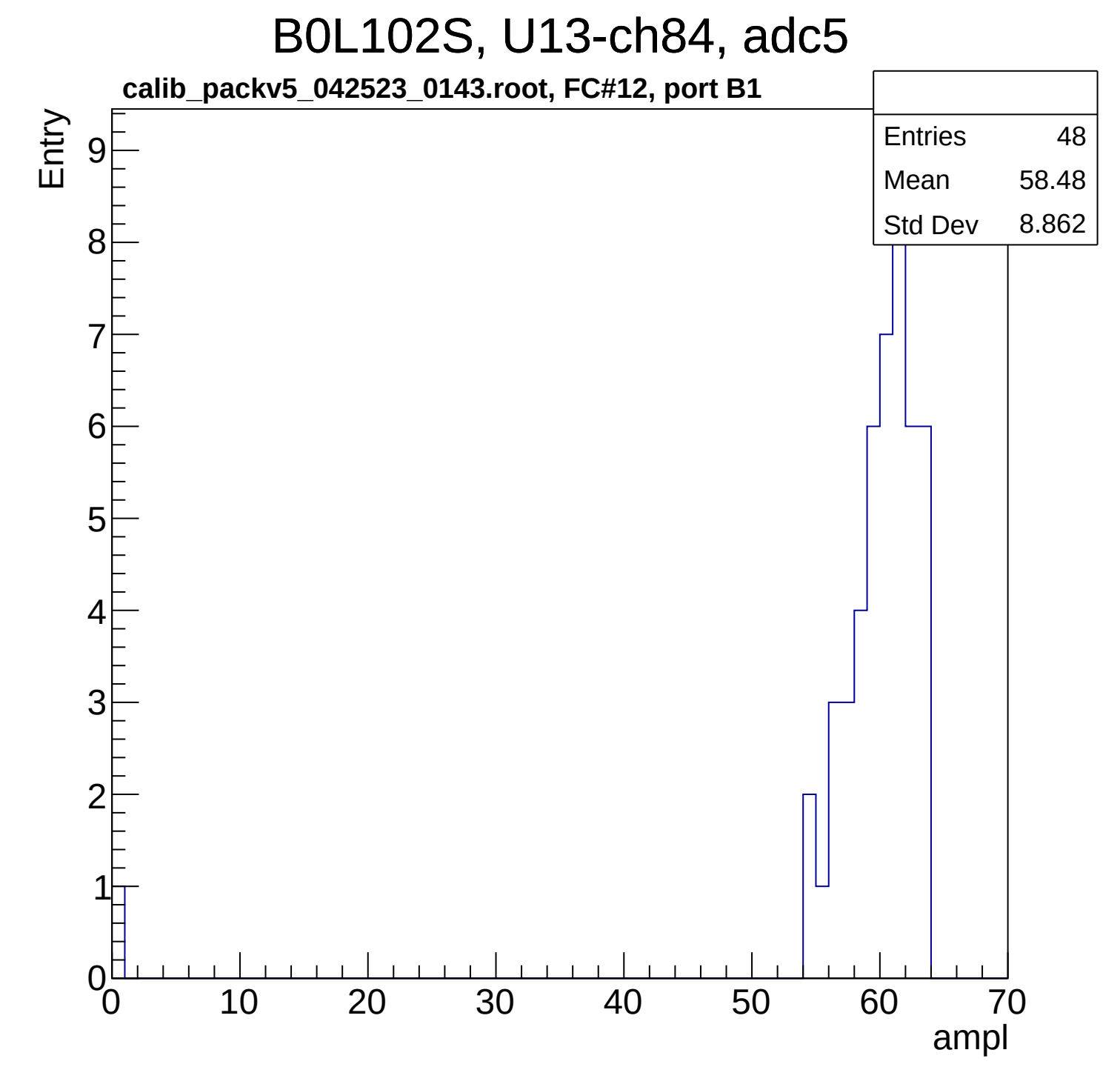
20
30
40
50
60
70
ampl


B0L102S, U13-ch84, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.48
Std Dev 8.862
Entry
ampl
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2
3
4
5
6
7
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9
0
10
20
30
40
50
60
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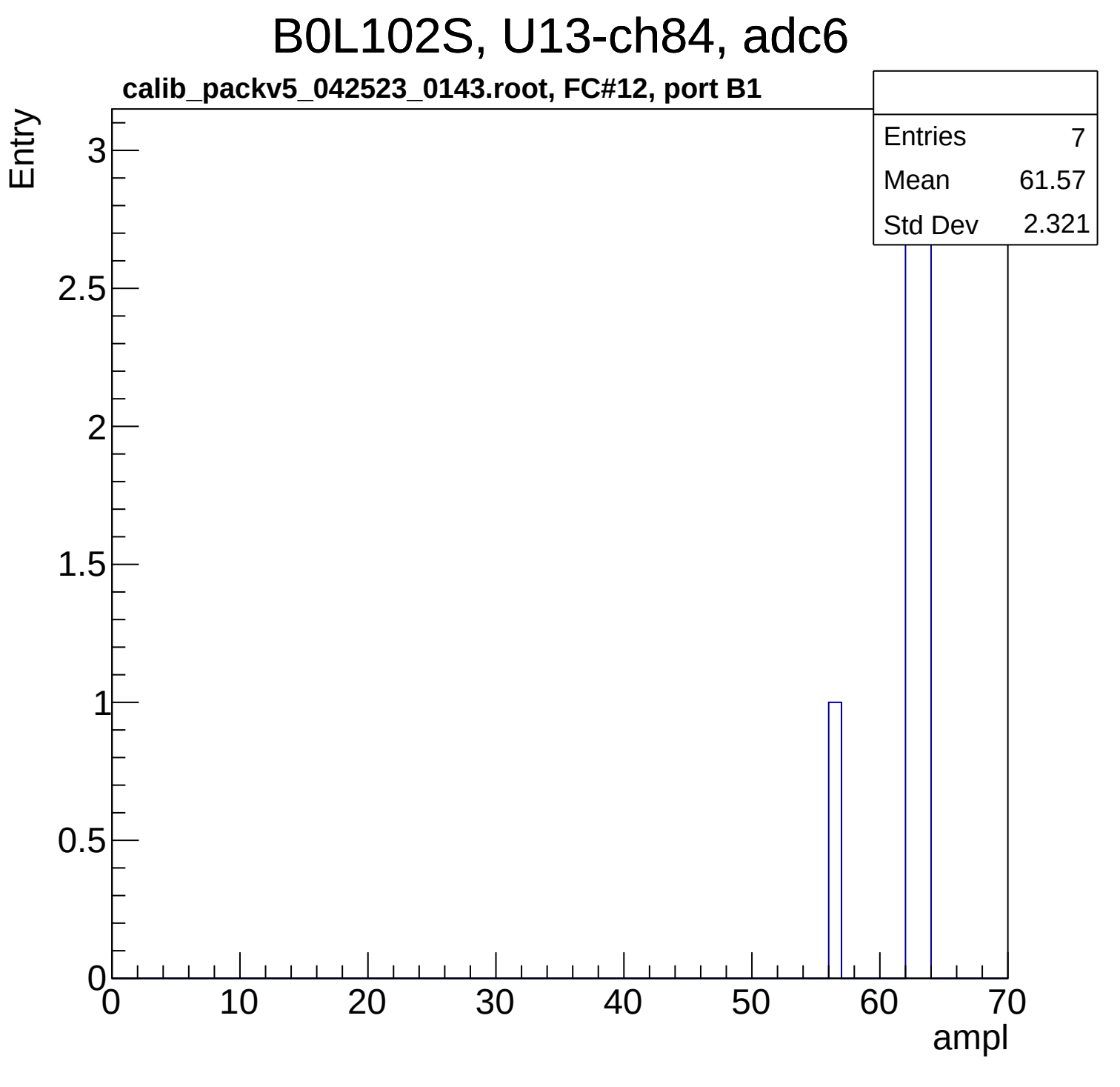
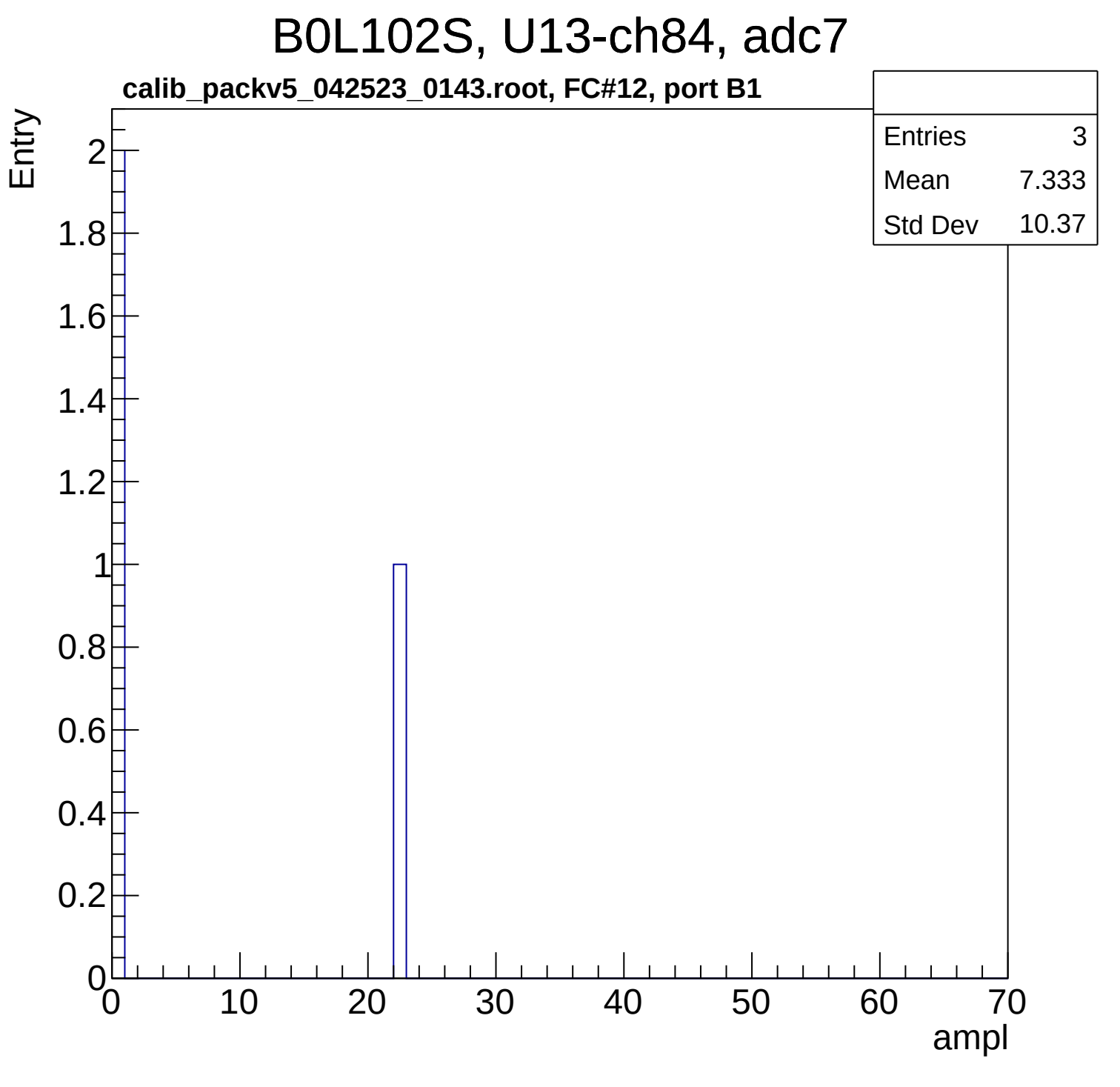

B0L102S, U13-ch84, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.57
Std Dev 2.321
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch84, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
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60
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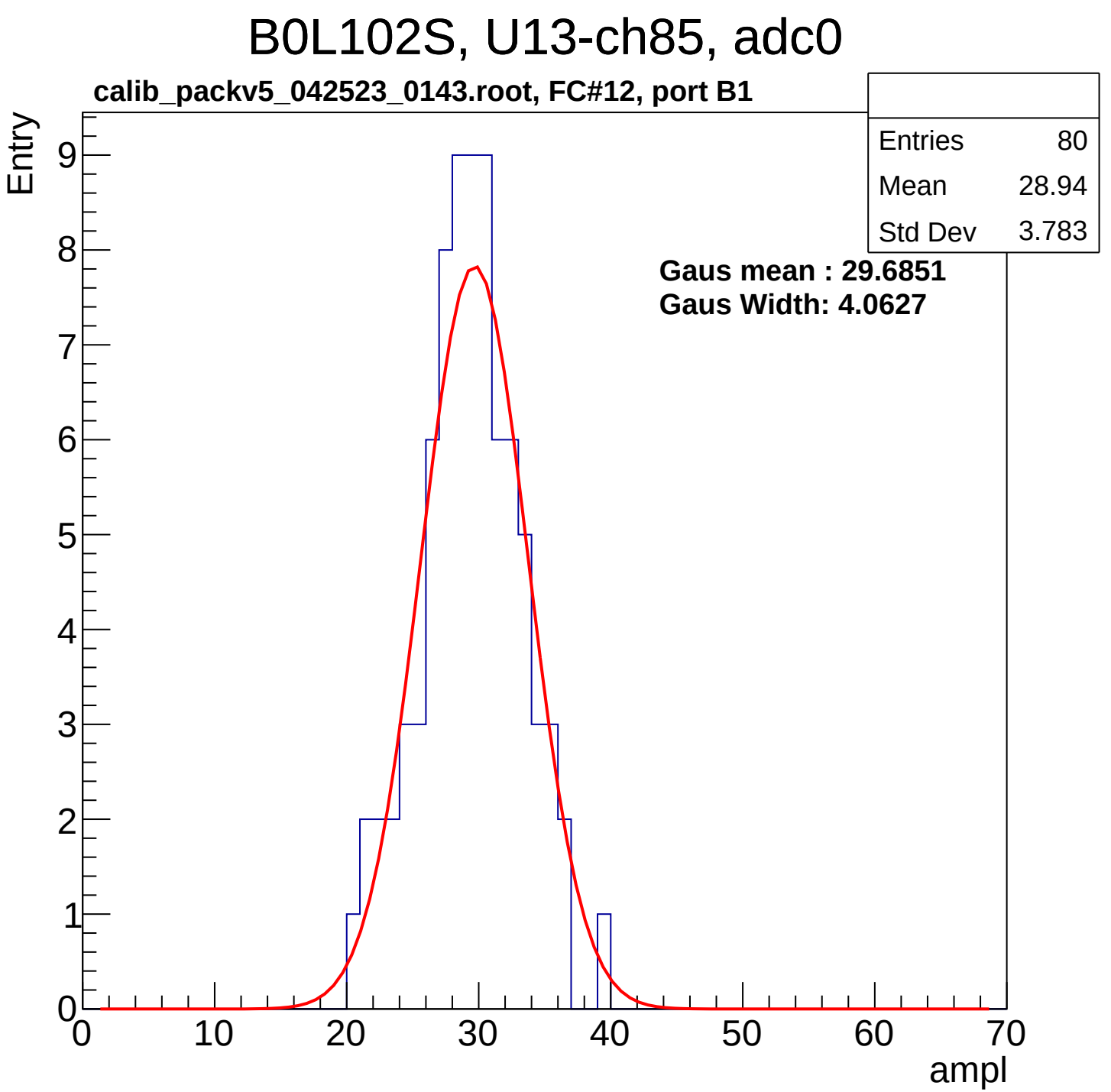
B0L102S, U13-ch85, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 28.94
Std Dev 3.783
Gaus mean : 29.6851
Gaus Width: 4.0627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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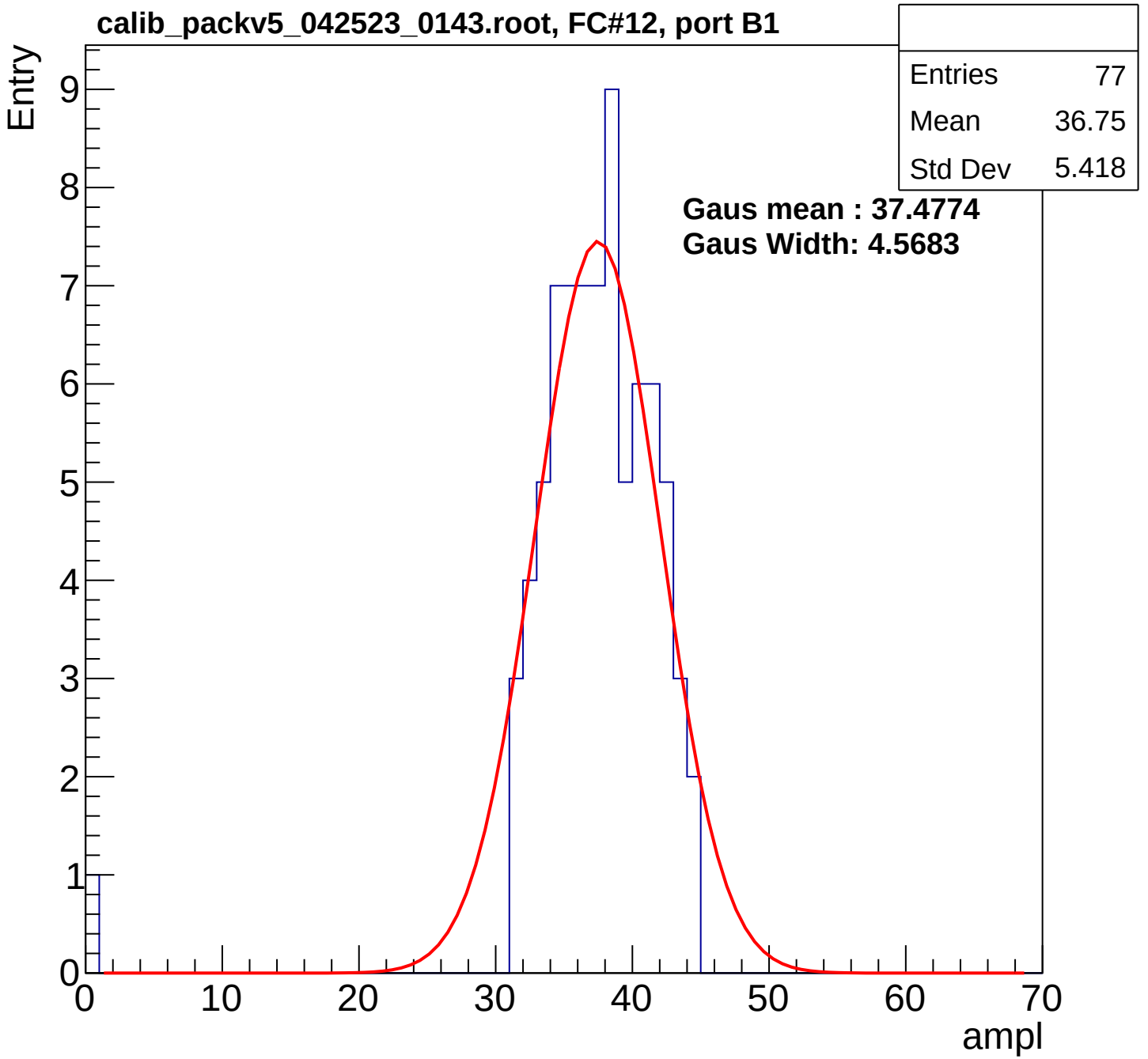
B0L102S, U13-ch85, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 36.75
Std Dev 5.418
Gaus mean : 37.4774
Gaus Width: 4.5683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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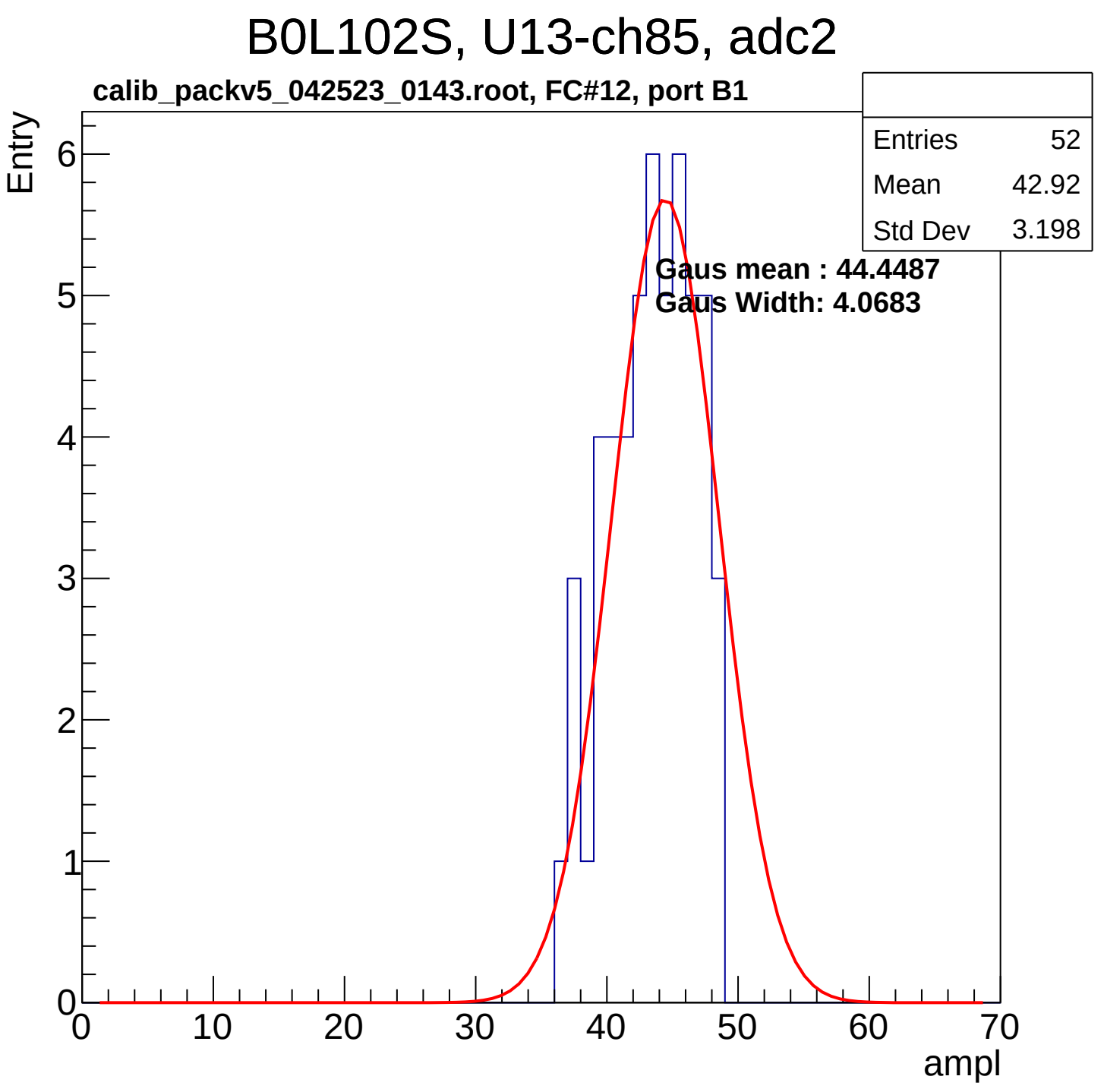
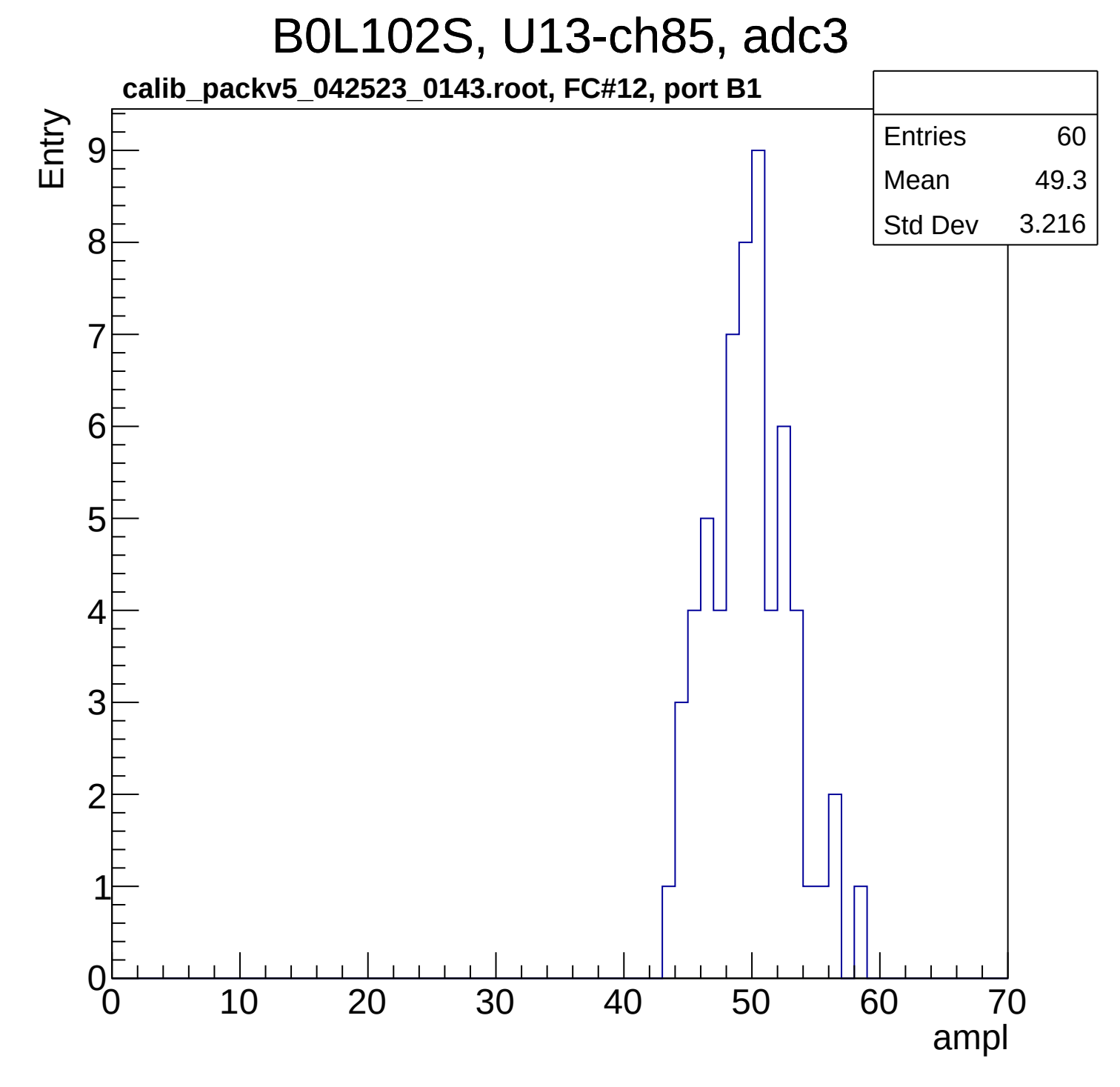

B0L102S, U13-ch85, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 42.92
Std Dev 3.198
Gaus mean : 44.4487
Gaus Width: 4.0683
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch85, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 49.3
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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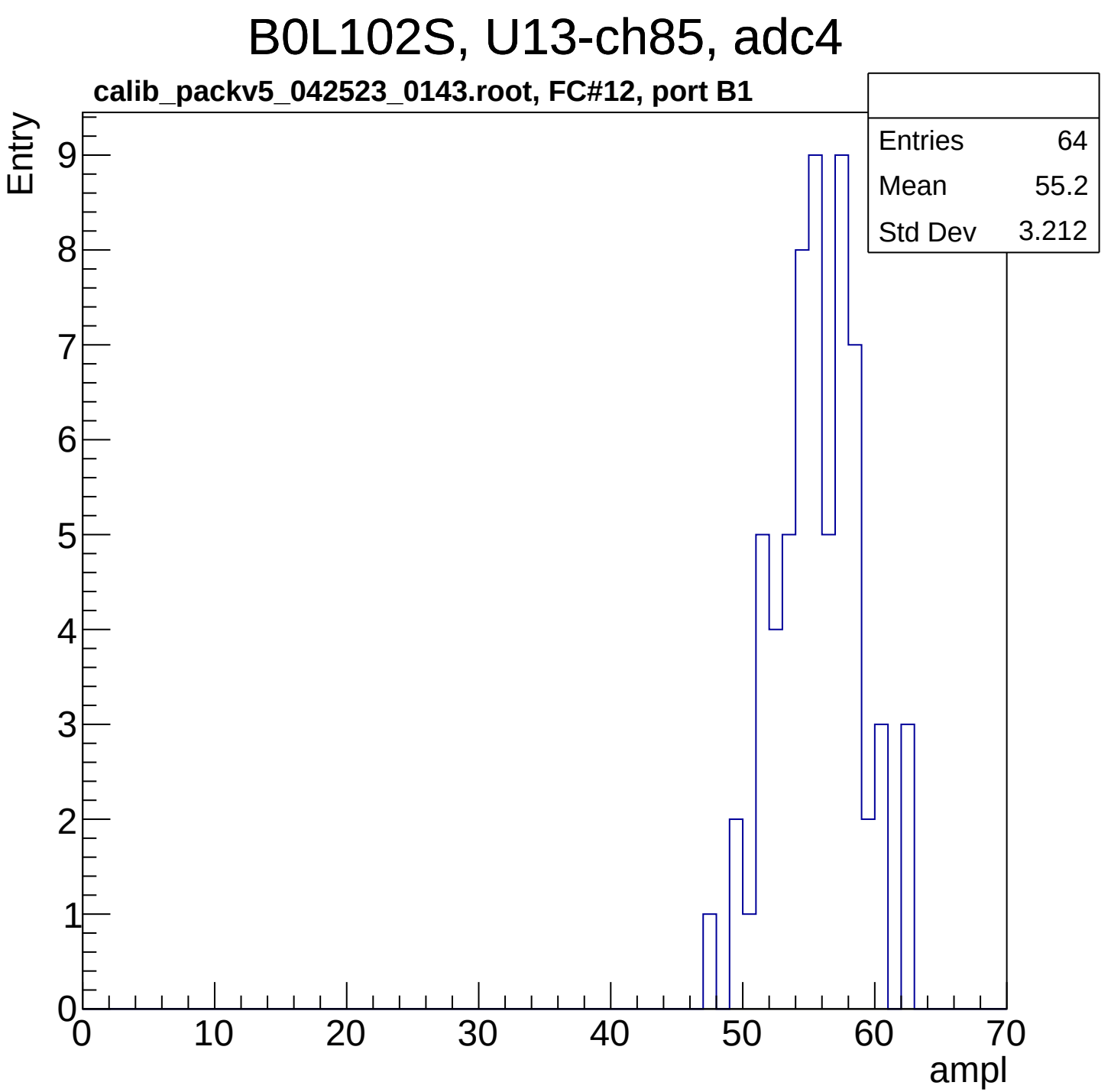
B0L102S, U13-ch85, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.2
Std Dev 3.212
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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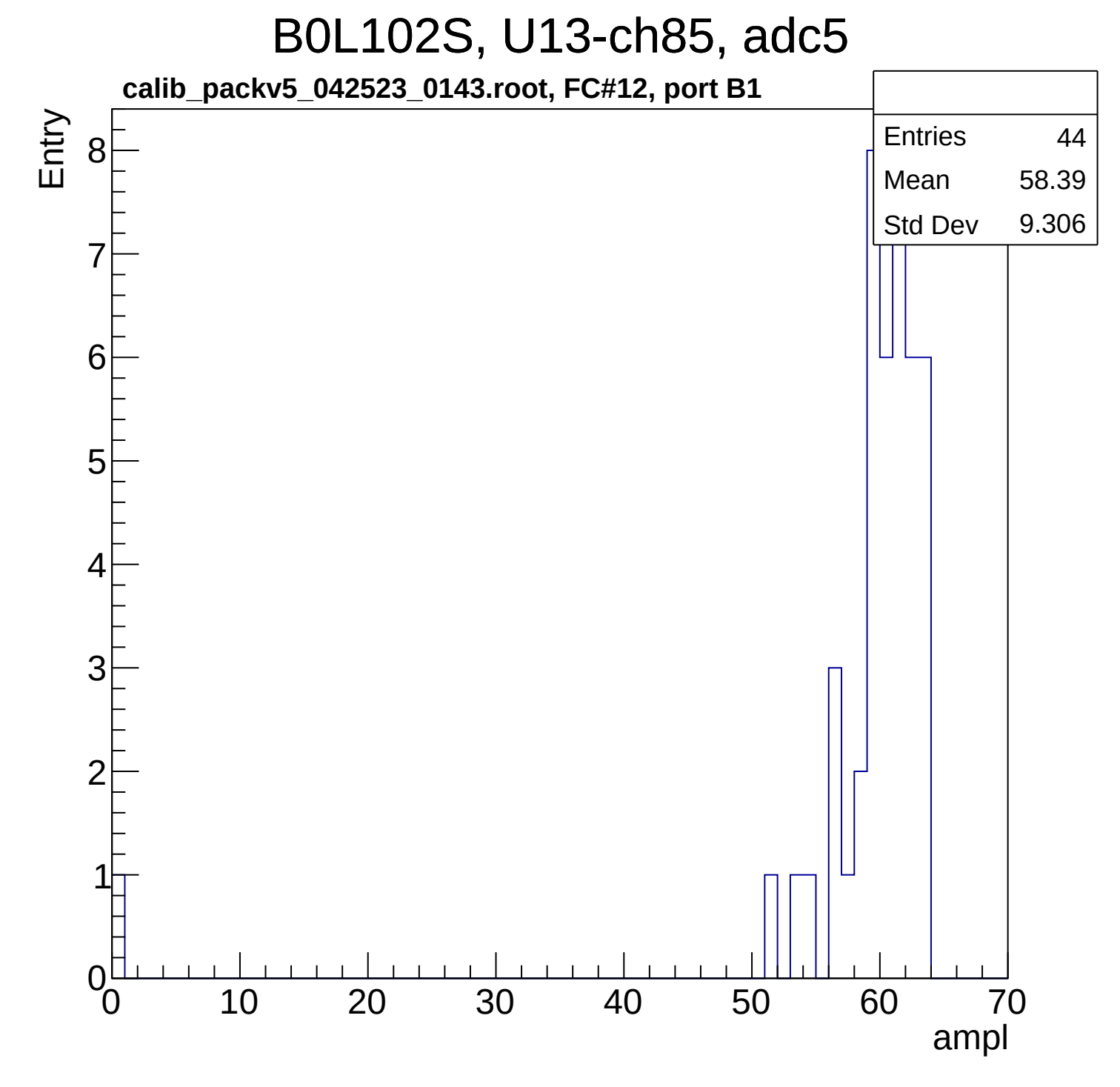
B0L102S, U13-ch85, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.39
Std Dev 9.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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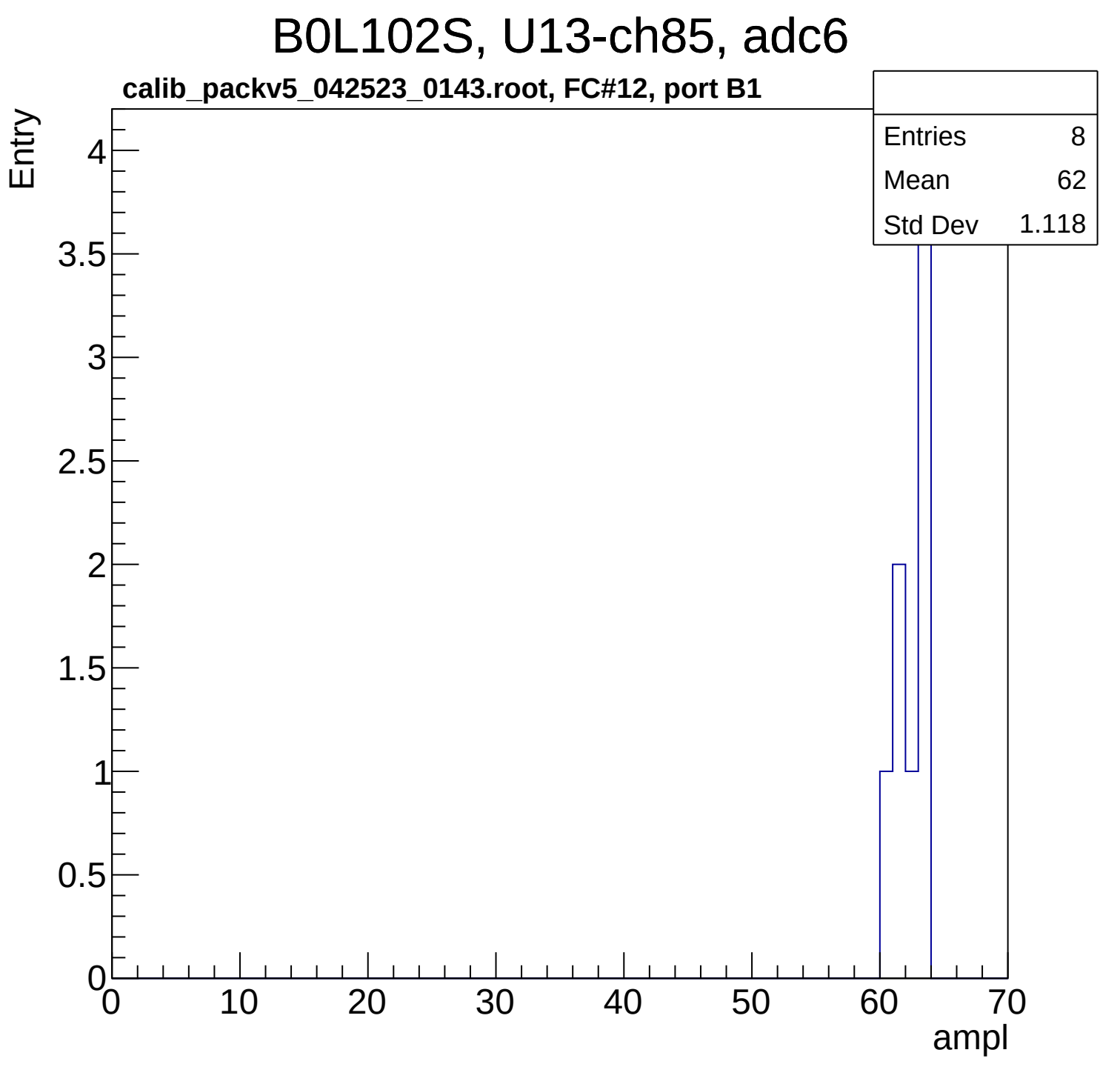
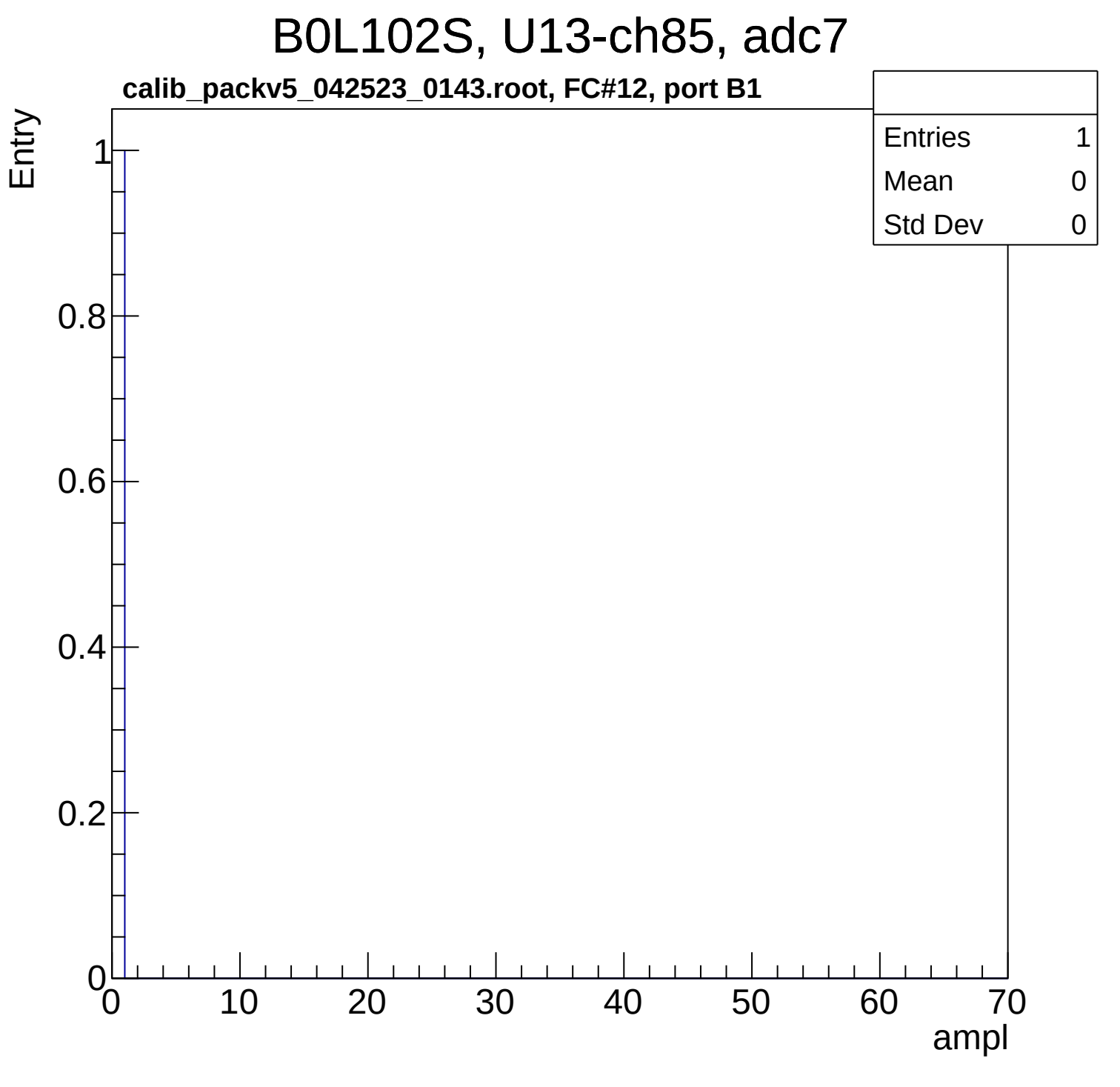
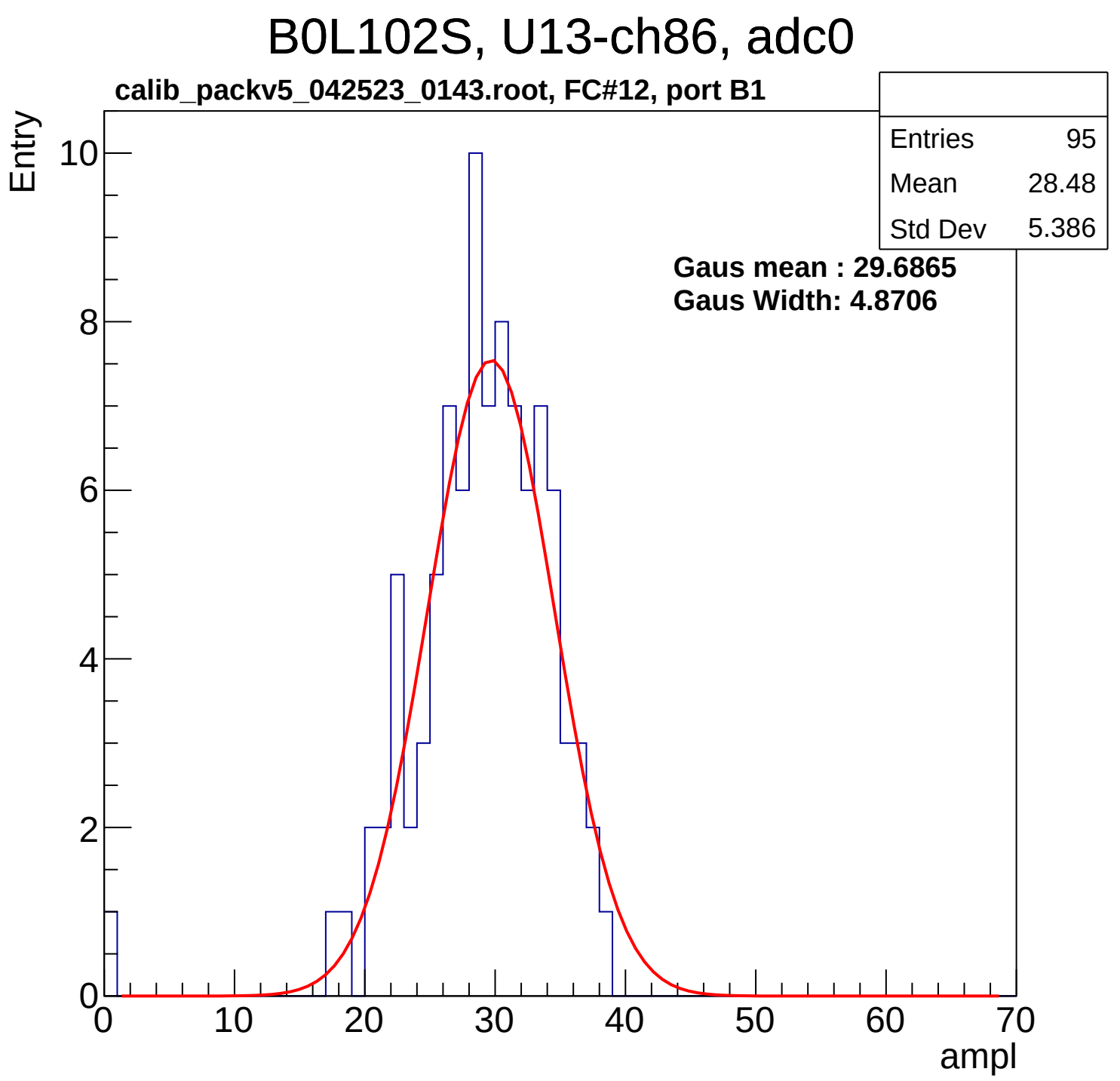
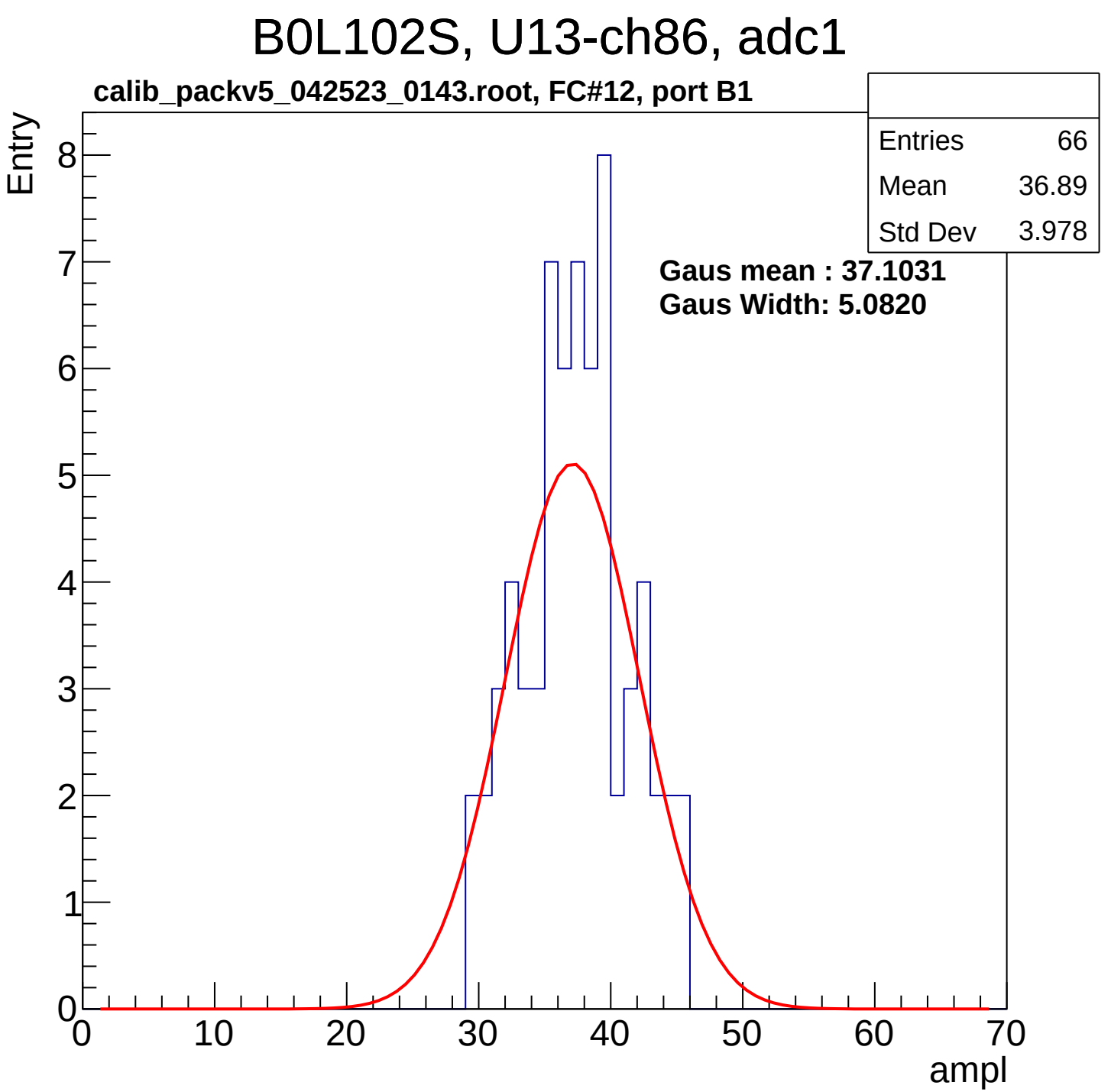

B0L102S, U13-ch85, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62
Std Dev 1.118
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch85, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch86, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 95
Mean 28.48
Std Dev 5.386
Gaus mean : 29.6865
Gaus Width: 4.8706
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch86, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.89
Std Dev 3.978
Gaus mean : 37.1031
Gaus Width: 5.0820
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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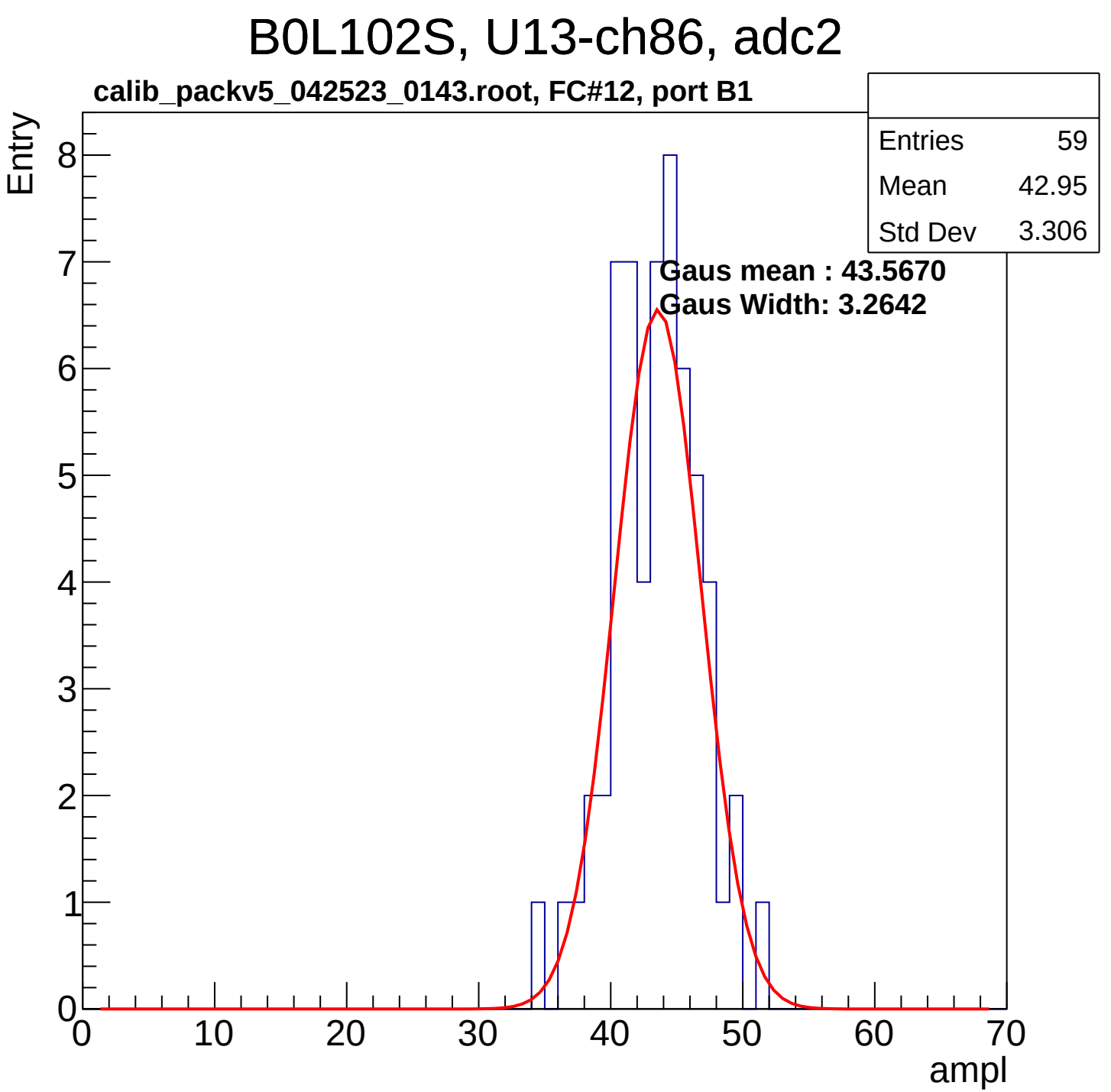
B0L102S, U13-ch86, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 42.95
Std Dev 3.306
Gaus mean : 43.5670
Gaus Width: 3.2642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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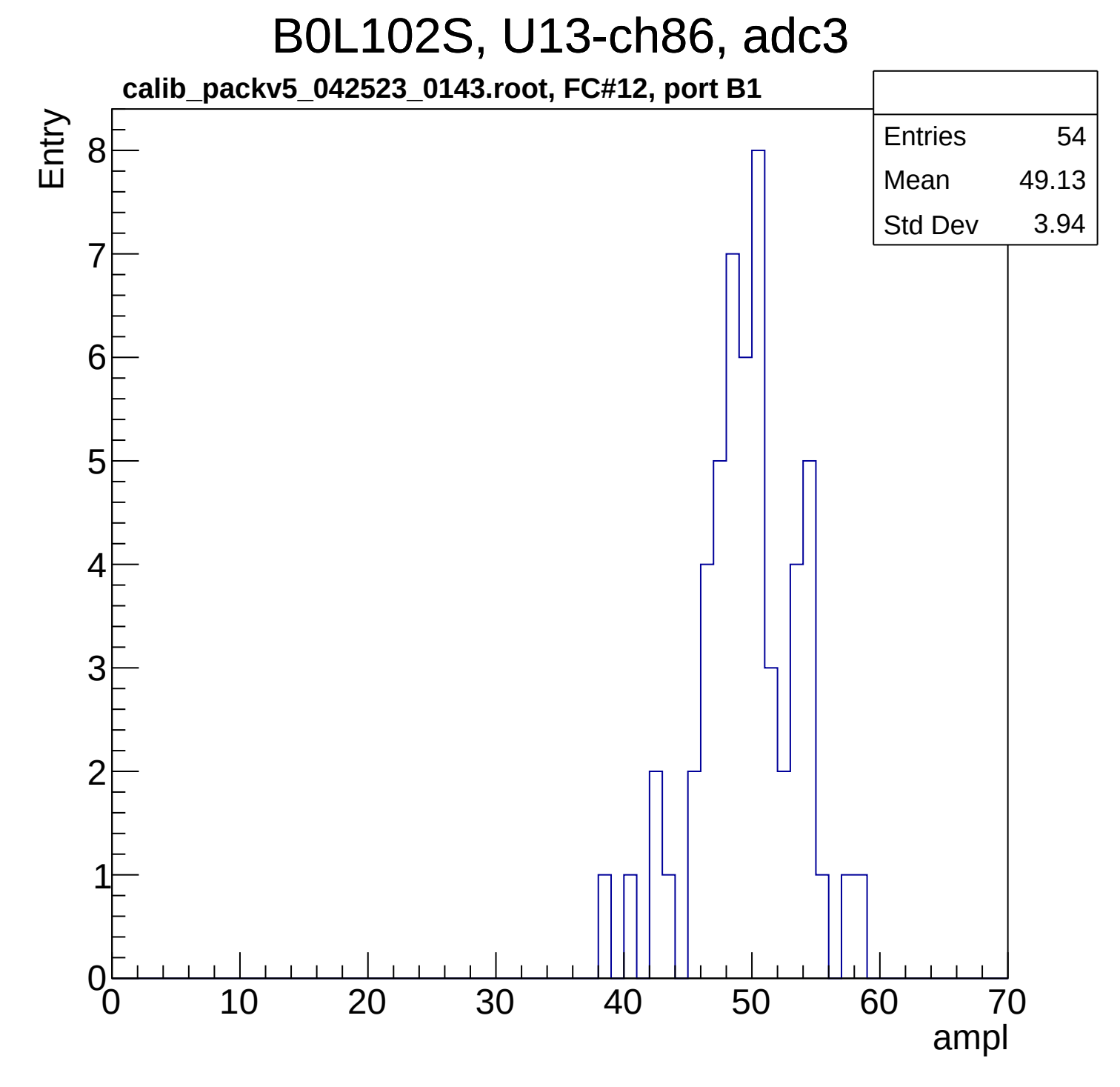
B0L102S, U13-ch86, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 49.13
Std Dev 3.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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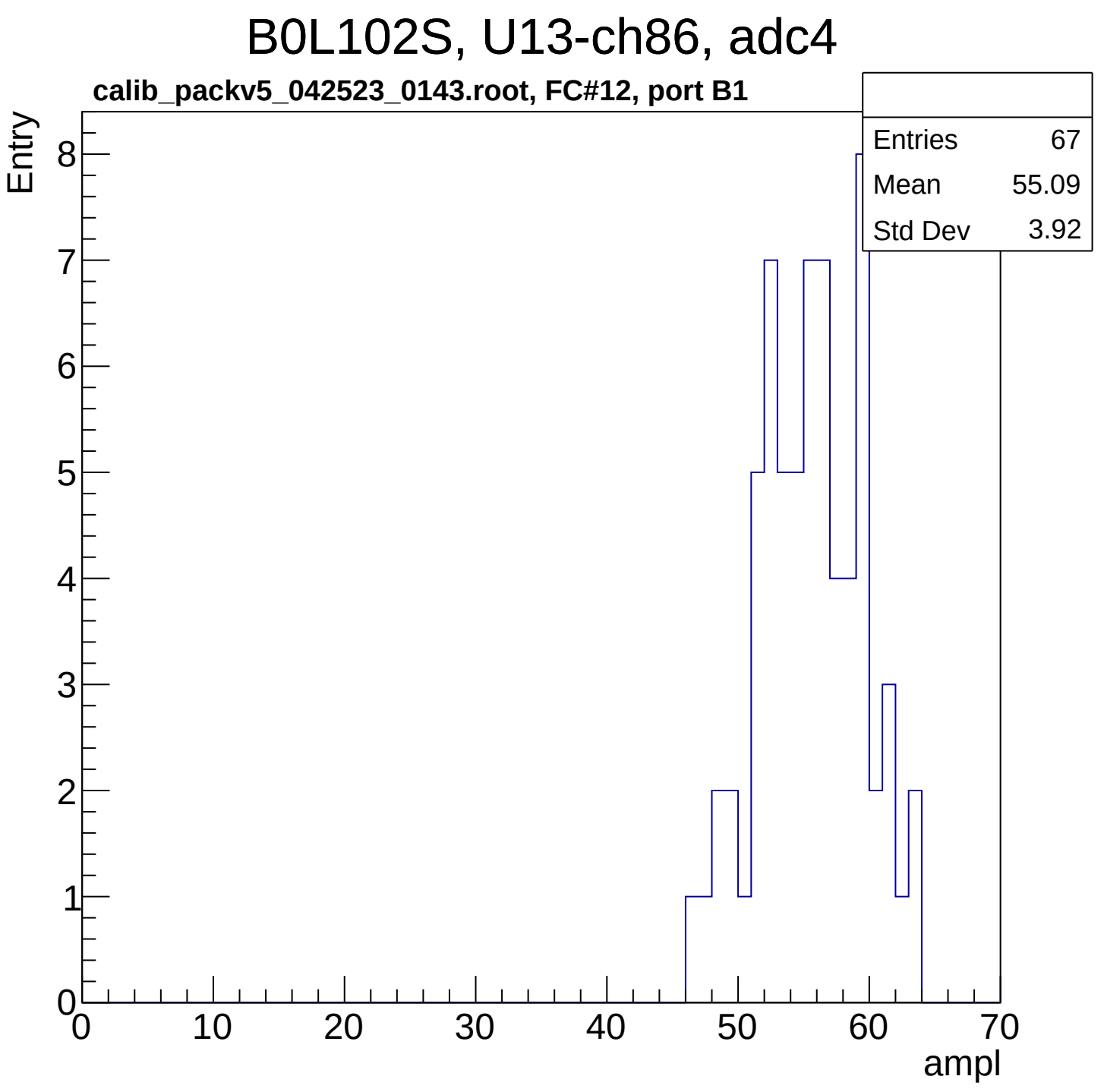
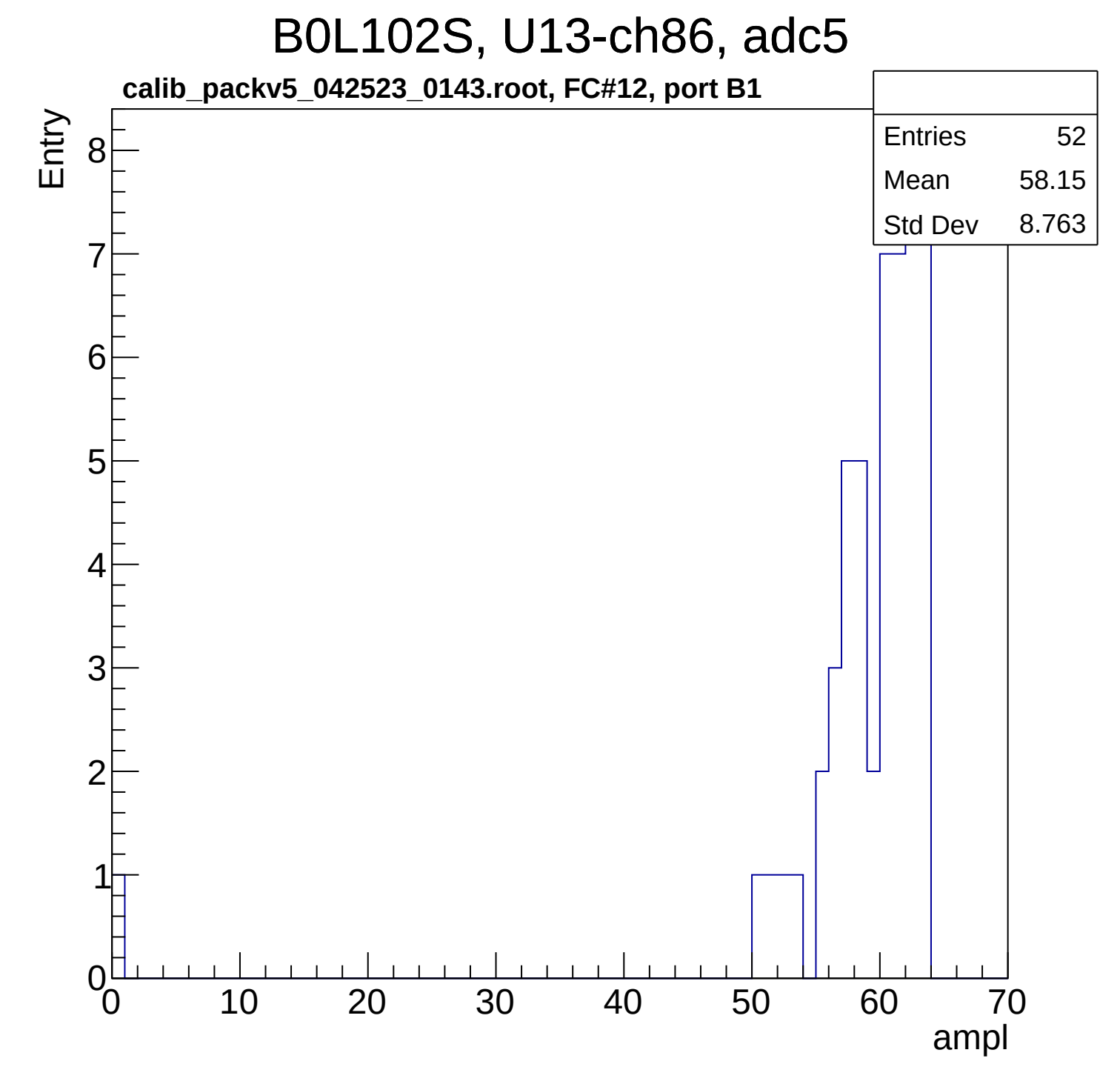
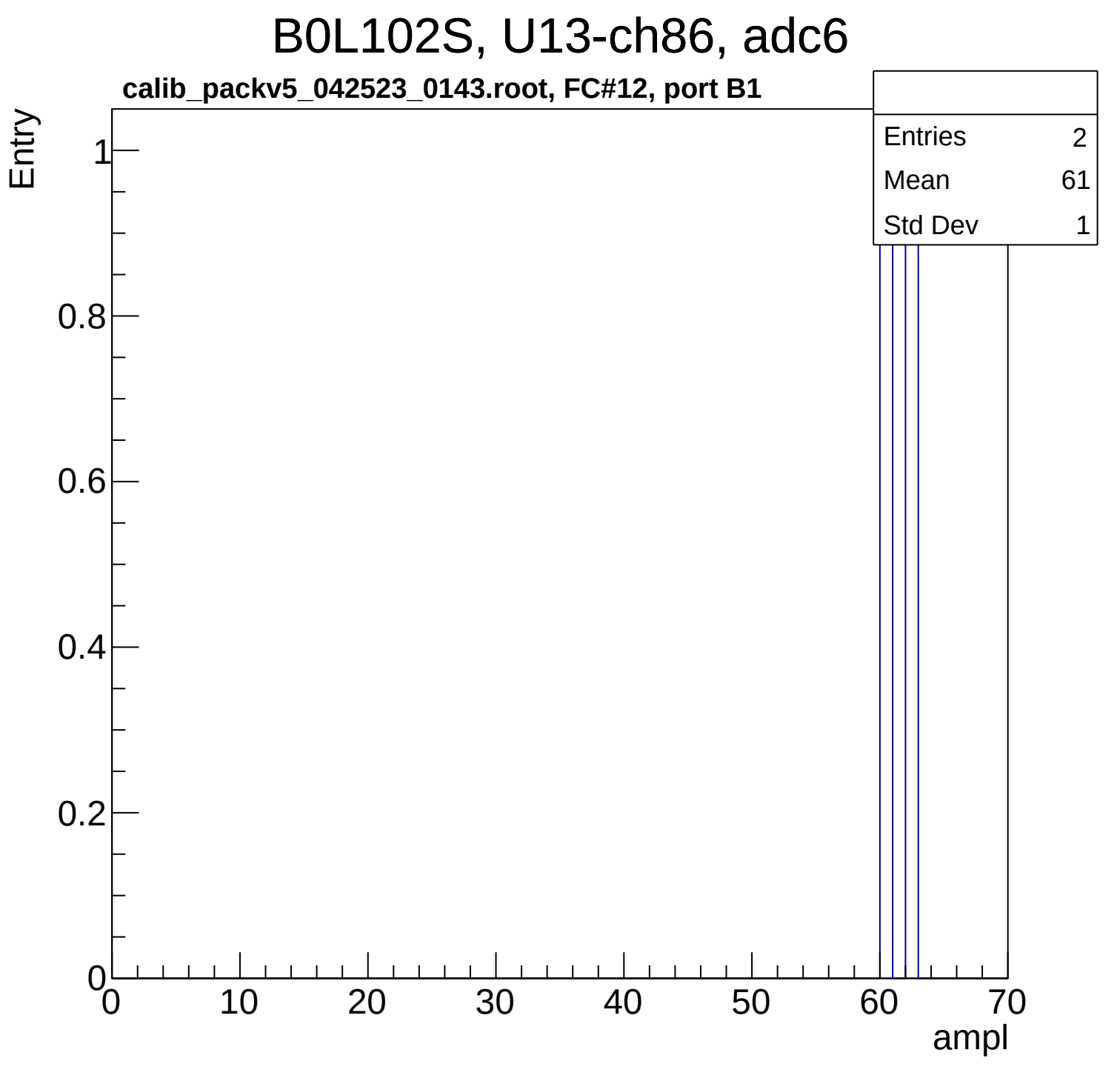
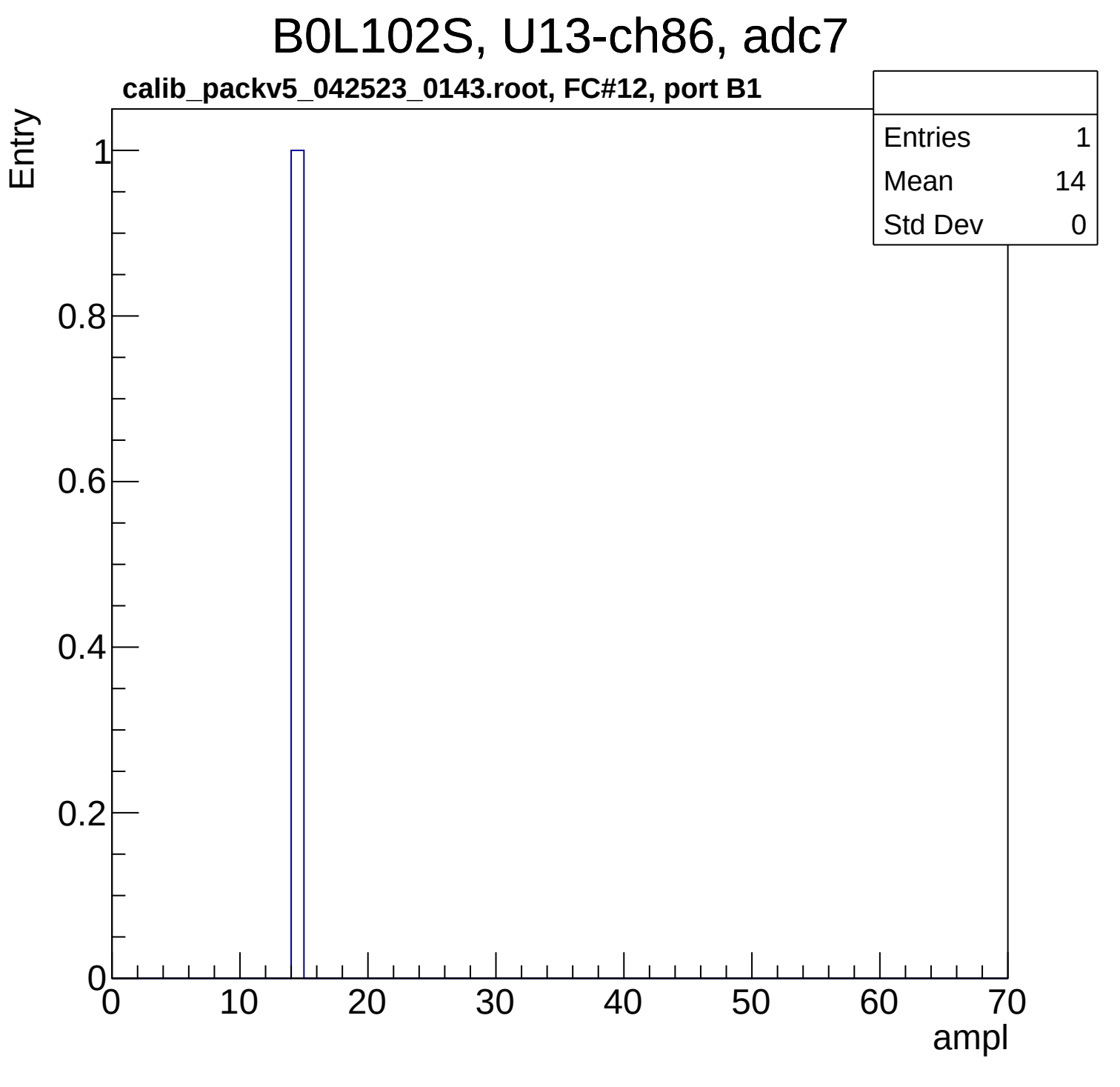
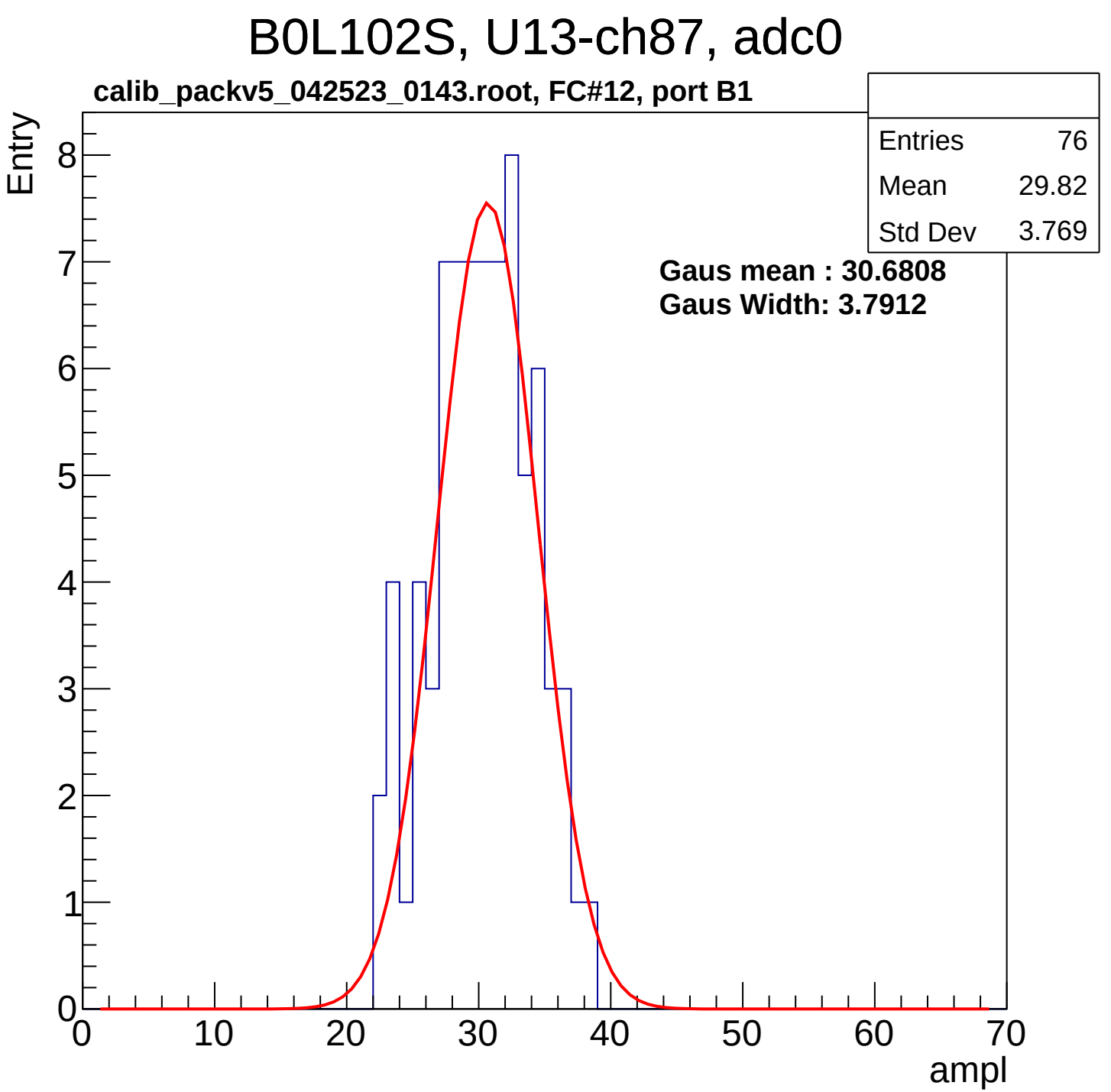

B0L102S, U13-ch86, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.09
Std Dev 3.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch86, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.15
Std Dev 8.763
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch86, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U13-ch86, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 14
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch87, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.82
Std Dev 3.769
Gaus mean : 30.6808
Gaus Width: 3.7912
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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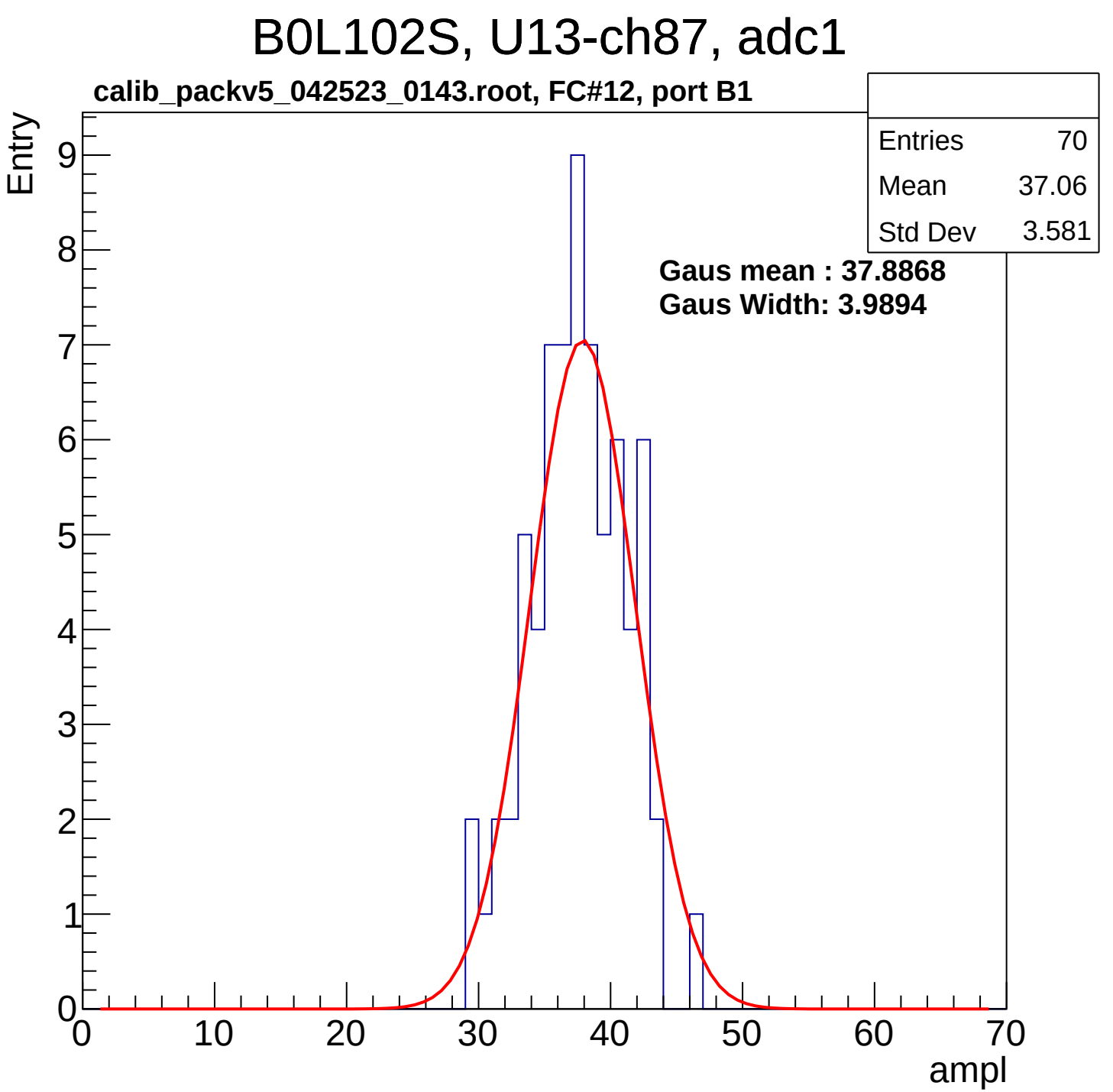
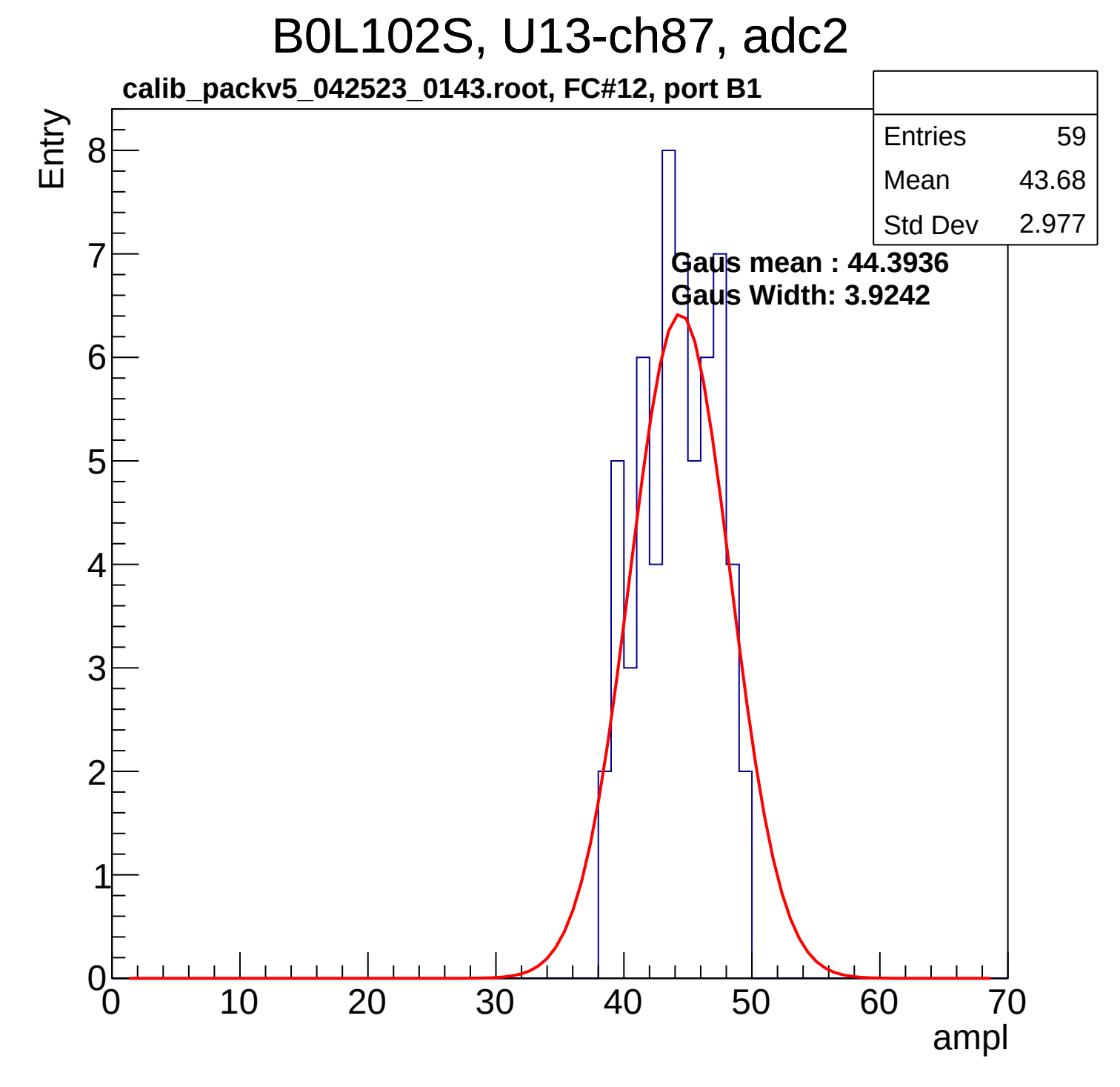
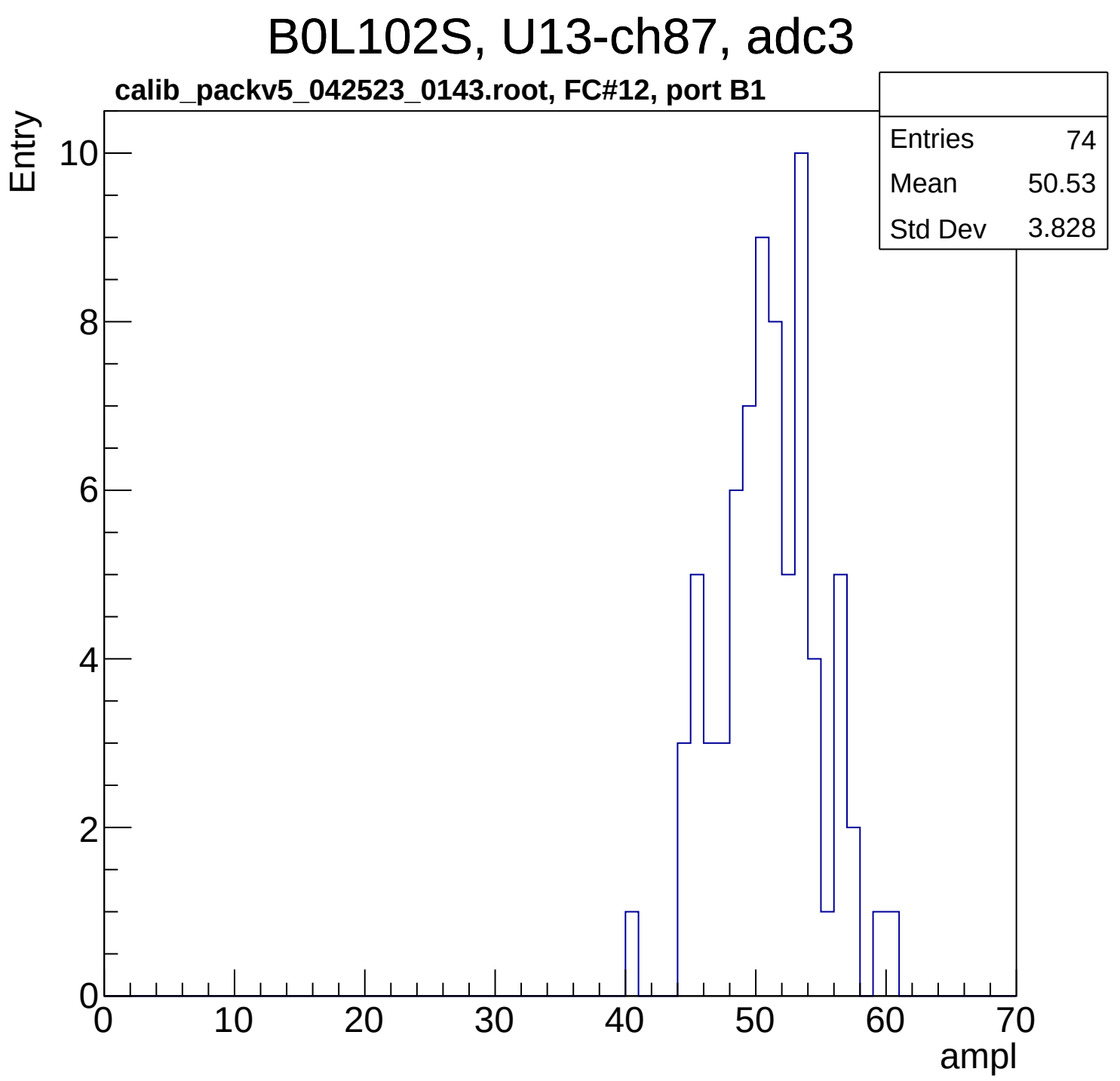
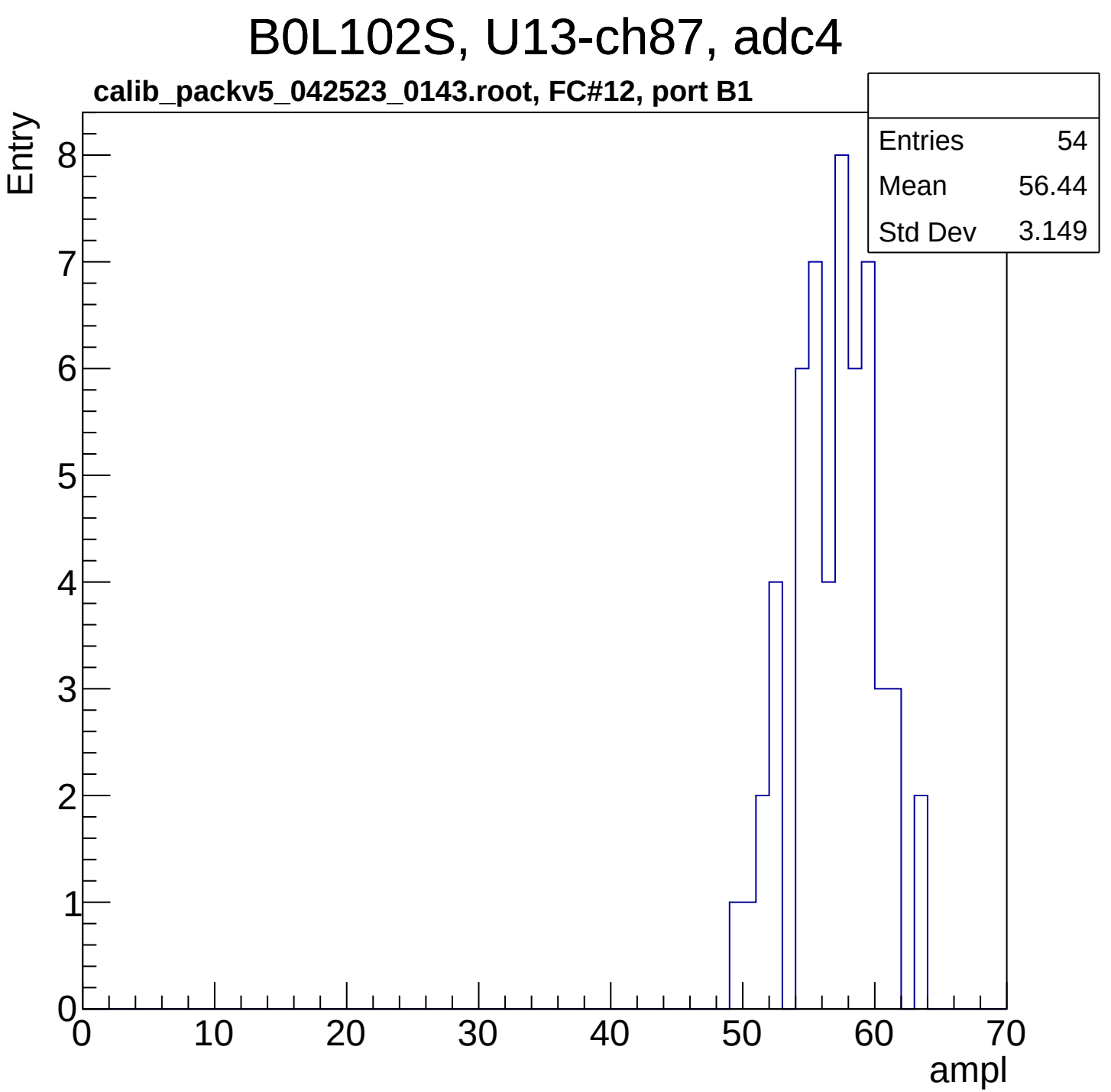

B0L102S, U13-ch87, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 37.06
Std Dev 3.581
Gaus mean : 37.8868
Gaus Width: 3.9894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch87, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.68
Std Dev 2.977
Gaus mean : 44.3936
Gaus Width: 3.9242
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch87, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 50.53
Std Dev 3.828
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch87, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.44
Std Dev 3.149
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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50
60
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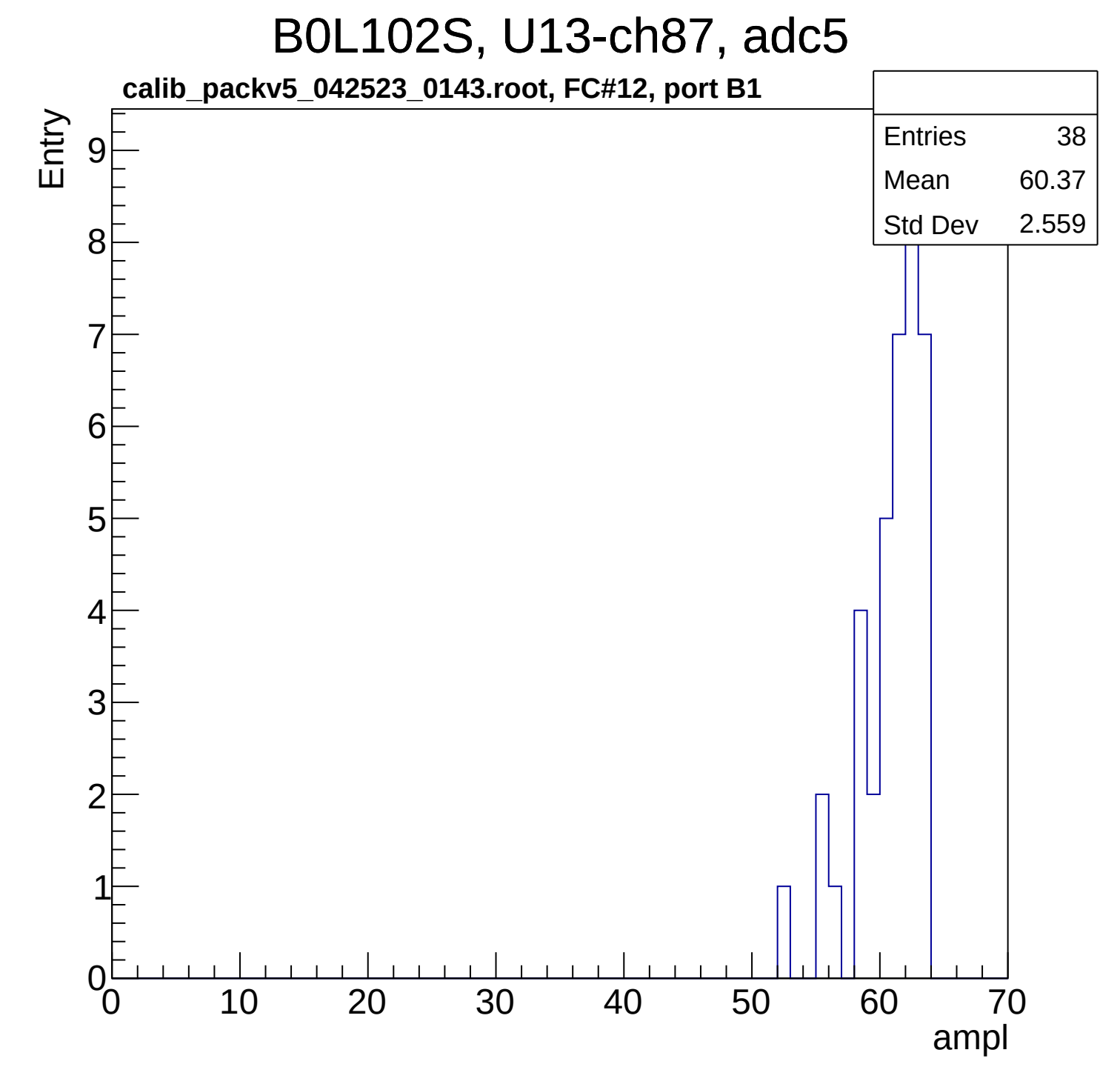
B0L102S, U13-ch87, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 60.37
Std Dev 2.559
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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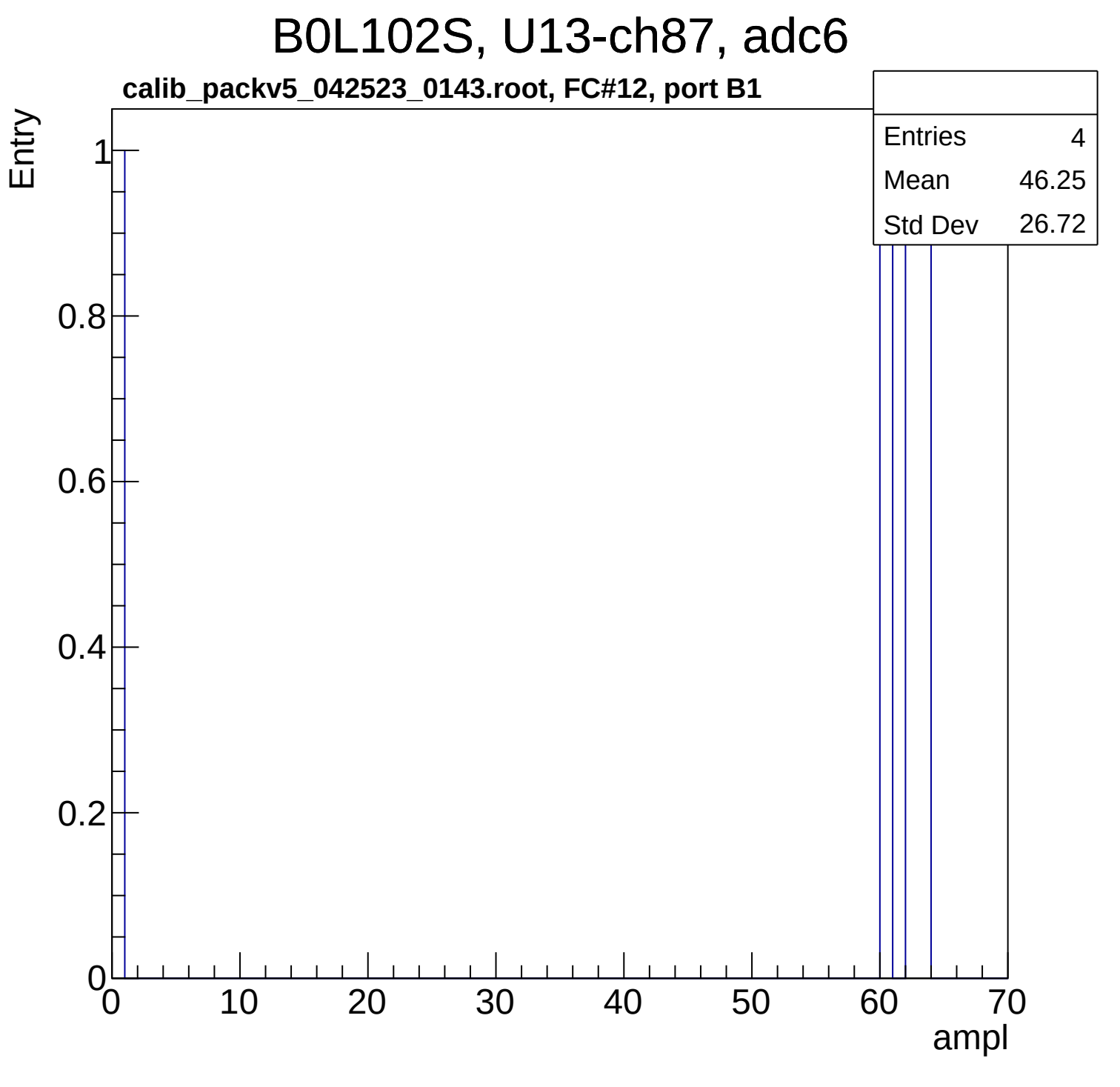
B0L102S, U13-ch87, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
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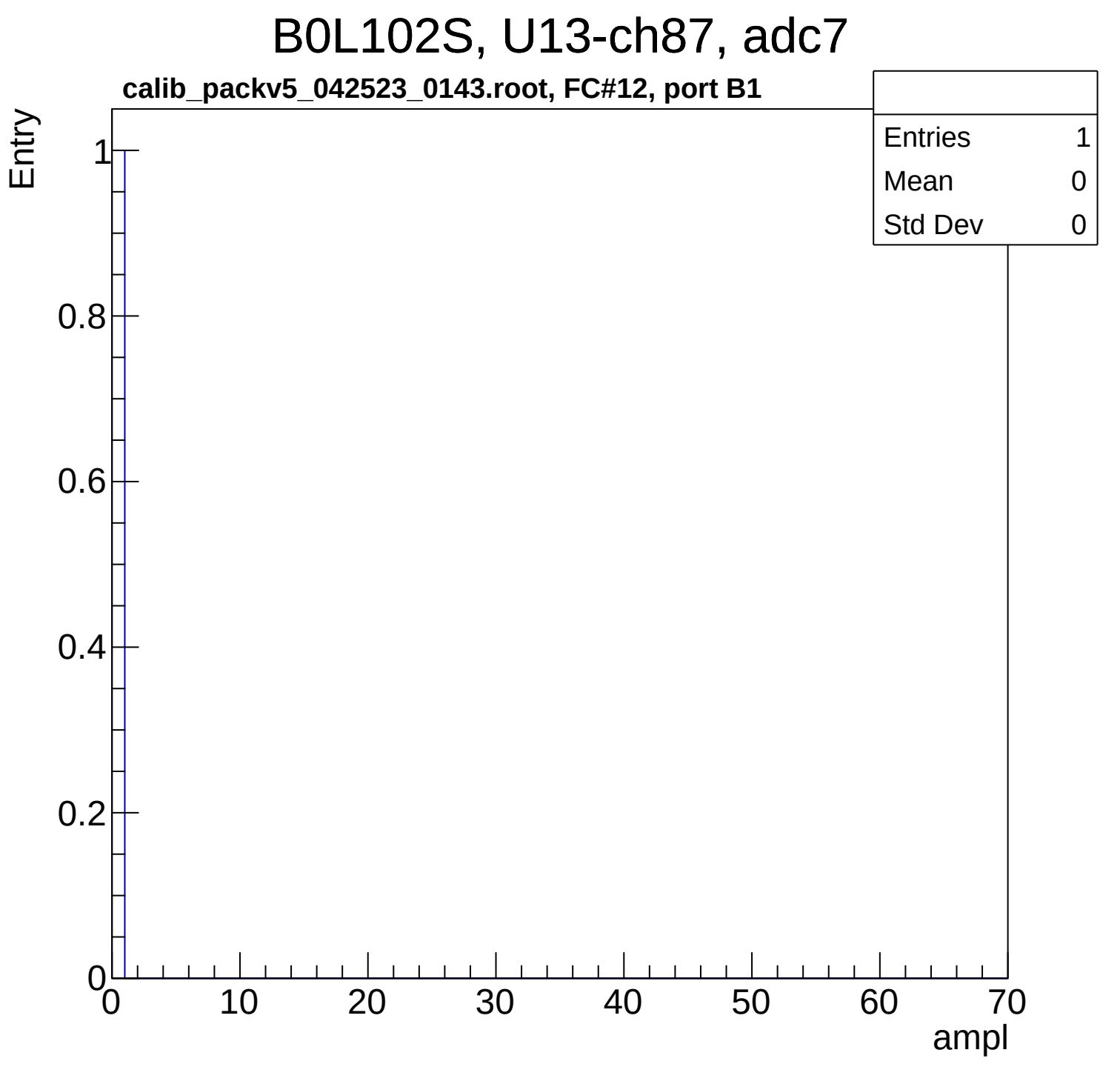
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch87, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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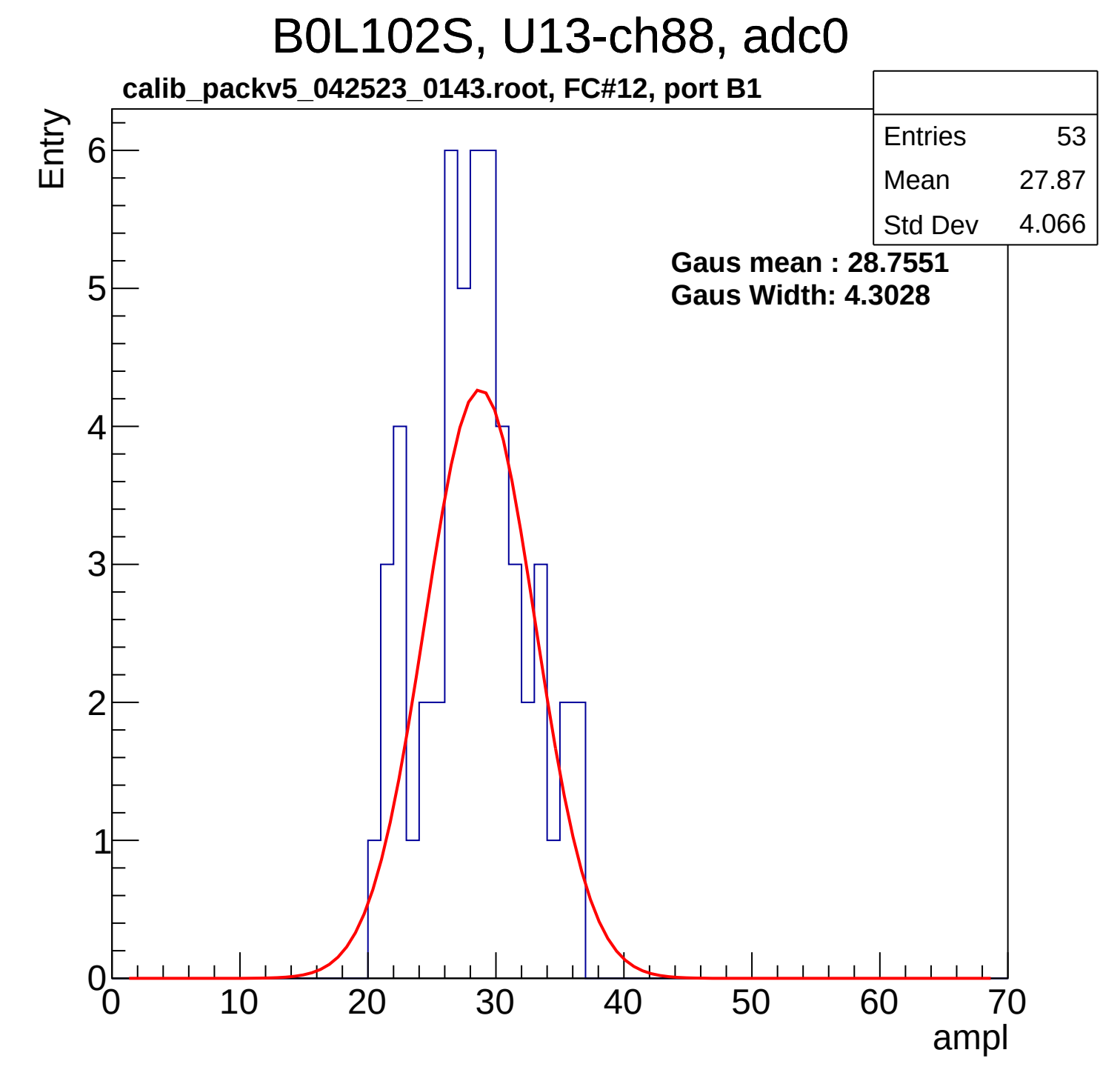
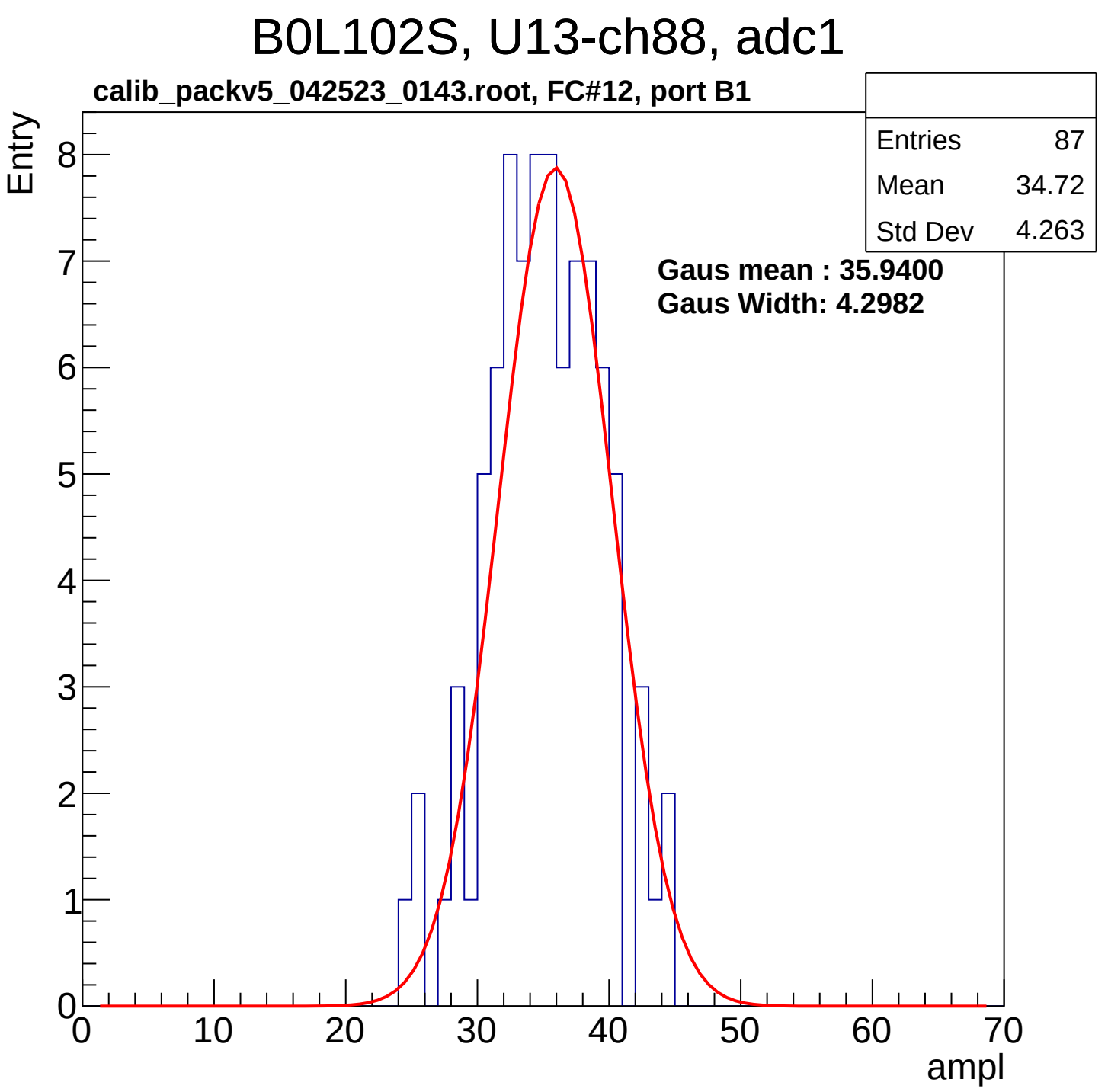
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch88, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 27.87
Std Dev 4.066
Gaus mean : 28.7551
Gaus Width: 4.3028
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch88, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 34.72
Std Dev 4.263
Gaus mean : 35.9400
Gaus Width: 4.2982
Entry
ampl
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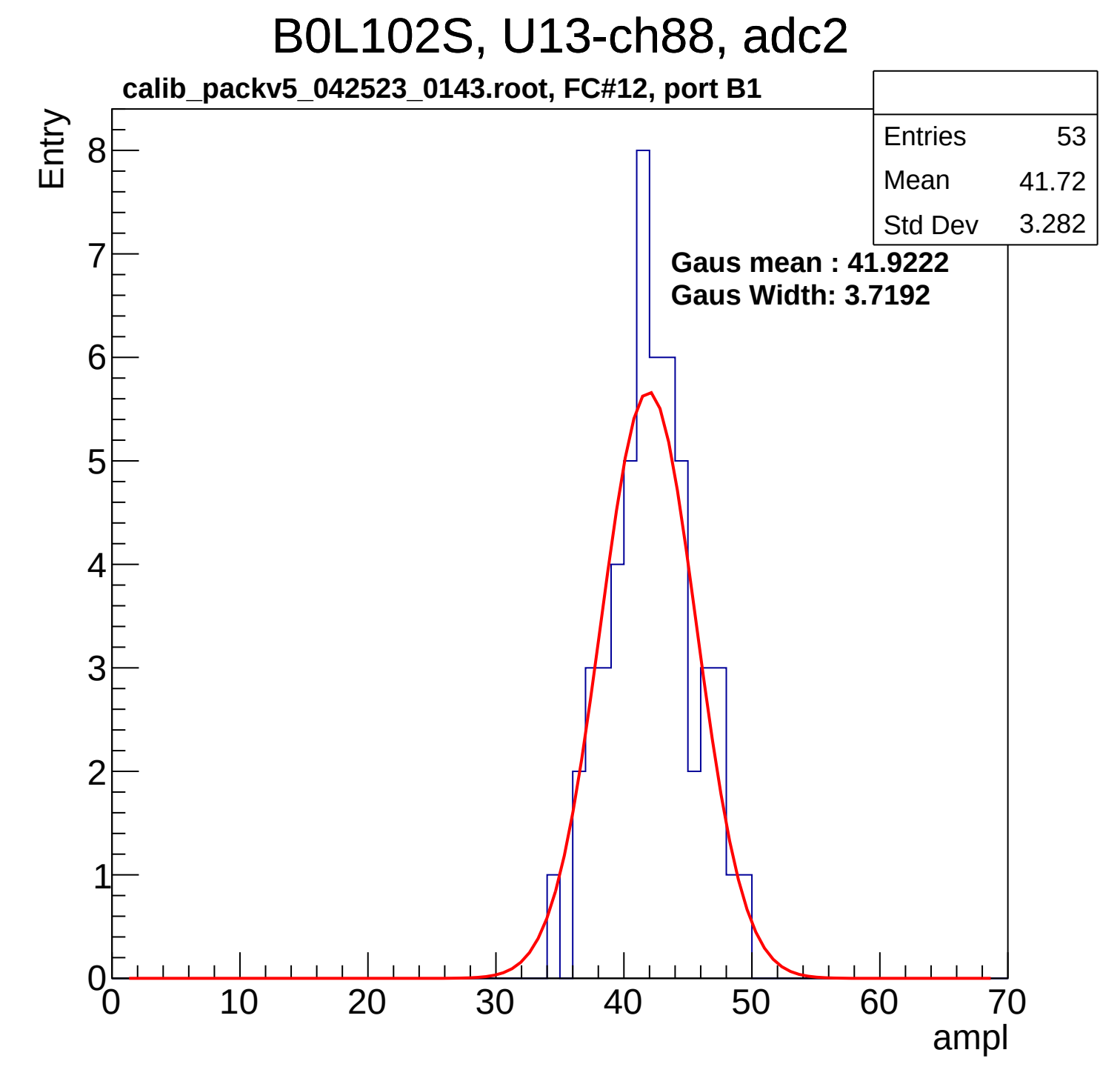
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch88, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 41.72
Std Dev 3.282
Gaus mean : 41.9222
Gaus Width: 3.7192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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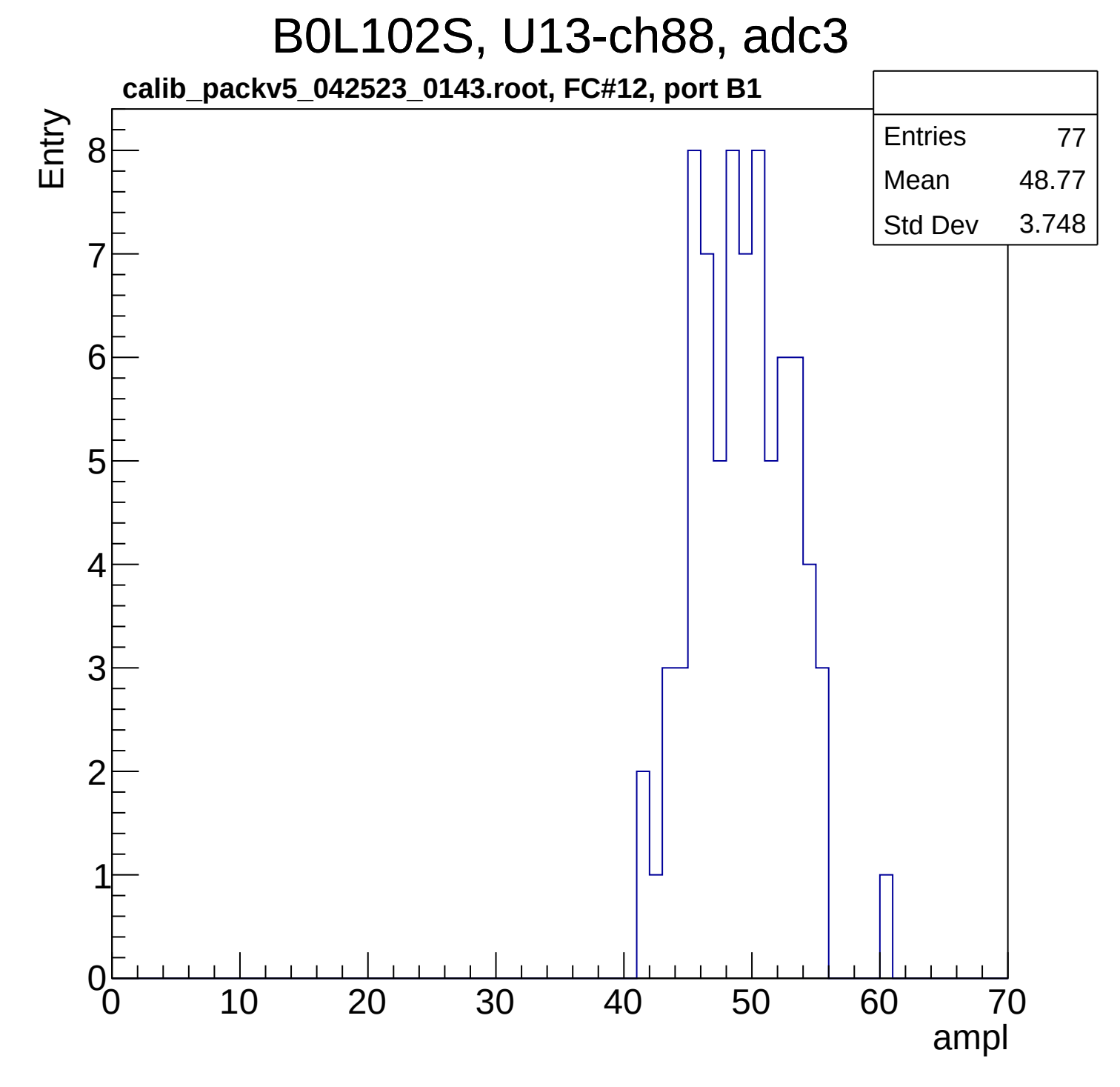
B0L102S, U13-ch88, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 48.77
Std Dev 3.748
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
50
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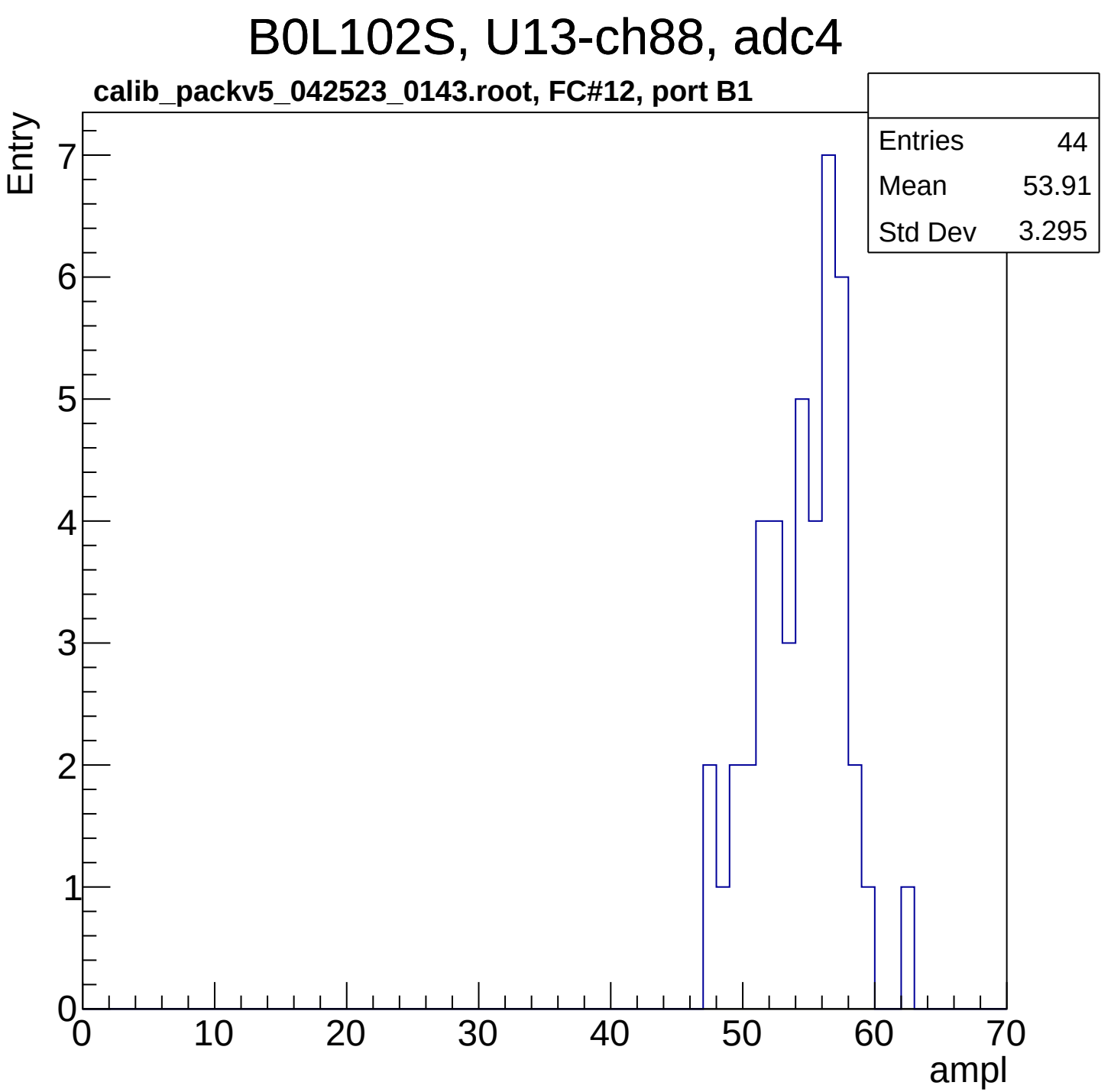
B0L102S, U13-ch88, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 53.91
Std Dev 3.295
Entry
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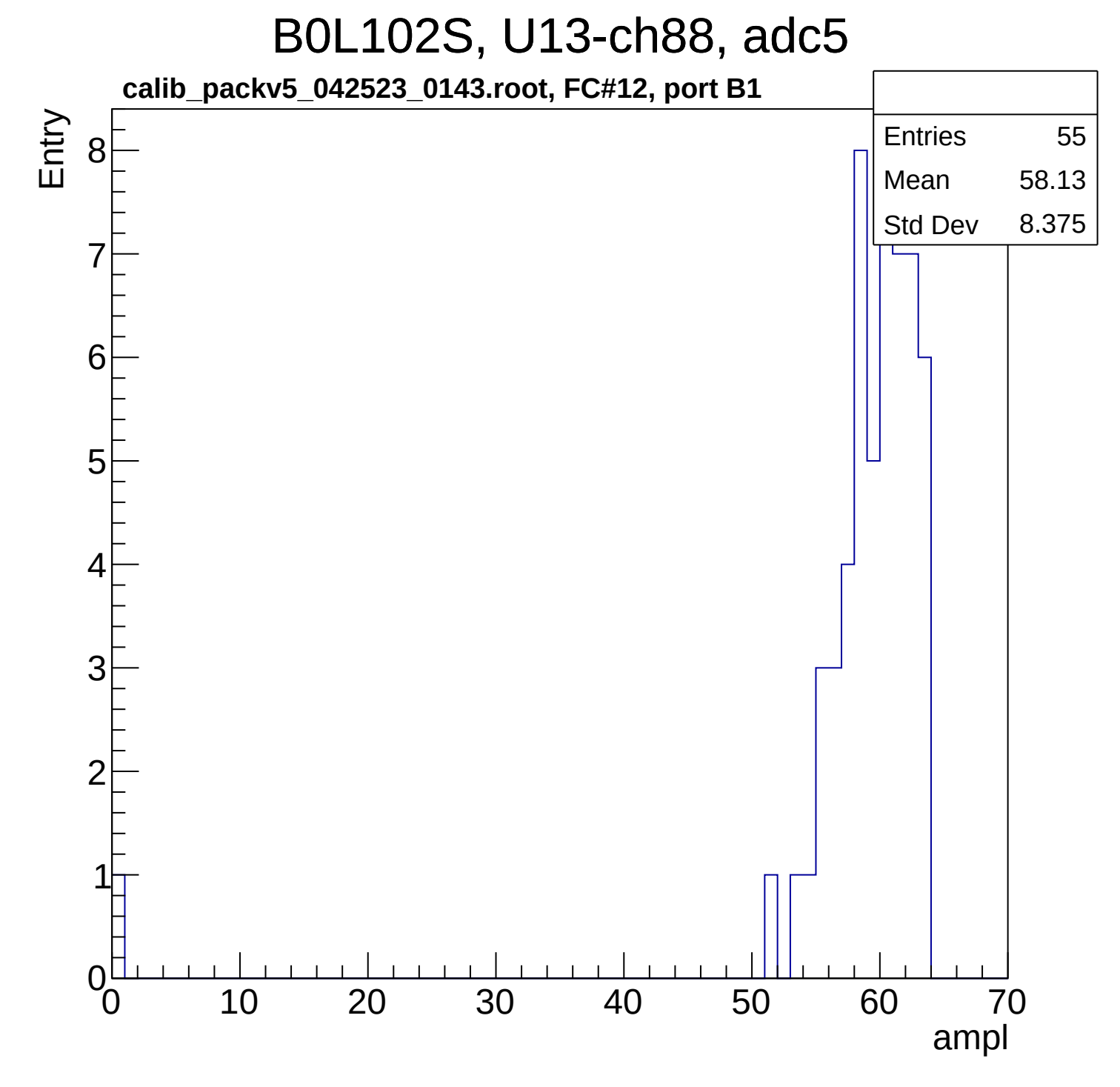
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch88, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.13
Std Dev 8.375
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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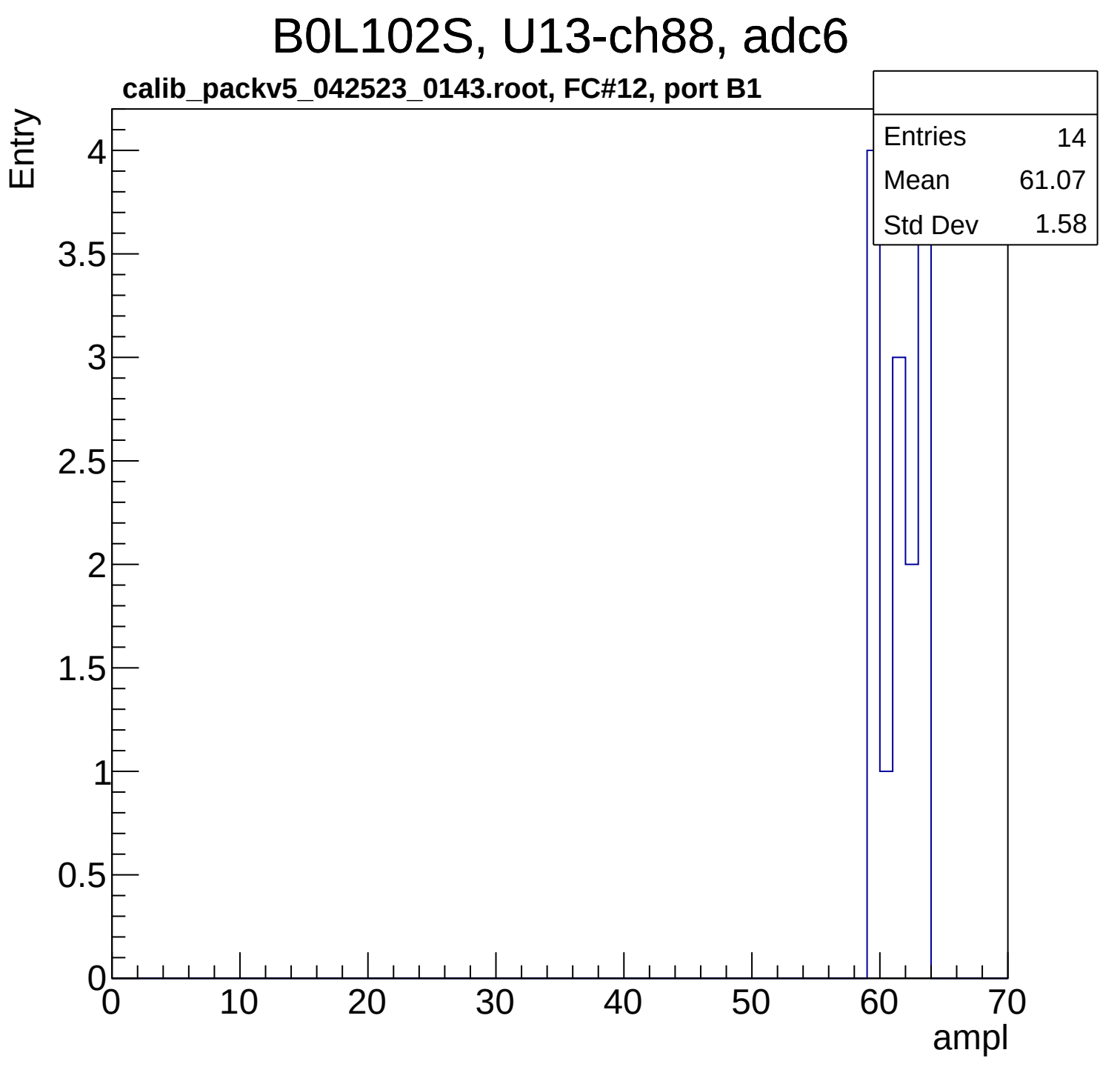
B0L102S, U13-ch88, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.07
Std Dev 1.58
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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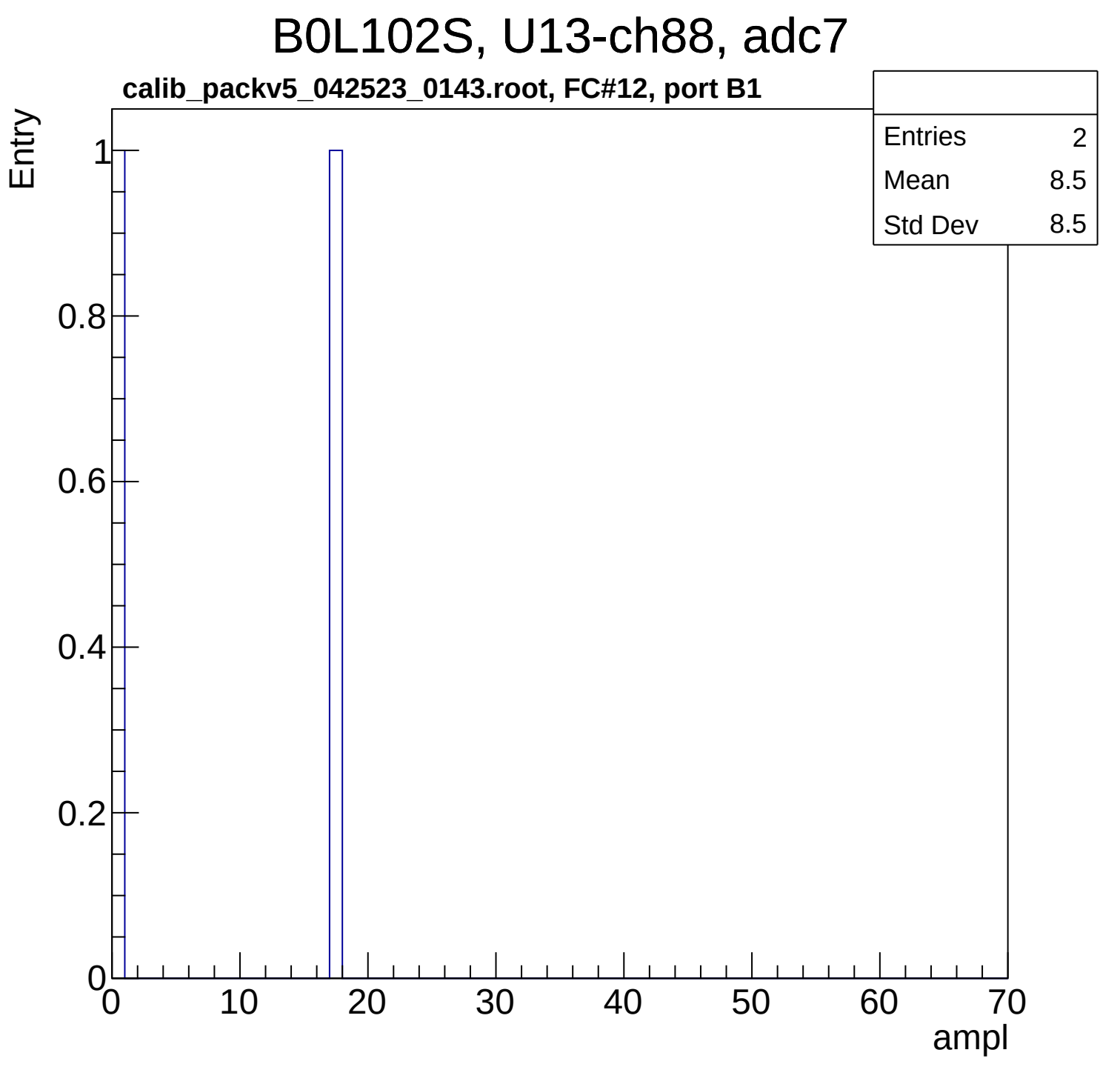
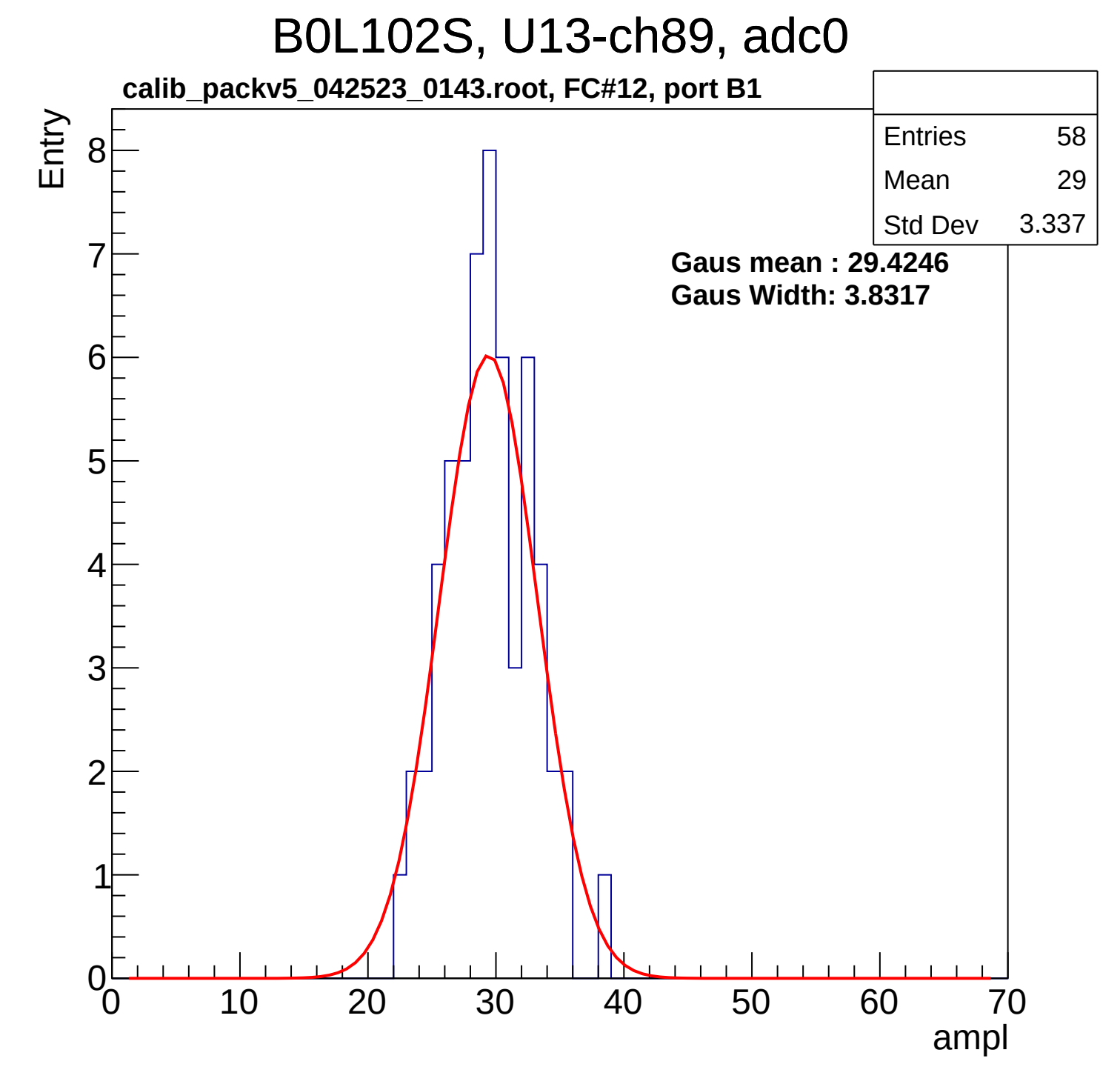

B0L102S, U13-ch88, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 8.5
Std Dev 8.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch89, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 29
Std Dev 3.337
Gaus mean : 29.4246
Gaus Width: 3.8317
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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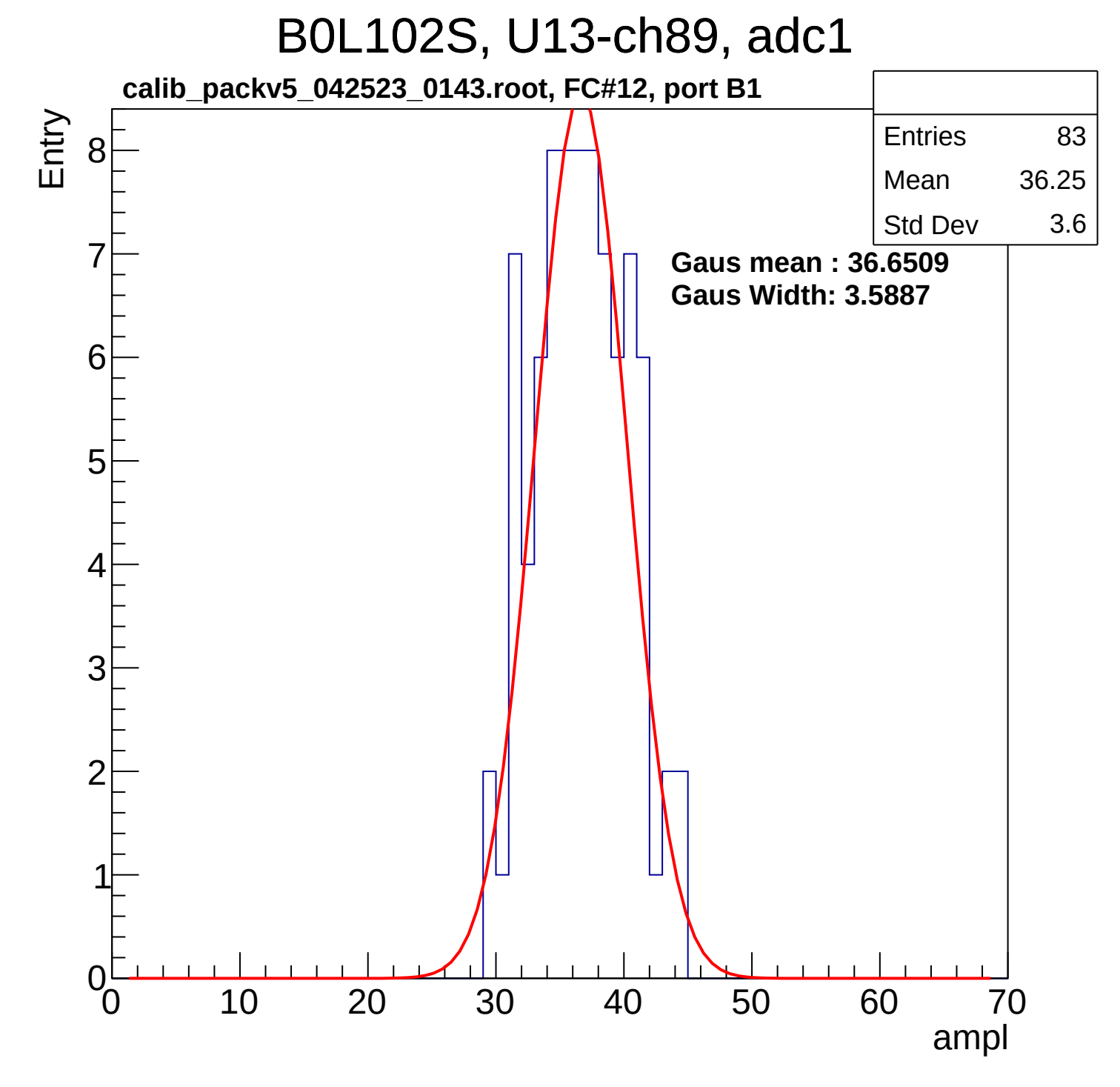
B0L102S, U13-ch89, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 36.25
Std Dev 3.6
Gaus mean : 36.6509
Gaus Width: 3.5887
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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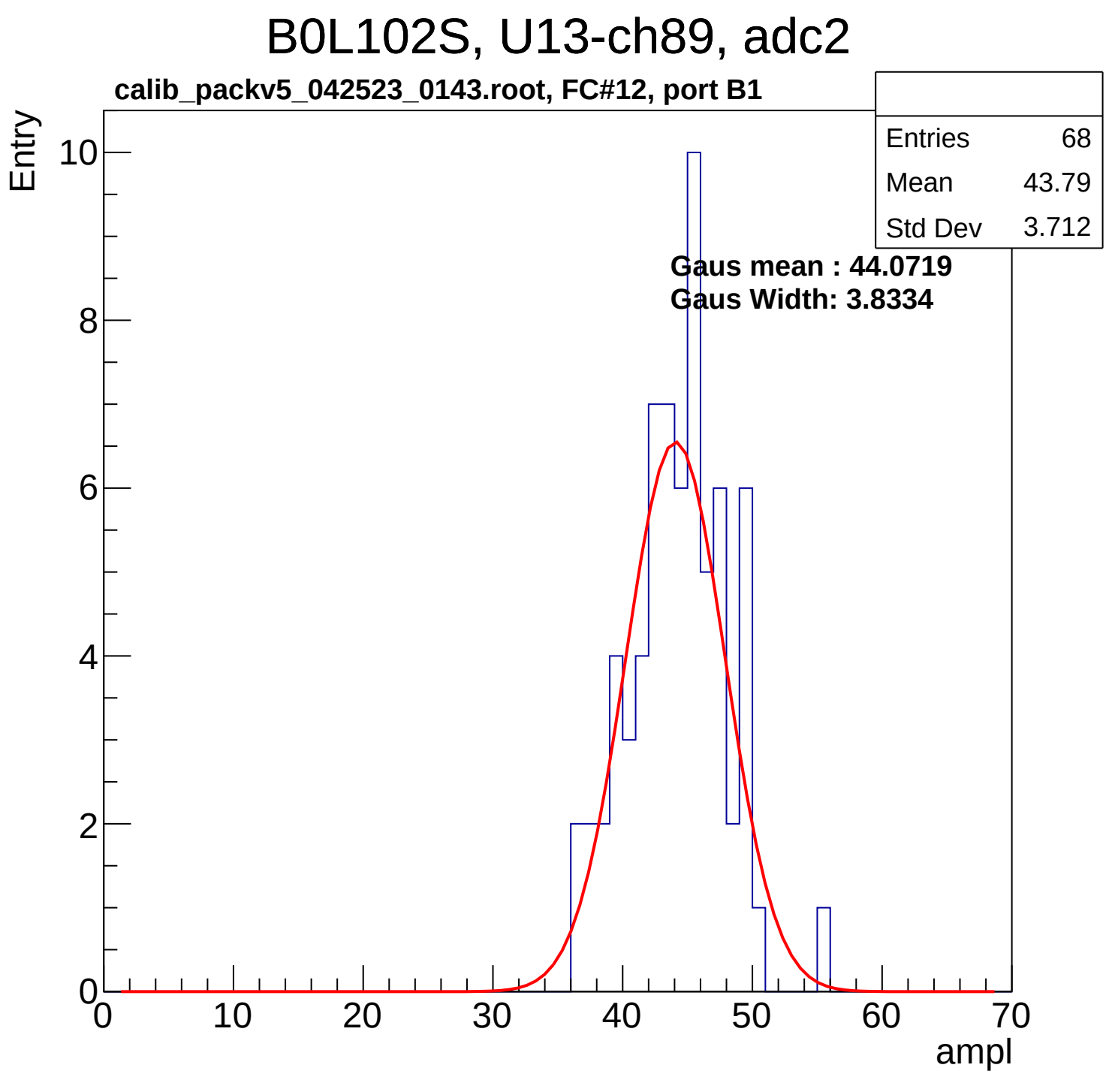
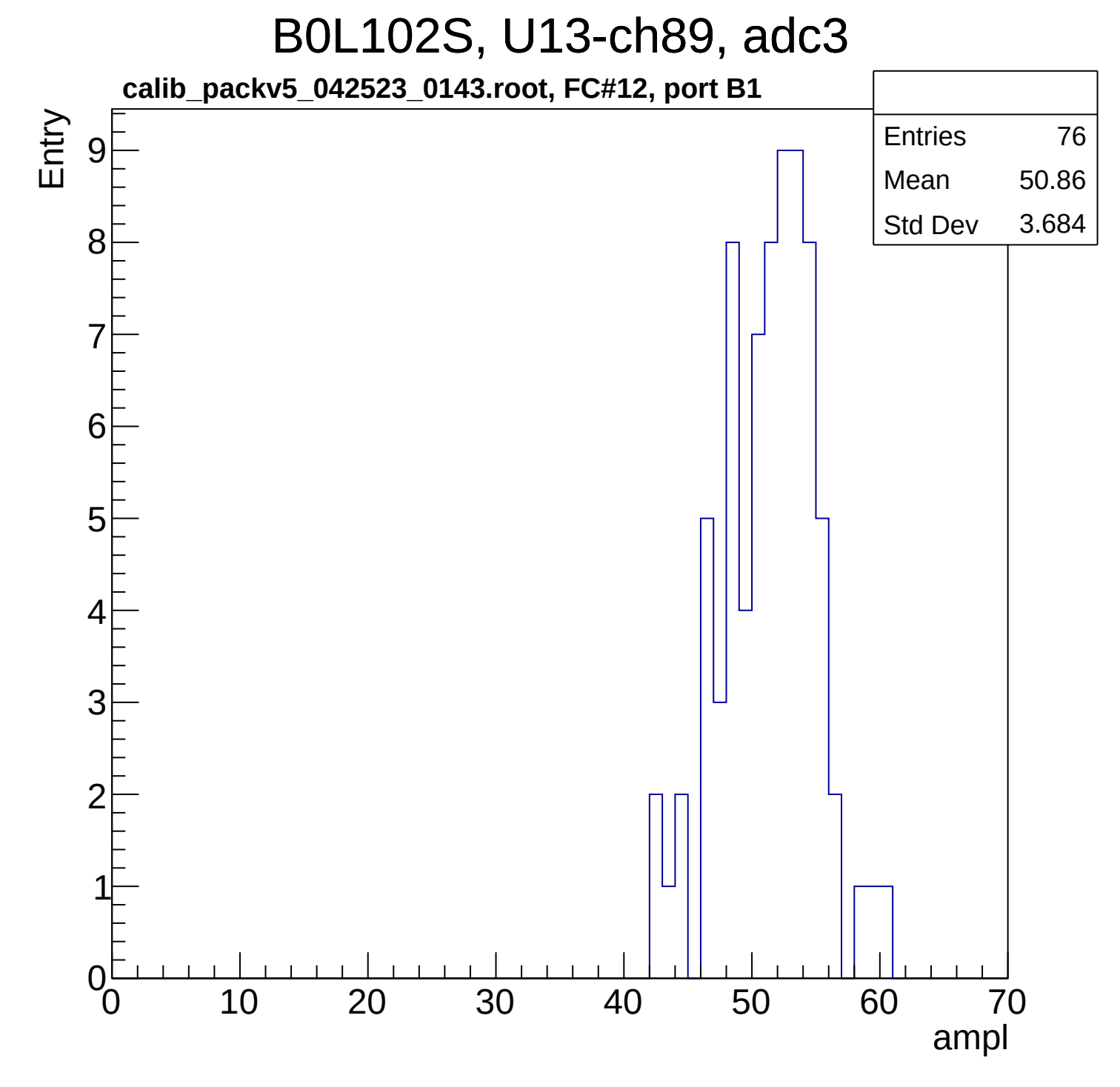

B0L102S, U13-ch89, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.79
Std Dev 3.712
Gaus mean : 44.0719
Gaus Width: 3.8334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch89, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 50.86
Std Dev 3.684
Entry
ampl
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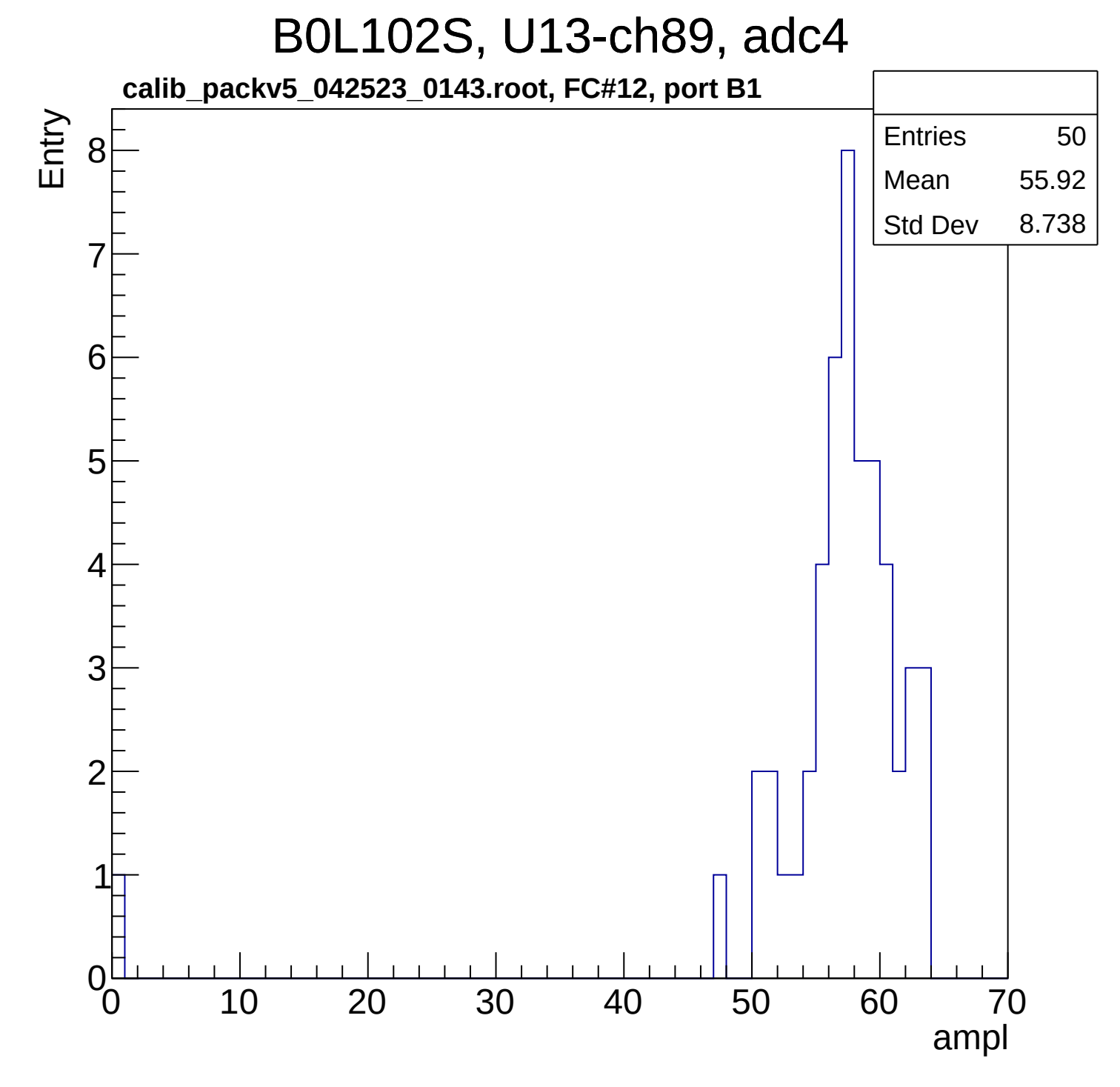
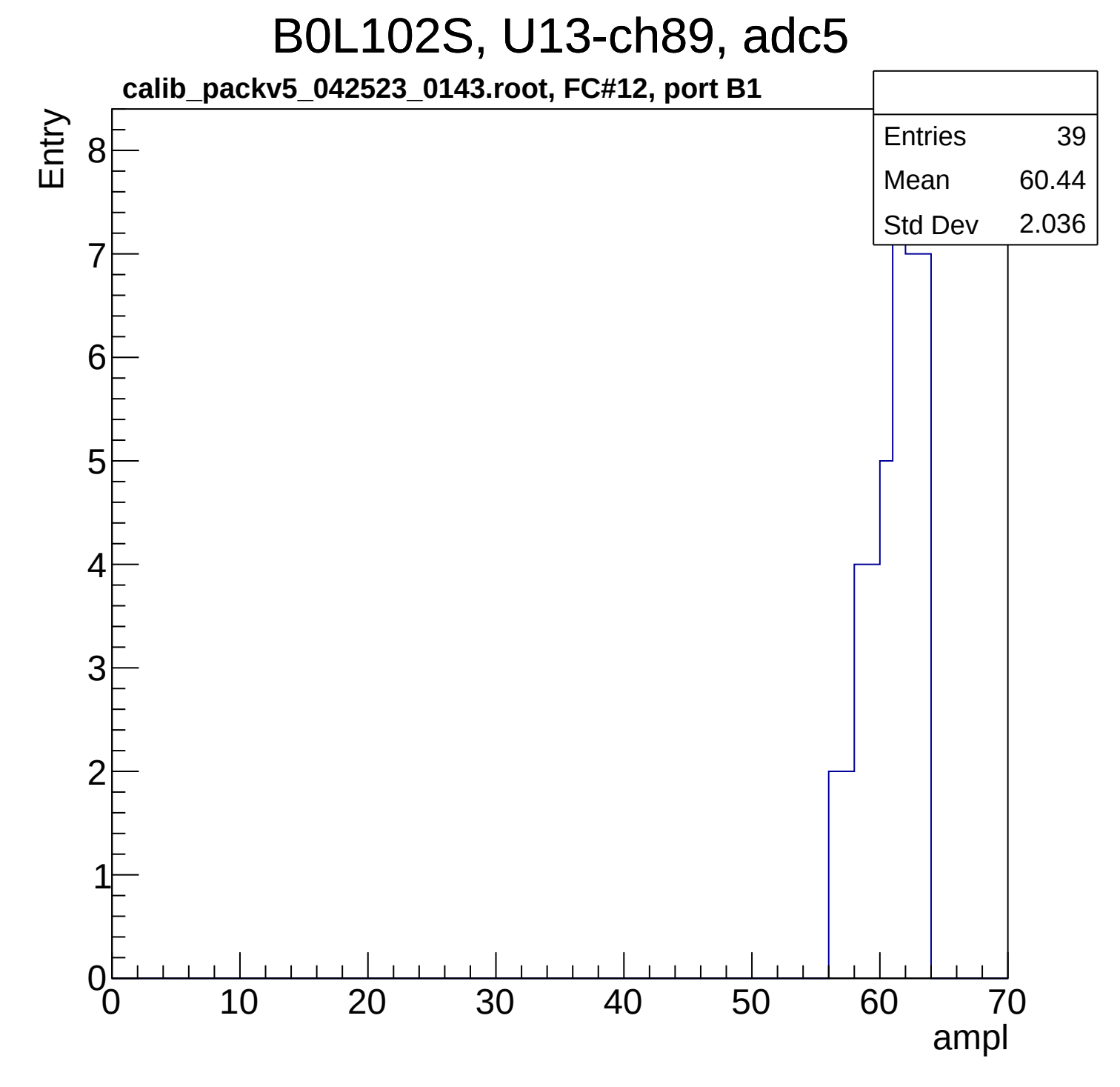
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch89, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 55.92
Std Dev 8.738
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch89, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 60.44
Std Dev 2.036
Entry
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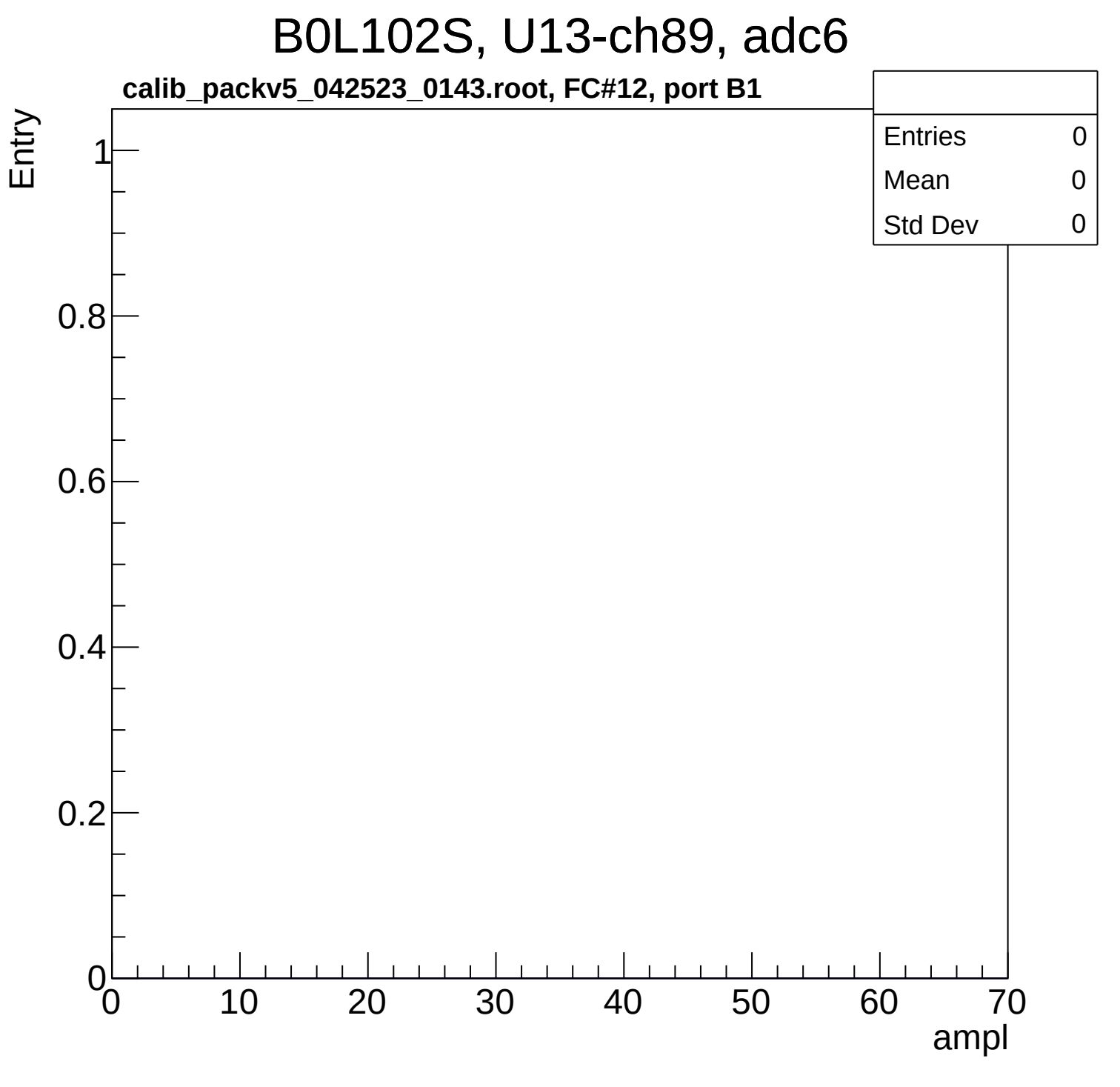
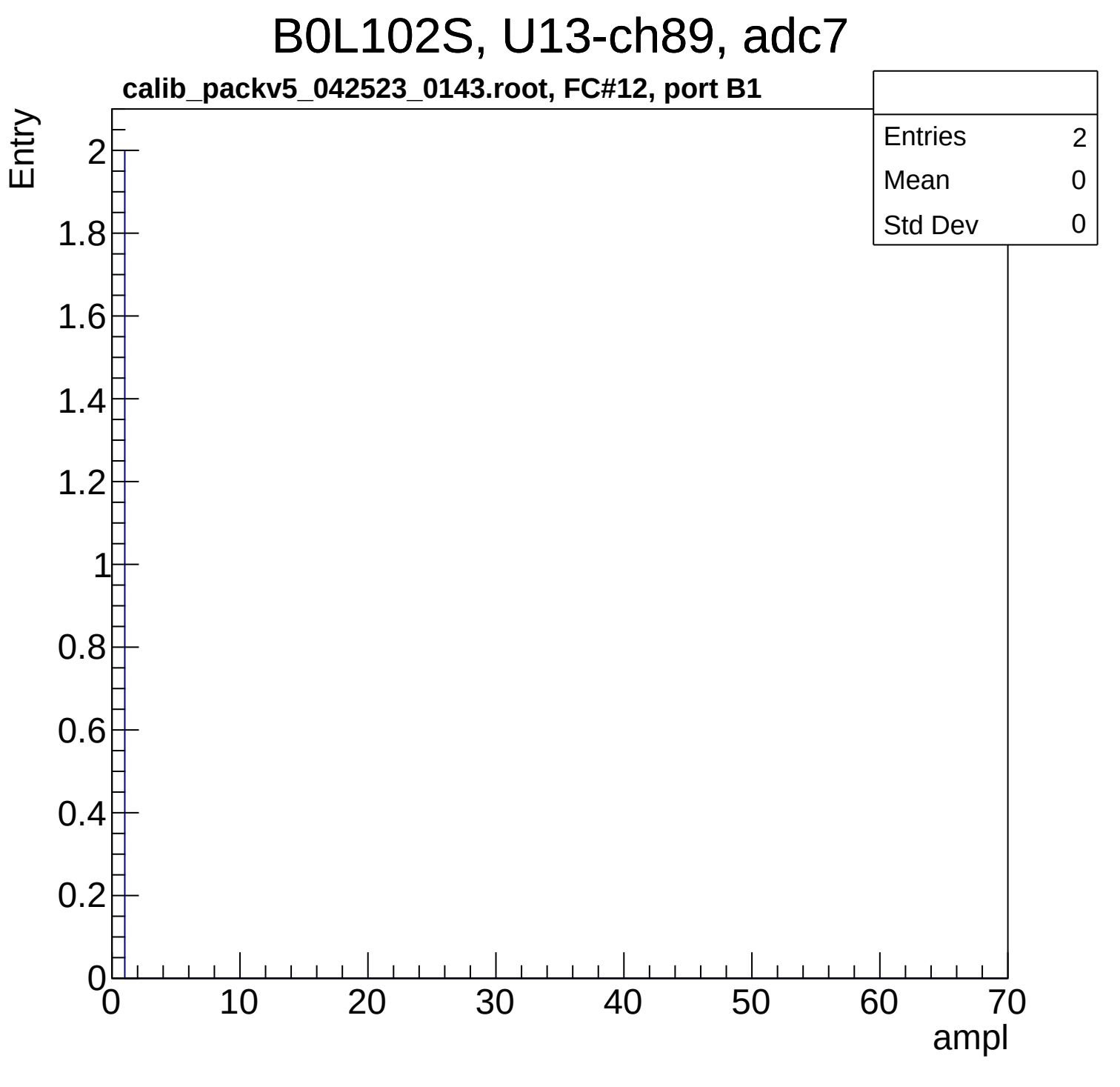
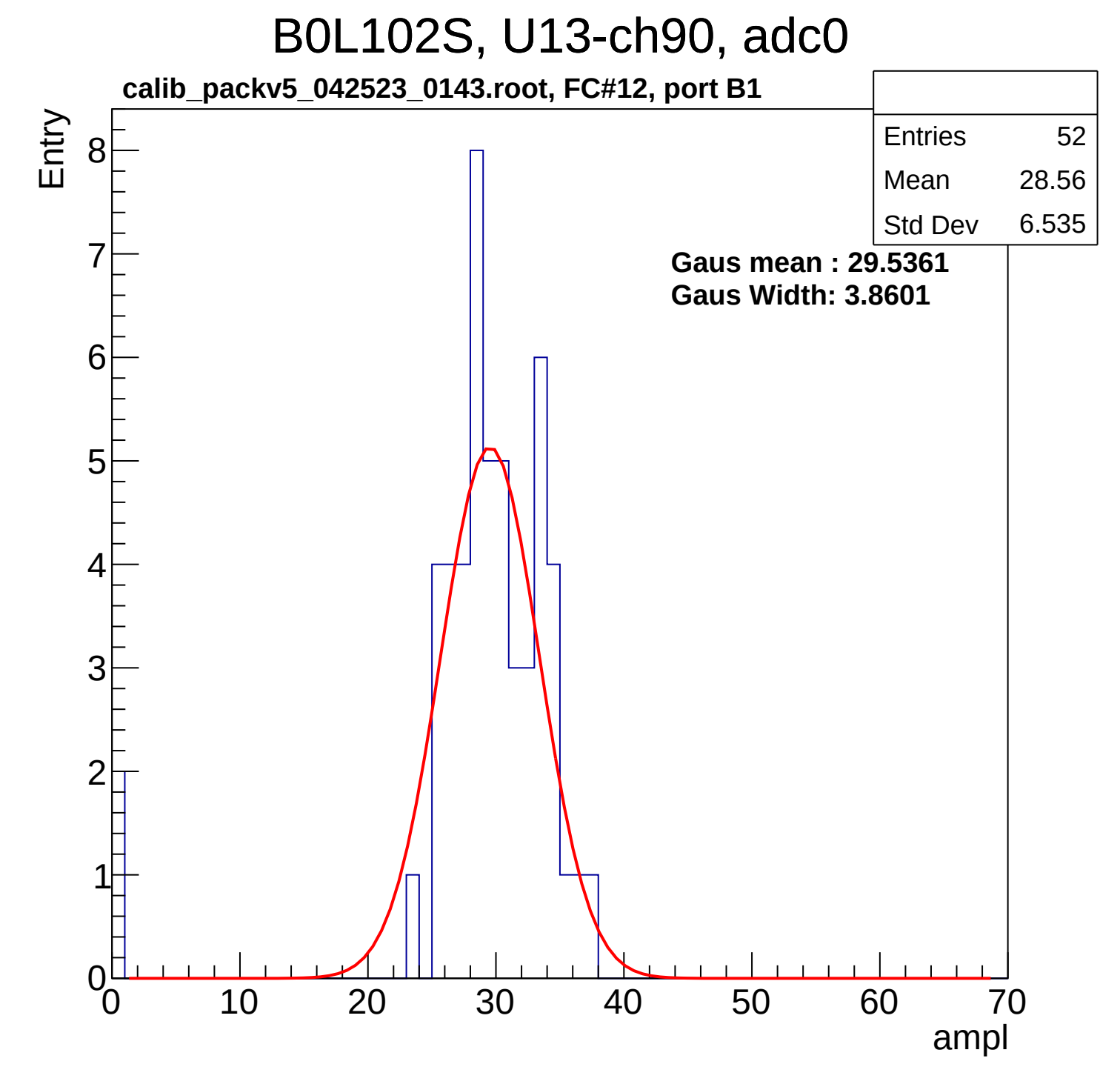
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch89, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch89, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch90, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 28.56
Std Dev 6.535
Gaus mean : 29.5361
Gaus Width: 3.8601
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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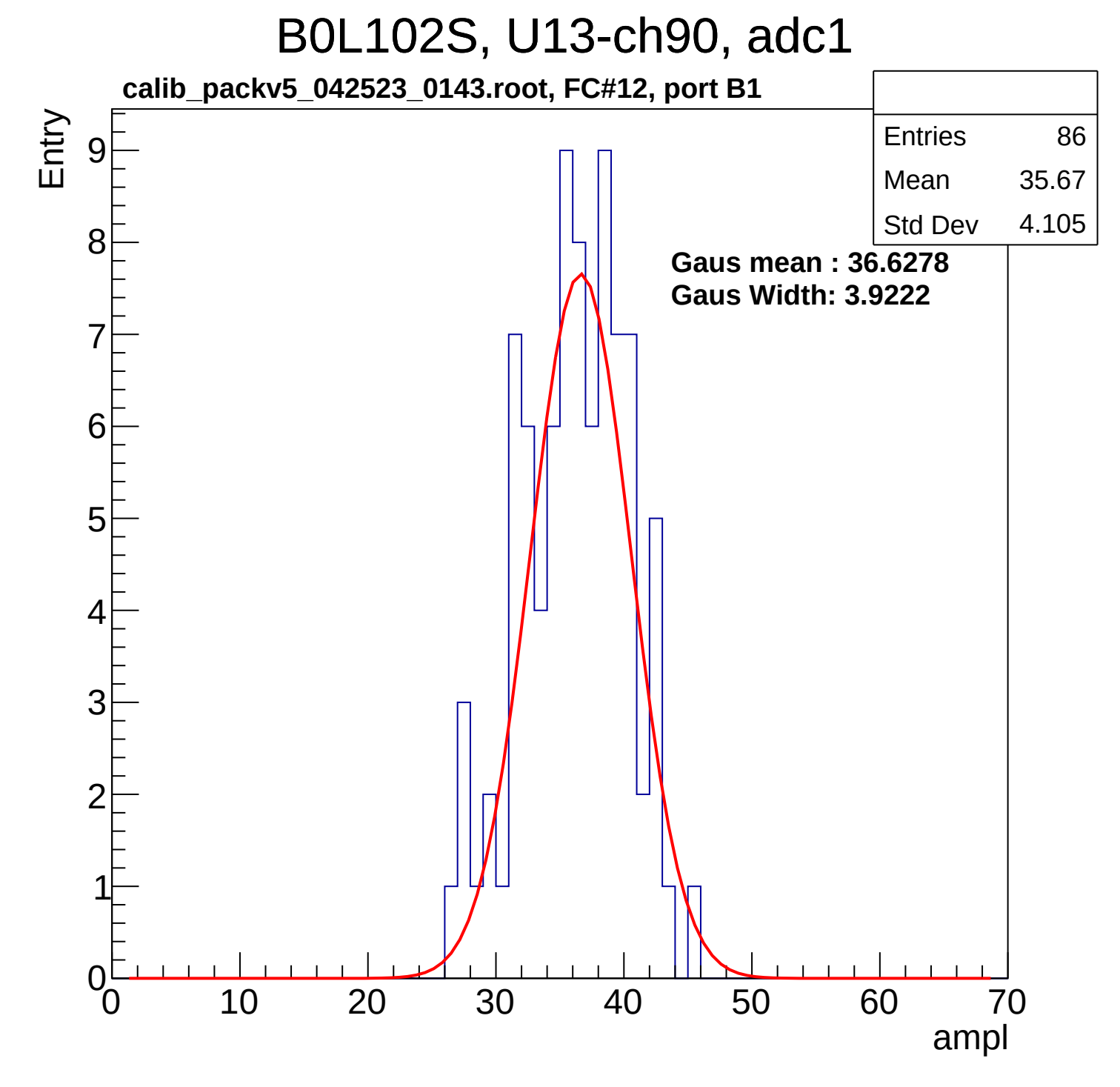
B0L102S, U13-ch90, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 35.67
Std Dev 4.105
Gaus mean : 36.6278
Gaus Width: 3.9222
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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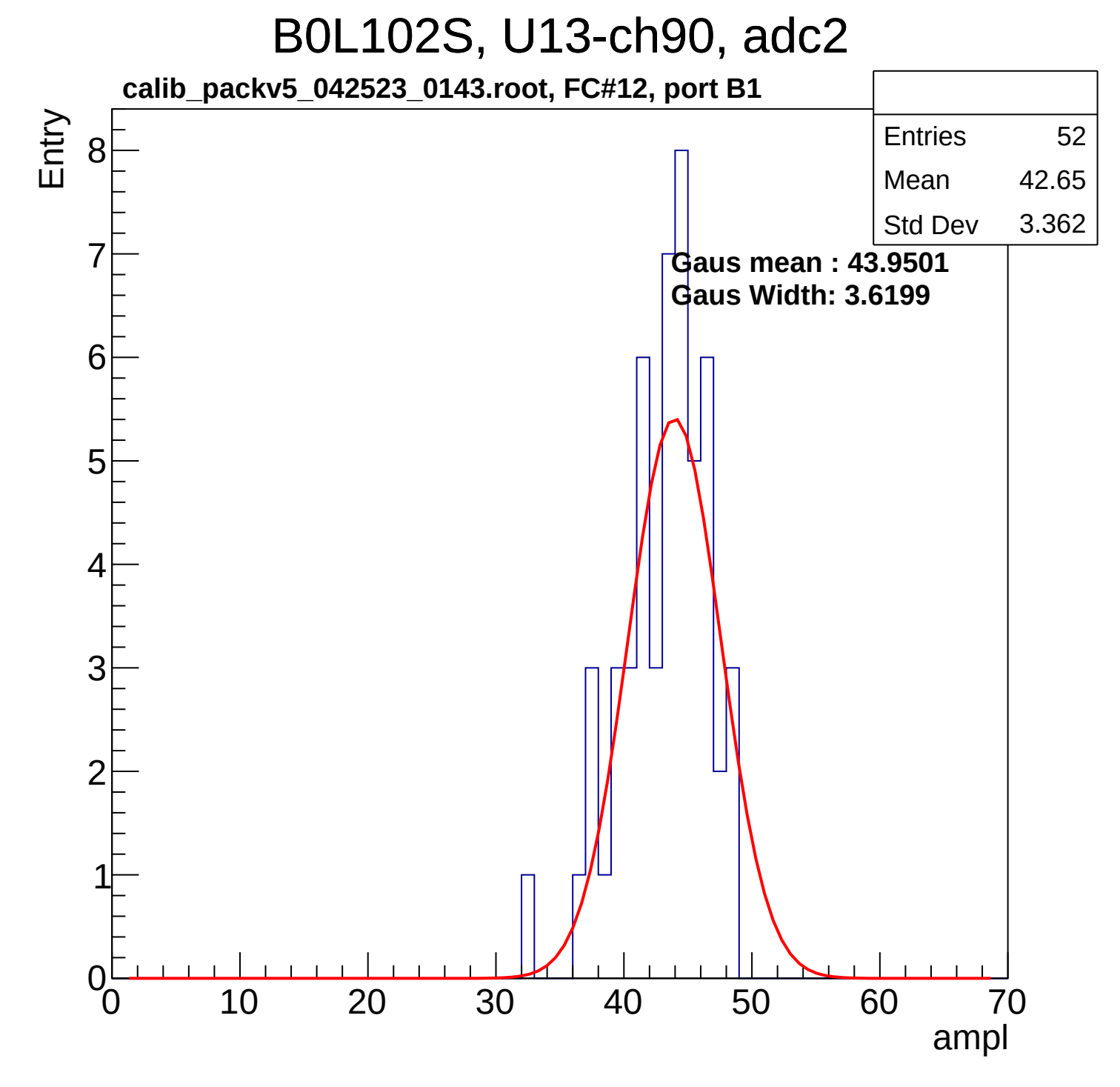
B0L102S, U13-ch90, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 42.65
Std Dev 3.362
Gaus mean : 43.9501
Gaus Width: 3.6199
Entry
ampl
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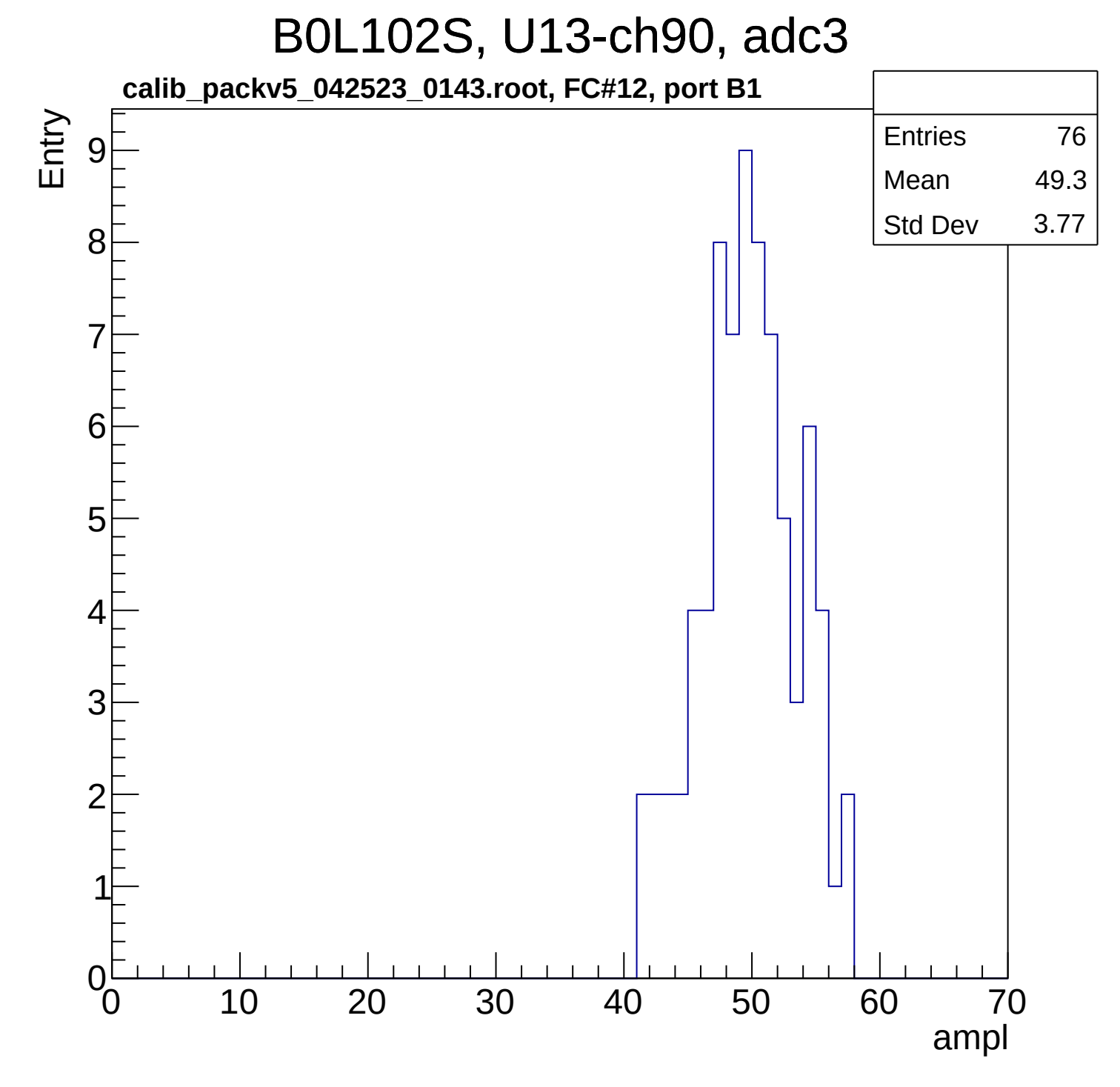
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch90, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.3
Std Dev 3.77
Entry
ampl
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2
3
4
5
6
7
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9
0
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20
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40
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60
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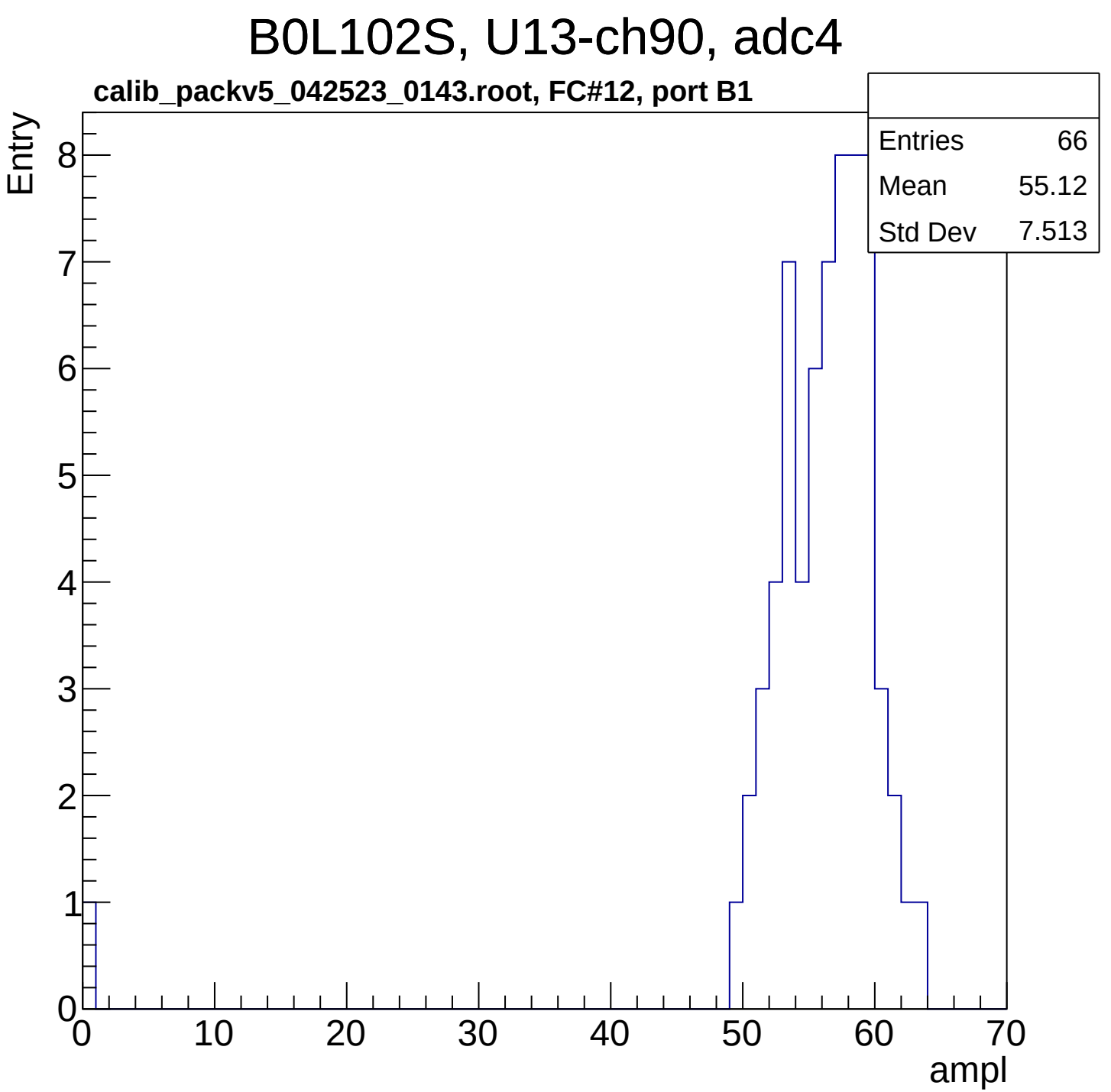
B0L102S, U13-ch90, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.12
Std Dev 7.513
Entry
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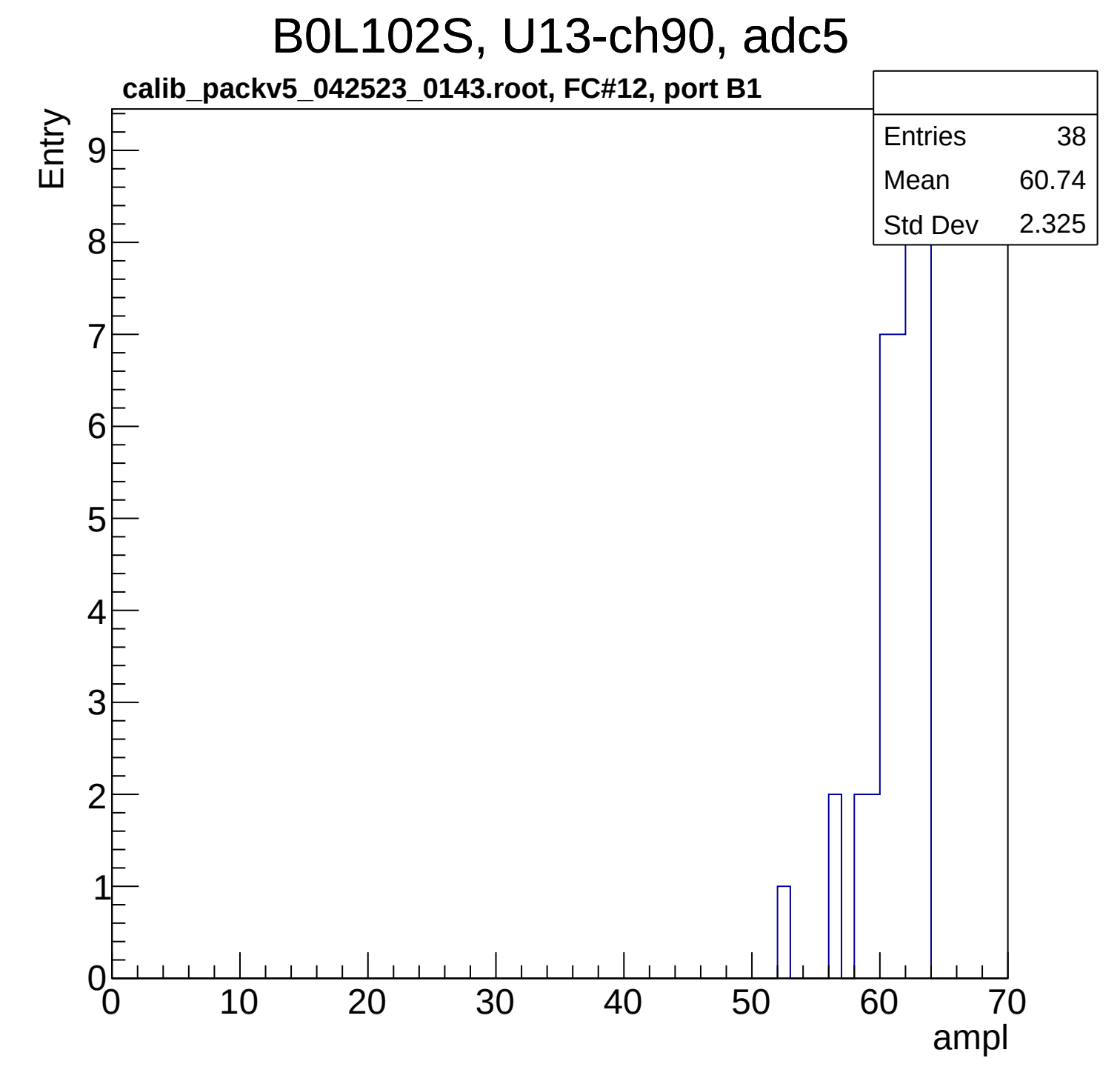
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch90, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 60.74
Std Dev 2.325
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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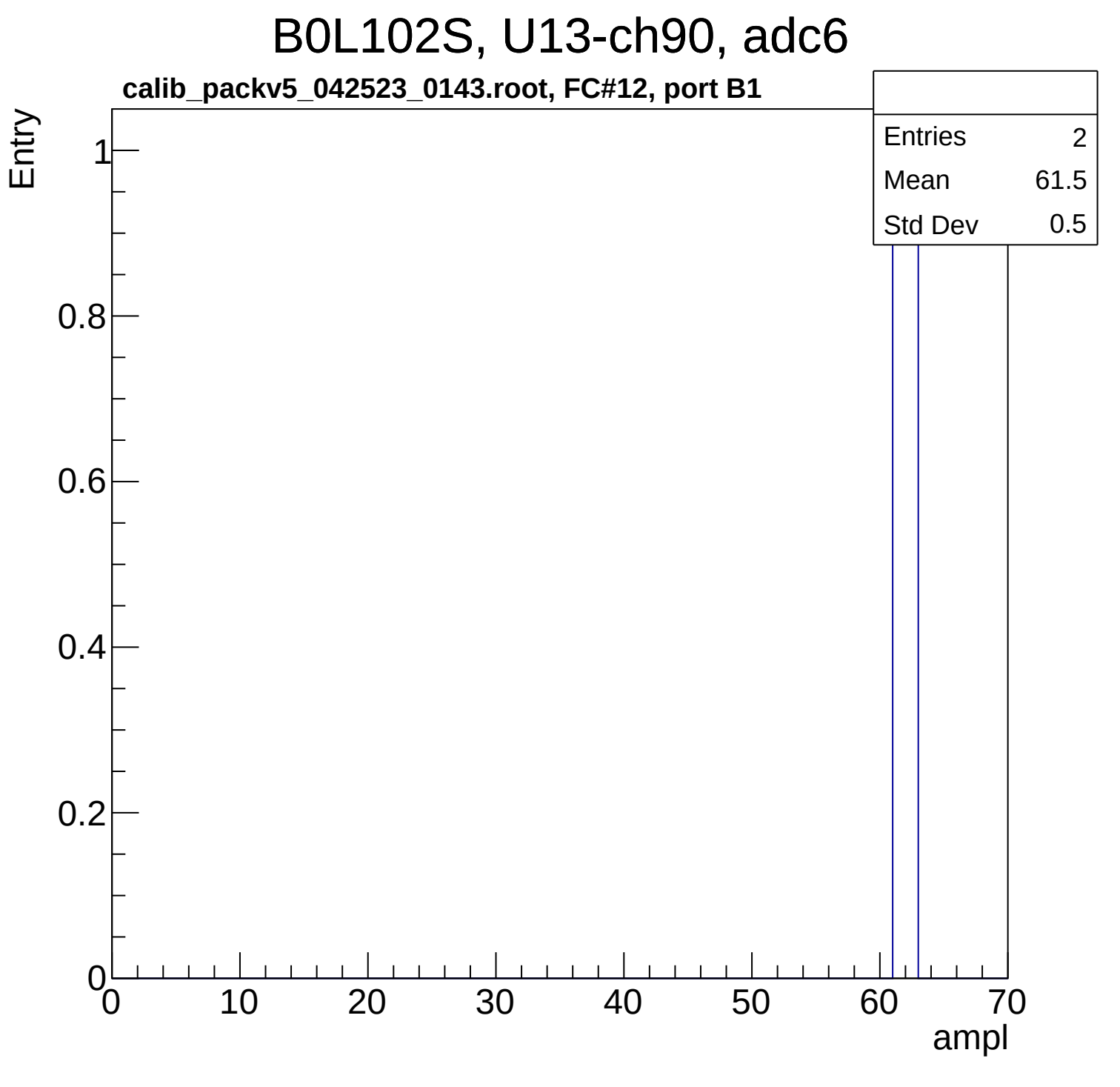
40
50
60
70


B0L102S, U13-ch90, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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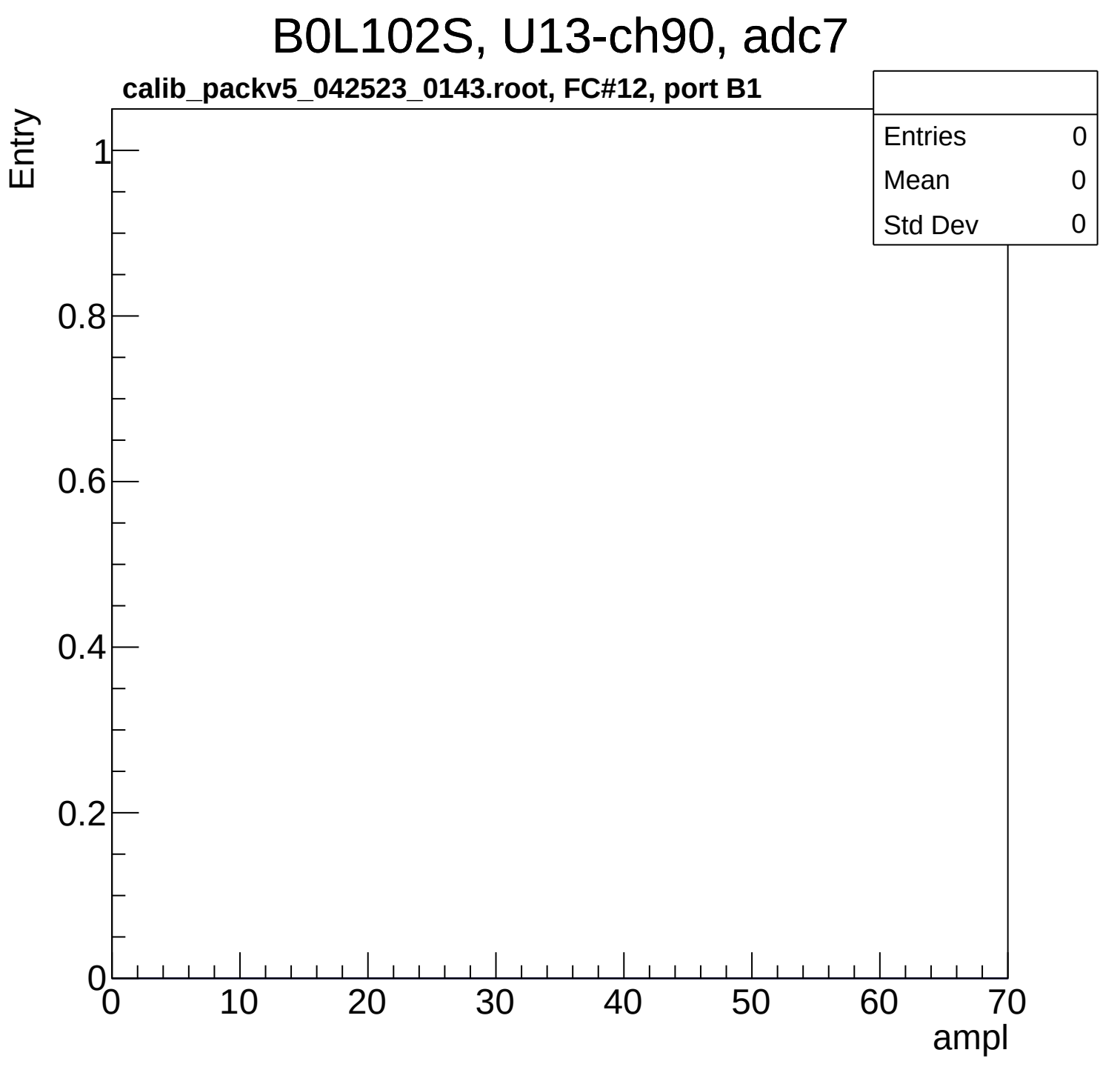
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch90, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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30
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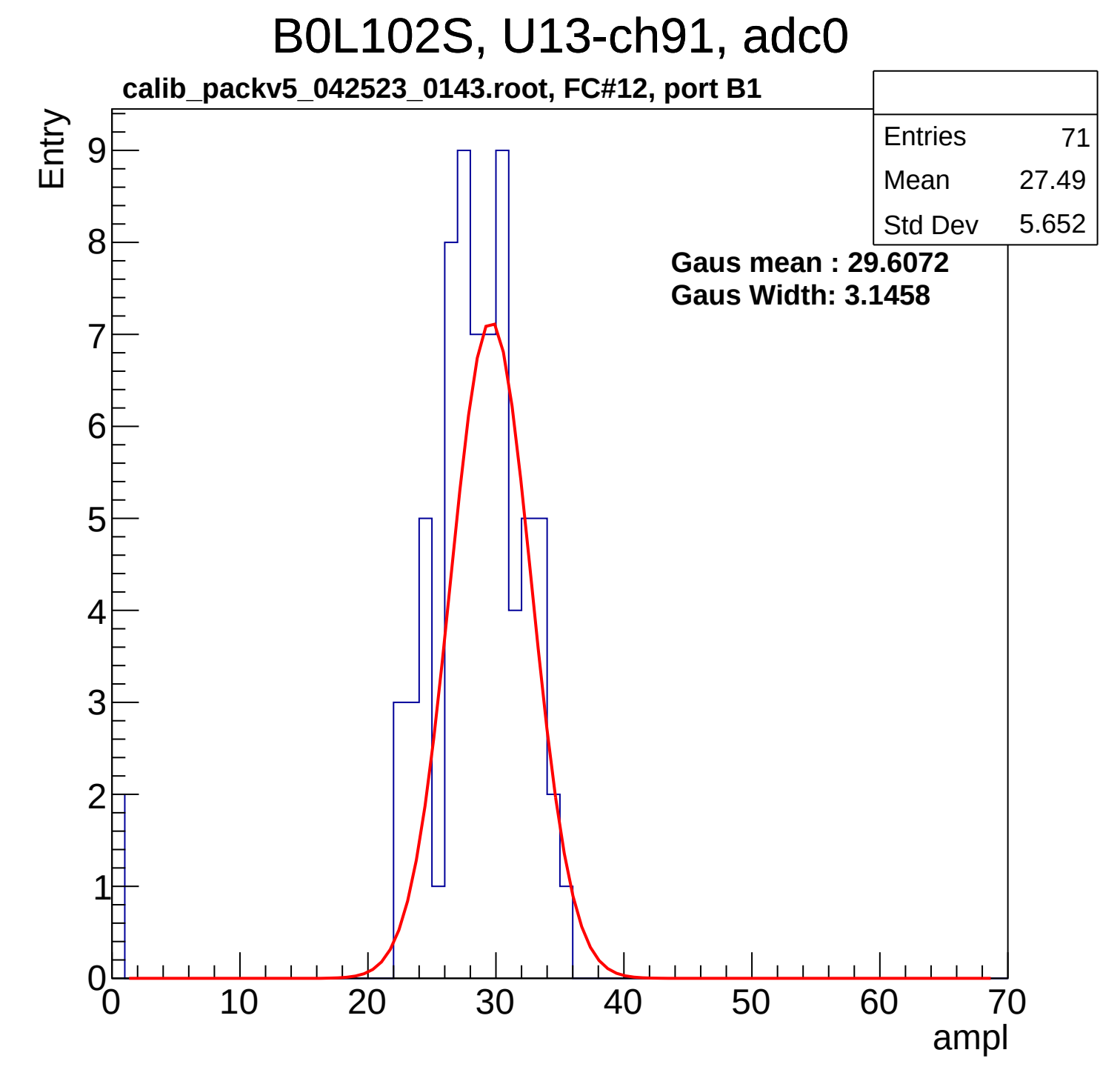
50
60
70
ampl


B0L102S, U13-ch91, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 27.49
Std Dev 5.652
Gaus mean : 29.6072
Gaus Width: 3.1458
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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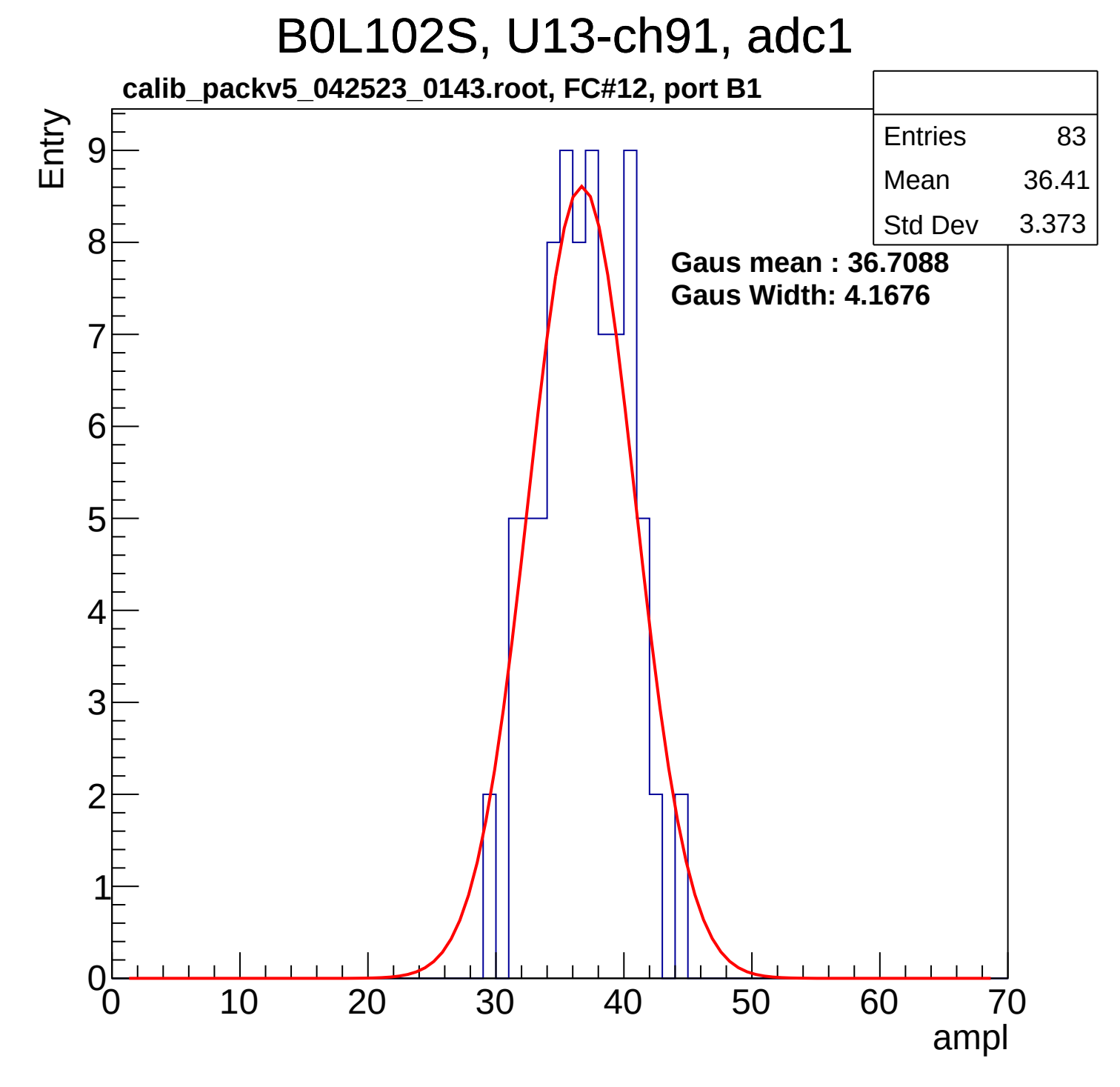
60
70


B0L102S, U13-ch91, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 36.41
Std Dev 3.373
Gaus mean : 36.7088
Gaus Width: 4.1676
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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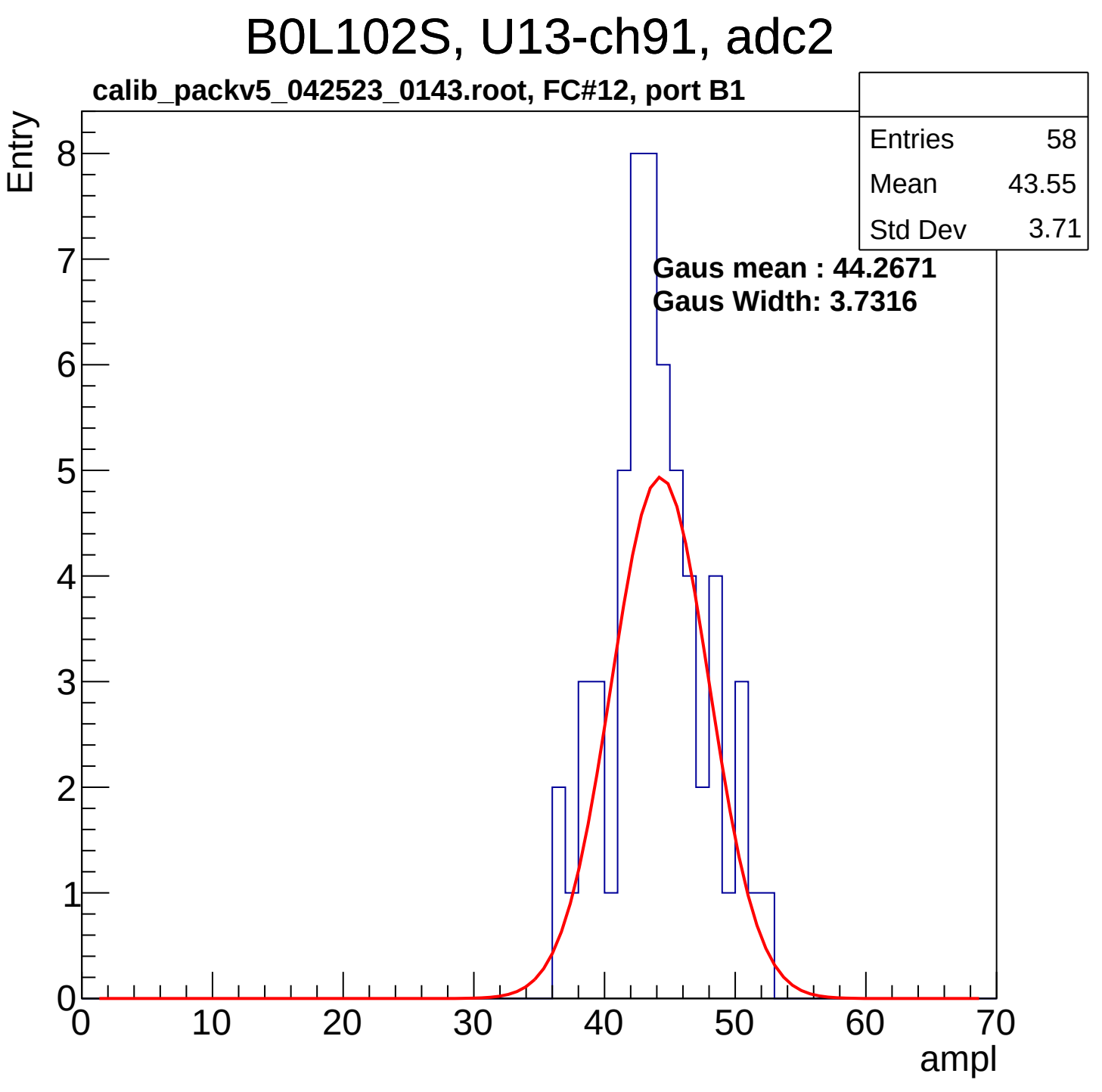
40
50
60
70


B0L102S, U13-ch91, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 43.55
Std Dev 3.71
Gaus mean : 44.2671
Gaus Width: 3.7316
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
50
60
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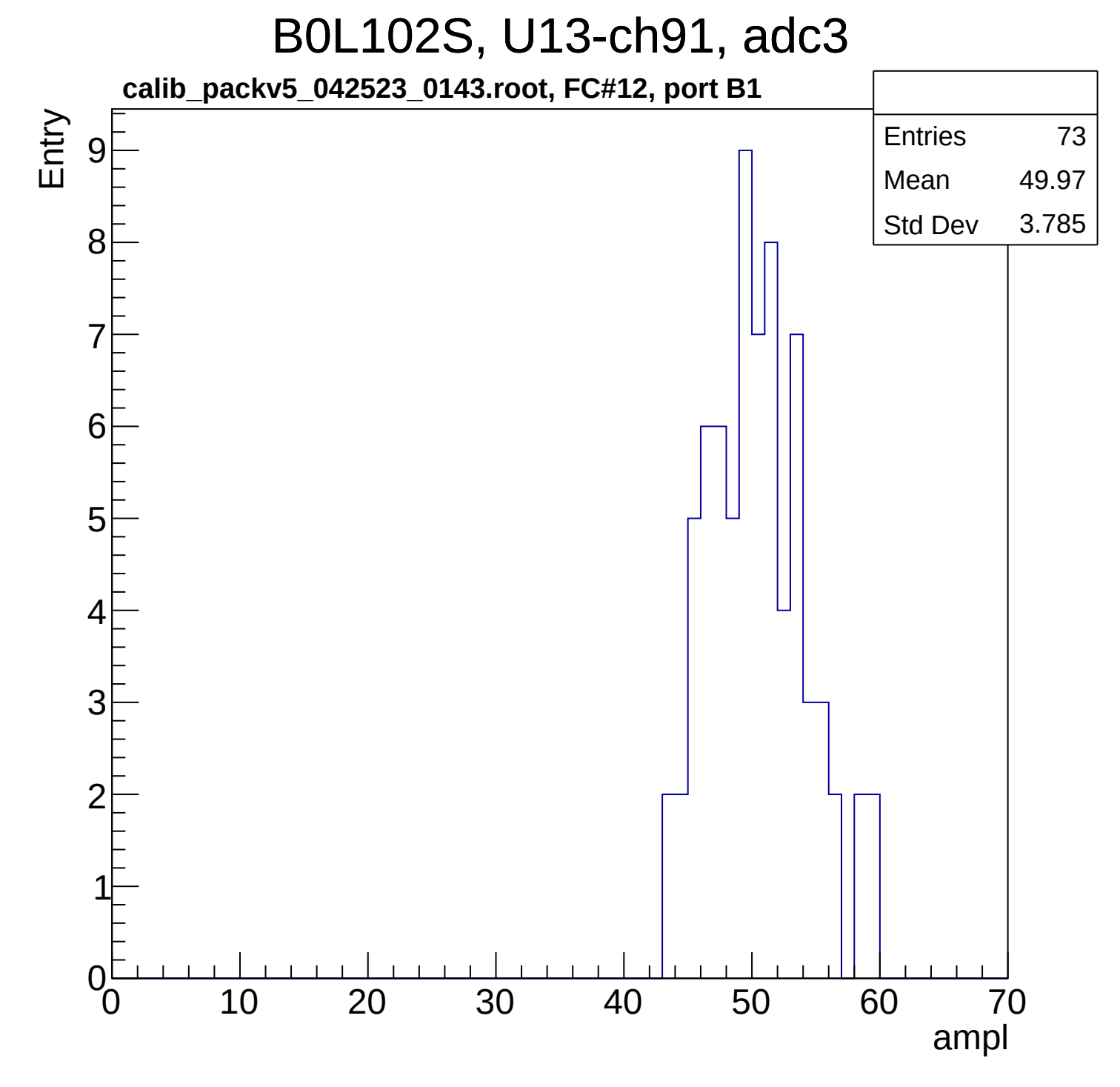
B0L102S, U13-ch91, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 49.97
Std Dev 3.785
Entry
ampl
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3
4
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6
7
8
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0
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40
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60
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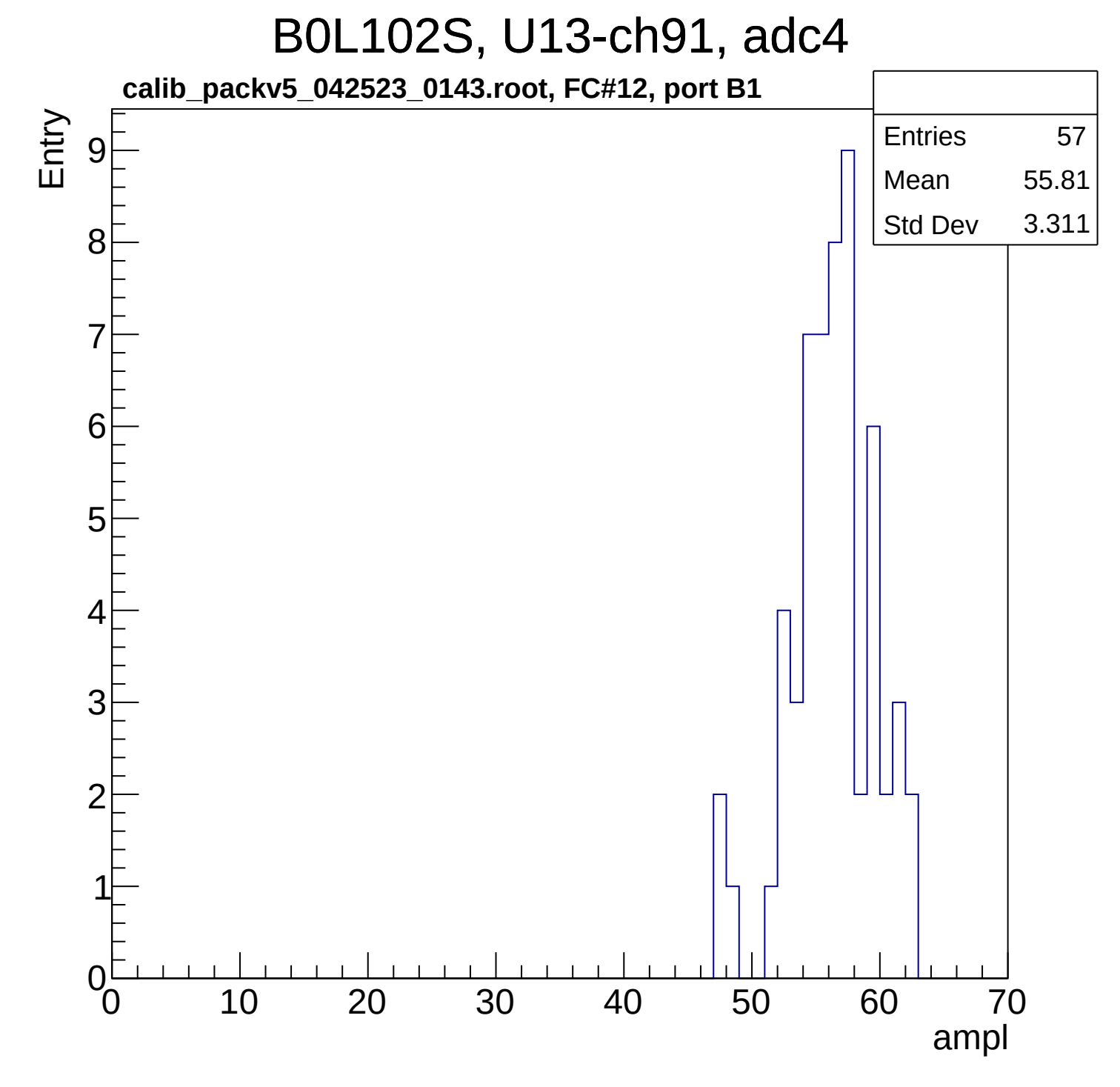
B0L102S, U13-ch91, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.81
Std Dev 3.311
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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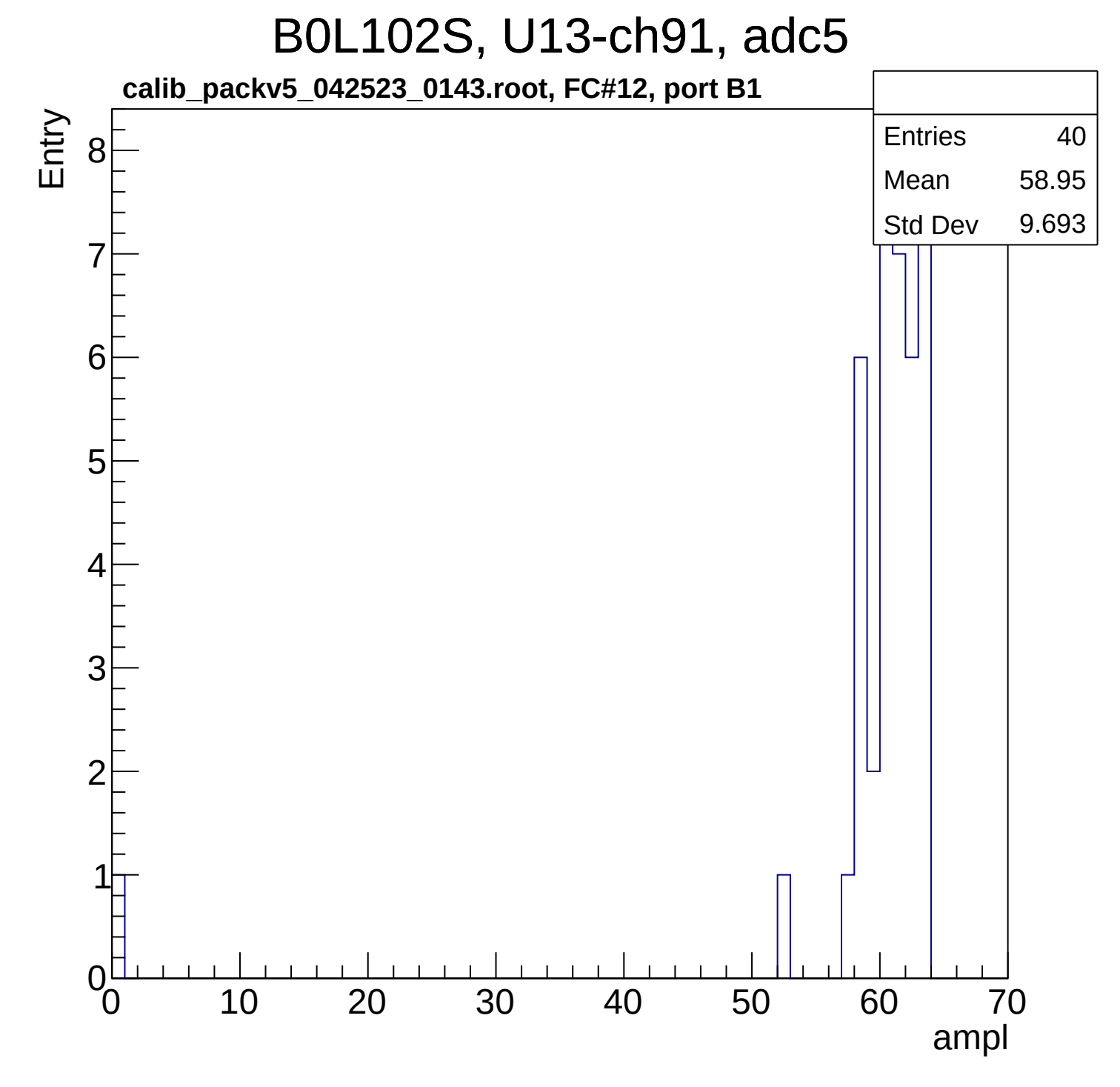
B0L102S, U13-ch91, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.95
Std Dev 9.693
Entry
ampl
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2
3
4
5
6
7
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0
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20
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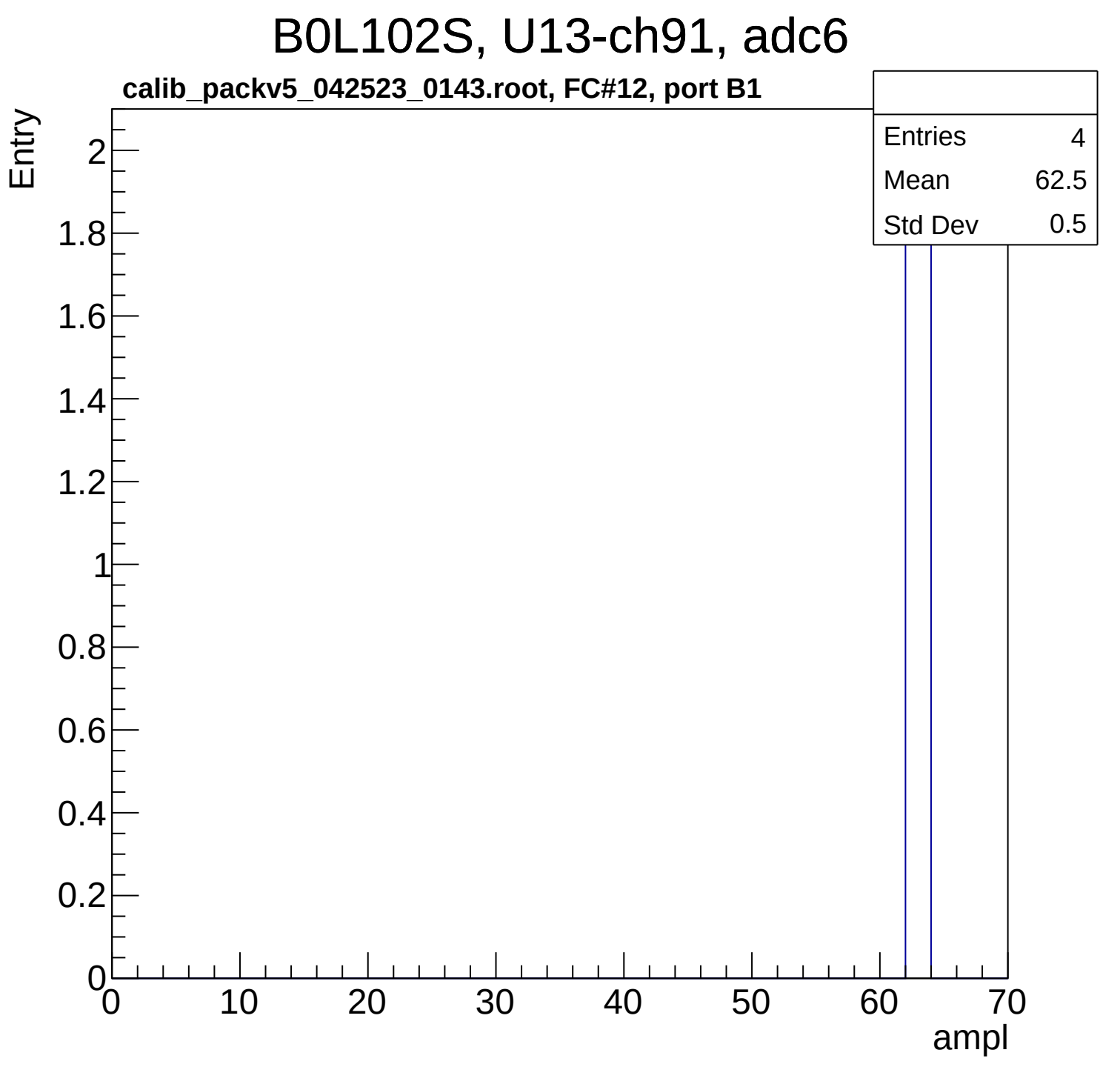
40
50
60
70


B0L102S, U13-ch91, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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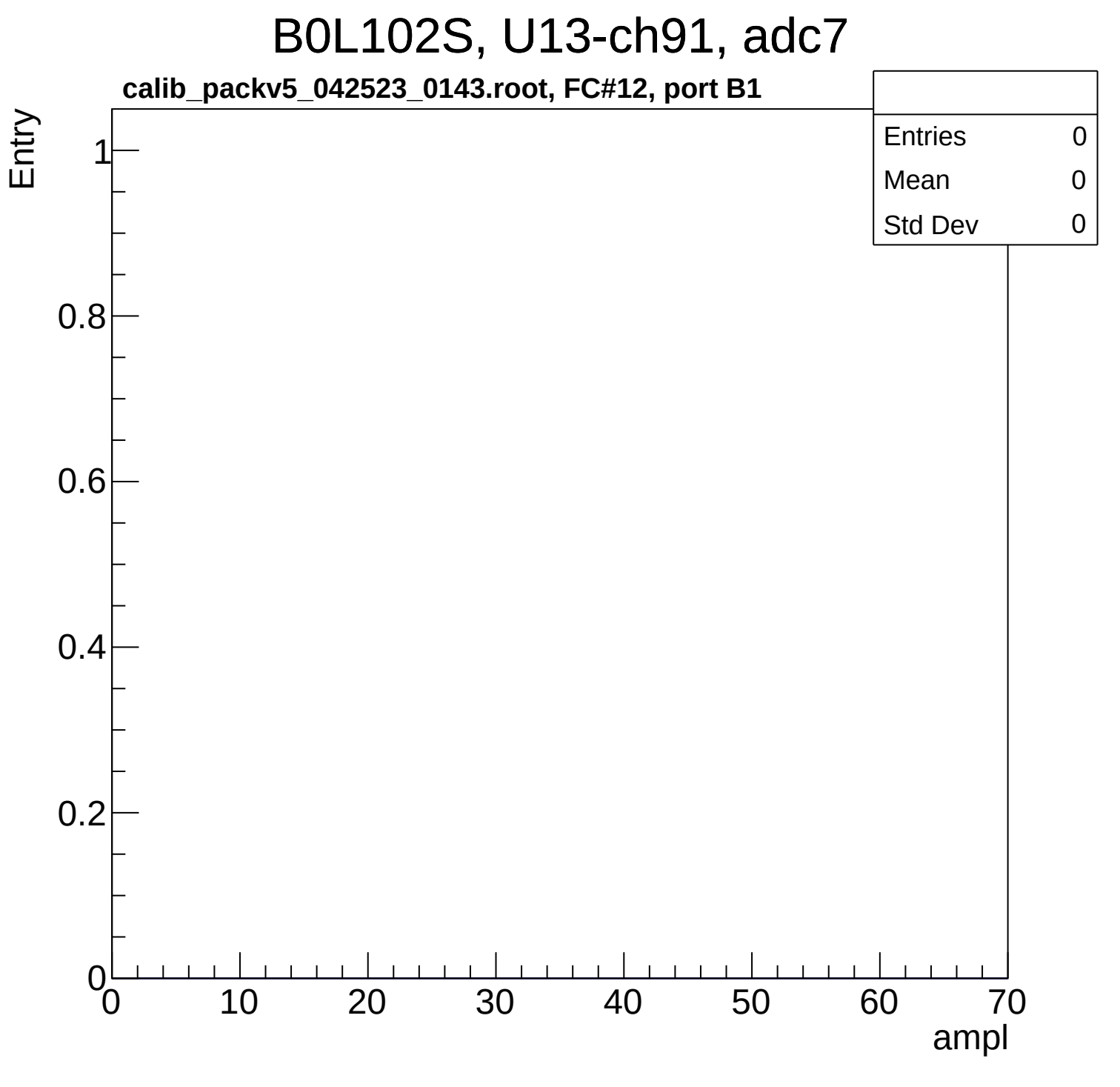
B0L102S, U13-ch91, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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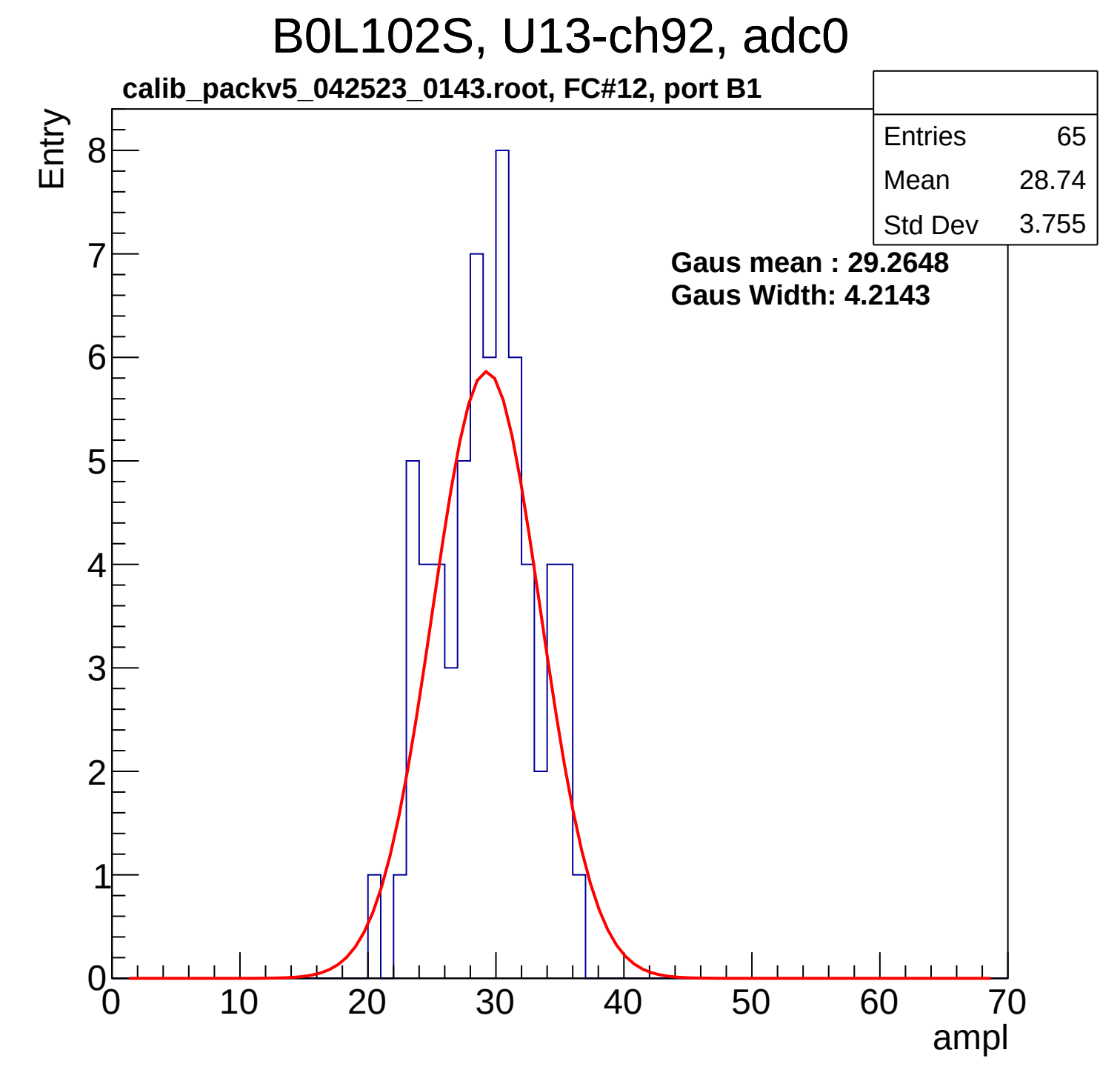
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch92, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.74
Std Dev 3.755
Gaus mean : 29.2648
Gaus Width: 4.2143
Entry
ampl
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1
2
3
4
5
6
7
8
0
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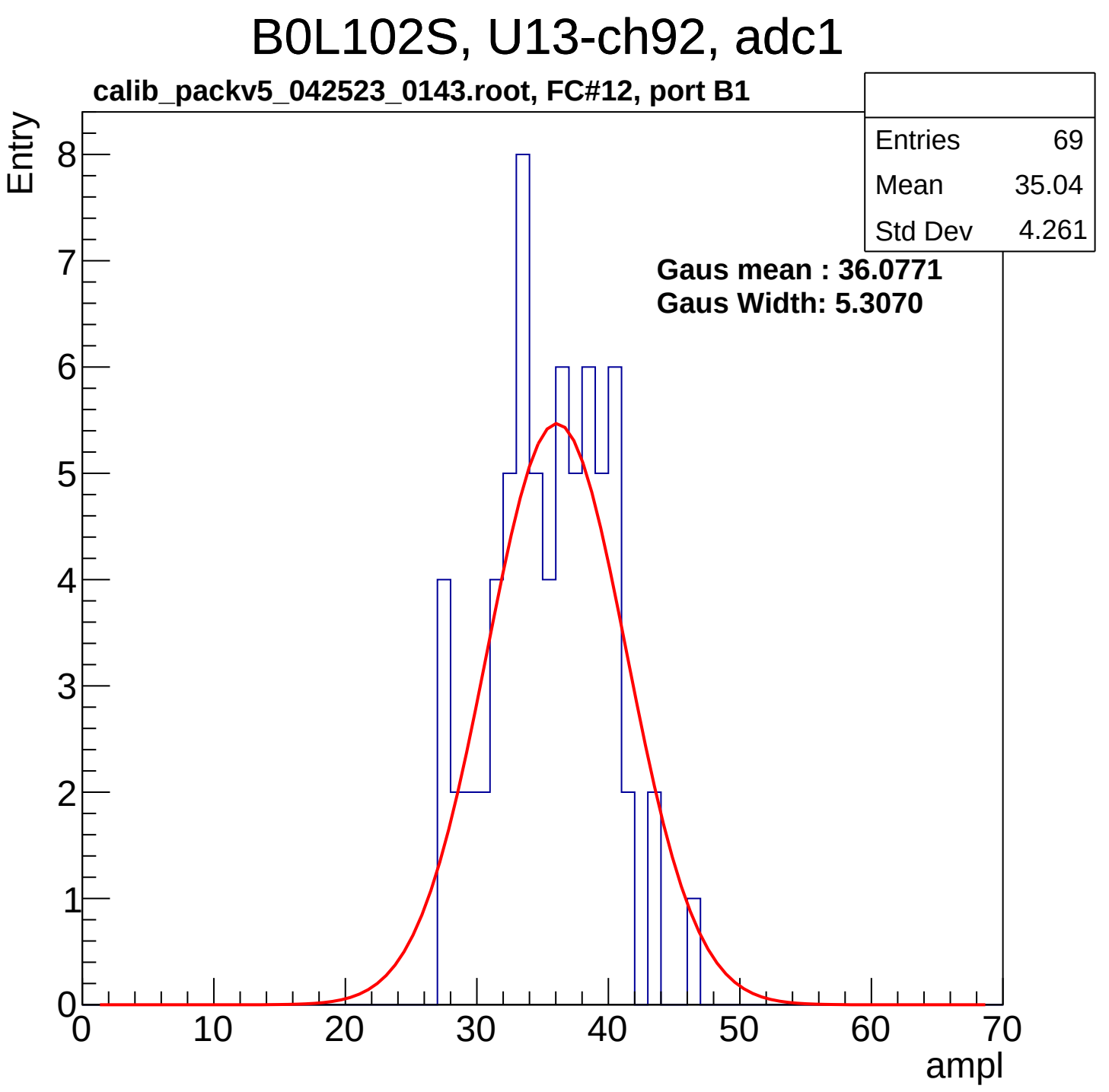
60
70


B0L102S, U13-ch92, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 35.04
Std Dev 4.261
Gaus mean : 36.0771
Gaus Width: 5.3070
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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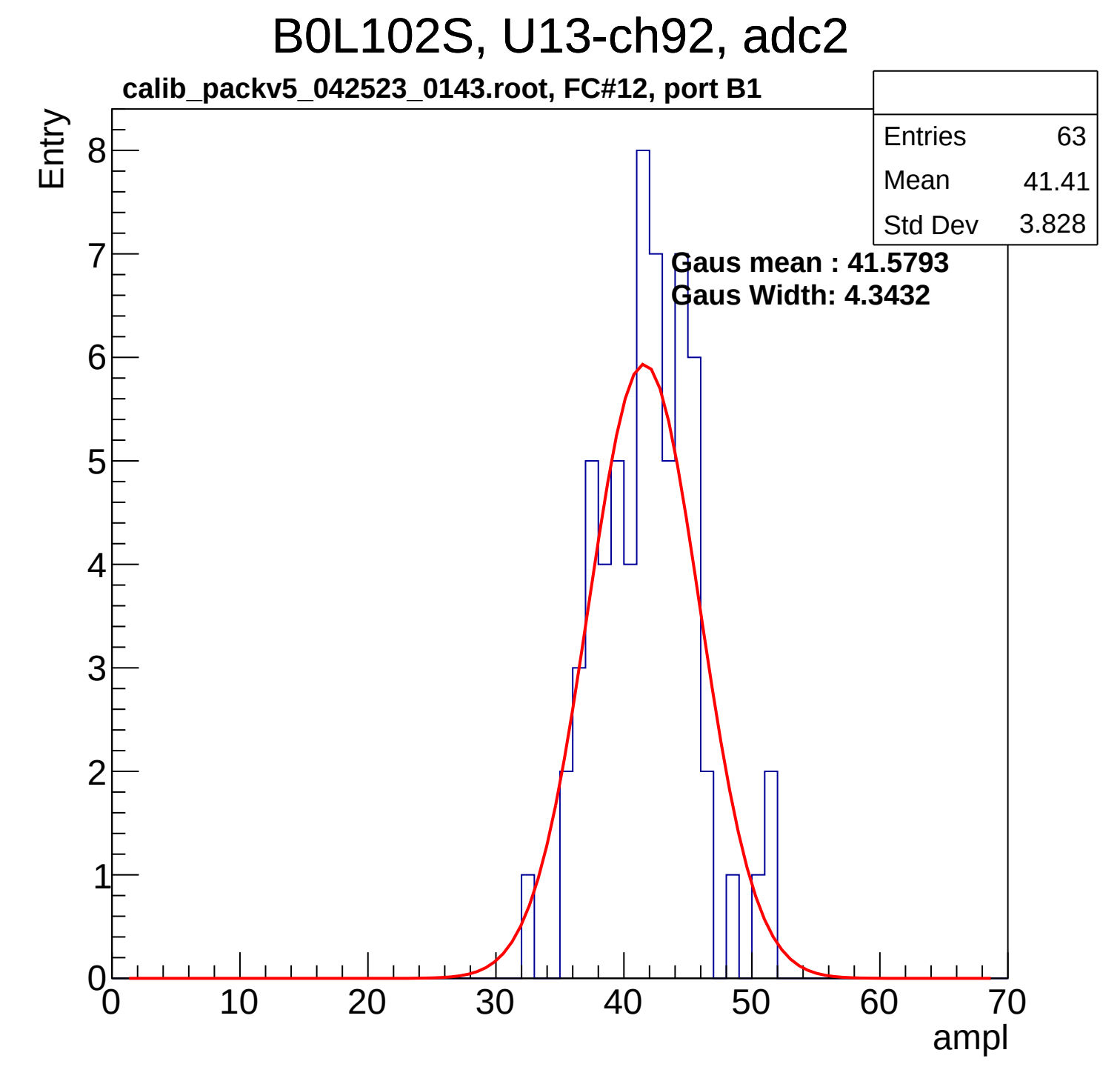
B0L102S, U13-ch92, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 41.41
Std Dev 3.828
Gaus mean : 41.5793
Gaus Width: 4.3432
Entry
ampl
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4
5
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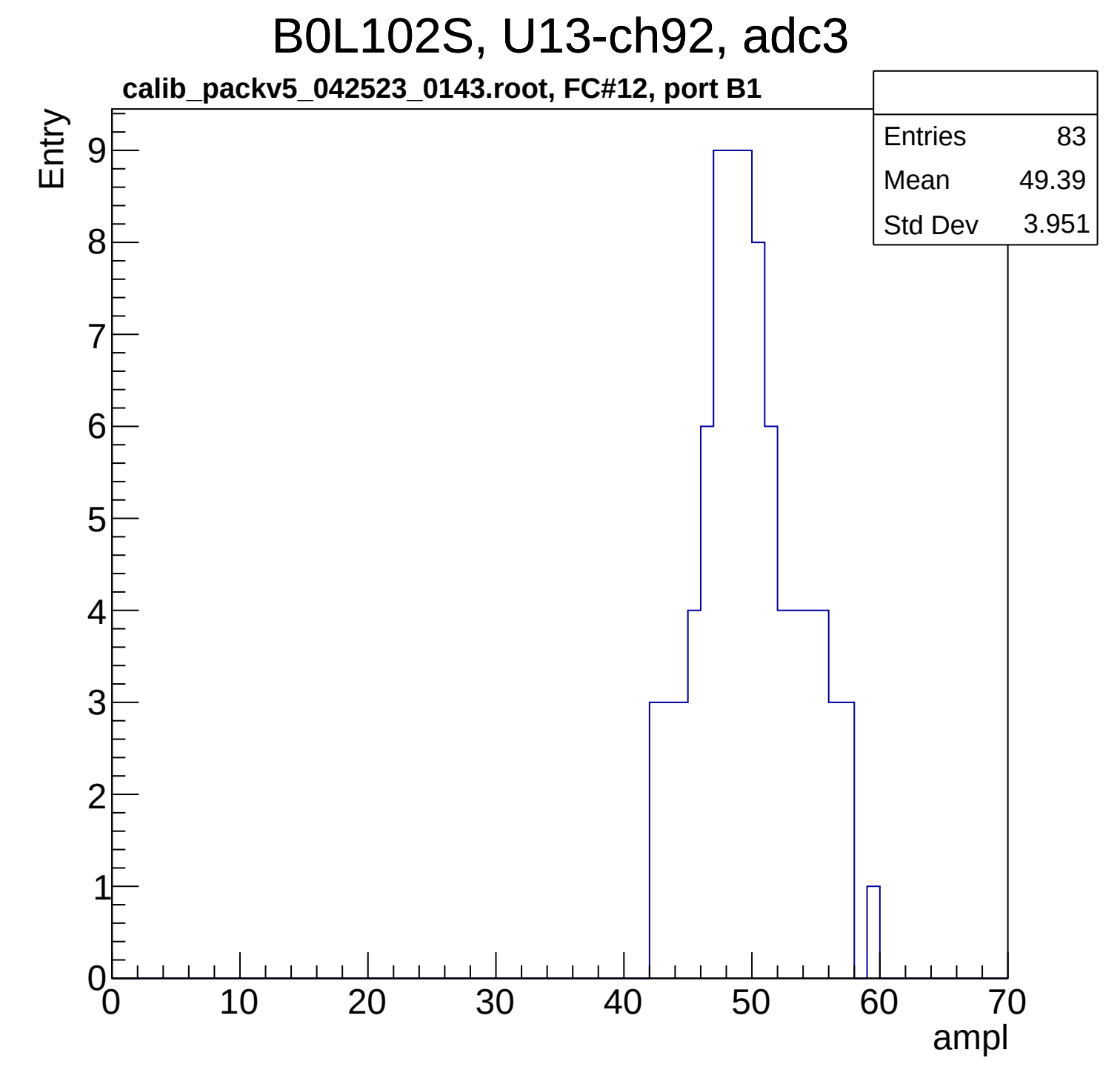
B0L102S, U13-ch92, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 49.39
Std Dev 3.951
Entry
ampl
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0
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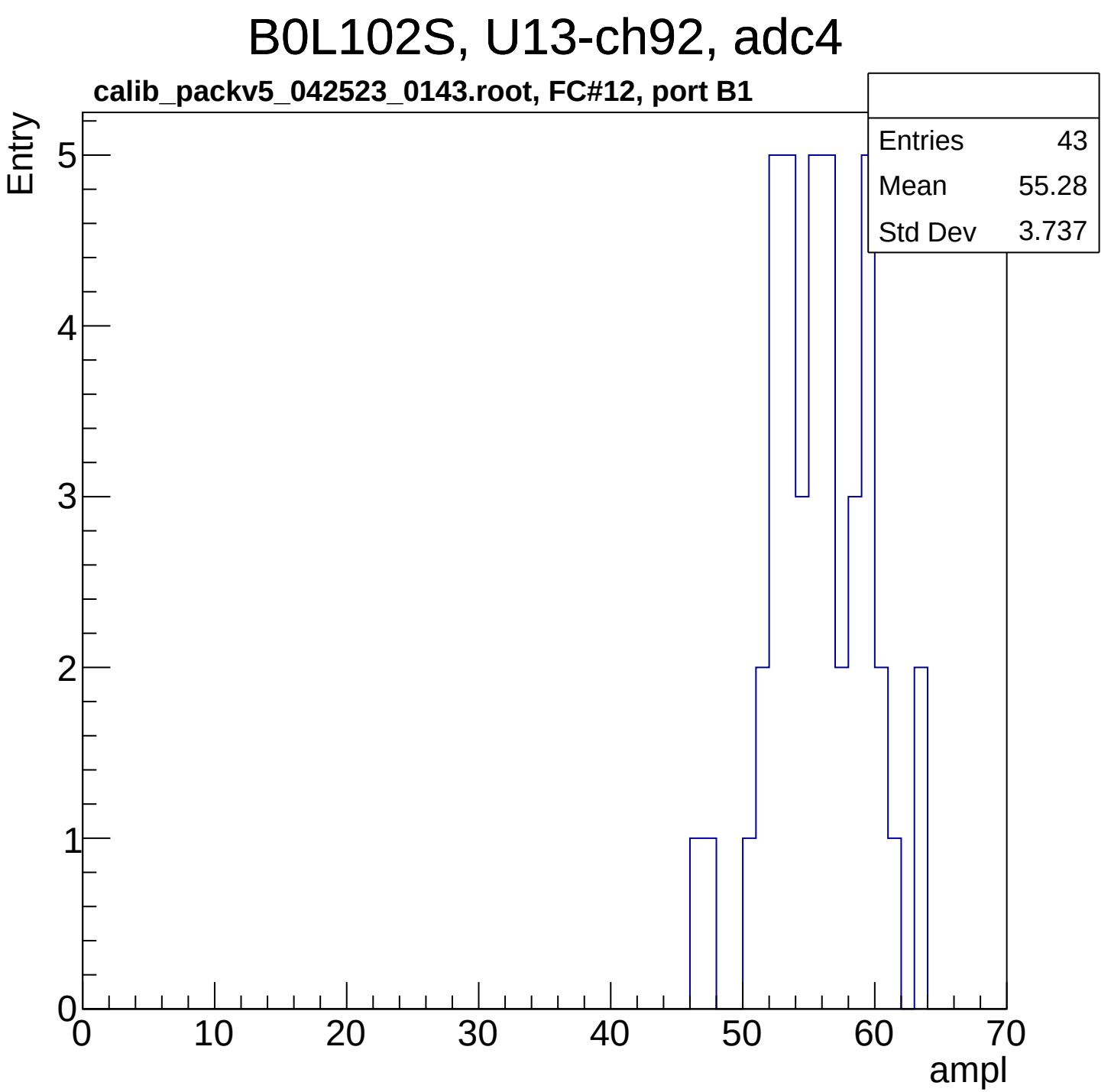
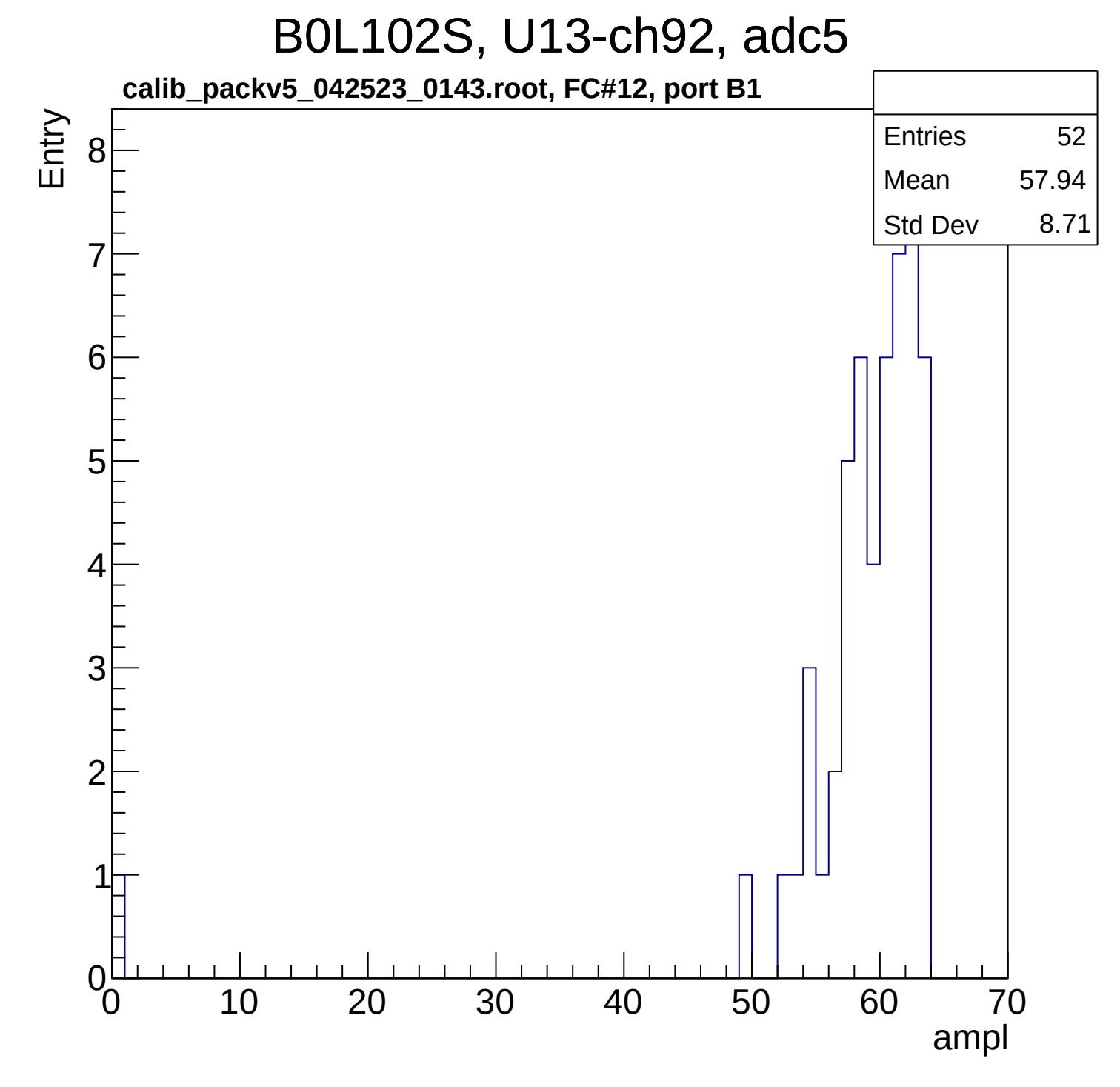
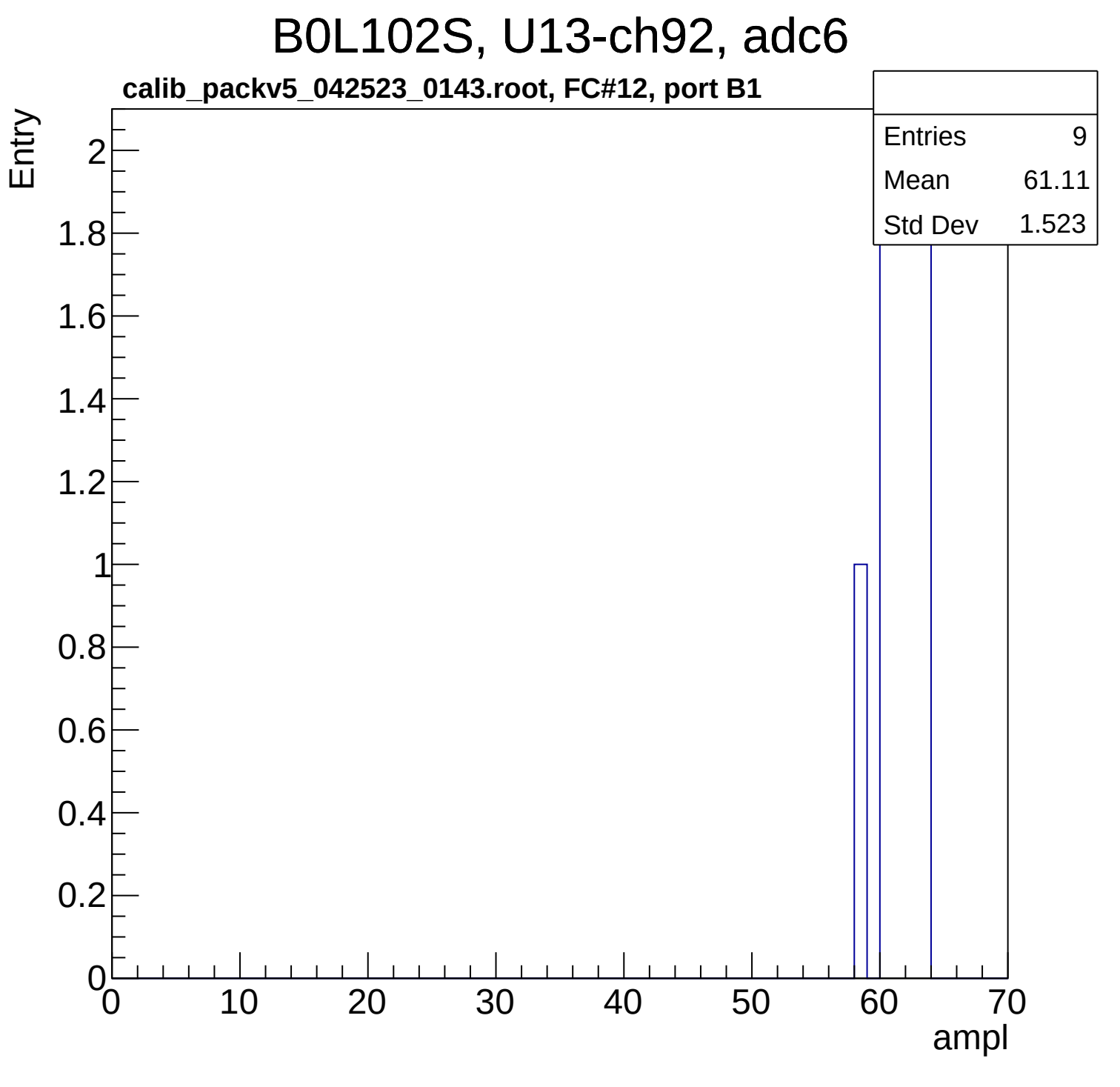
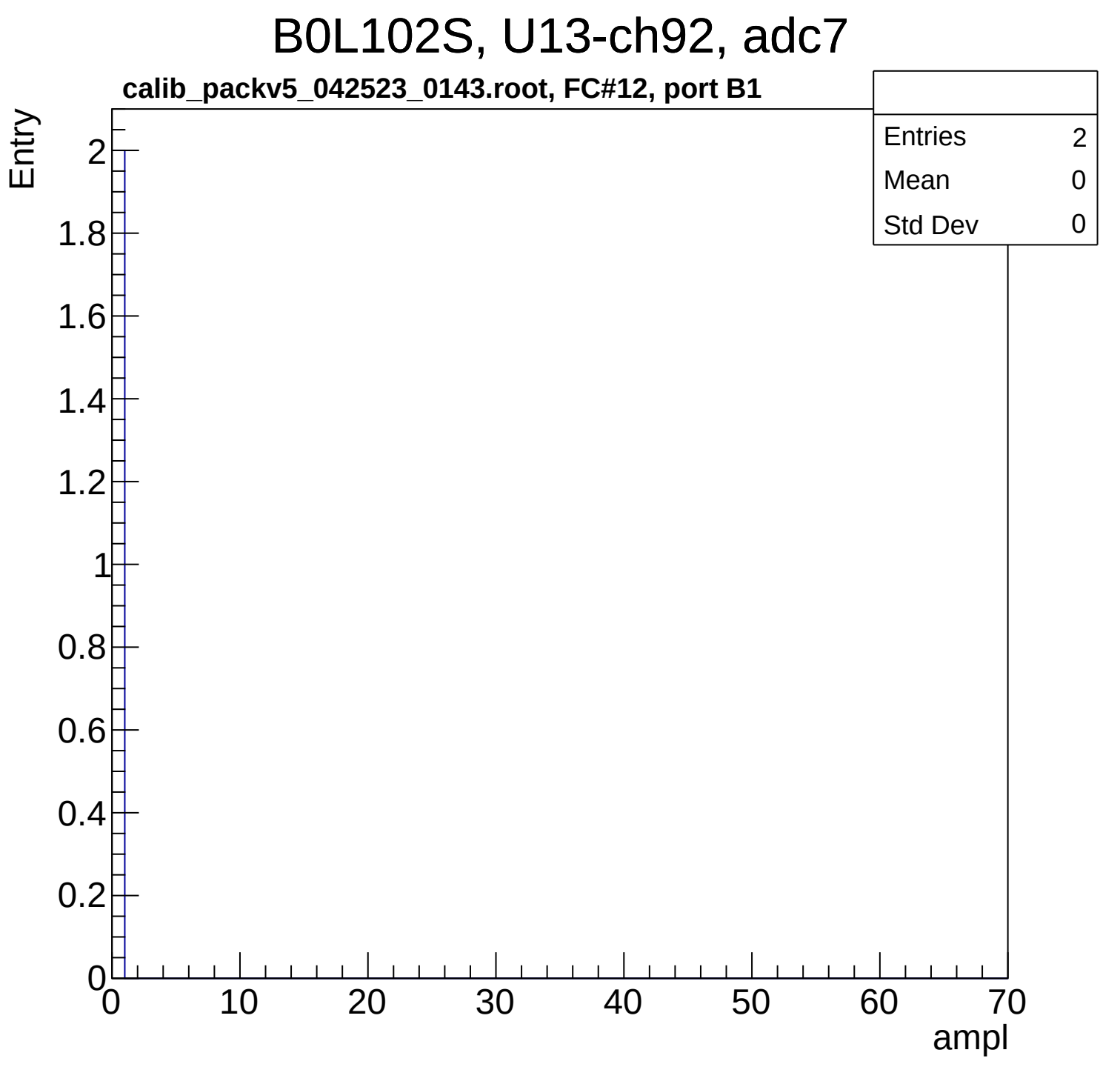
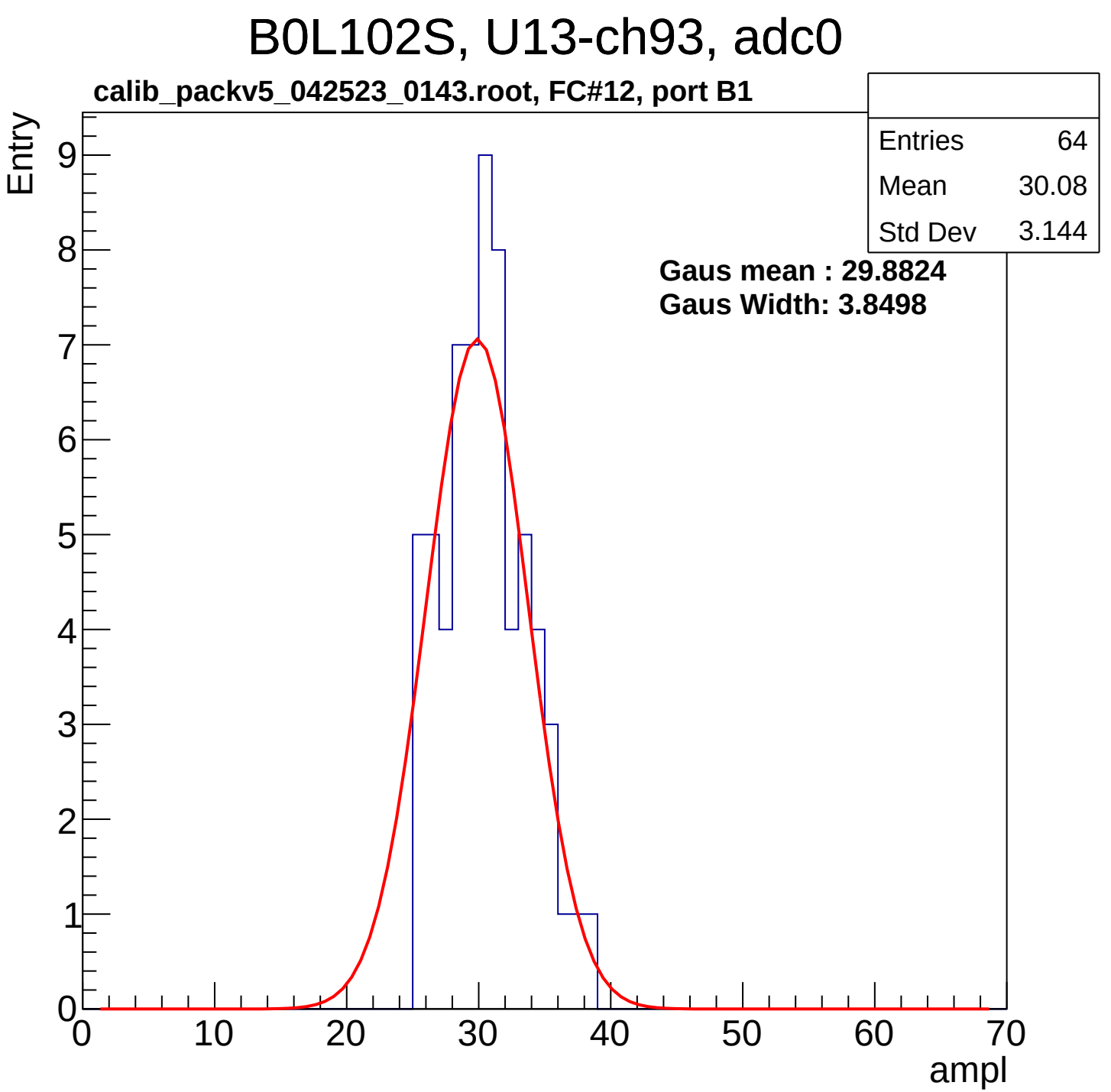
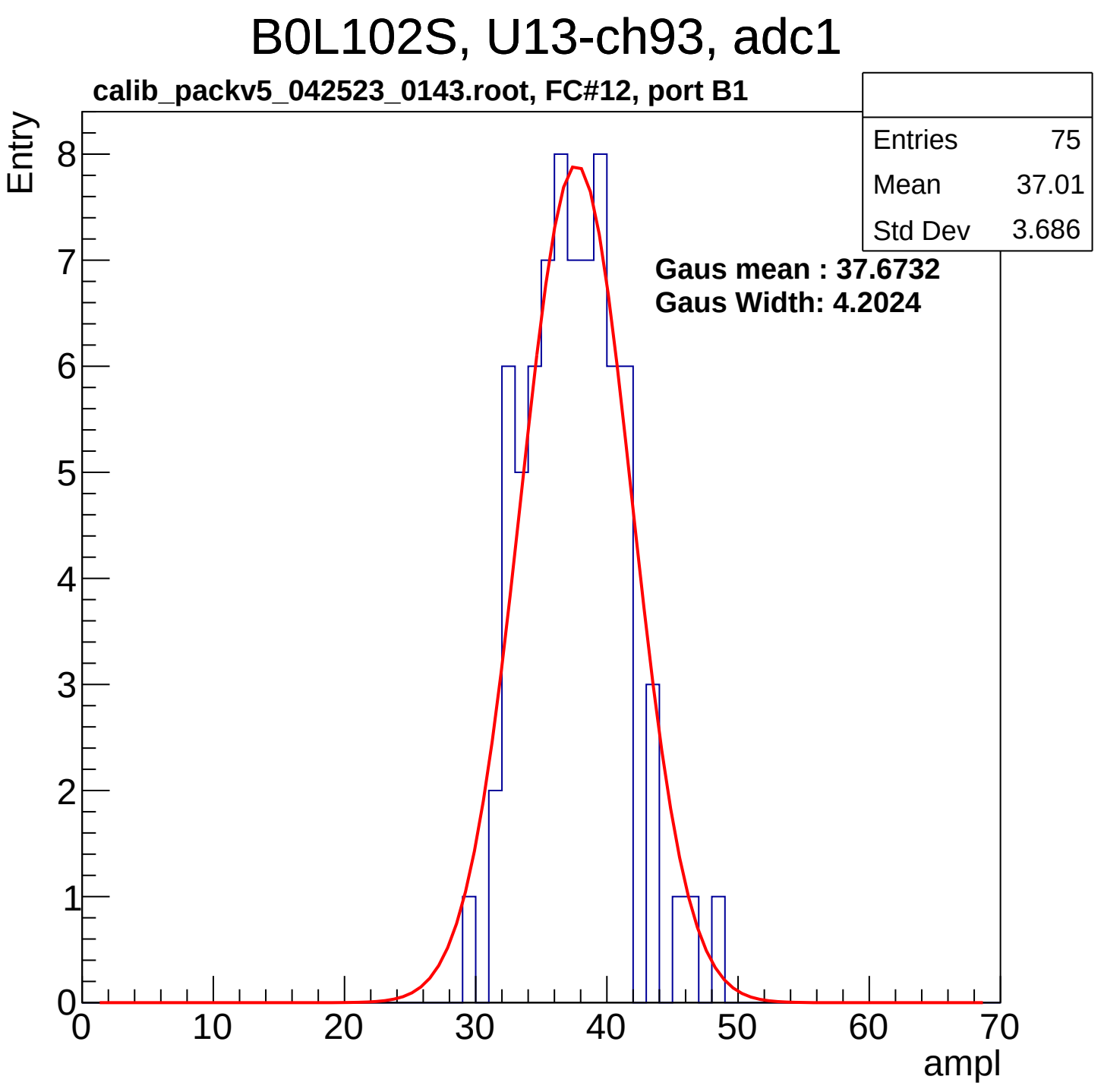

B0L102S, U13-ch92, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.28
Std Dev 3.737
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

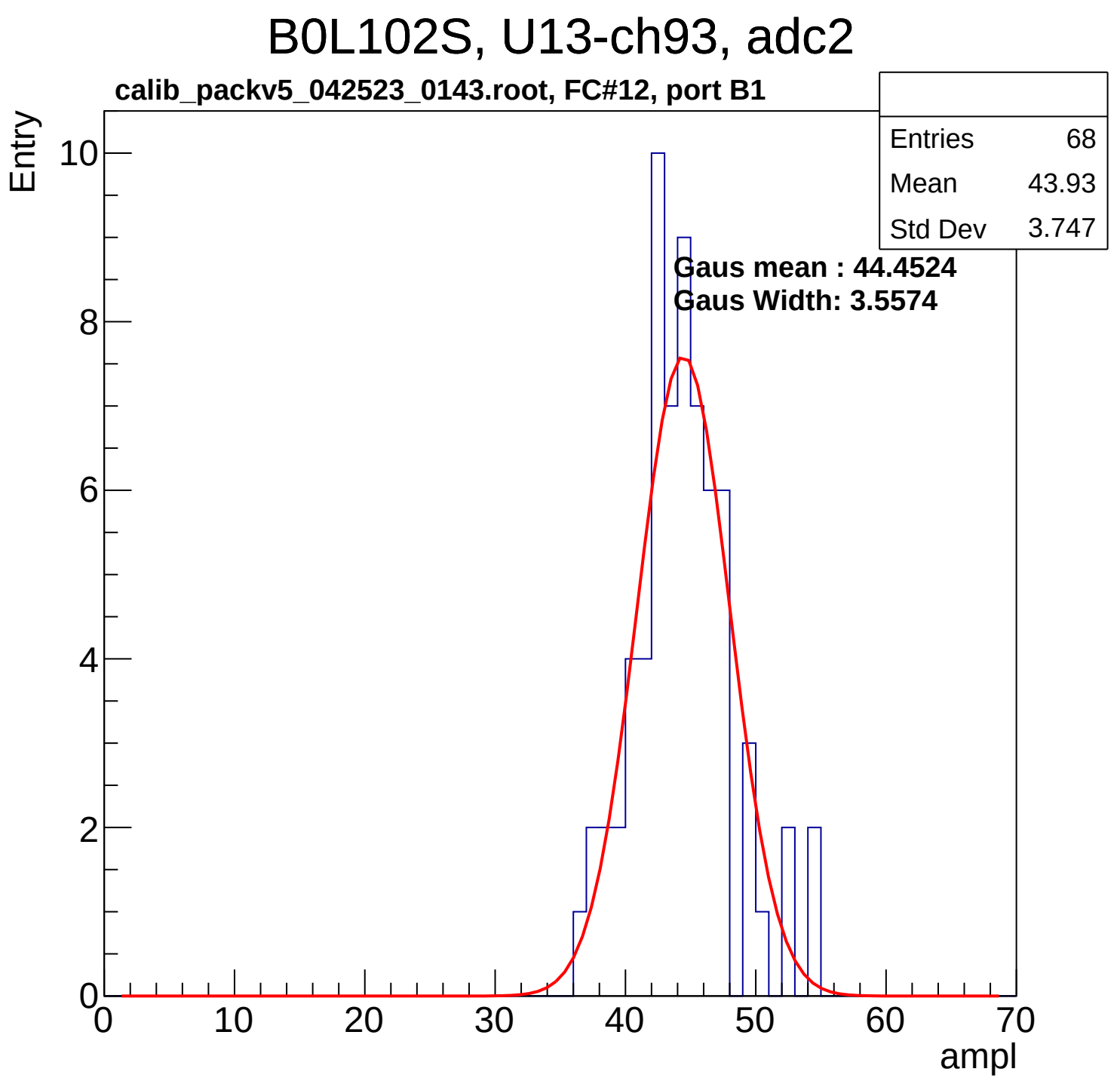

B0L102S, U13-ch92, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 57.94
Std Dev 8.71
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

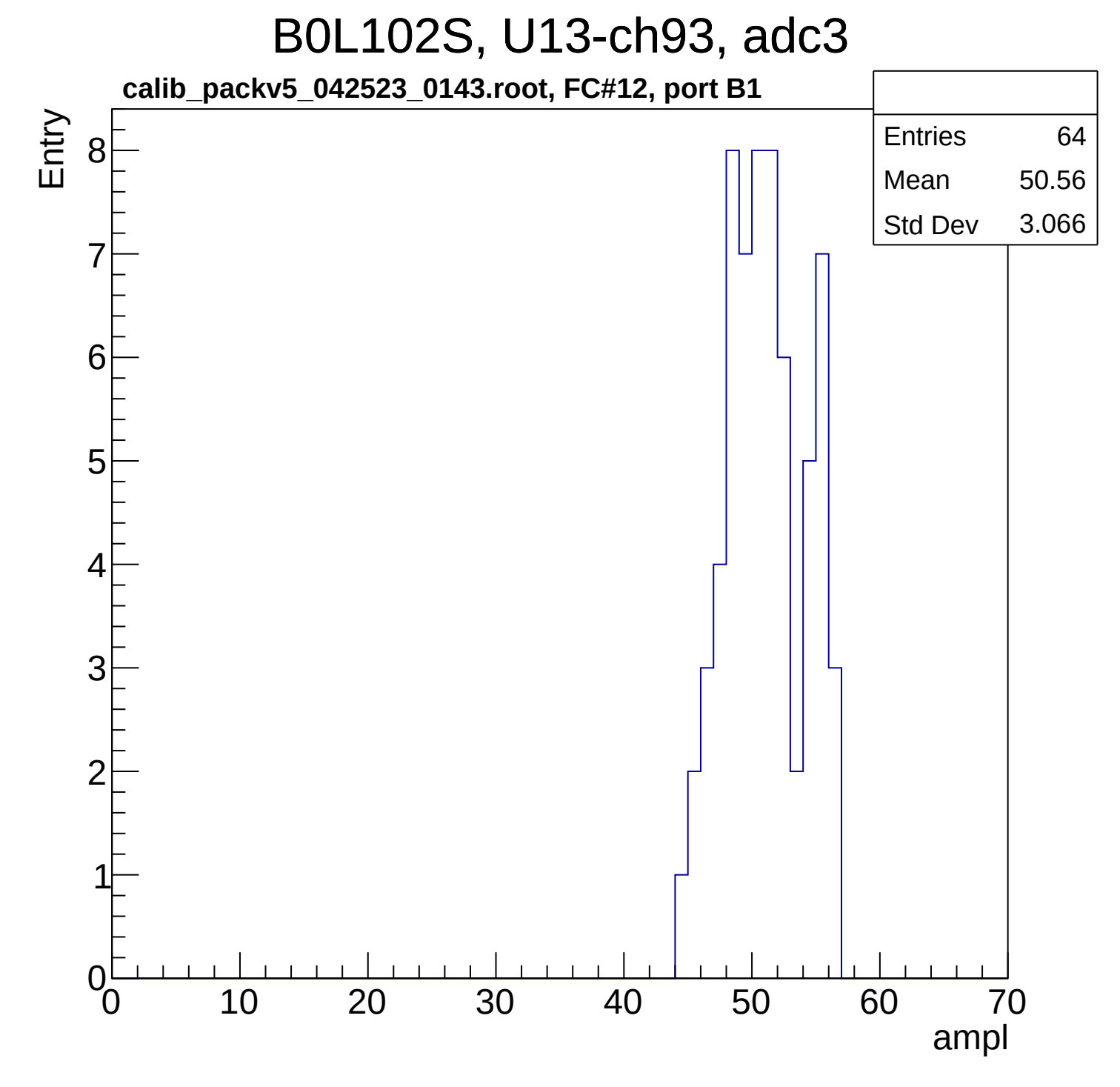

B0L102S, U13-ch92, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.11
Std Dev 1.523
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

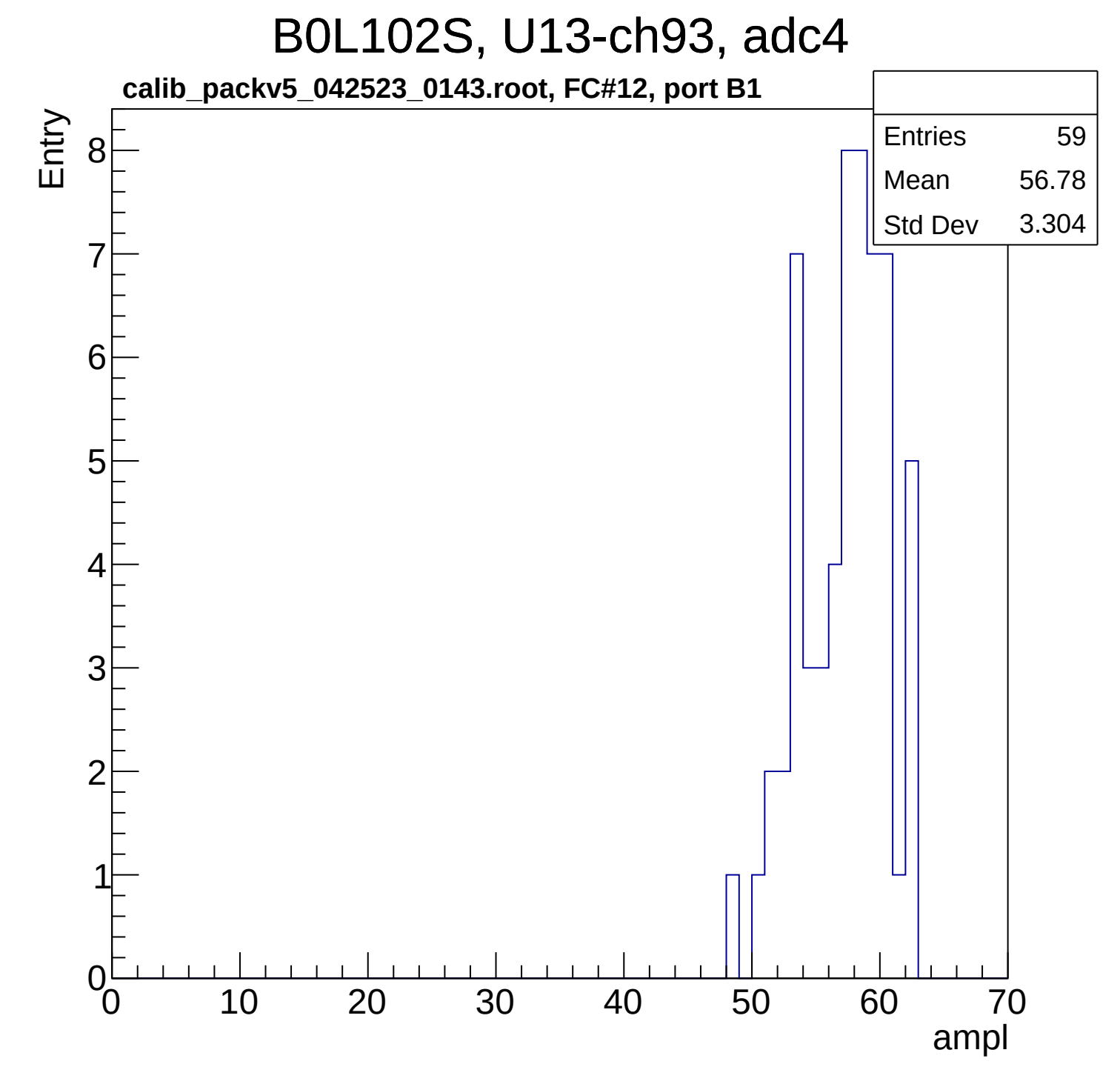

B0L102S, U13-ch92, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

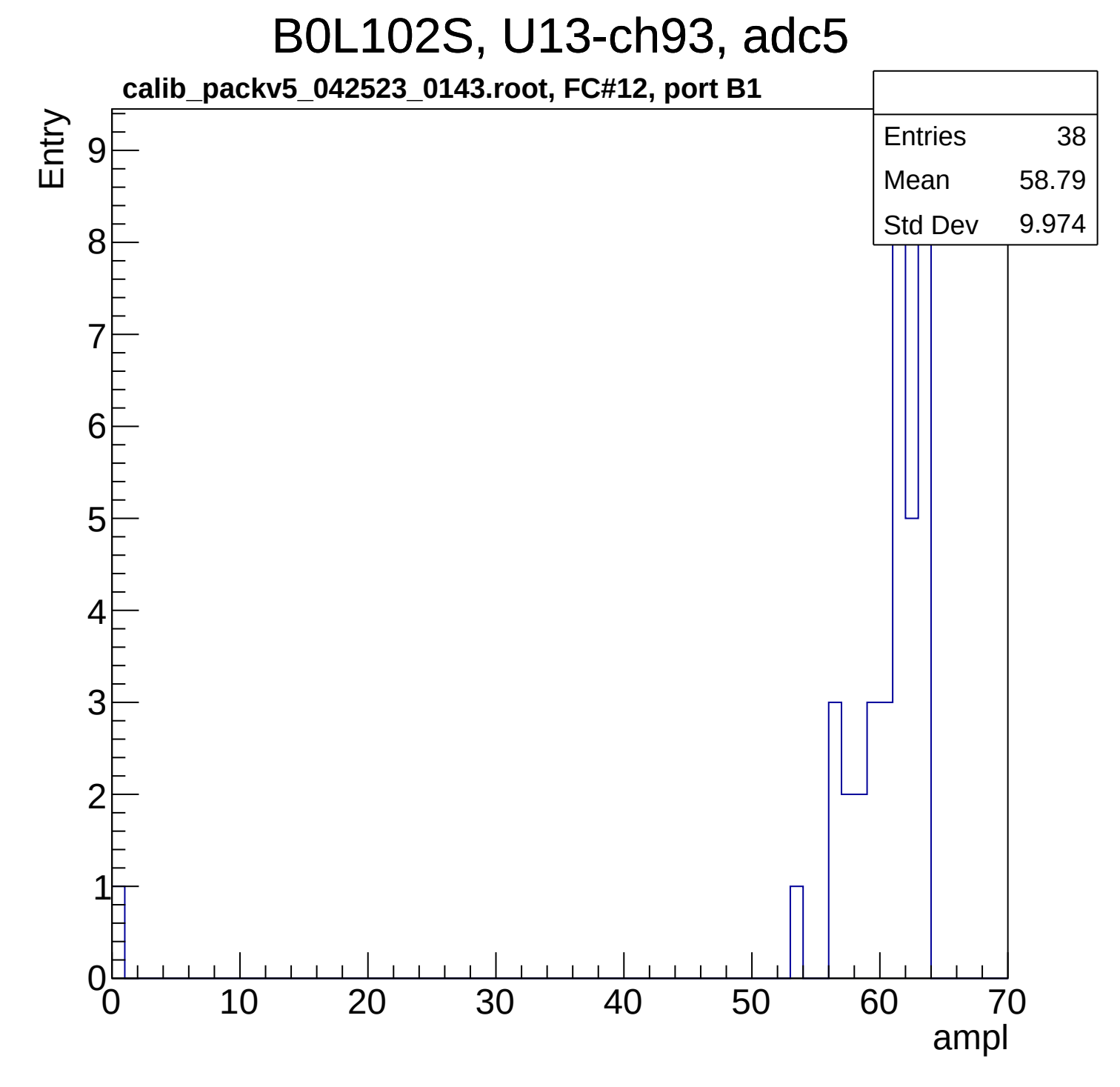

B0L102S, U13-ch93, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 30.08
Std Dev 3.144
Gaus mean : 29.8824
Gaus Width: 3.8498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

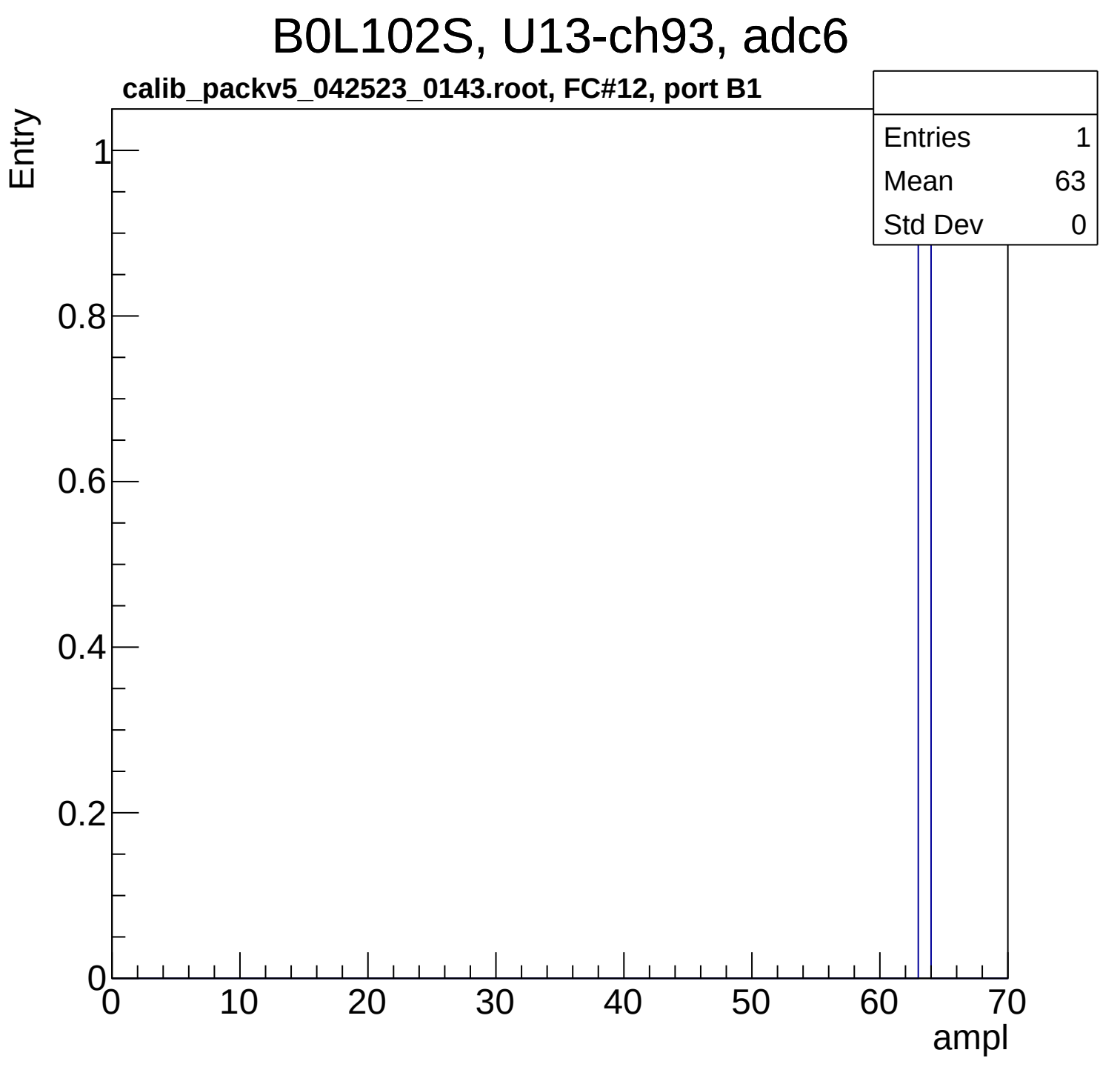

B0L102S, U13-ch93, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.01
Std Dev 3.686
Gaus mean : 37.6732
Gaus Width: 4.2024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

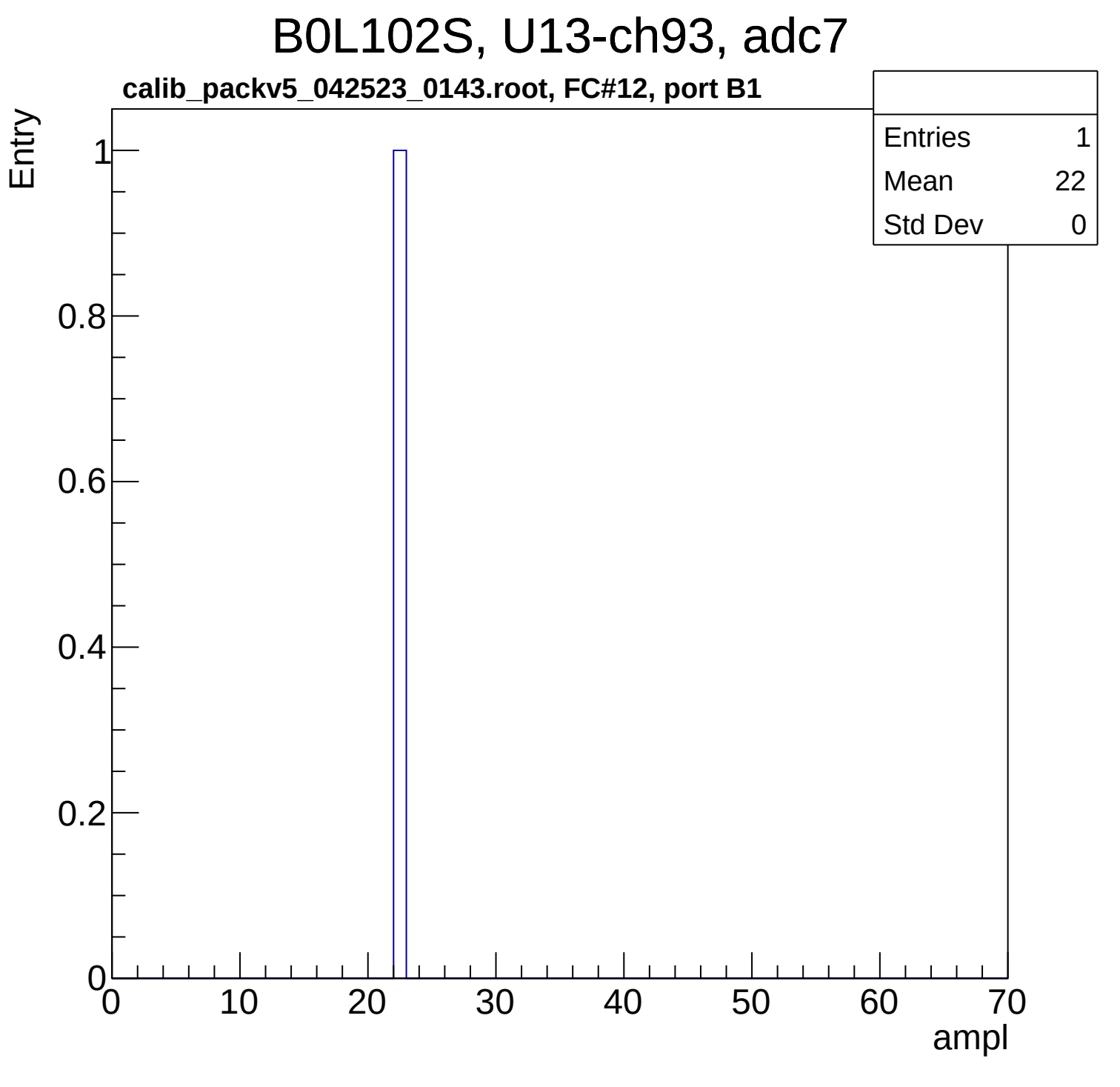

B0L102S, U13-ch93, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.93
Std Dev 3.747
Gaus mean : 44.4524
Gaus Width: 3.5574
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

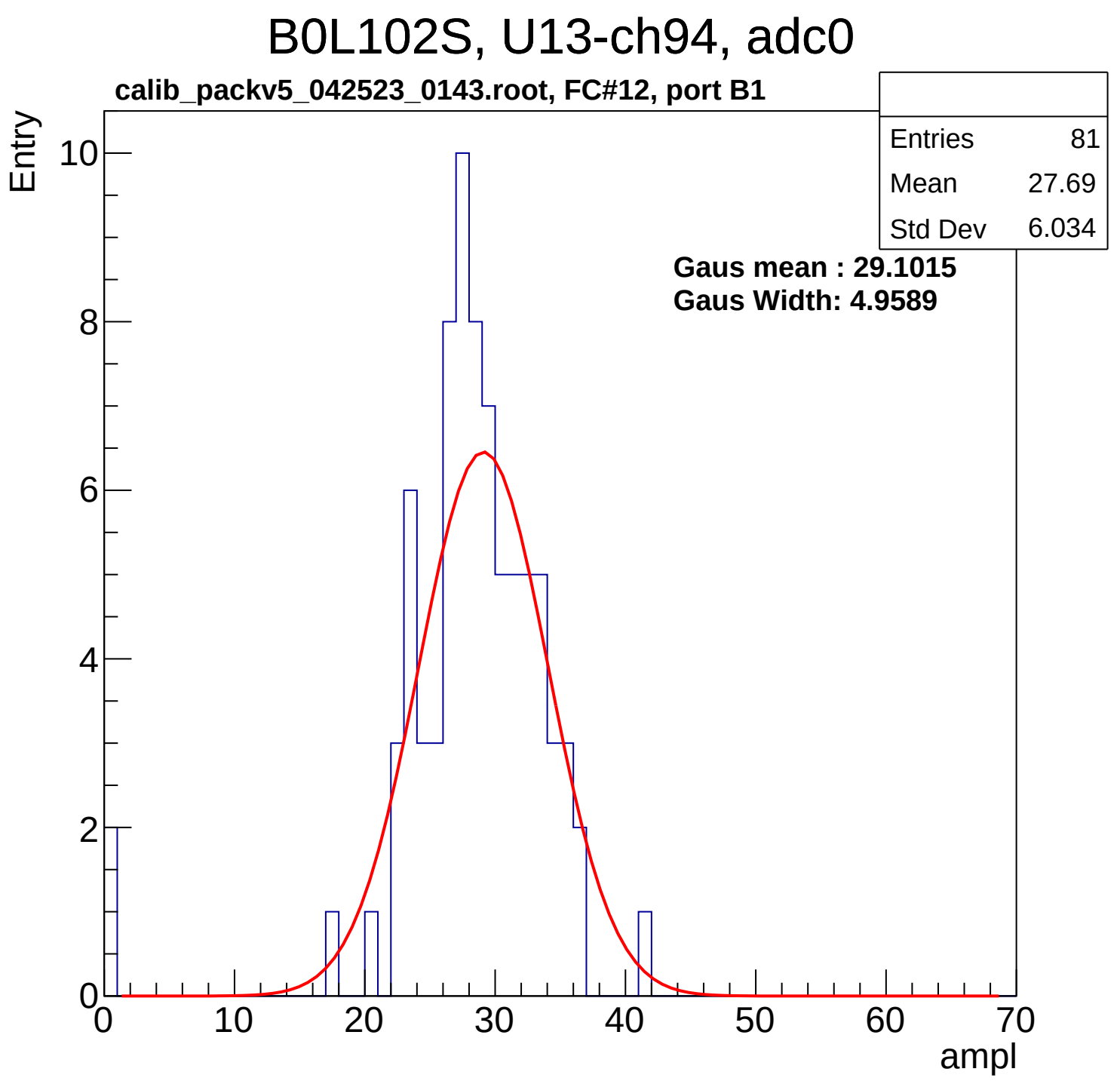

B0L102S, U13-ch93, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 50.56
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

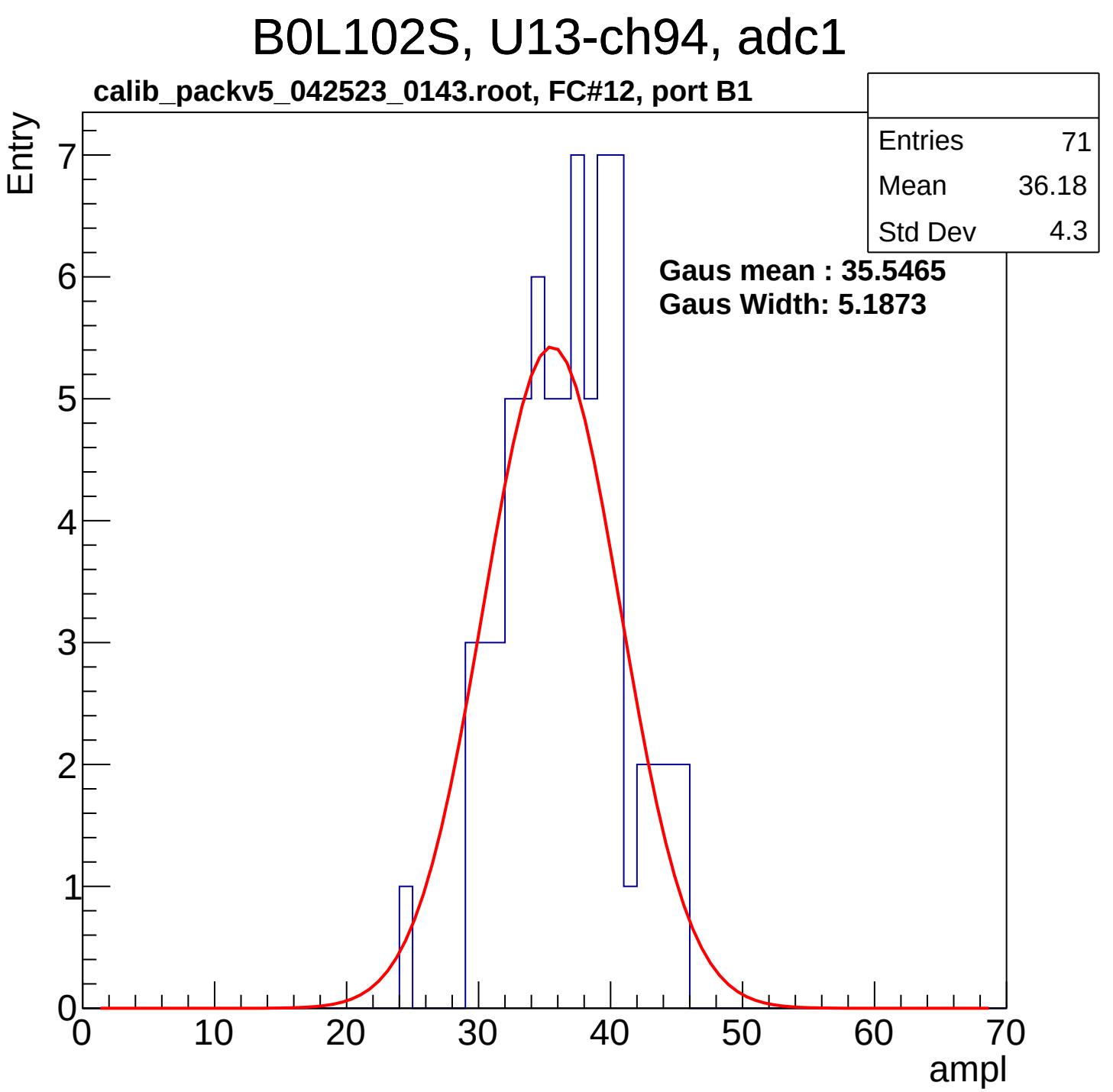

B0L102S, U13-ch93, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.78
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

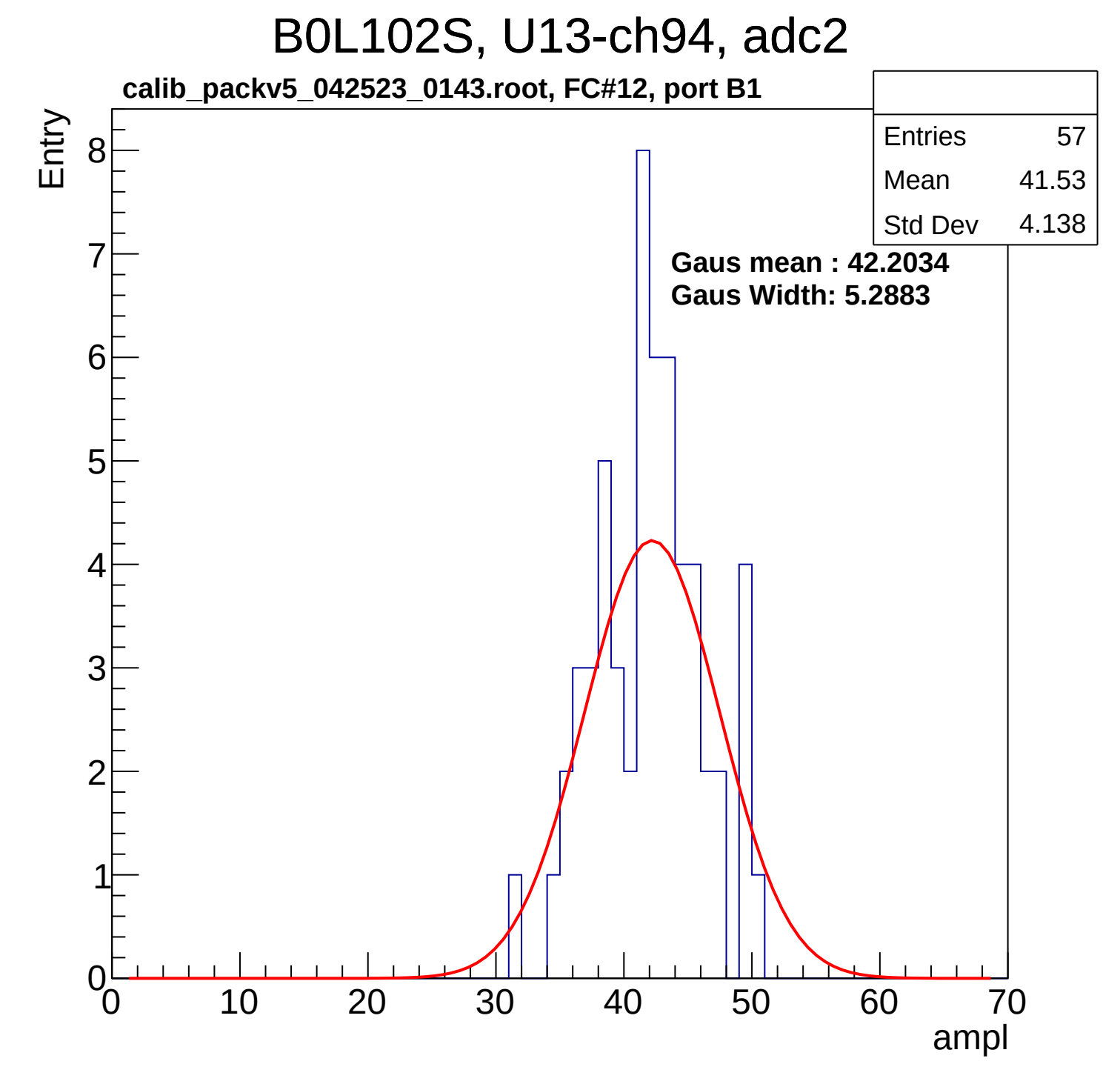

B0L102S, U13-ch93, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.79
Std Dev 9.974
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch93, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch93, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch94, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 27.69
Std Dev 6.034
Gaus mean : 29.1015
Gaus Width: 4.9589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch94, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 36.18
Std Dev 4.3
Gaus mean : 35.5465
Gaus Width: 5.1873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch94, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 41.53
Std Dev 4.138
Gaus mean : 42.2034
Gaus Width: 5.2883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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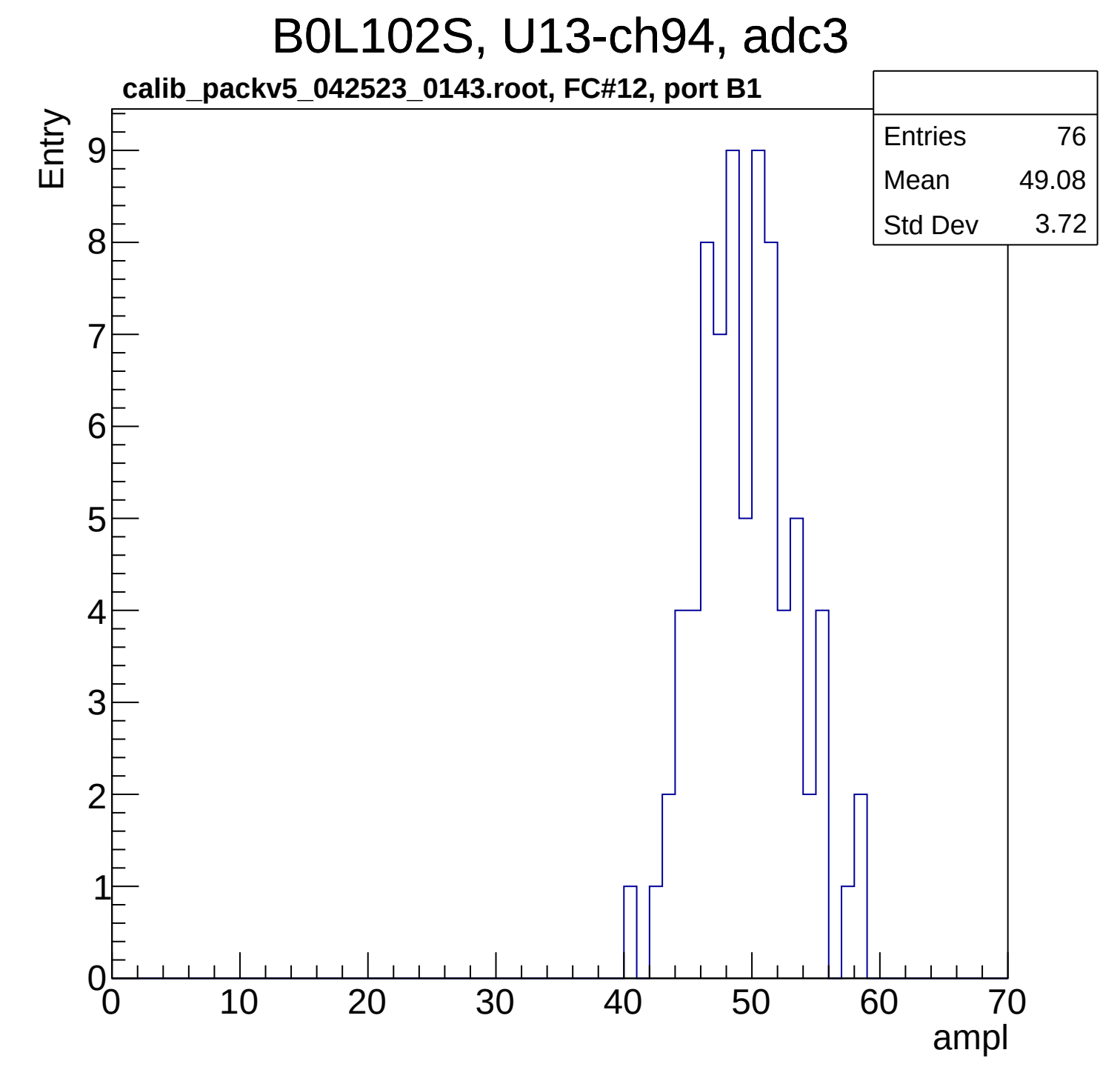
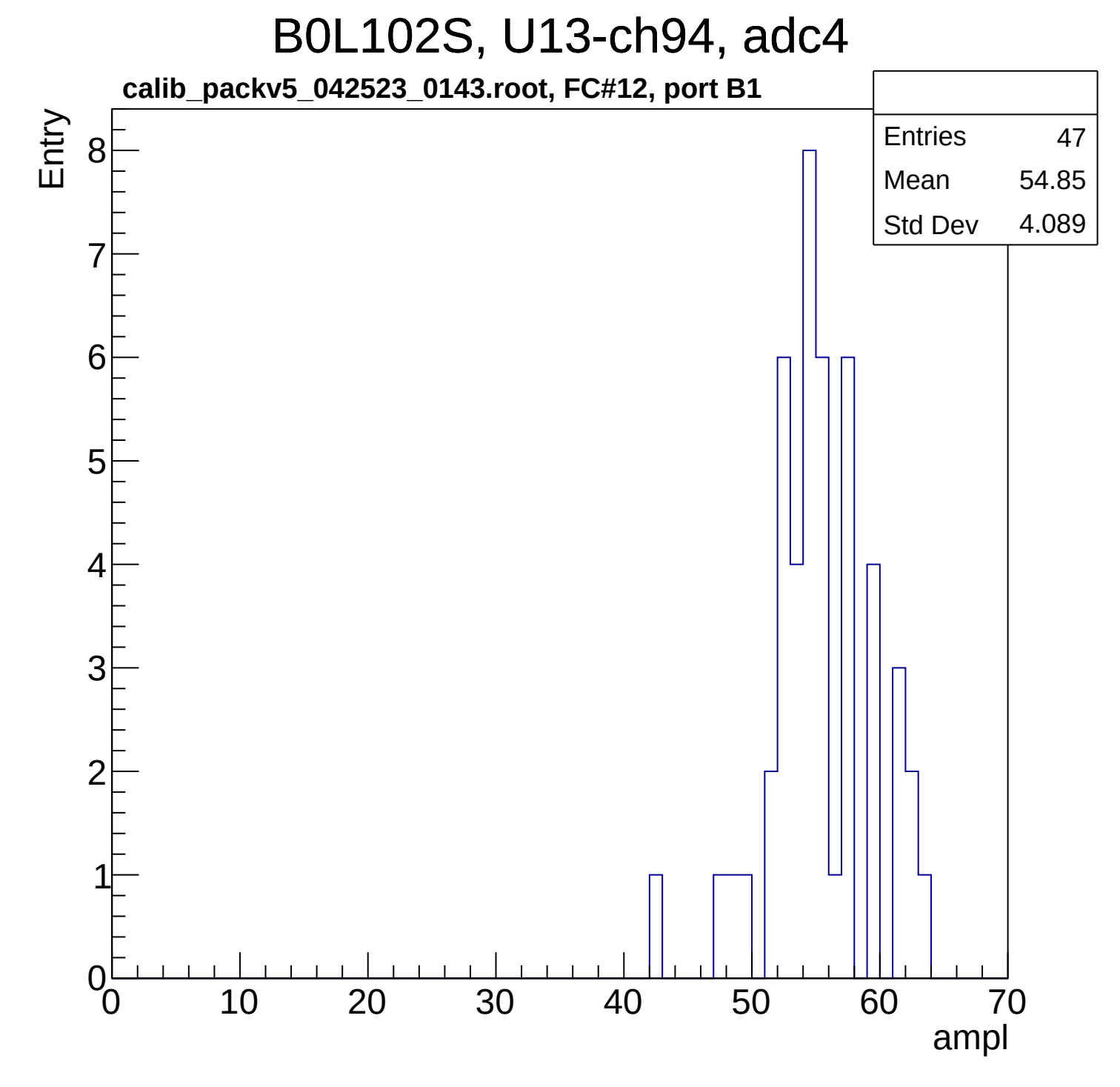
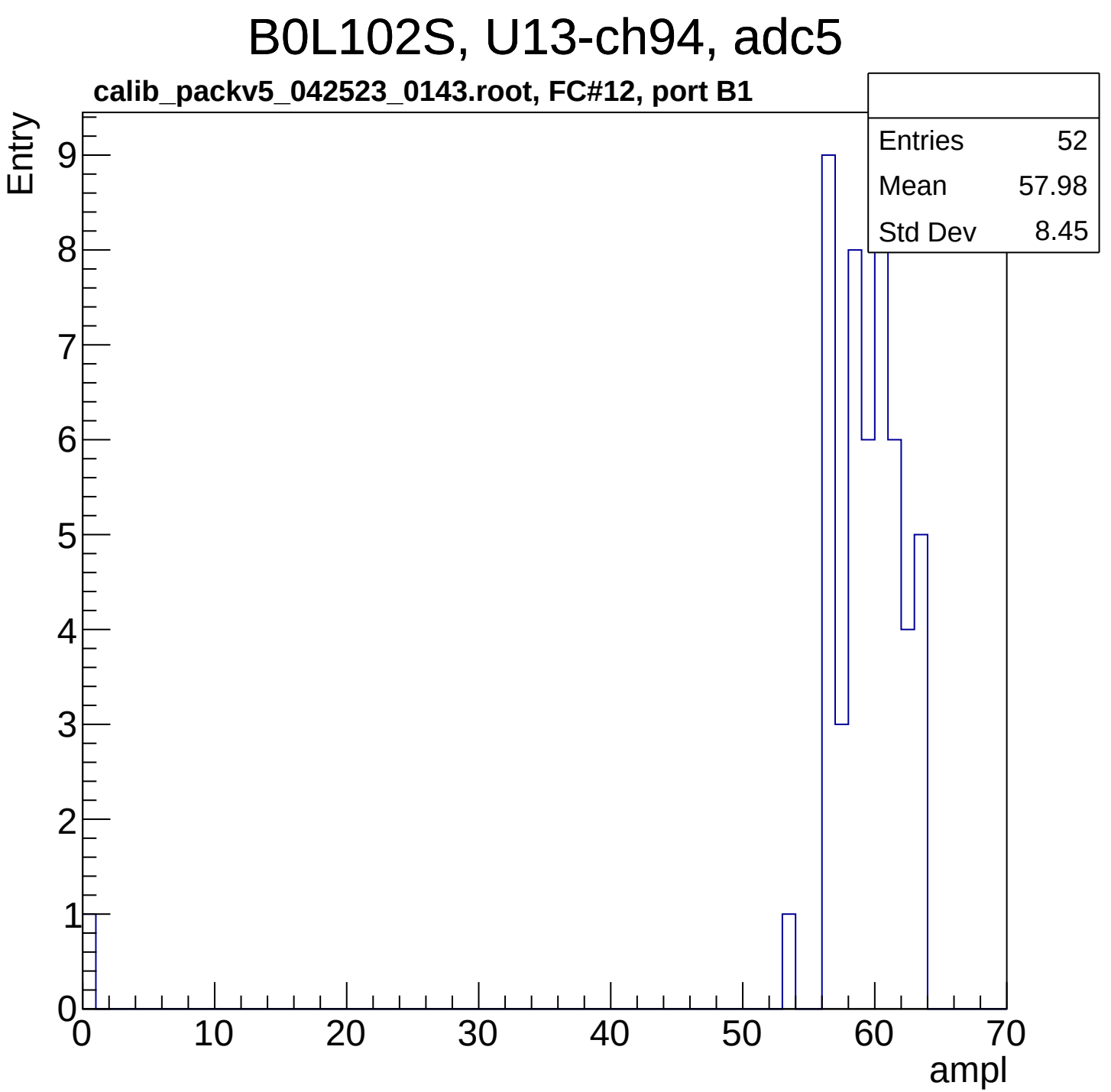

B0L102S, U13-ch94, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.08
Std Dev 3.72
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch94, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 54.85
Std Dev 4.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch94, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 57.98
Std Dev 8.45
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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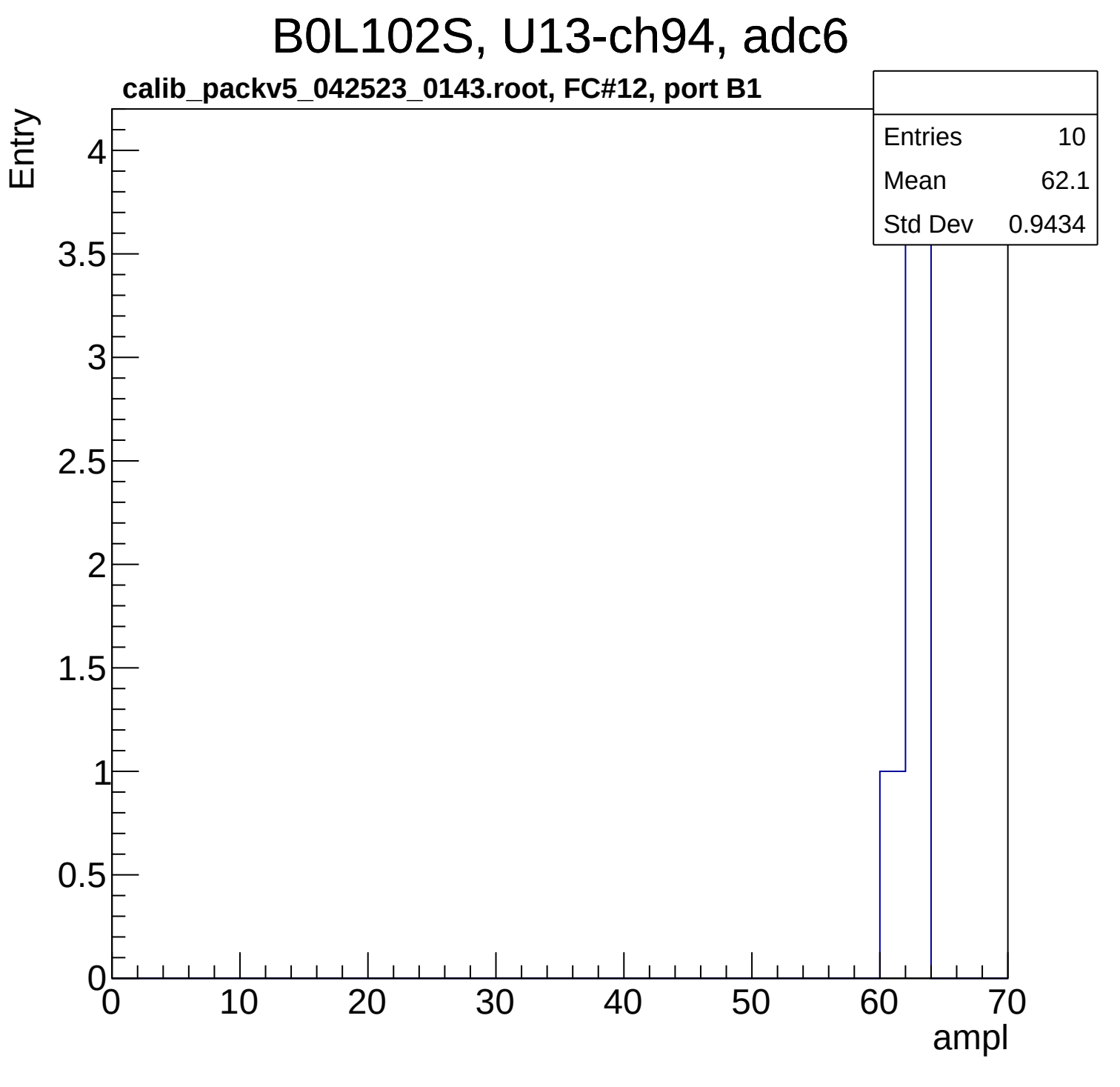
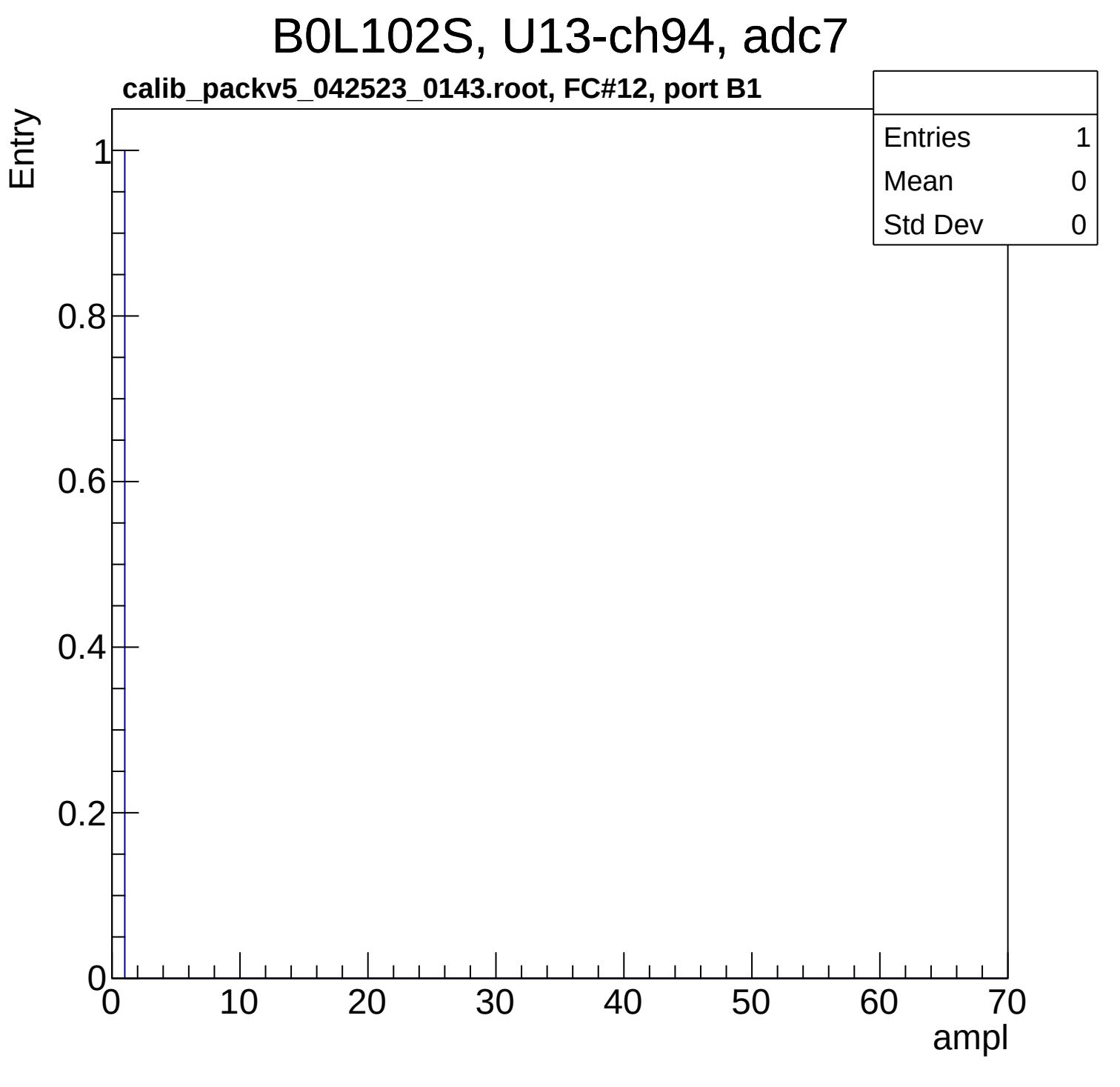
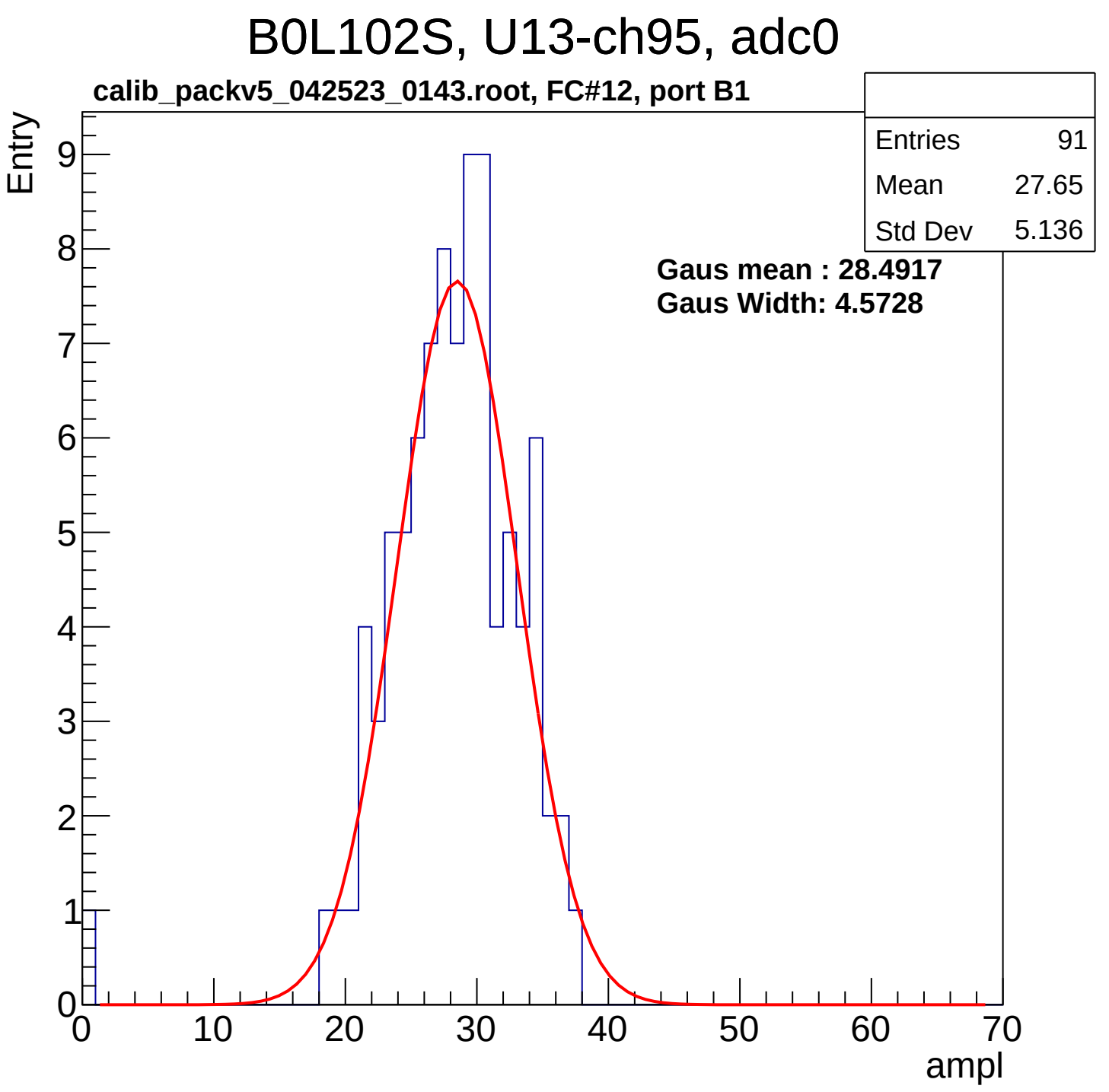
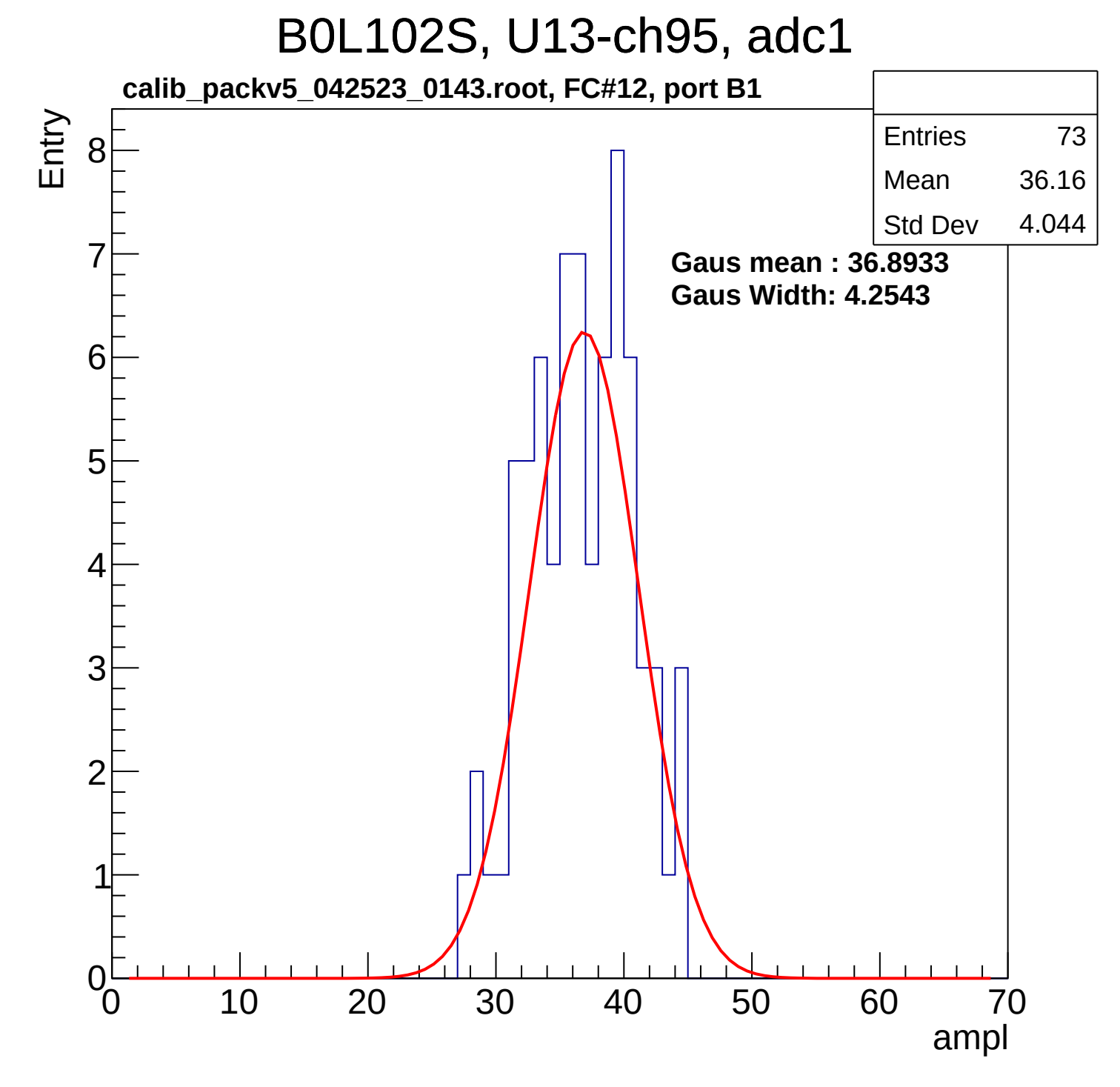
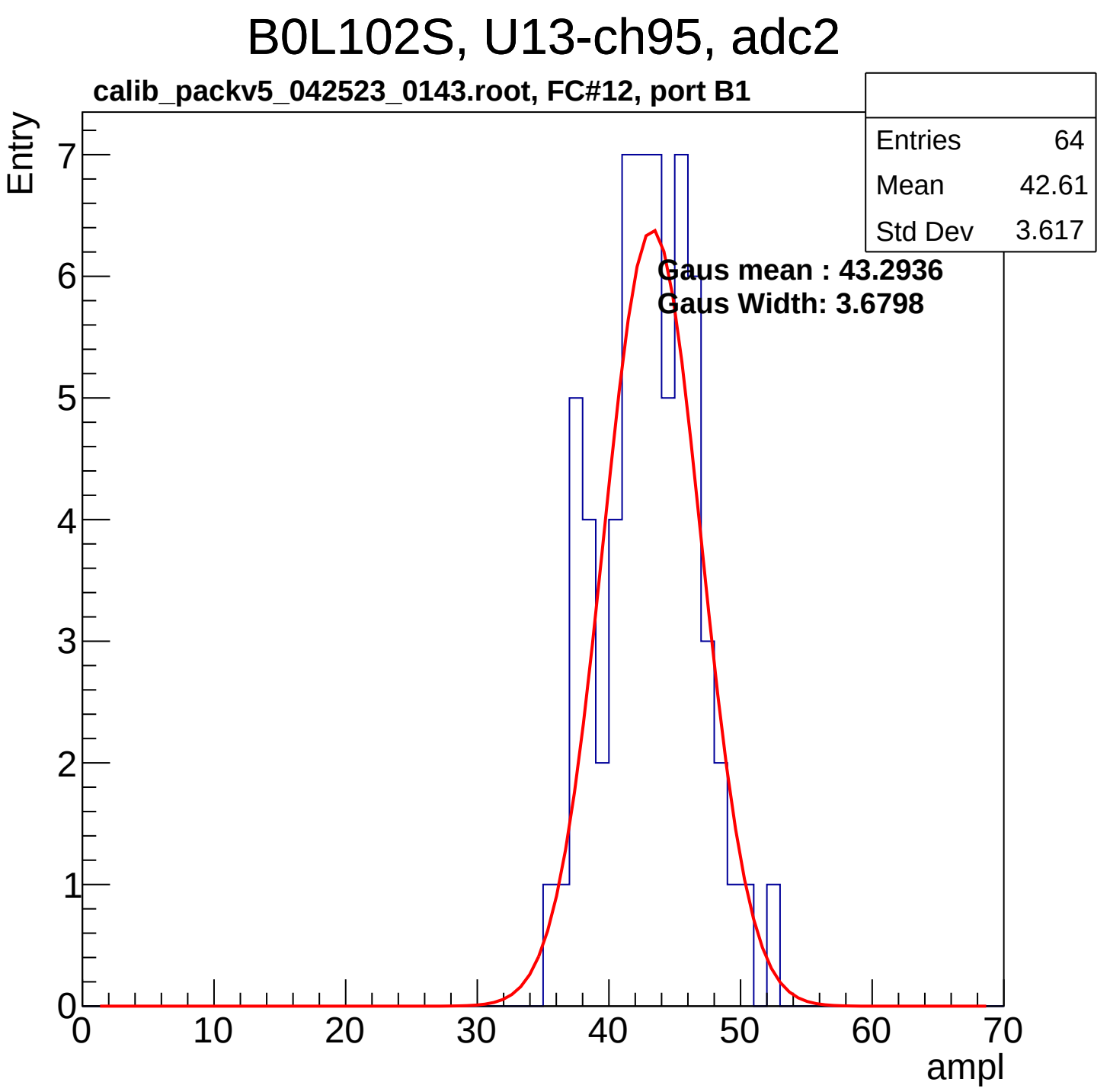
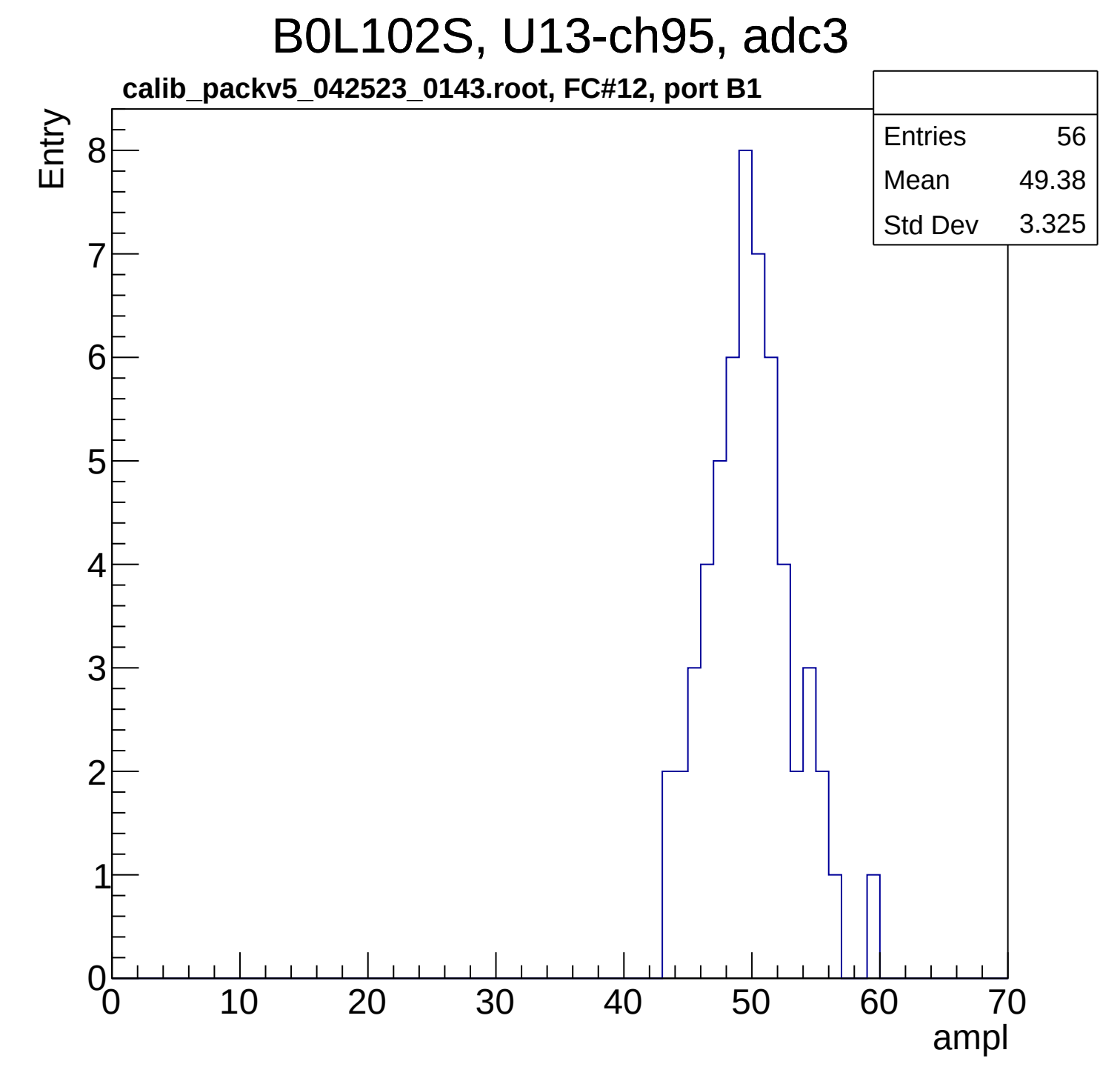

B0L102S, U13-ch94, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch94, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch95, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 91
Mean 27.65
Std Dev 5.136
Gaus mean : 28.4917
Gaus Width: 4.5728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.16
Std Dev 4.044
Gaus mean : 36.8933
Gaus Width: 4.2543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.61
Std Dev 3.617
Gaus mean : 43.2936
Gaus Width: 3.6798
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 49.38
Std Dev 3.325
Entry
ampl
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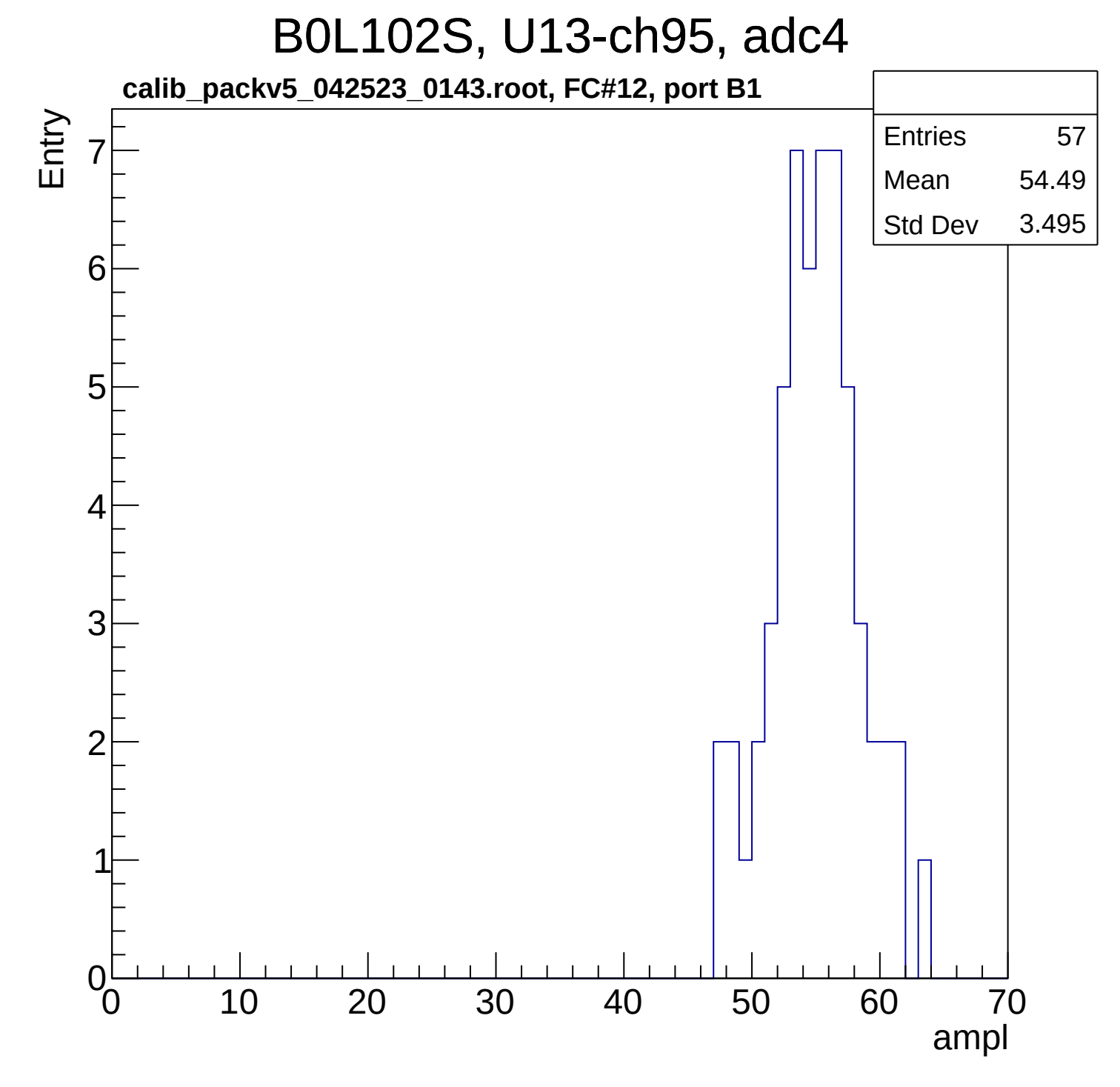
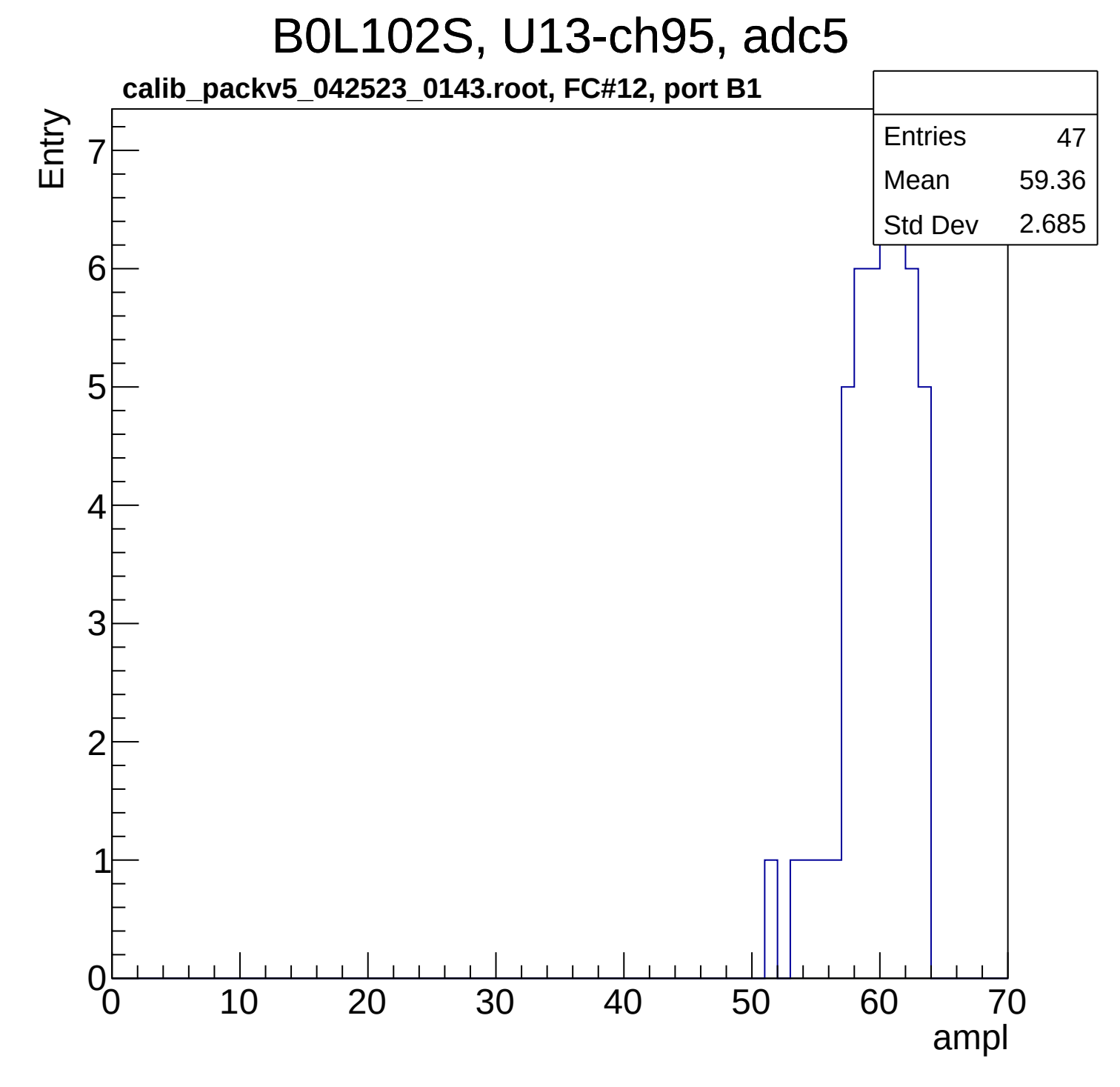
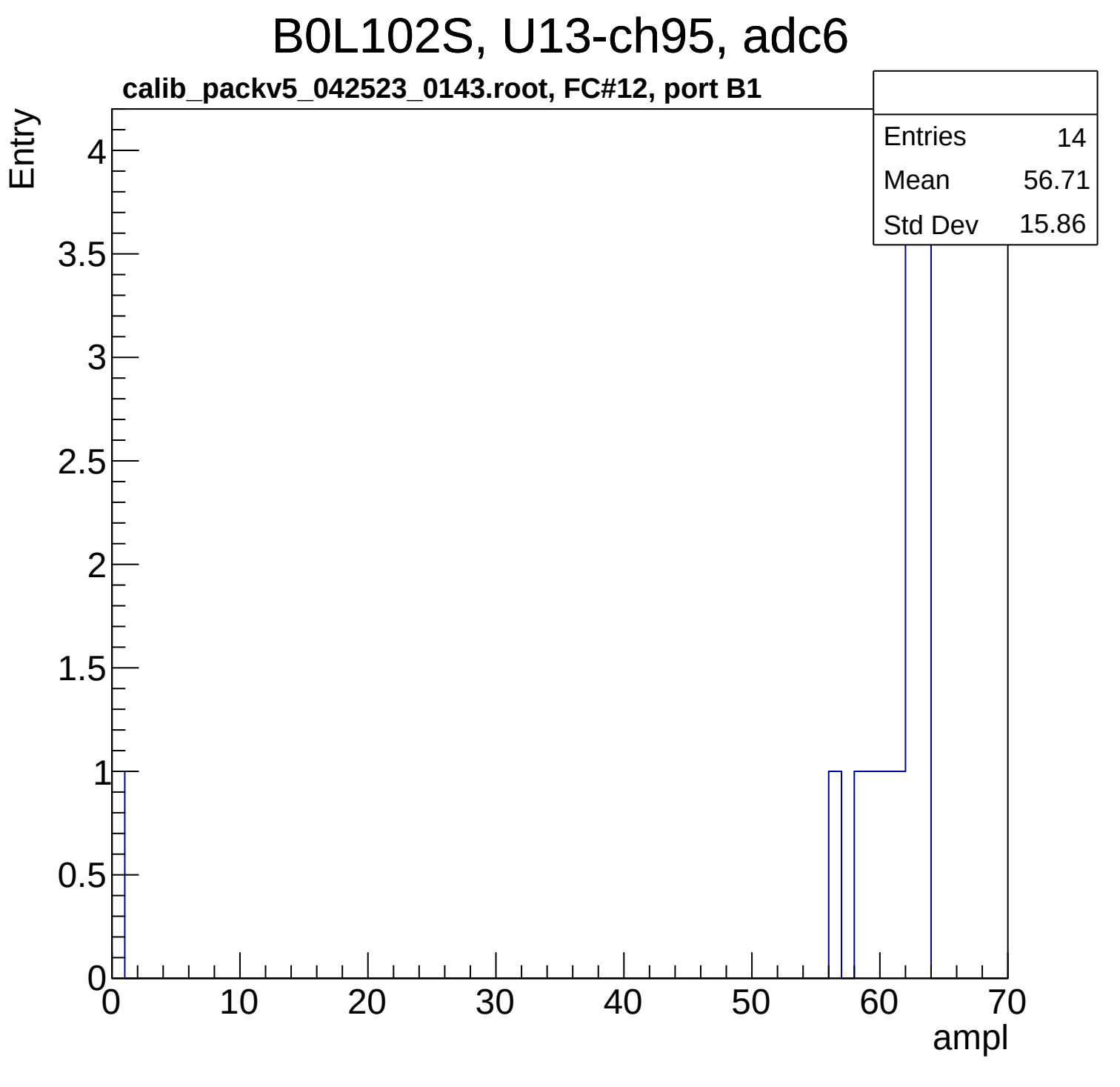
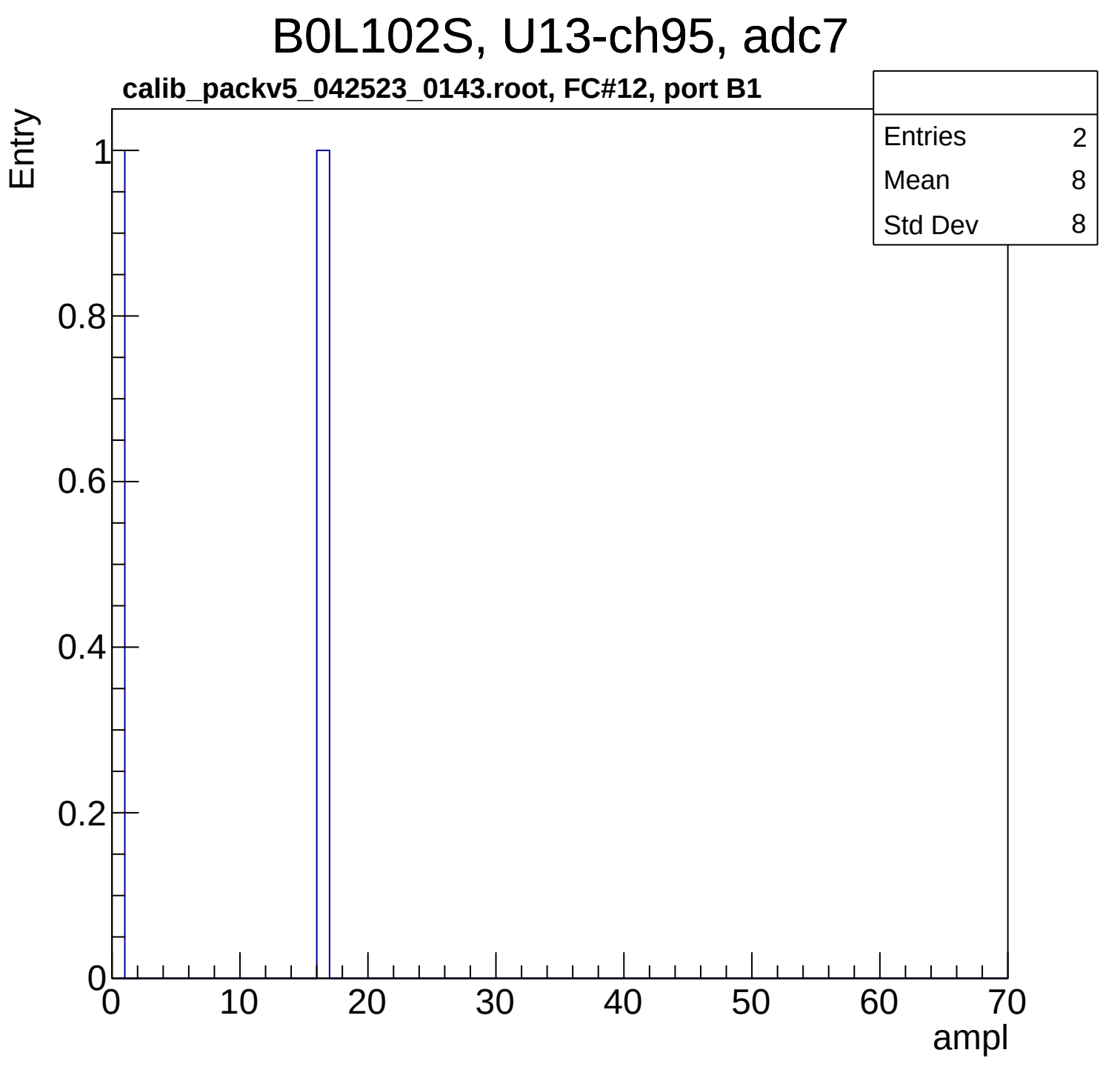
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.49
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.36
Std Dev 2.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.71
Std Dev 15.86
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch95, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 8
Std Dev 8
Entry
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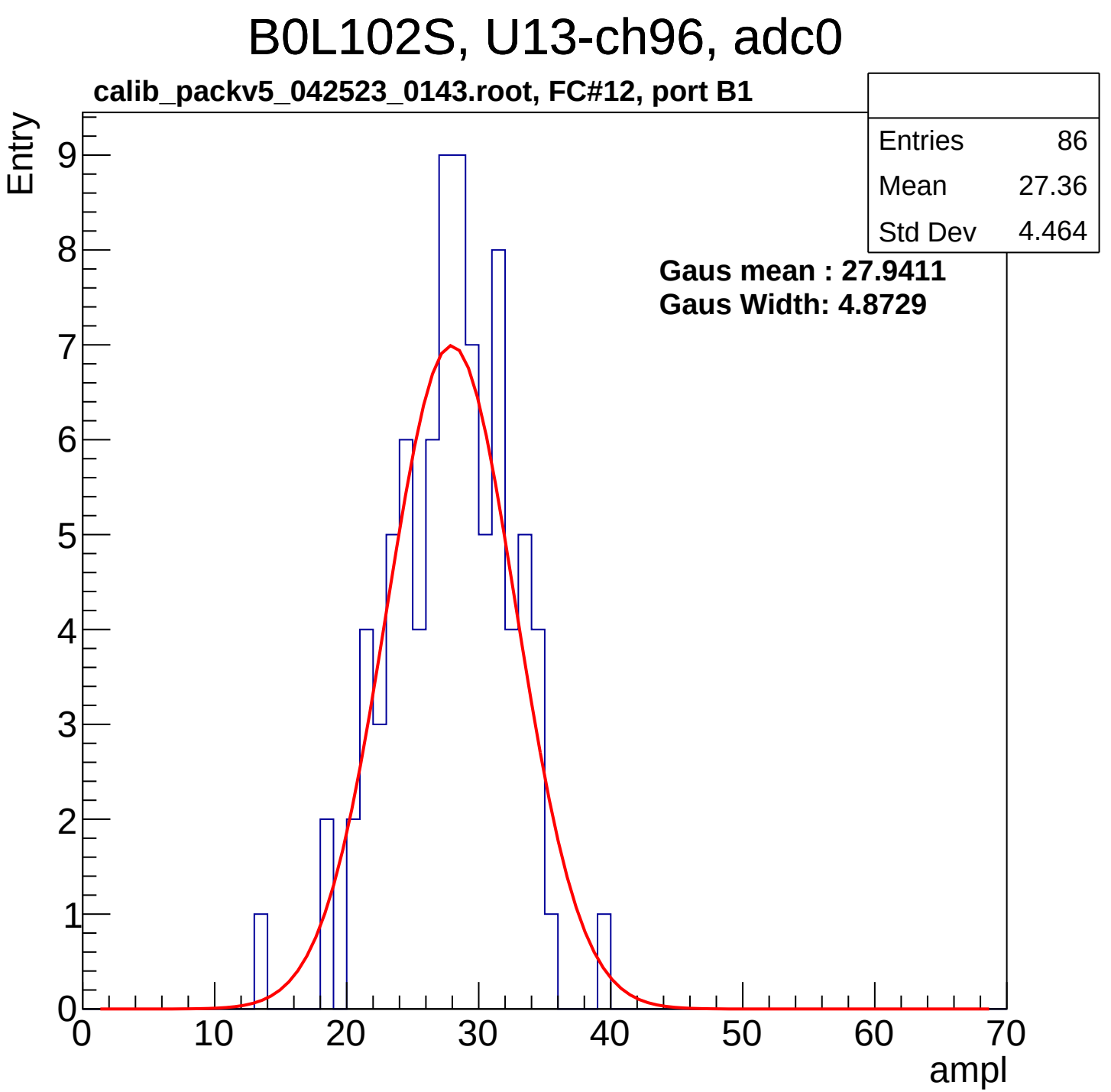
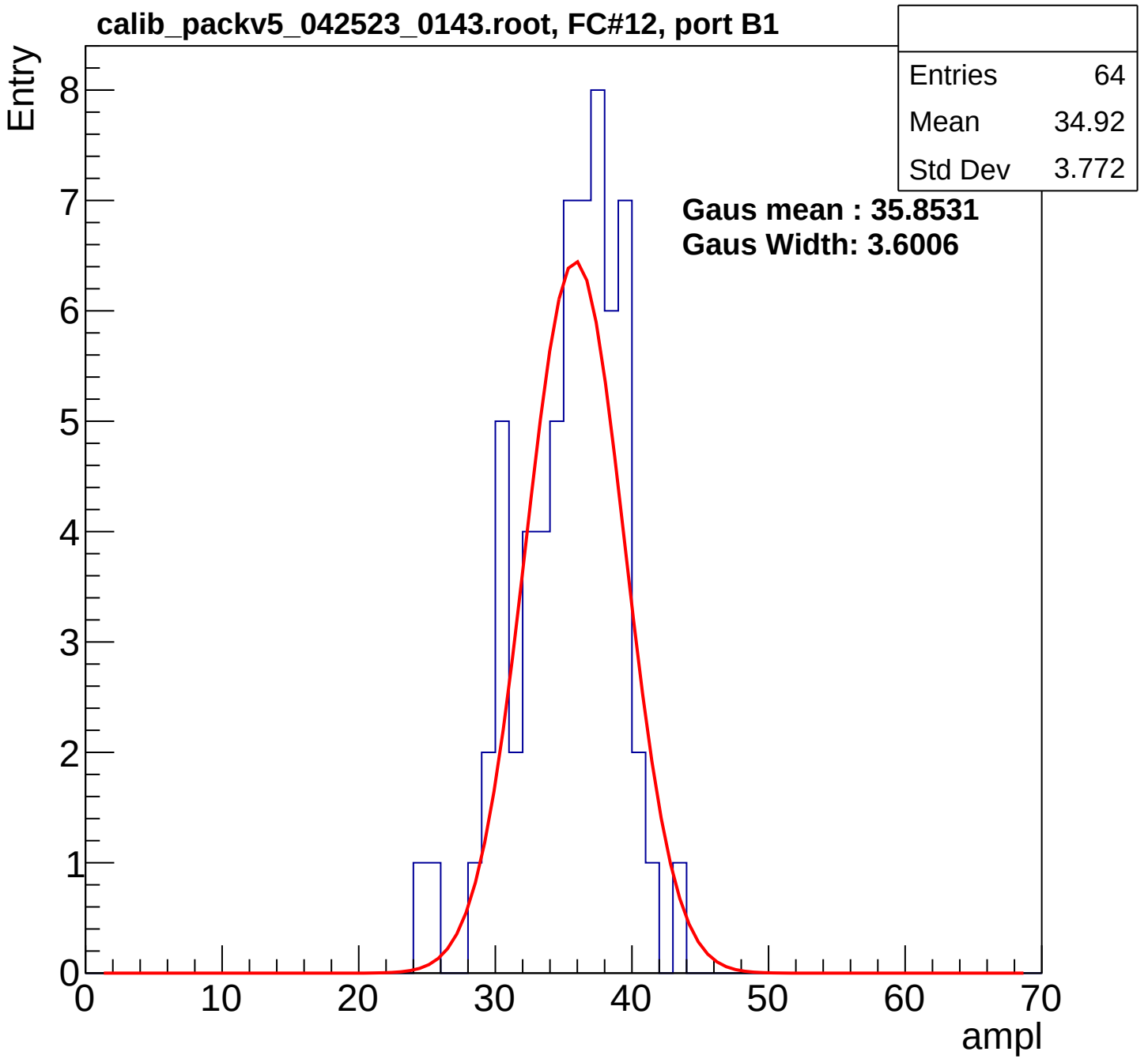
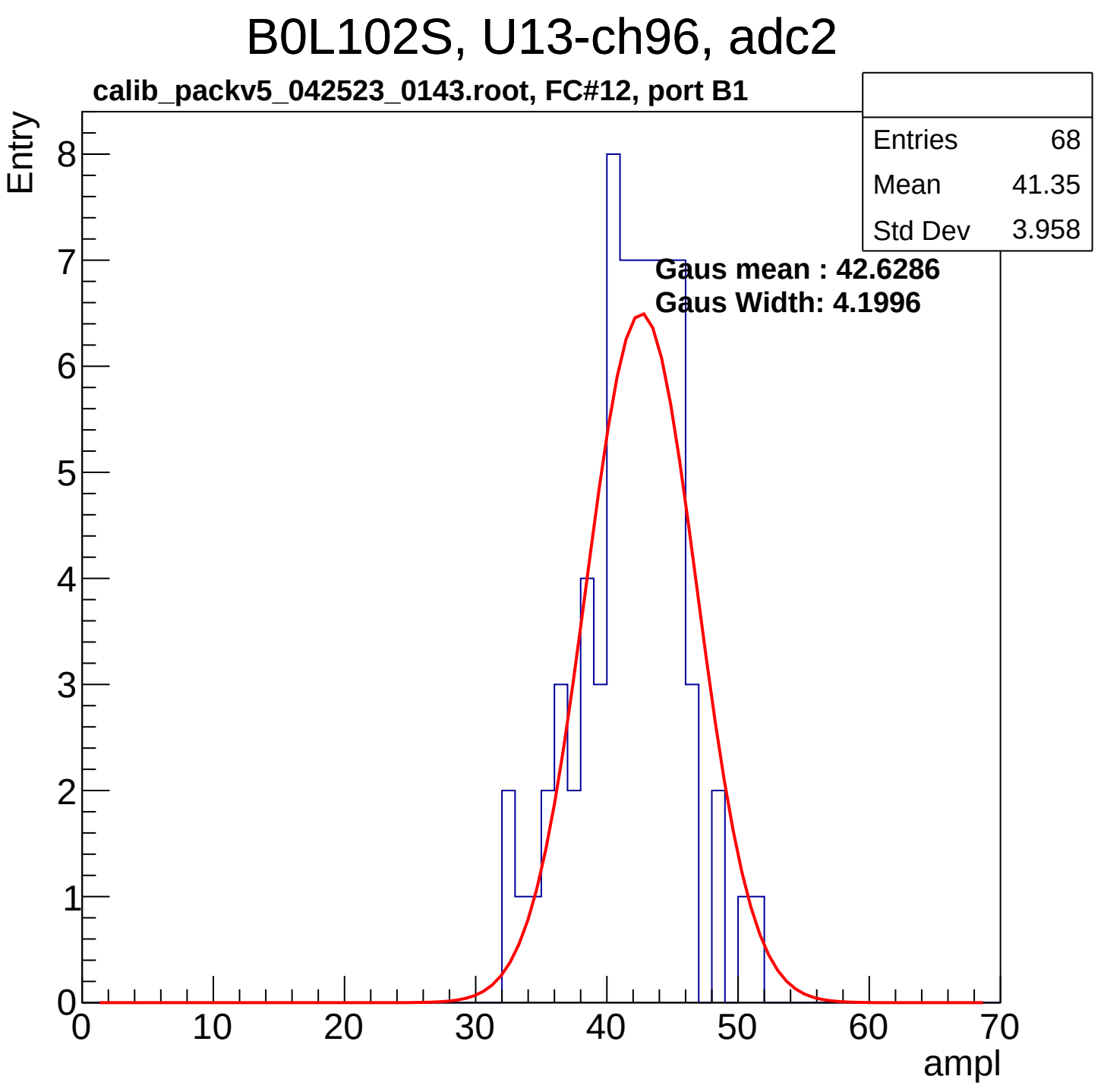
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch96, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 27.36
Std Dev 4.464
Gaus mean : 27.9411
Gaus Width: 4.8729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch96, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 34.92
Std Dev 3.772
Gaus mean : 35.8531
Gaus Width: 3.6006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch96, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.35
Std Dev 3.958
Gaus mean : 42.6286
Gaus Width: 4.1996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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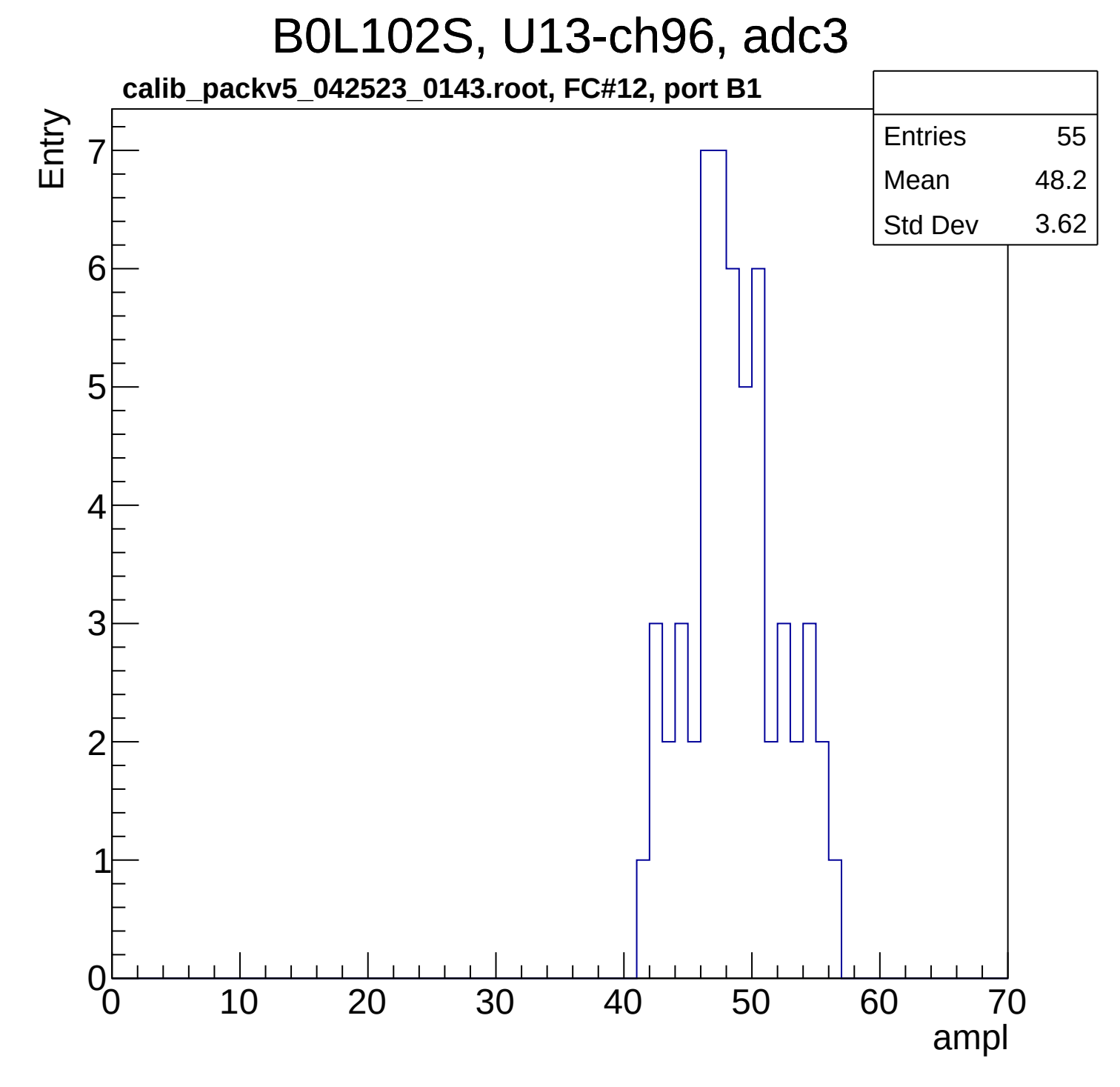
B0L102S, U13-ch96, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 48.2
Std Dev 3.62
Entry
0
1
2
3
4
5
6
7
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10
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30
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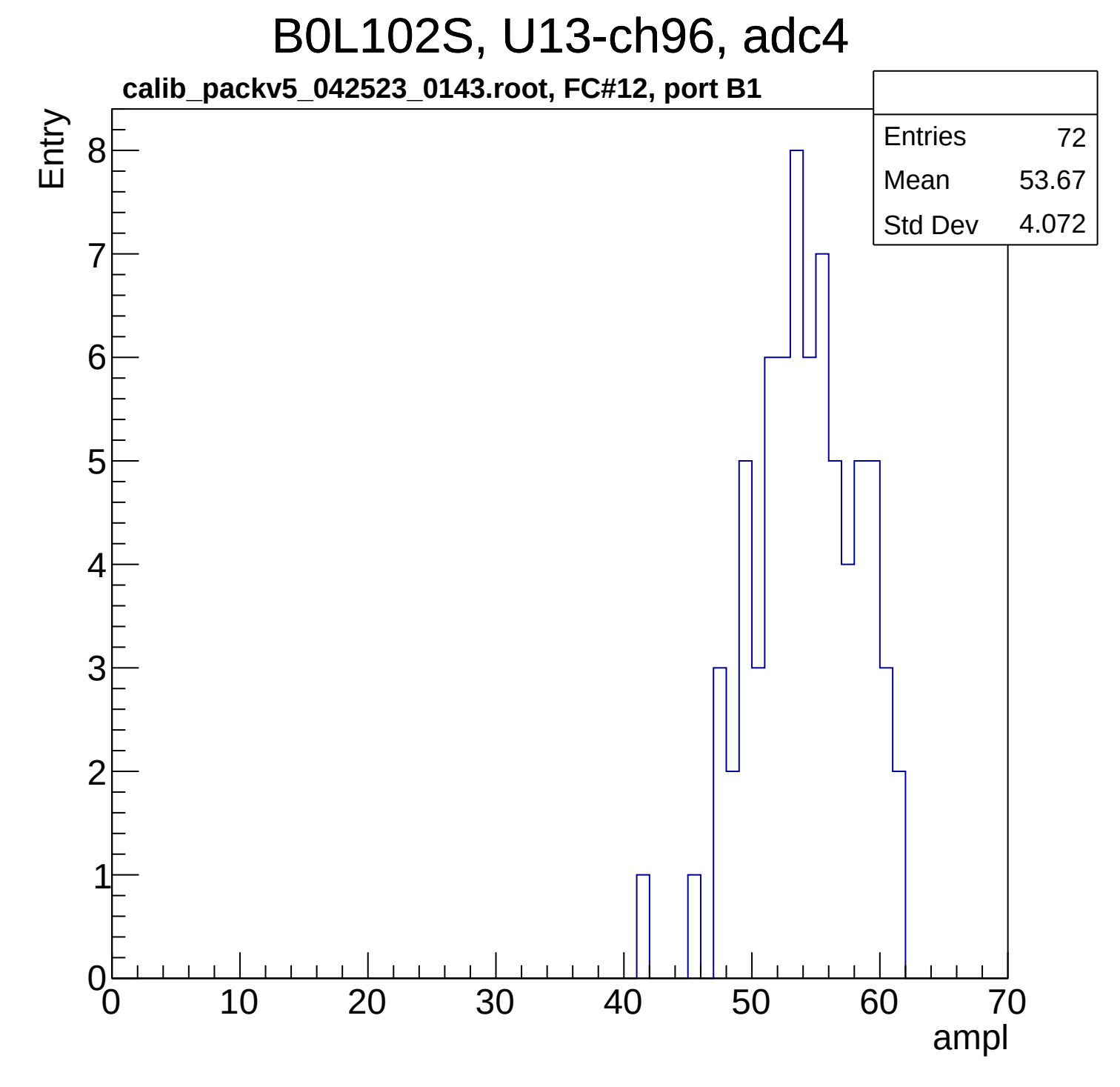
50
60
70
ampl


B0L102S, U13-ch96, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 53.67
Std Dev 4.072
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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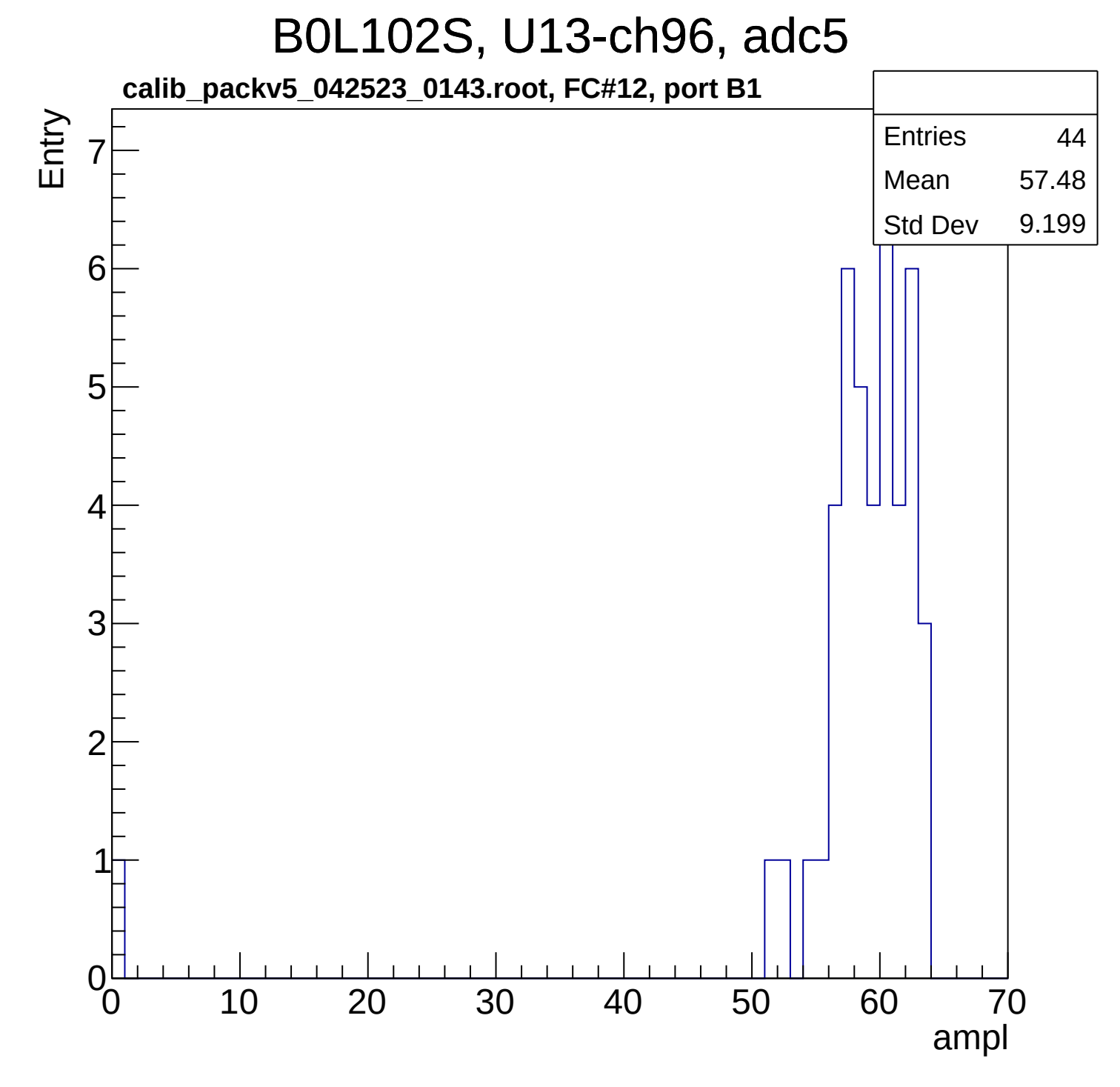
B0L102S, U13-ch96, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 57.48
Std Dev 9.199
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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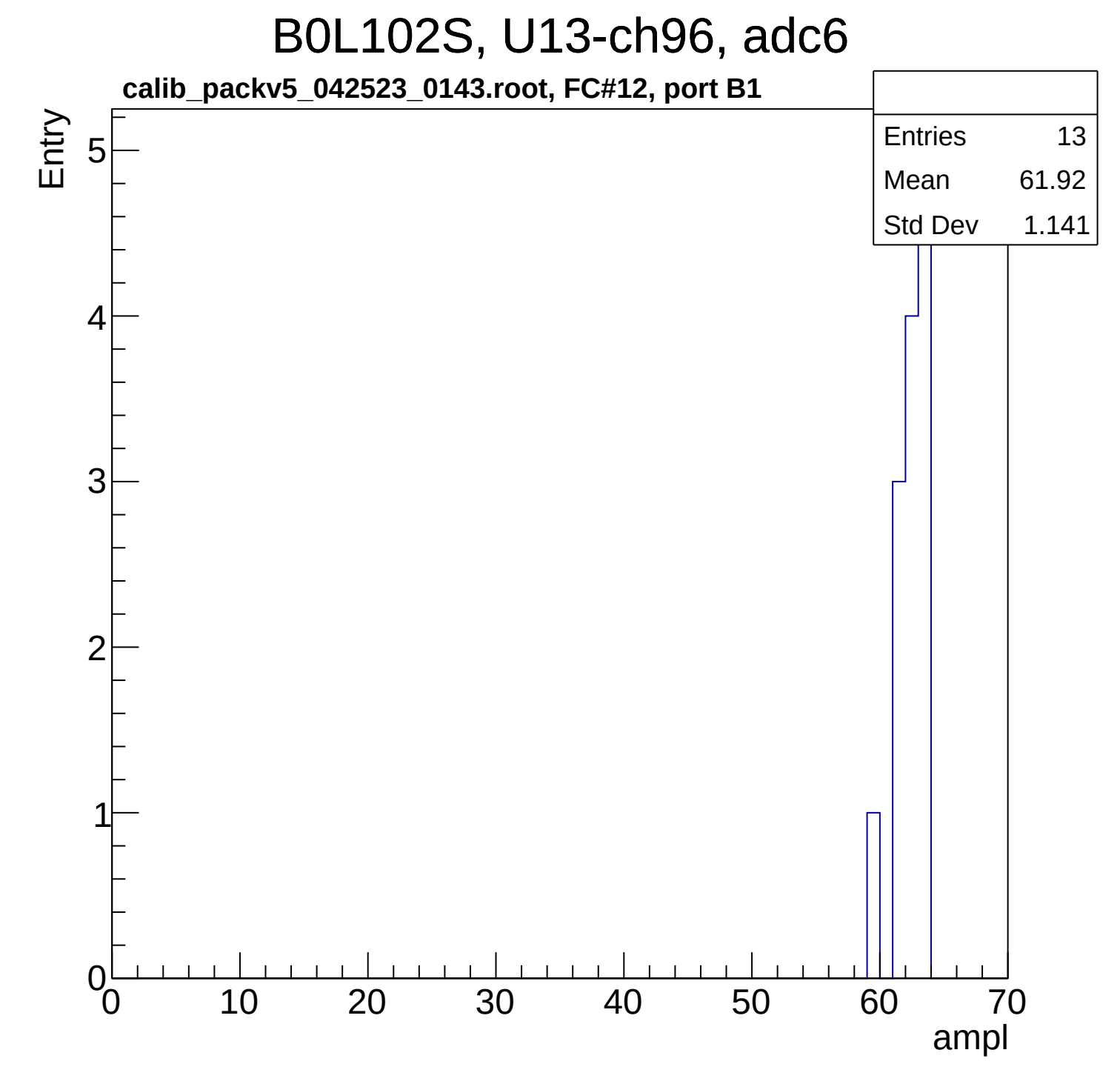
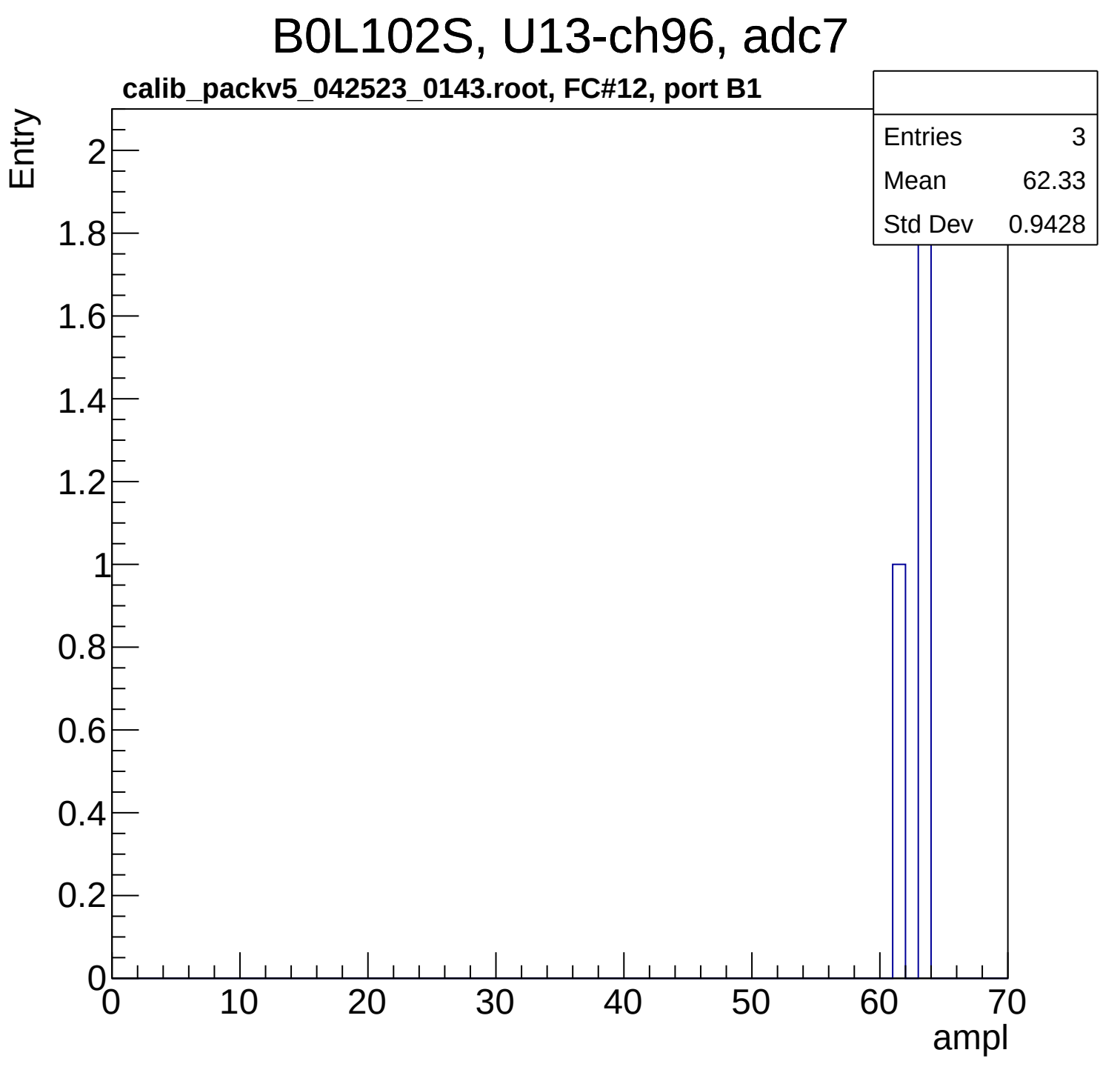

B0L102S, U13-ch96, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 61.92
Std Dev 1.141
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U13-ch96, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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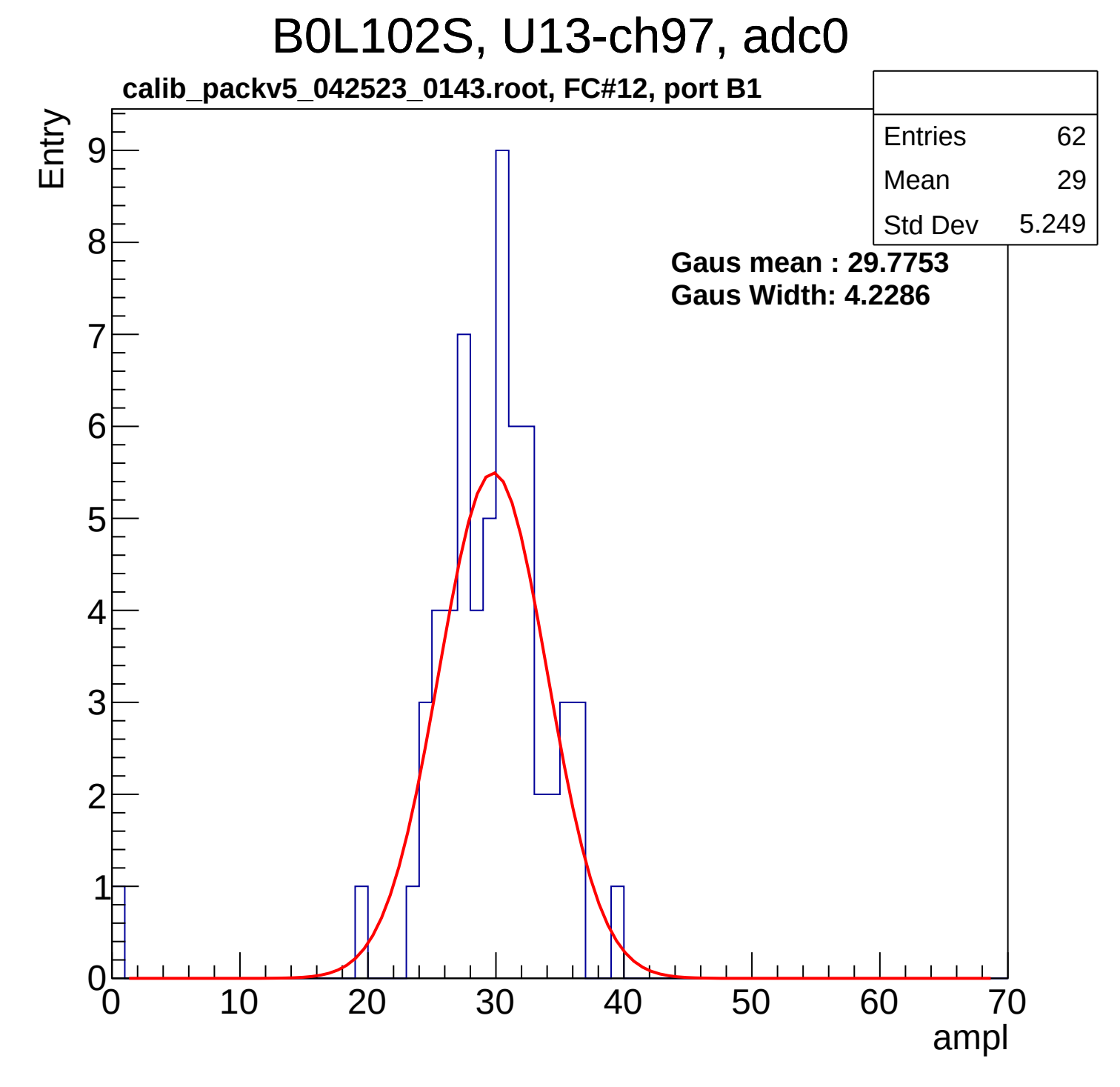
B0L102S, U13-ch97, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29
Std Dev 5.249
Gaus mean : 29.7753
Gaus Width: 4.2286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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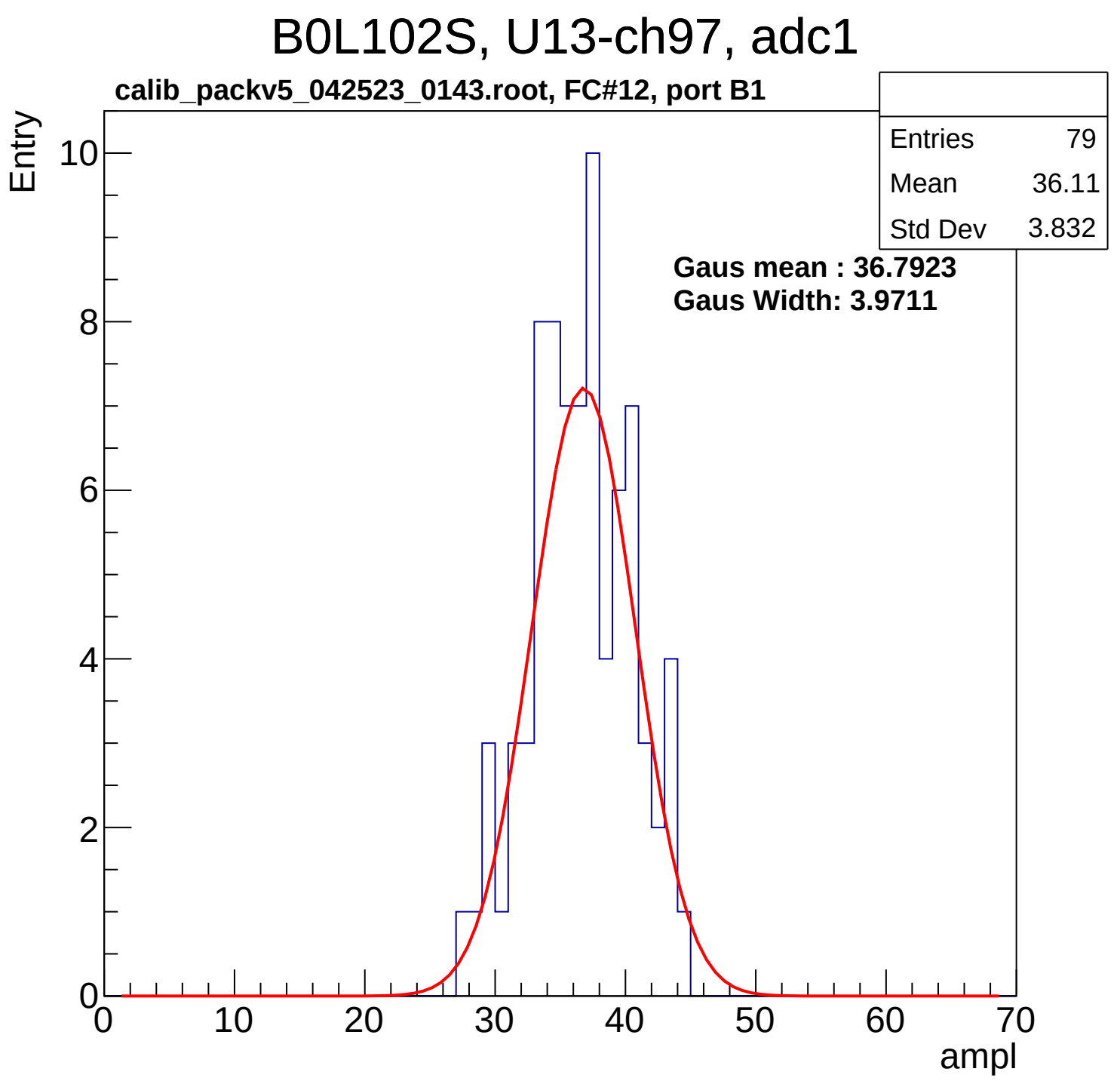
B0L102S, U13-ch97, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 36.11
Std Dev 3.832
Gaus mean : 36.7923
Gaus Width: 3.9711
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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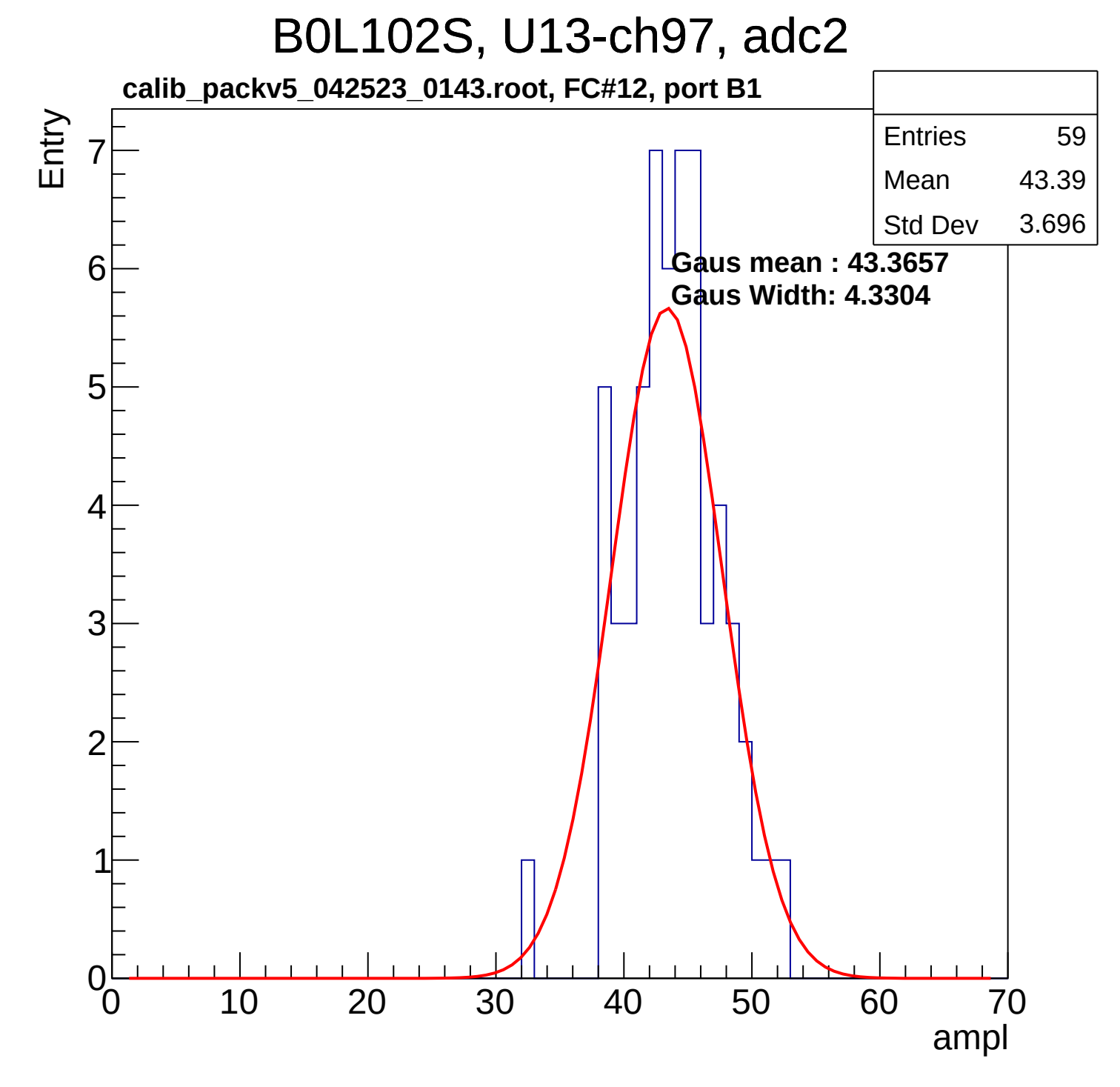
B0L102S, U13-ch97, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.39
Std Dev 3.696
Gaus mean : 43.3657
Gaus Width: 4.3304
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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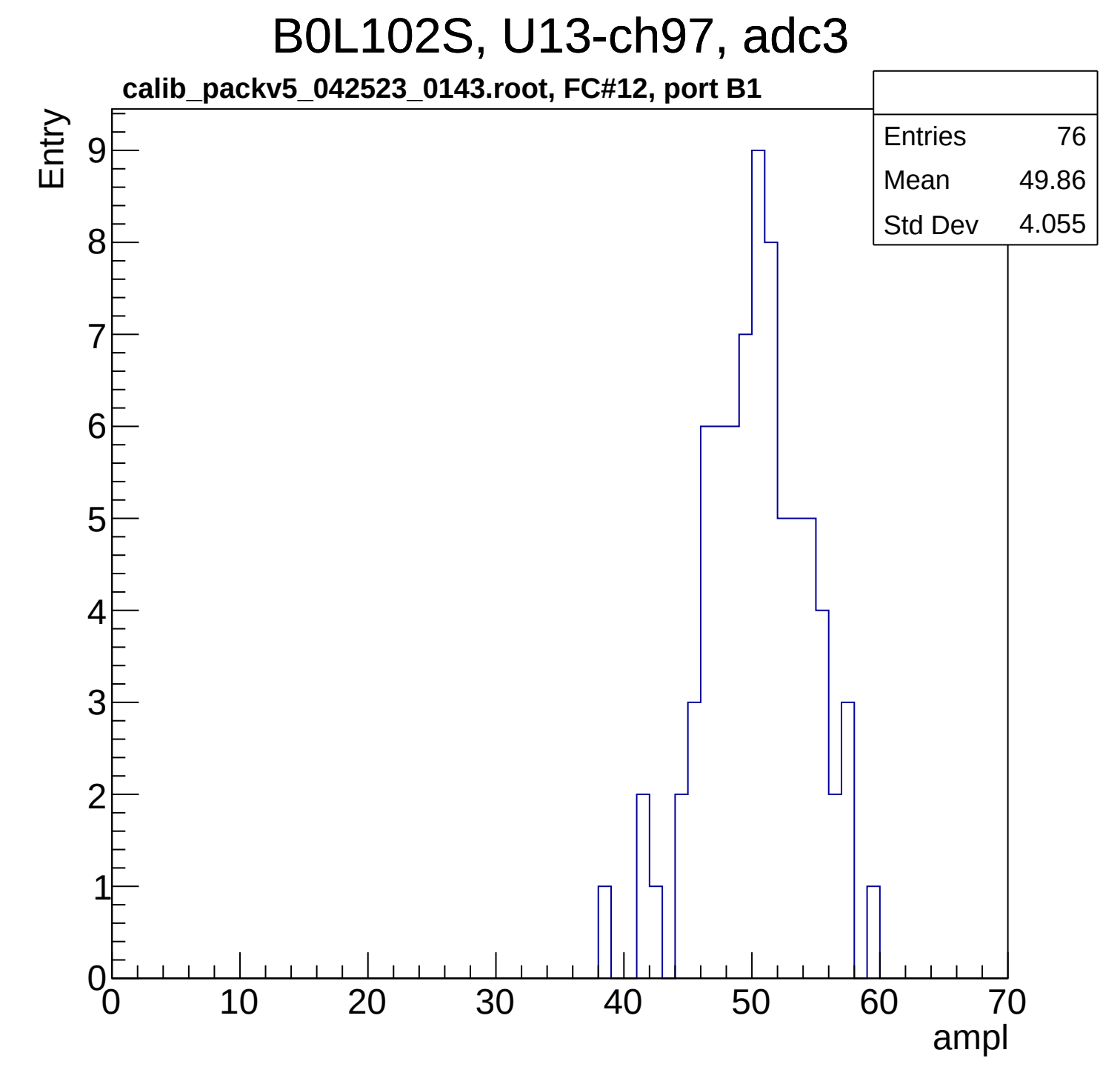
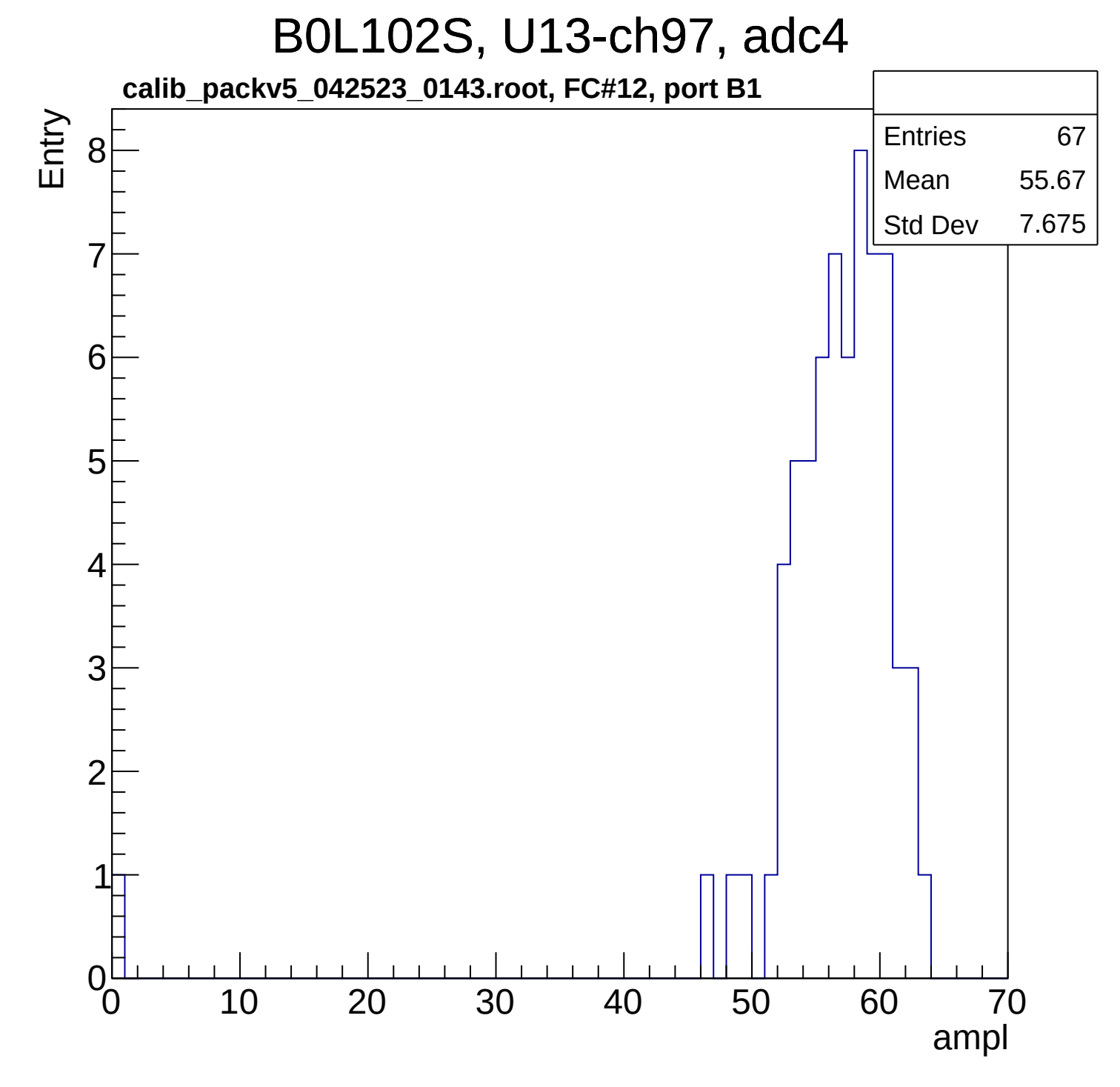

B0L102S, U13-ch97, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.86
Std Dev 4.055
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch97, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.67
Std Dev 7.675
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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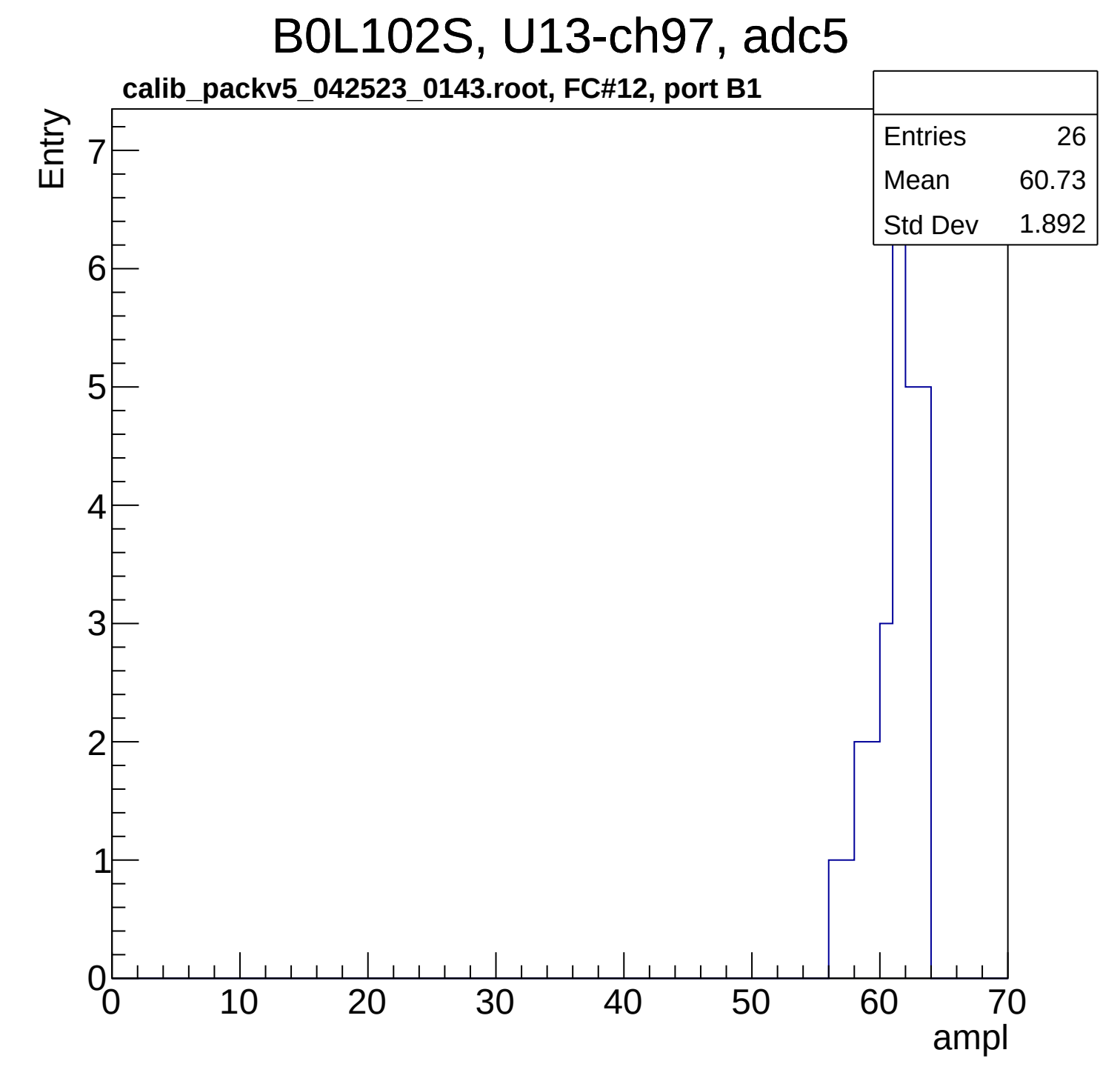
B0L102S, U13-ch97, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 26
Mean 60.73
Std Dev 1.892
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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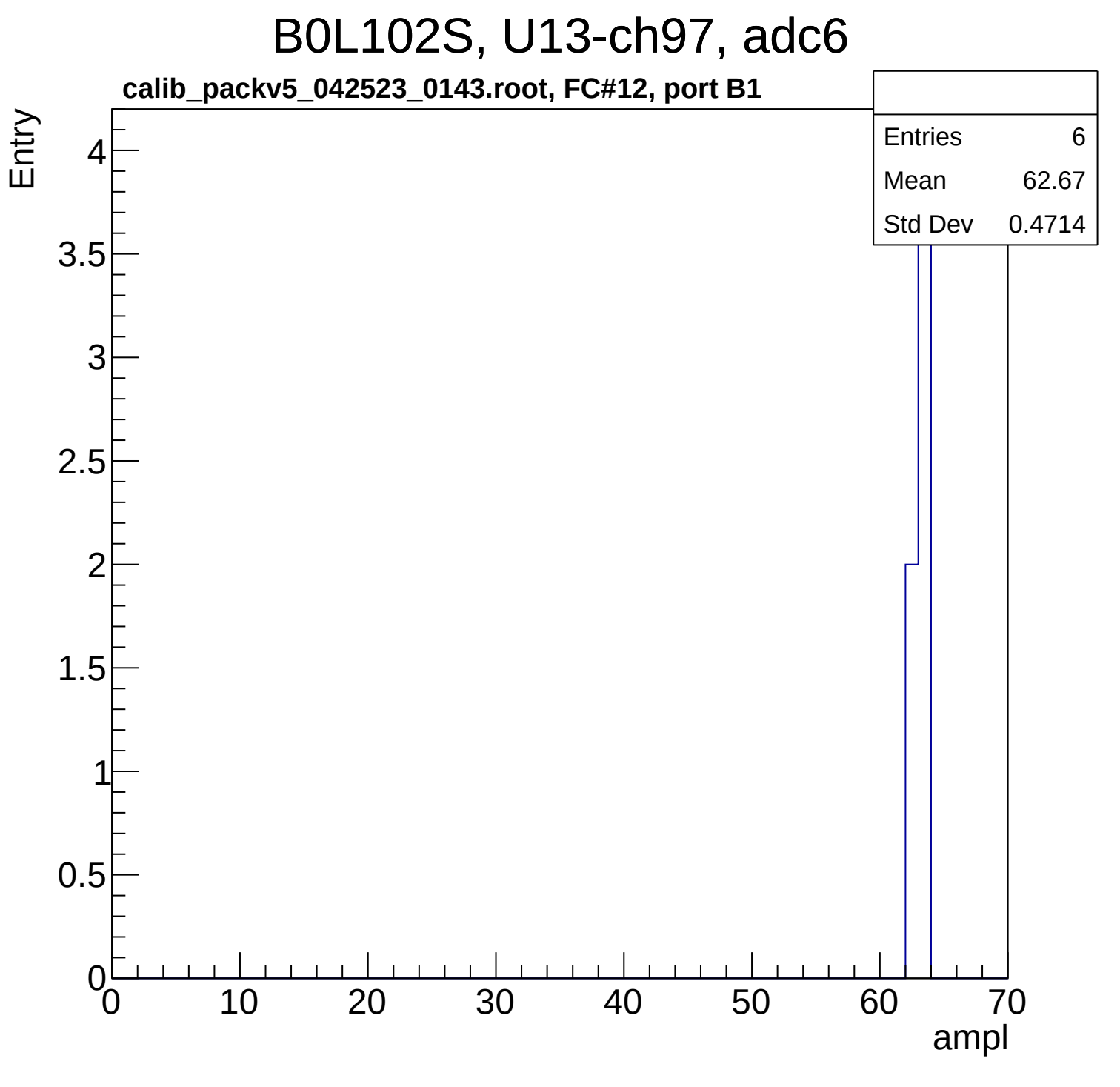
B0L102S, U13-ch97, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
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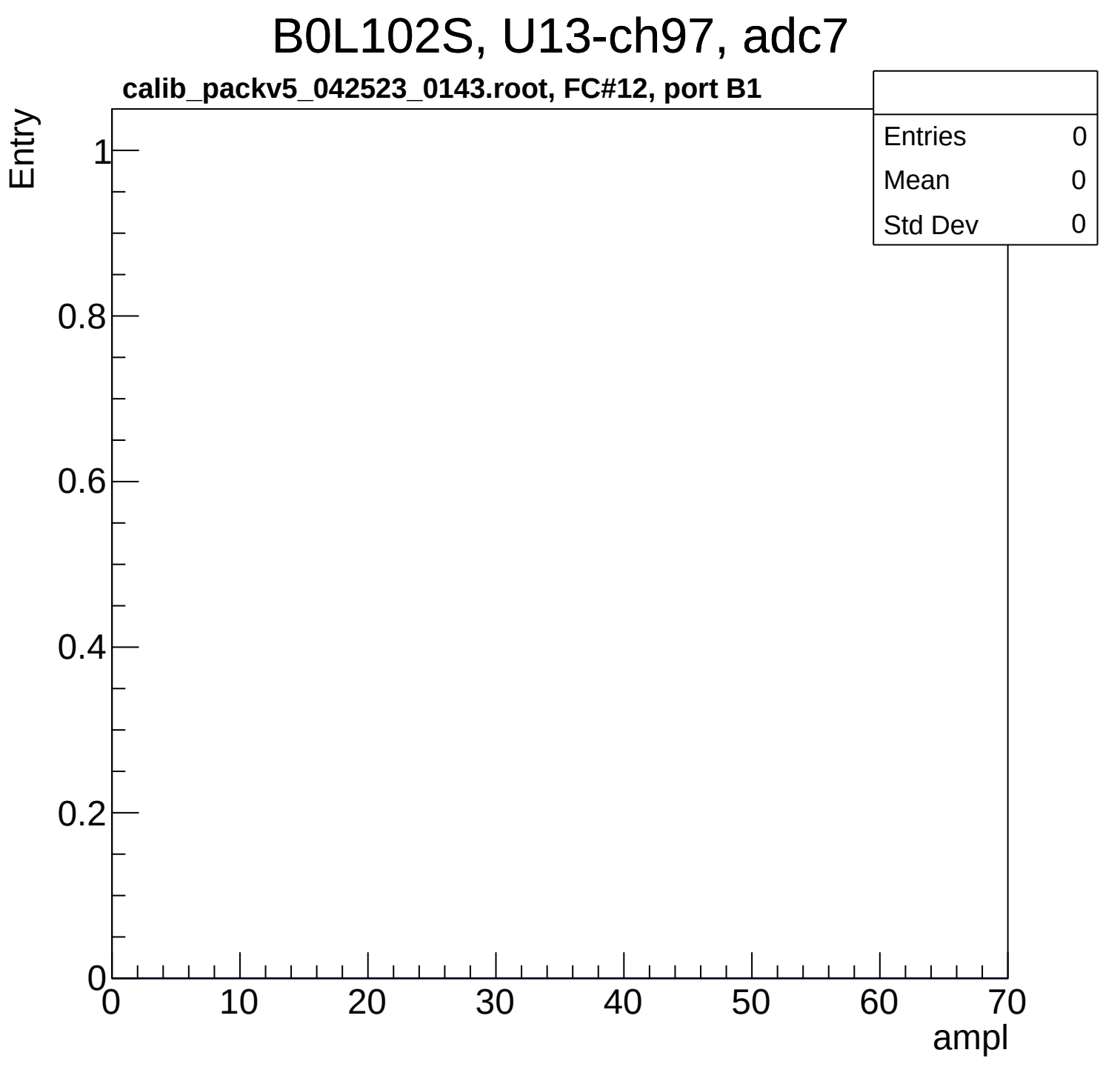
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch97, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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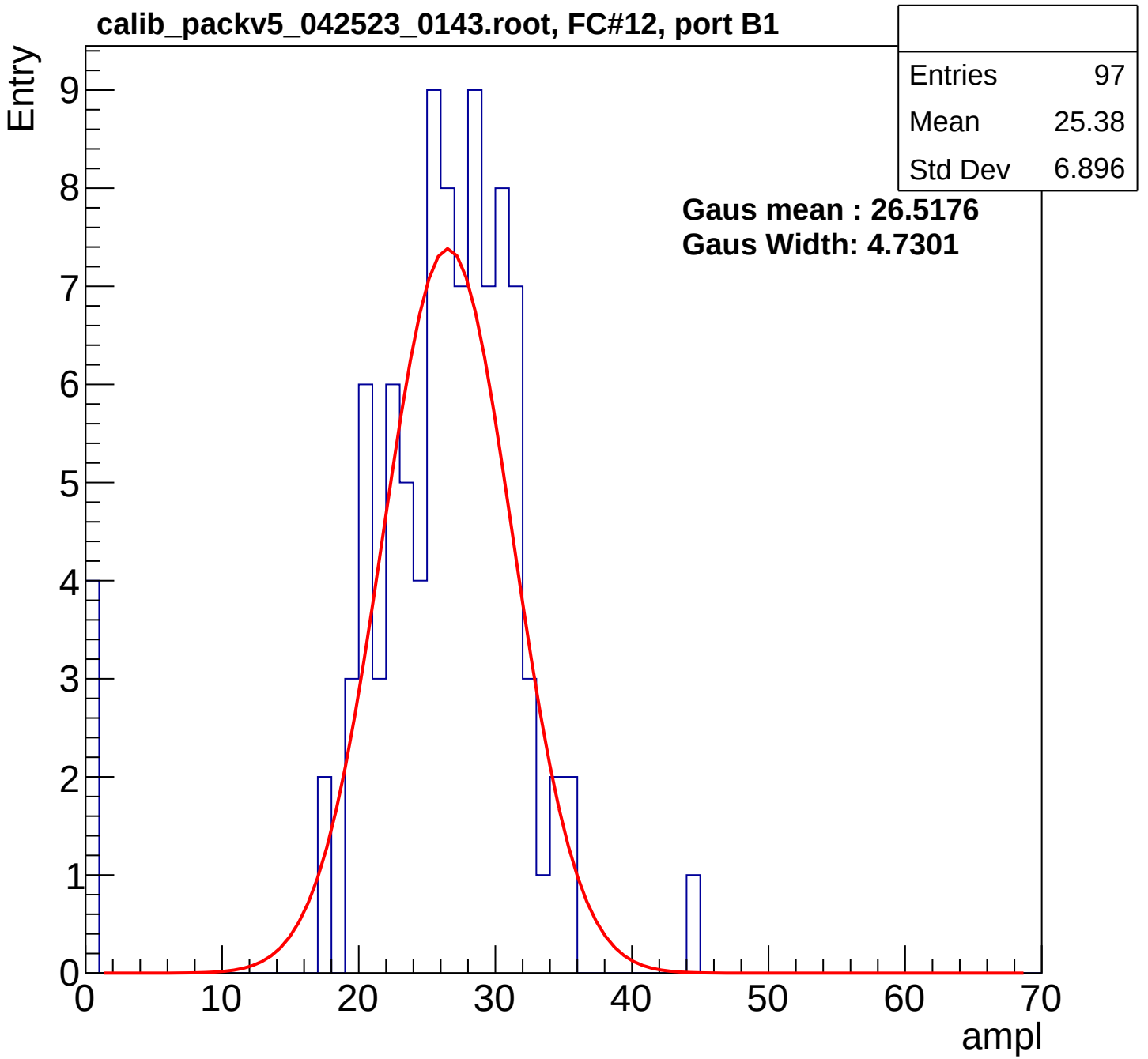
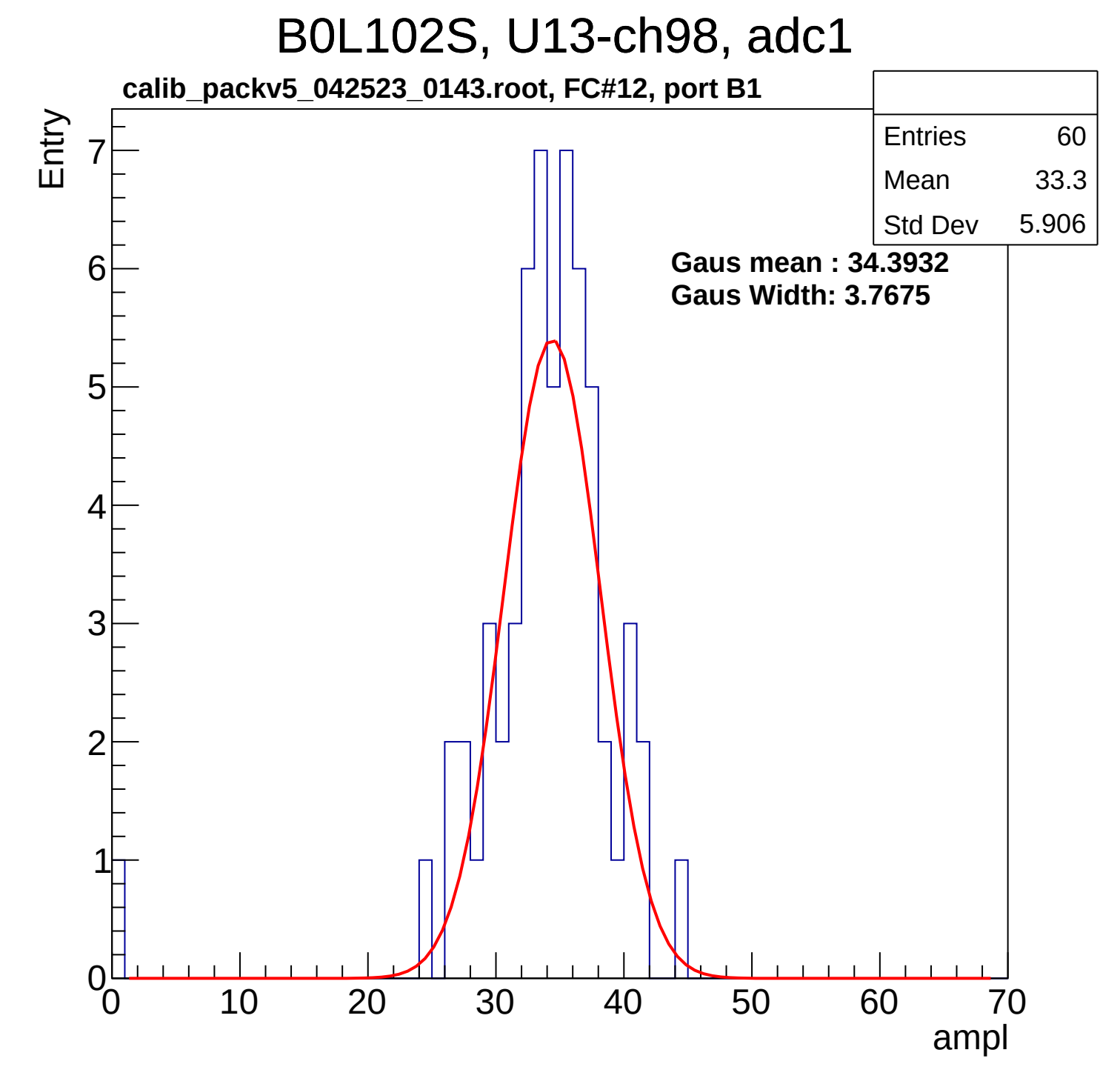
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch98, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 97
Mean 25.38
Std Dev 6.896
Gaus mean : 26.5176
Gaus Width: 4.7301
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch98, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 33.3
Std Dev 5.906
Gaus mean : 34.3932
Gaus Width: 3.7675
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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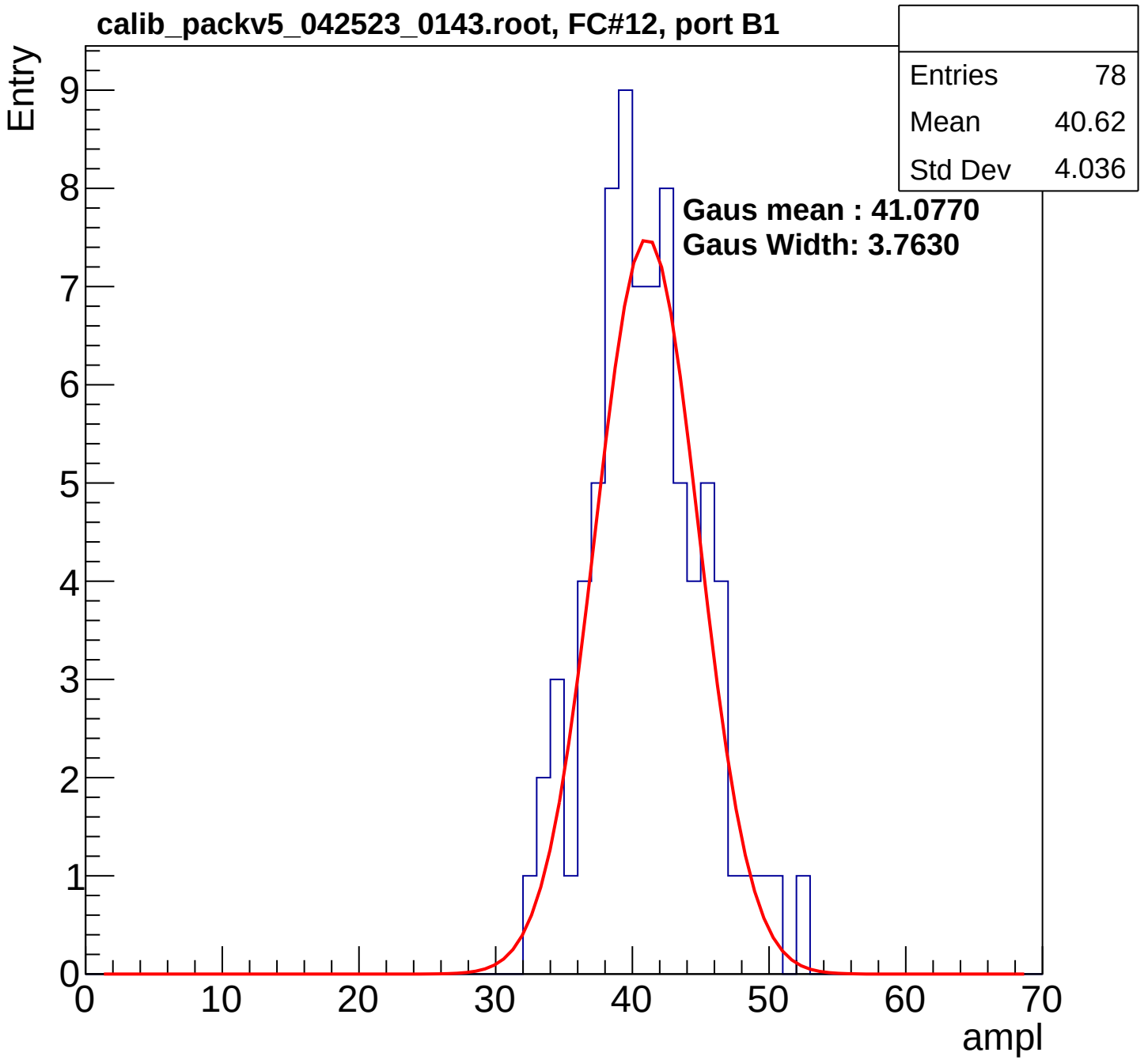
B0L102S, U13-ch98, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 40.62
Std Dev 4.036
Gaus mean : 41.0770
Gaus Width: 3.7630
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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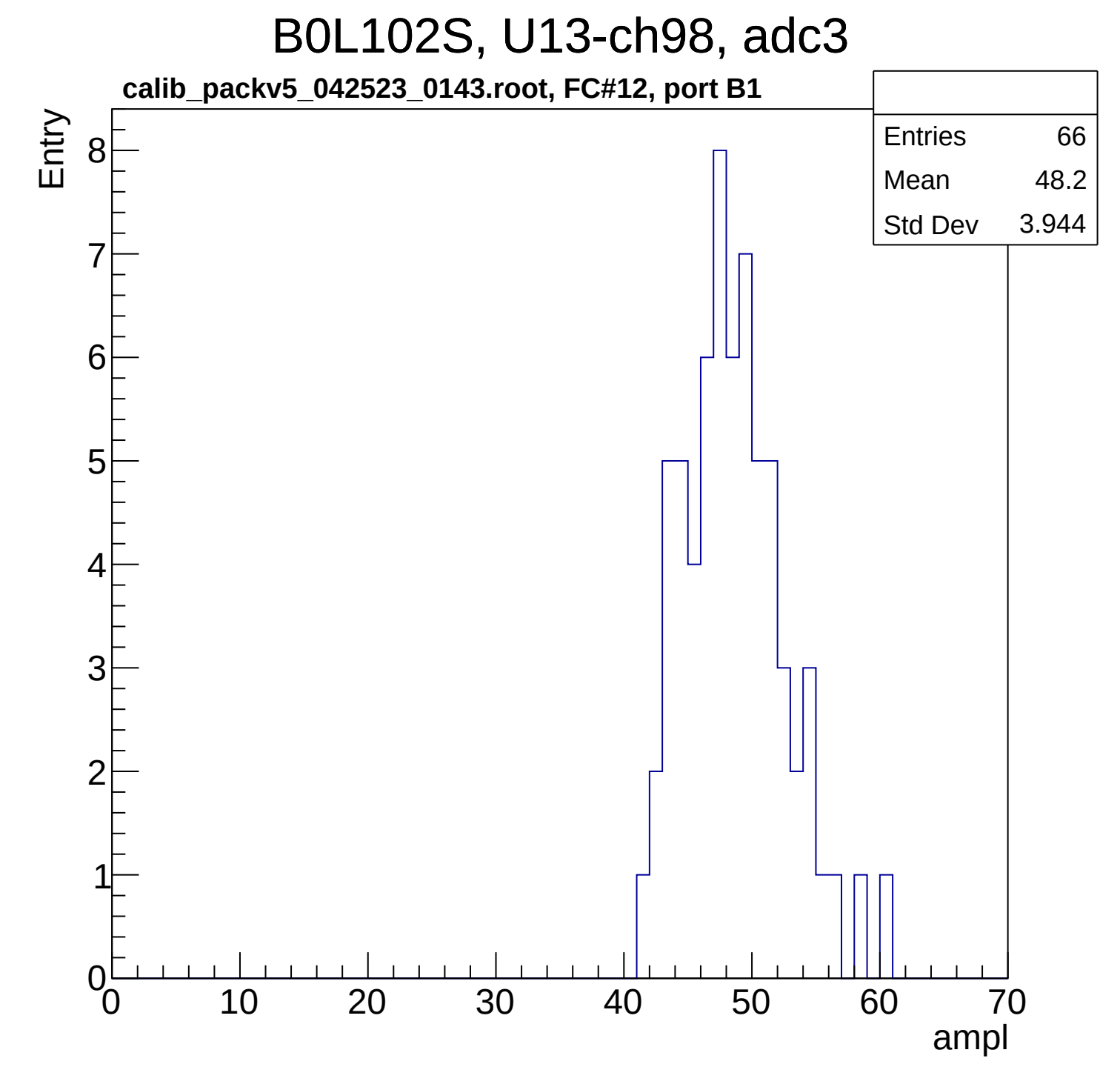
B0L102S, U13-ch98, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 48.2
Std Dev 3.944
Entry
0
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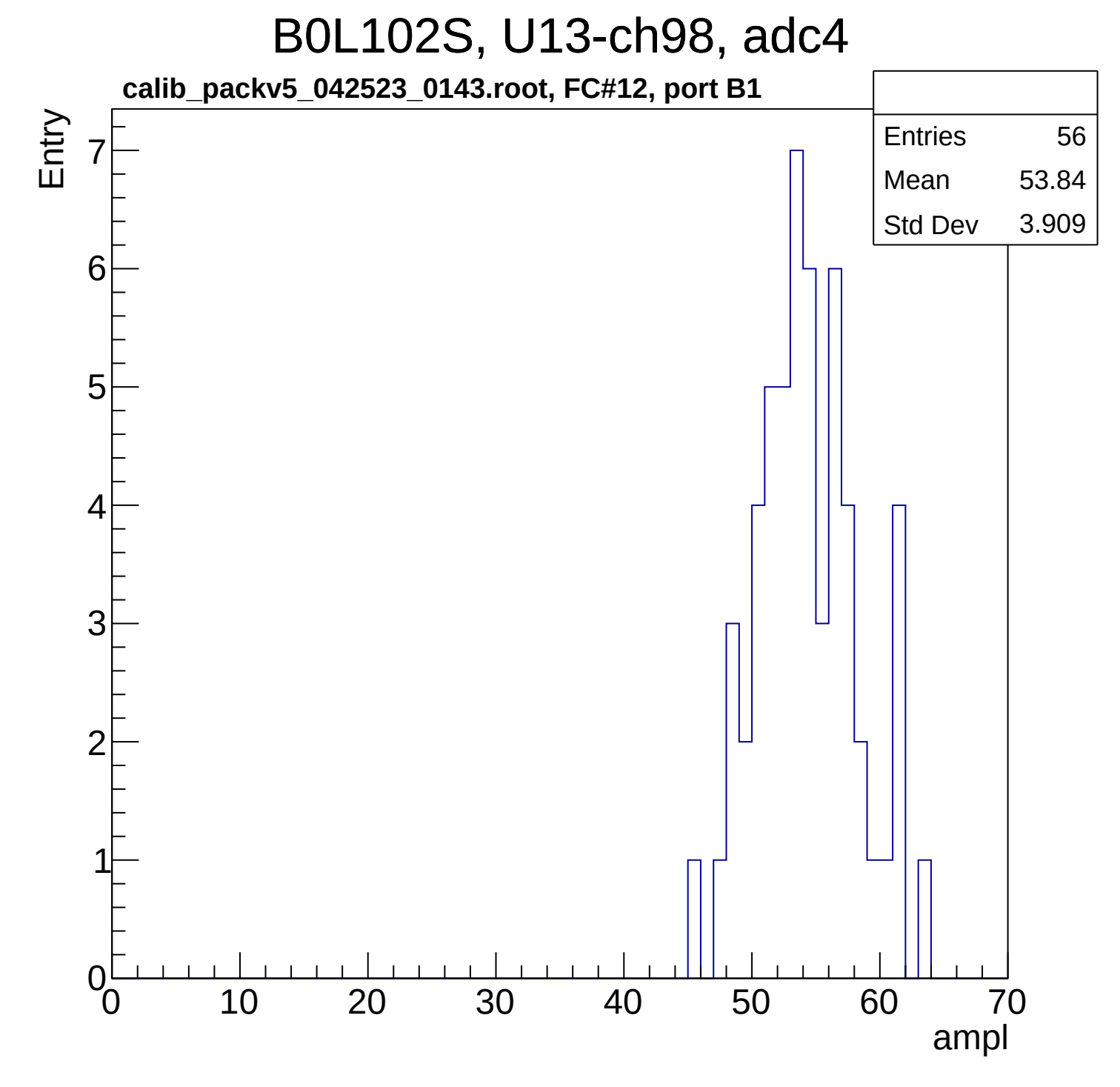
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch98, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 53.84
Std Dev 3.909
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
50
60
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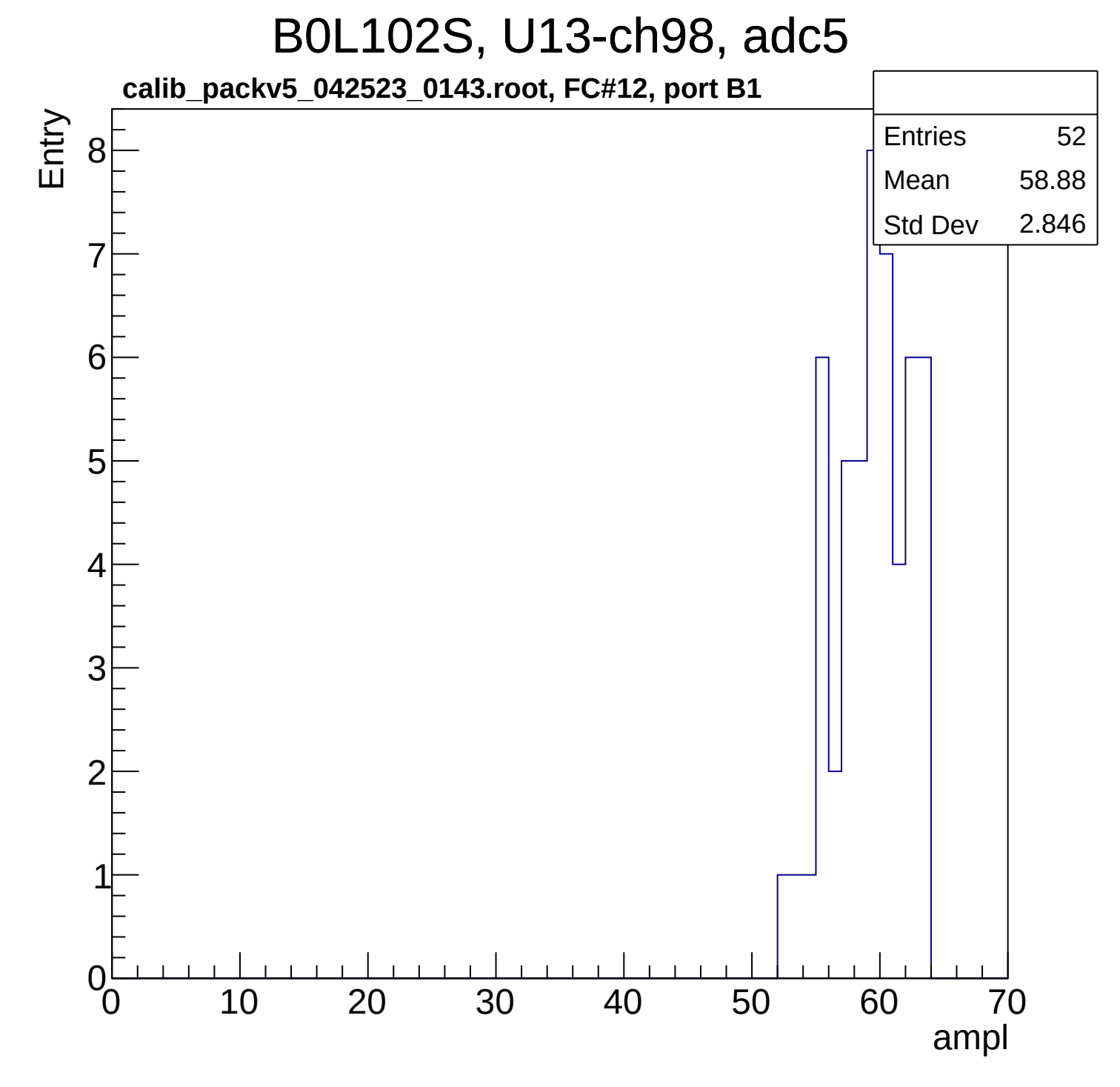
B0L102S, U13-ch98, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.88
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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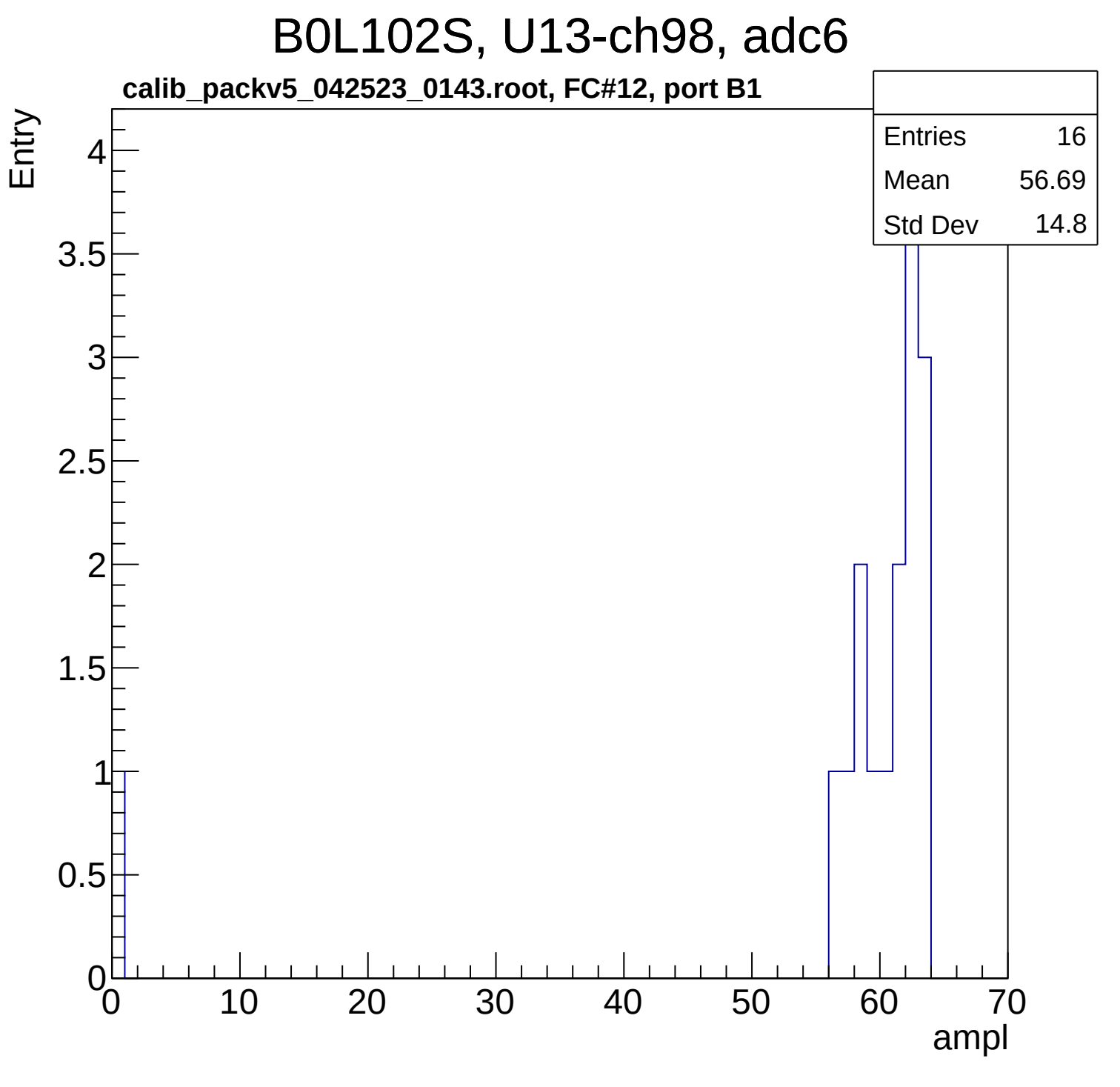
B0L102S, U13-ch98, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 56.69
Std Dev 14.8
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
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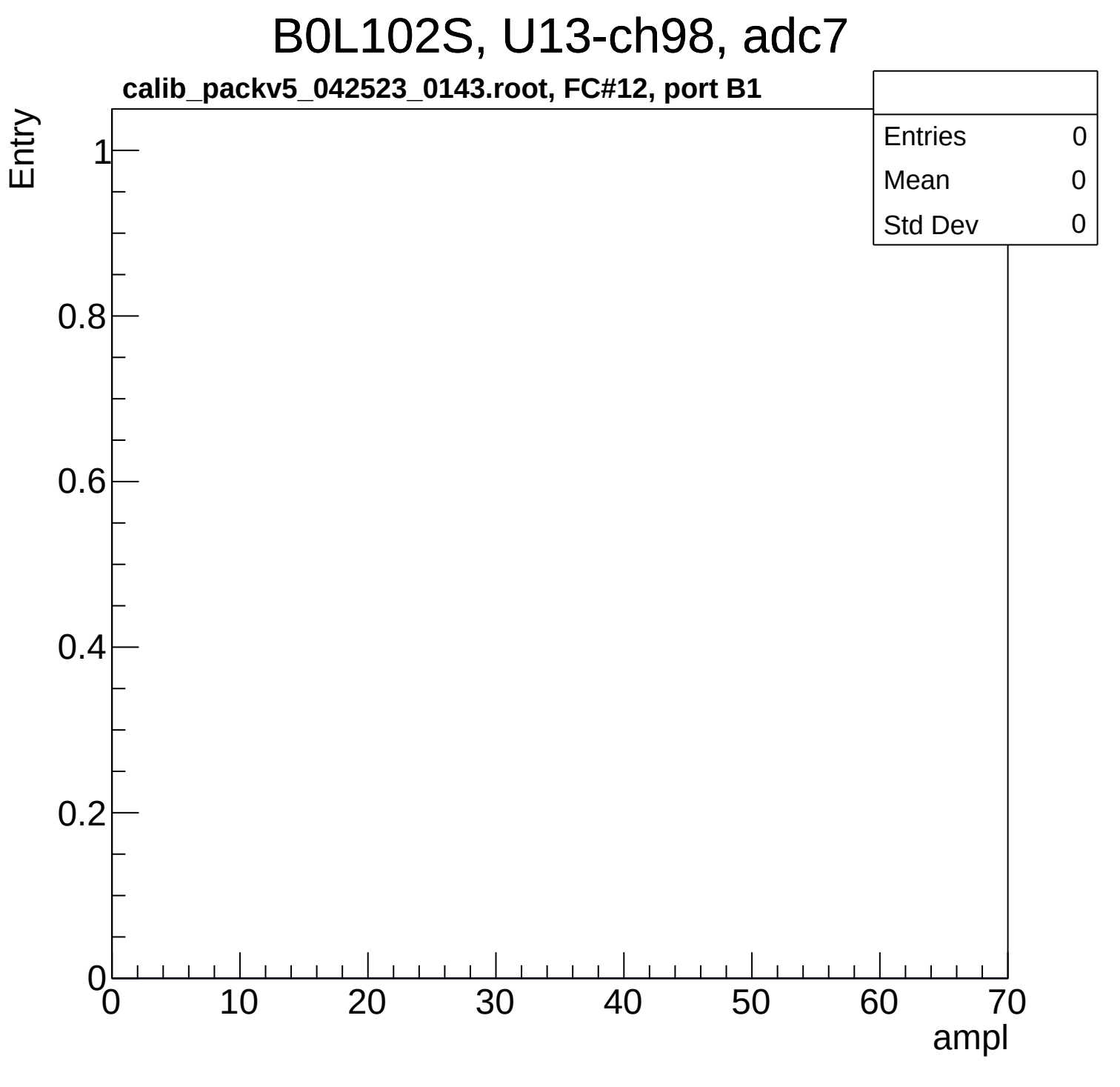
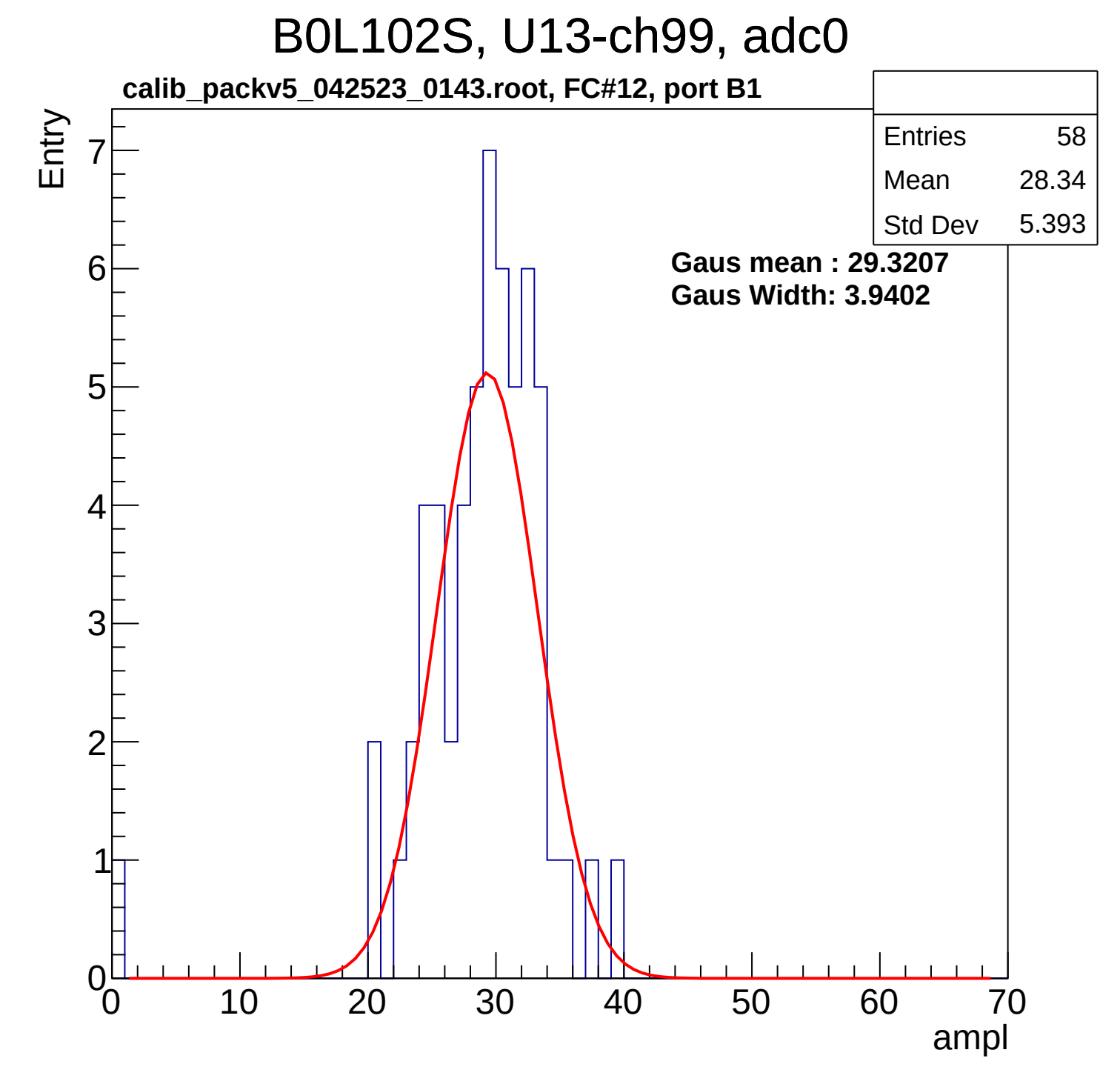
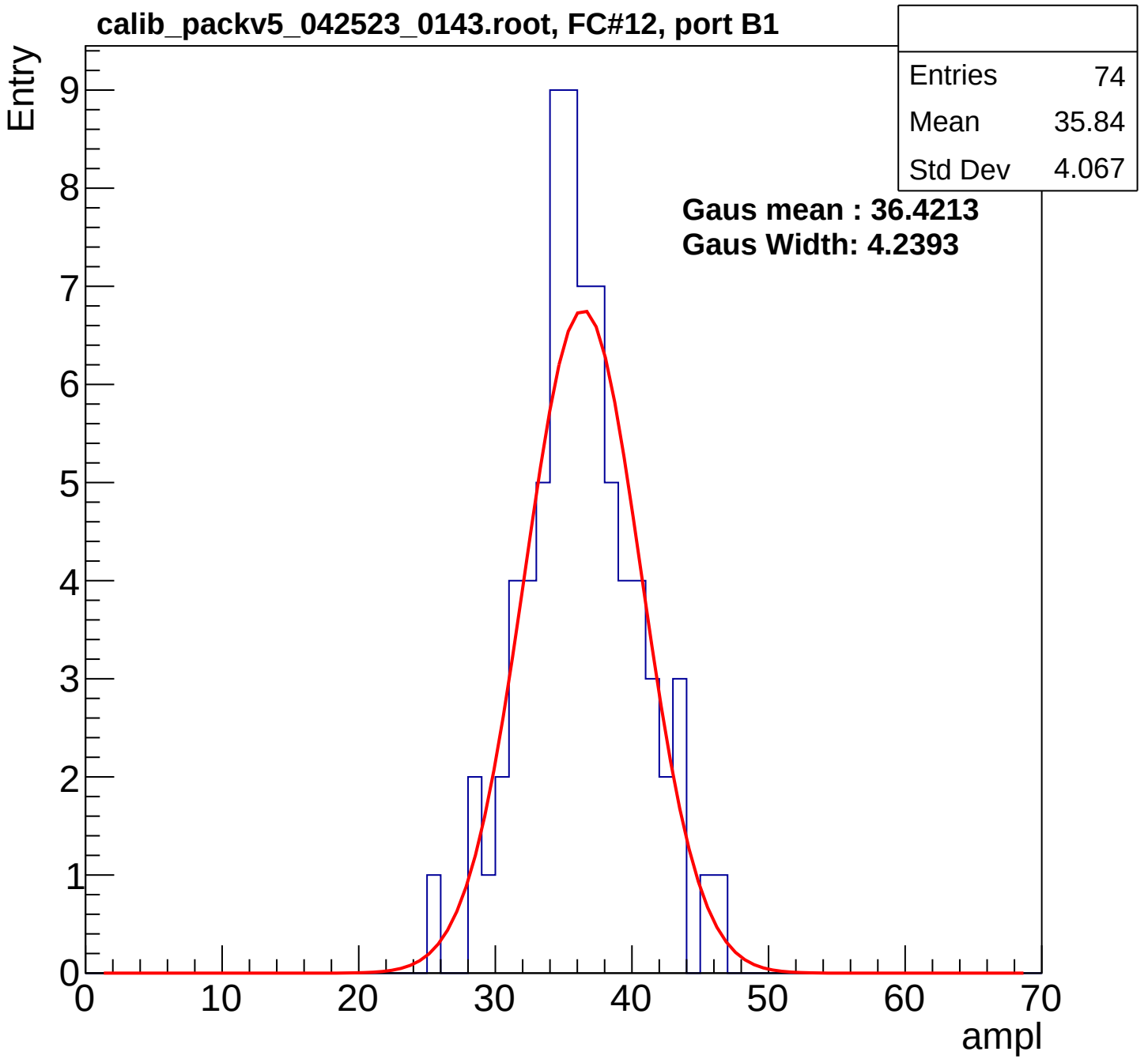
60
70


B0L102S, U13-ch98, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch99, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 28.34
Std Dev 5.393
Gaus mean : 29.3207
Gaus Width: 3.9402
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch99, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 35.84
Std Dev 4.067
Gaus mean : 36.4213
Gaus Width: 4.2393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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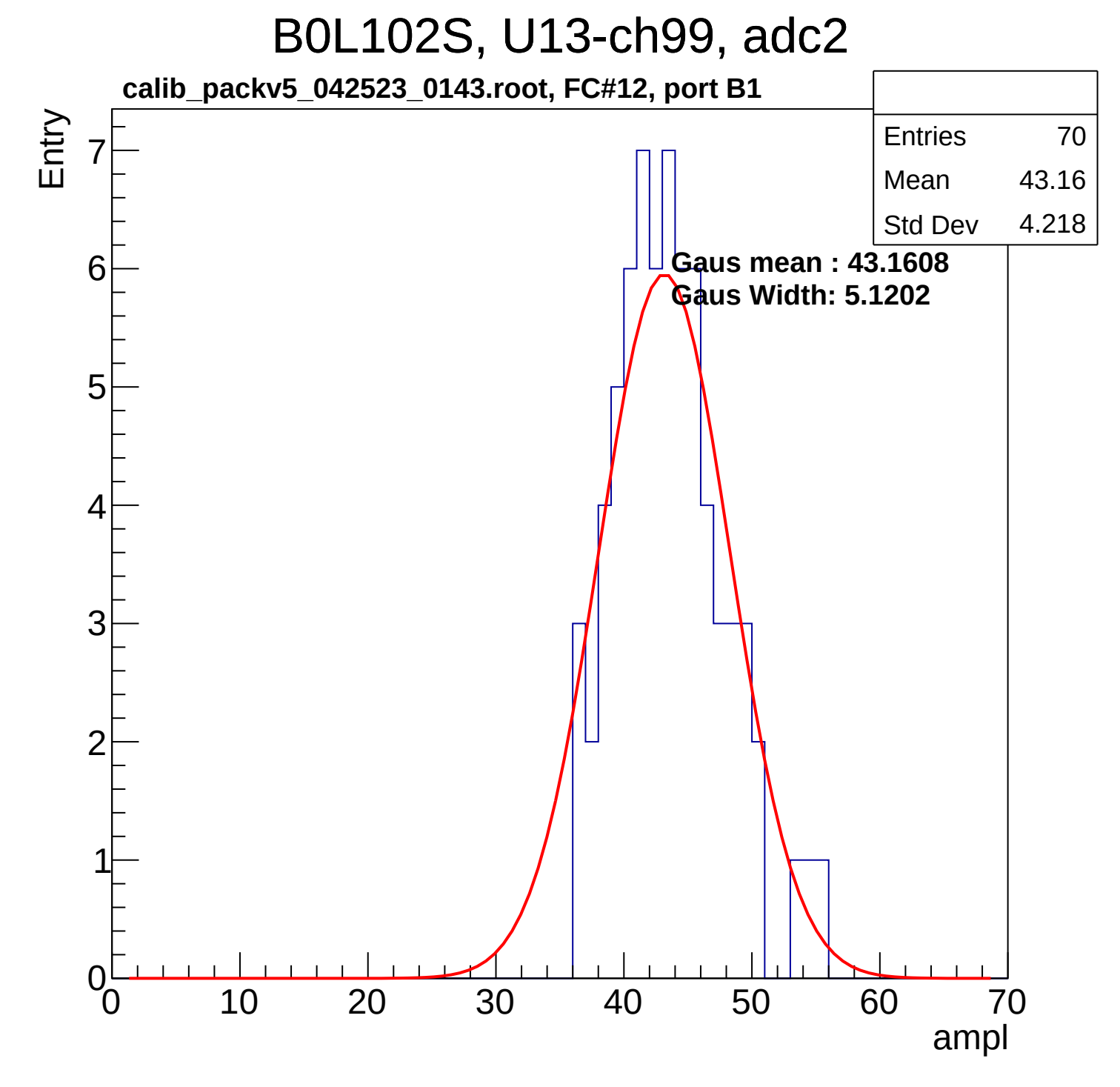
B0L102S, U13-ch99, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.16
Std Dev 4.218
Gaus mean : 43.1608
Gaus Width: 5.1202
Entry
ampl
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1
2
3
4
5
6
7
0
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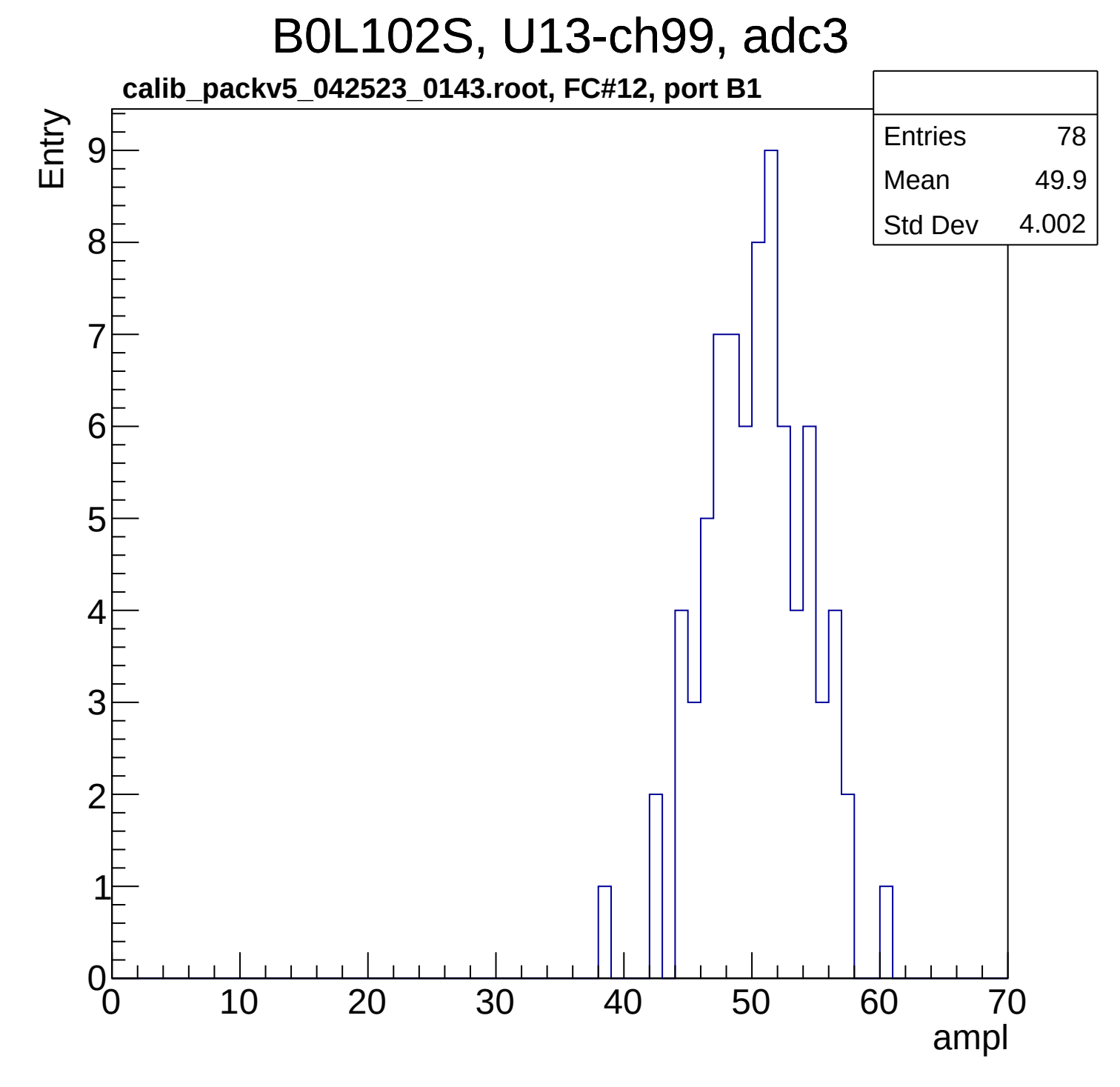
60
70


B0L102S, U13-ch99, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 49.9
Std Dev 4.002
Entry
ampl
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3
4
5
6
7
8
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0
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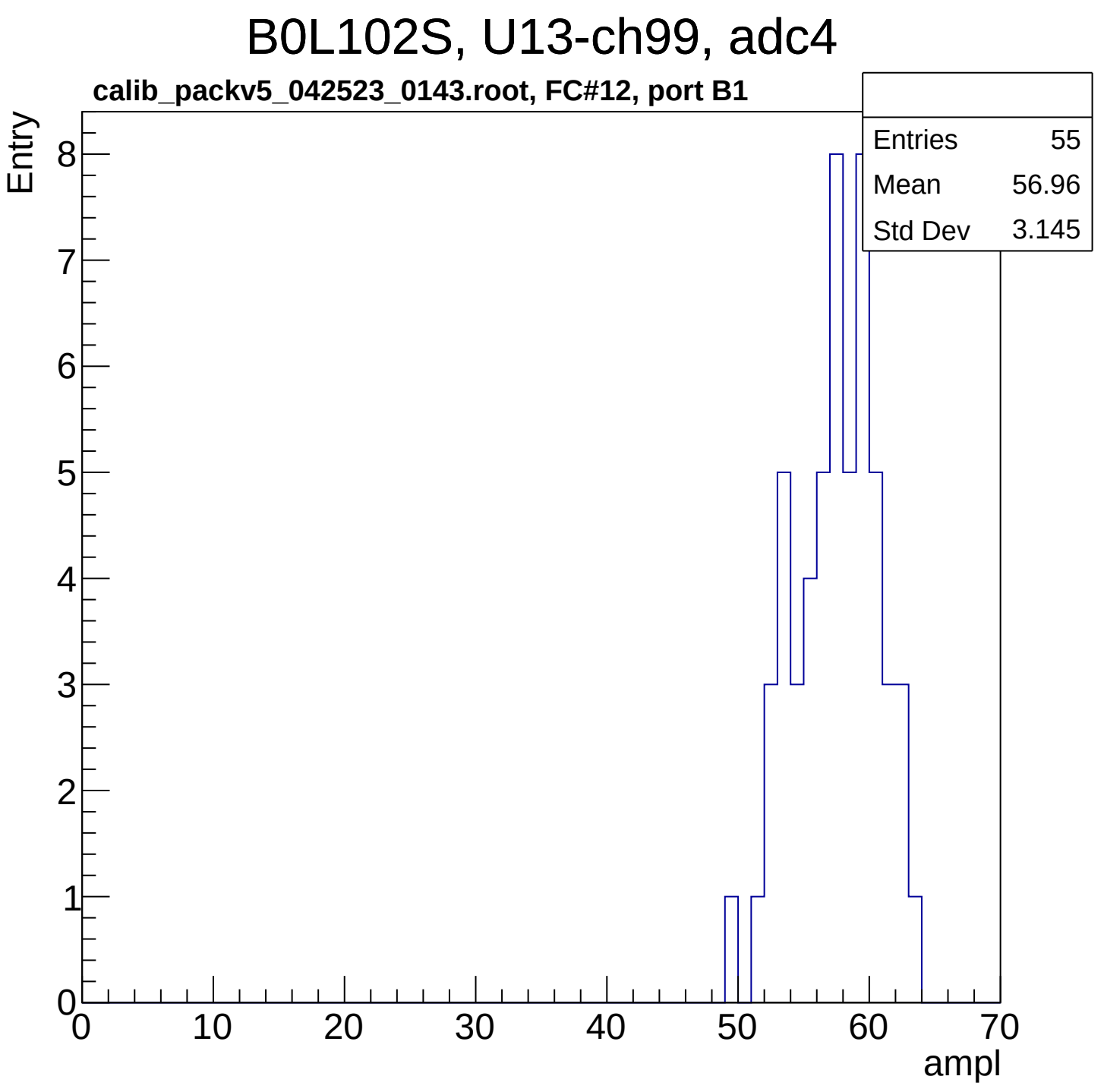
B0L102S, U13-ch99, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.96
Std Dev 3.145
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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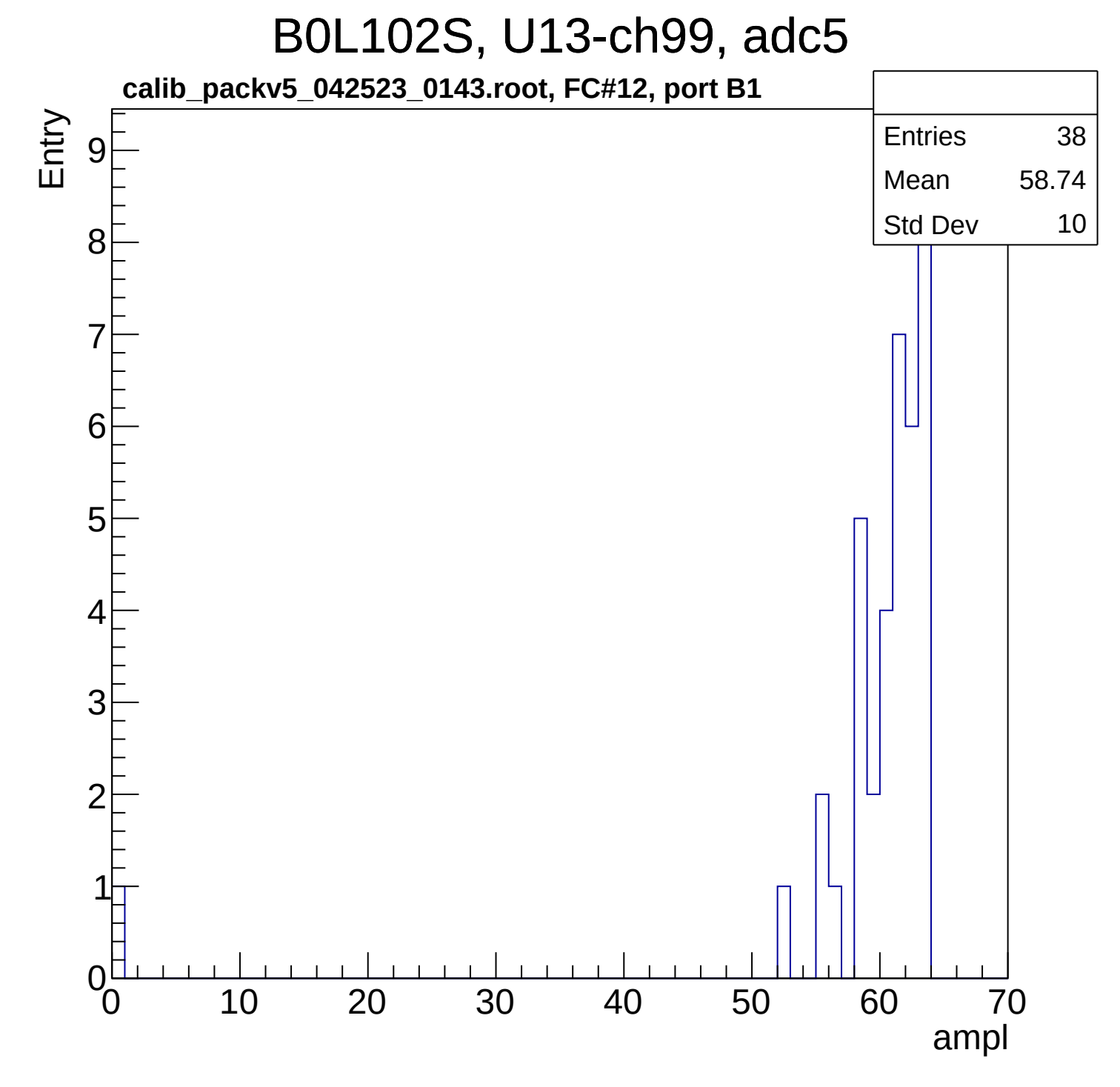
B0L102S, U13-ch99, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.74
Std Dev 10
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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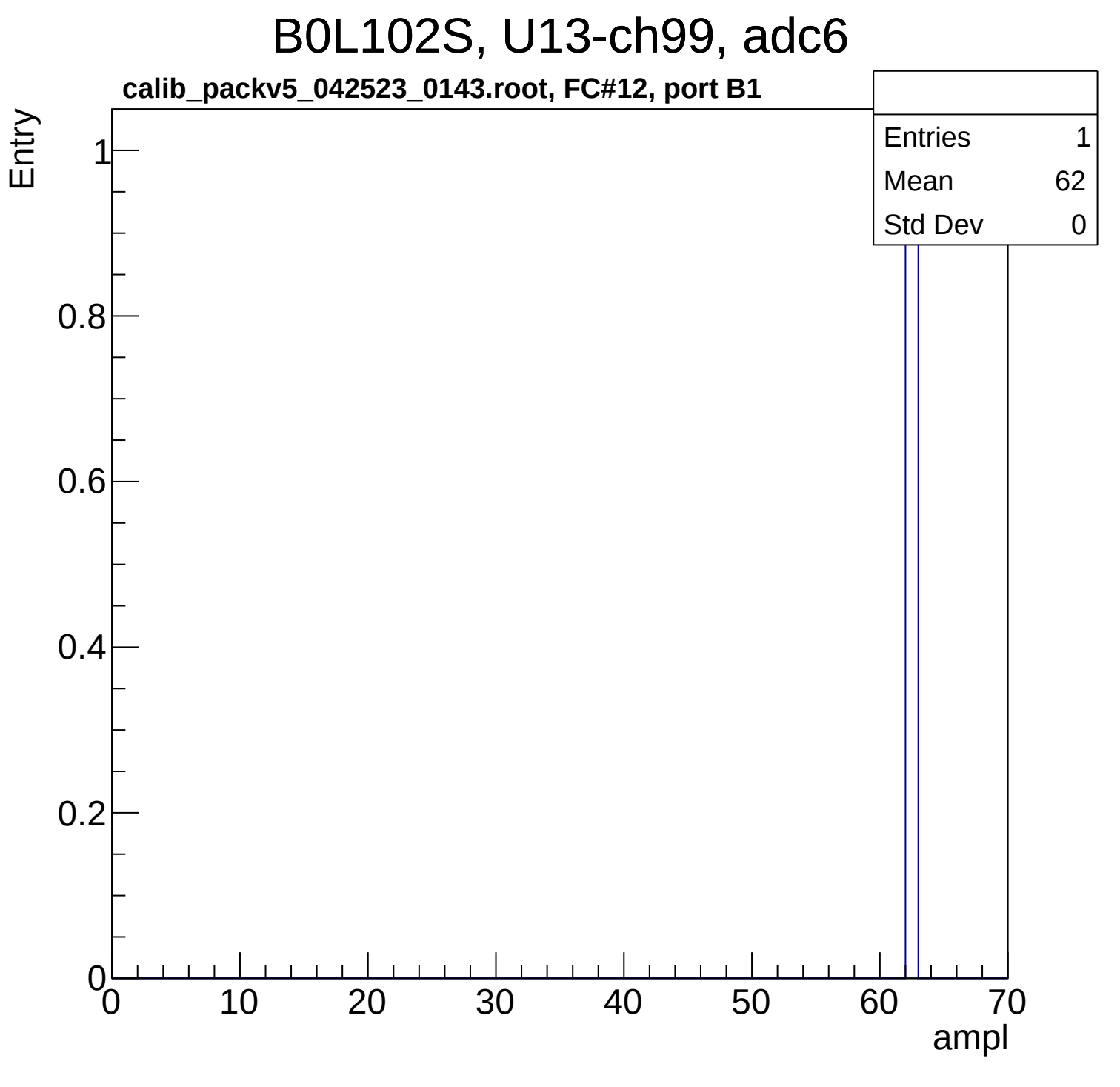
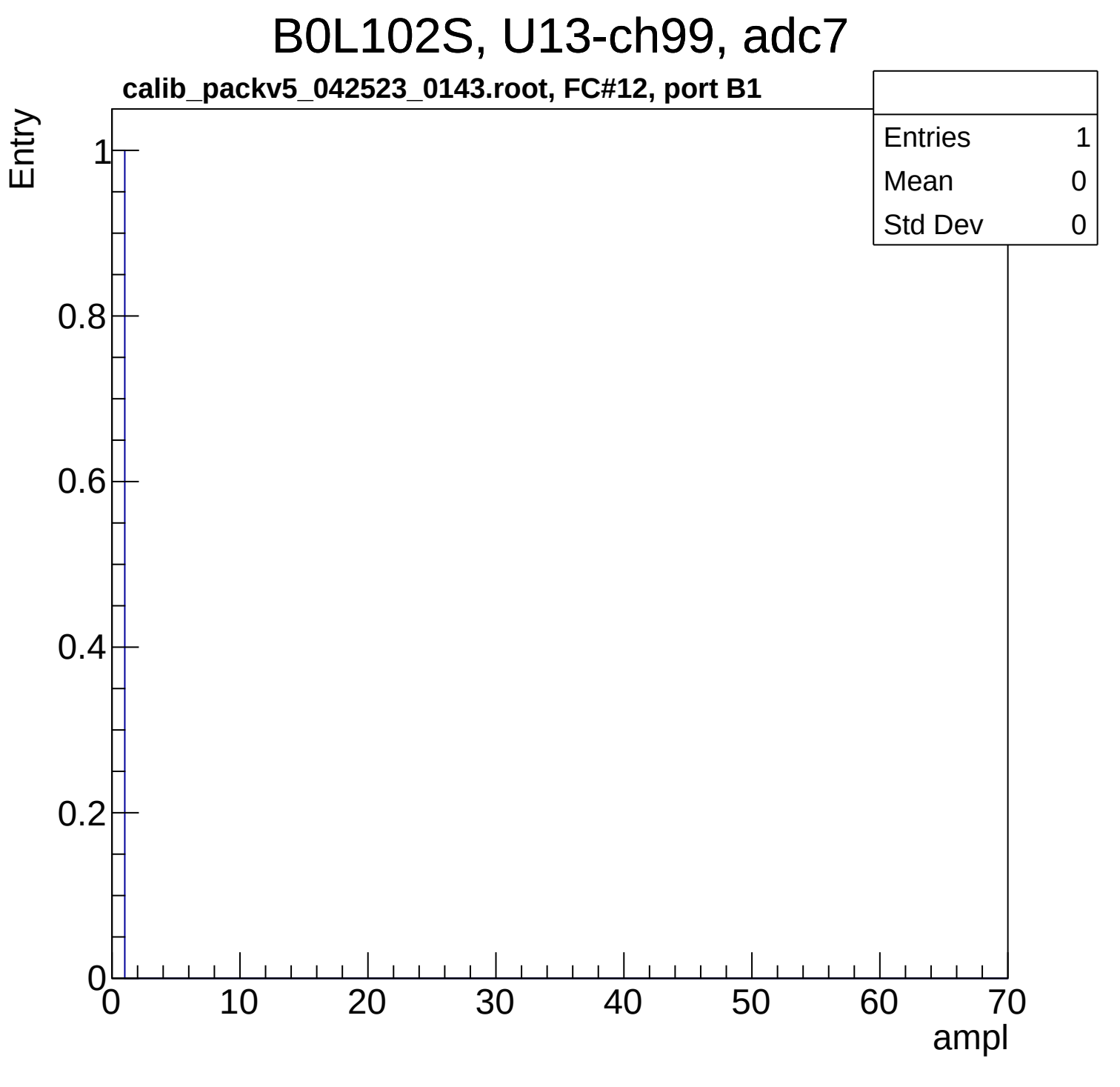

B0L102S, U13-ch99, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch99, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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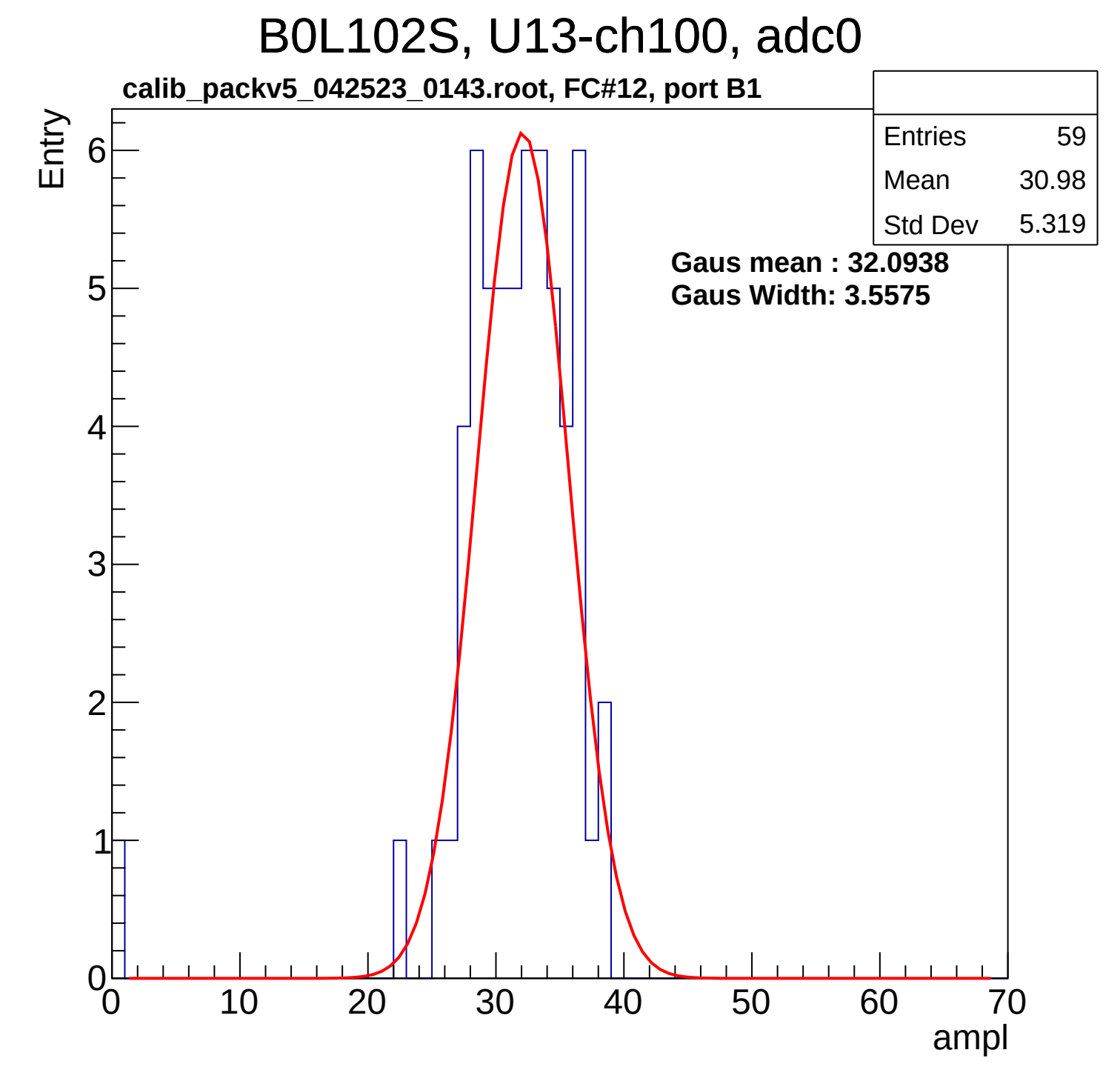
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch100, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 30.98
Std Dev 5.319
Gaus mean : 32.0938
Gaus Width: 3.5575
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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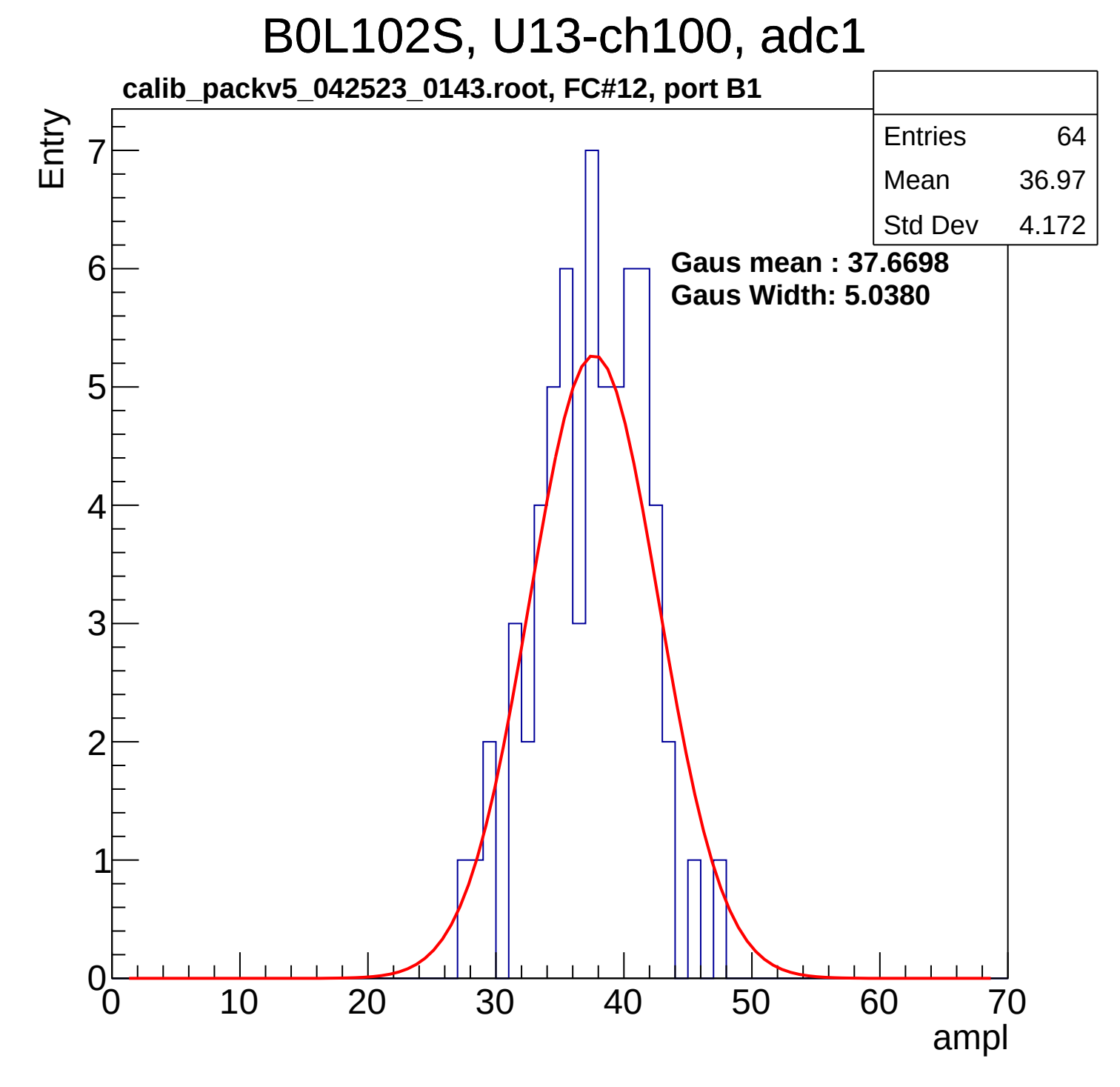
B0L102S, U13-ch100, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.97
Std Dev 4.172
Gaus mean : 37.6698
Gaus Width: 5.0380
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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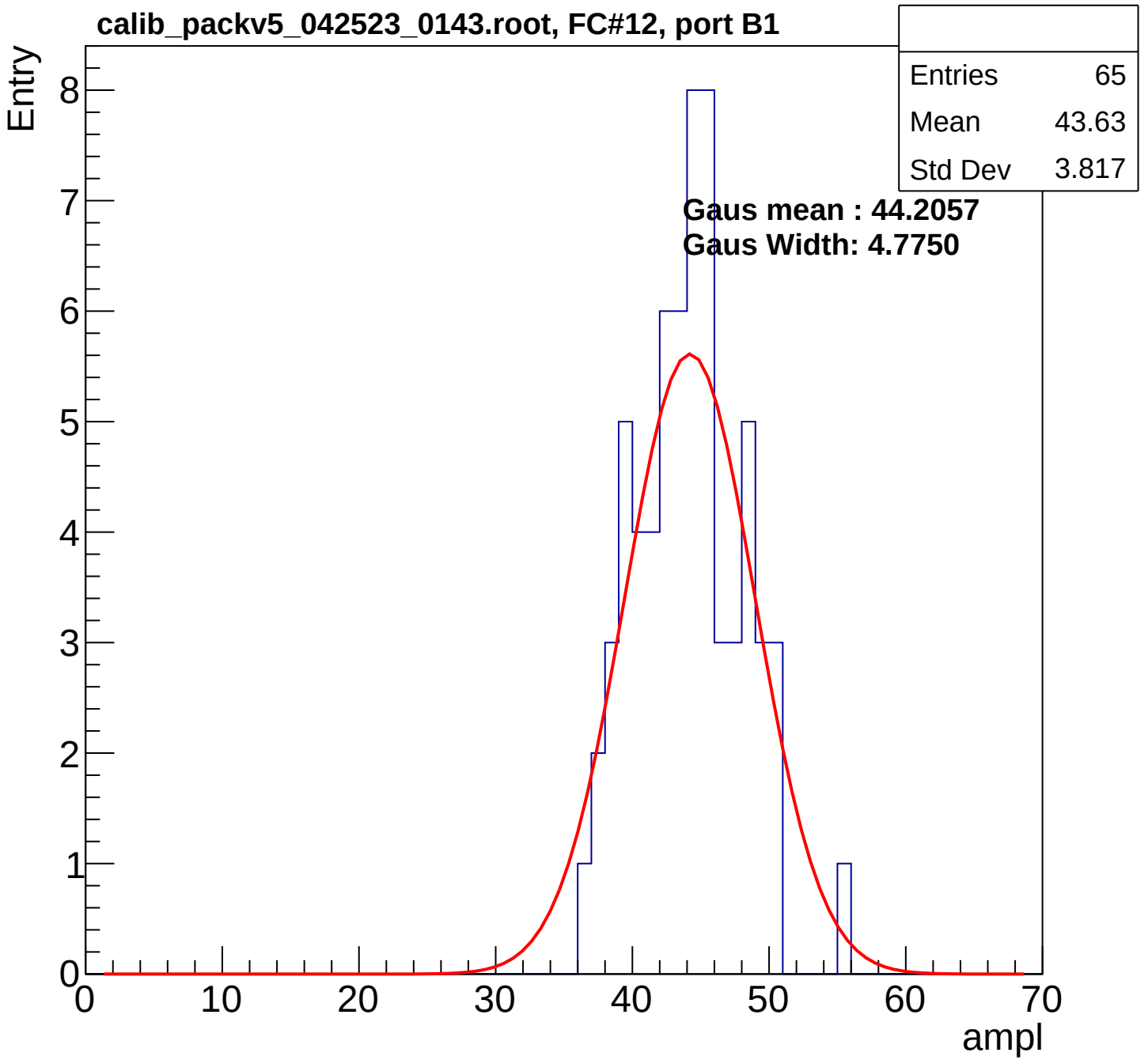
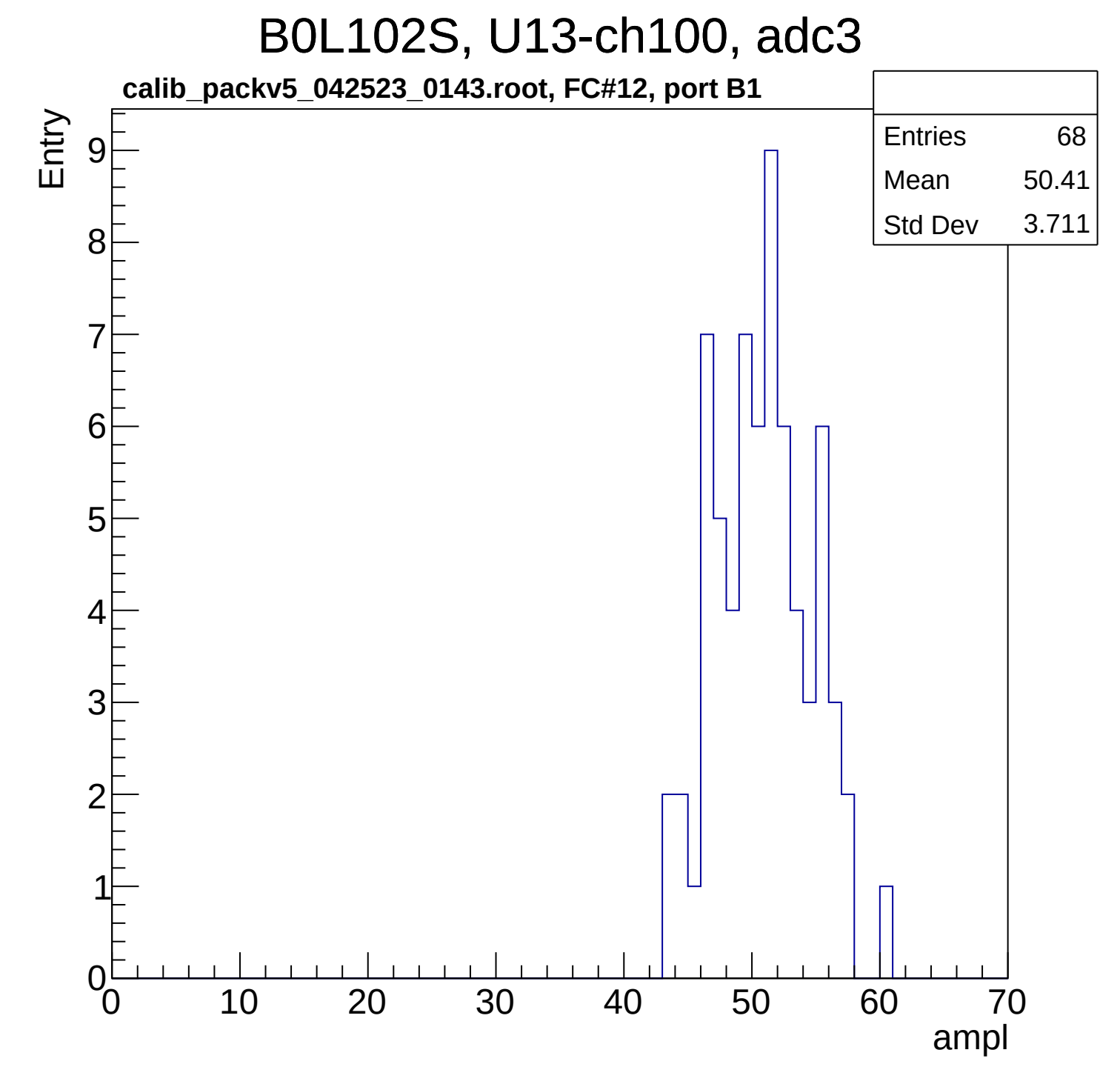

B0L102S, U13-ch100, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.63
Std Dev 3.817
Gaus mean : 44.2057
Gaus Width: 4.7750
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch100, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 50.41
Std Dev 3.711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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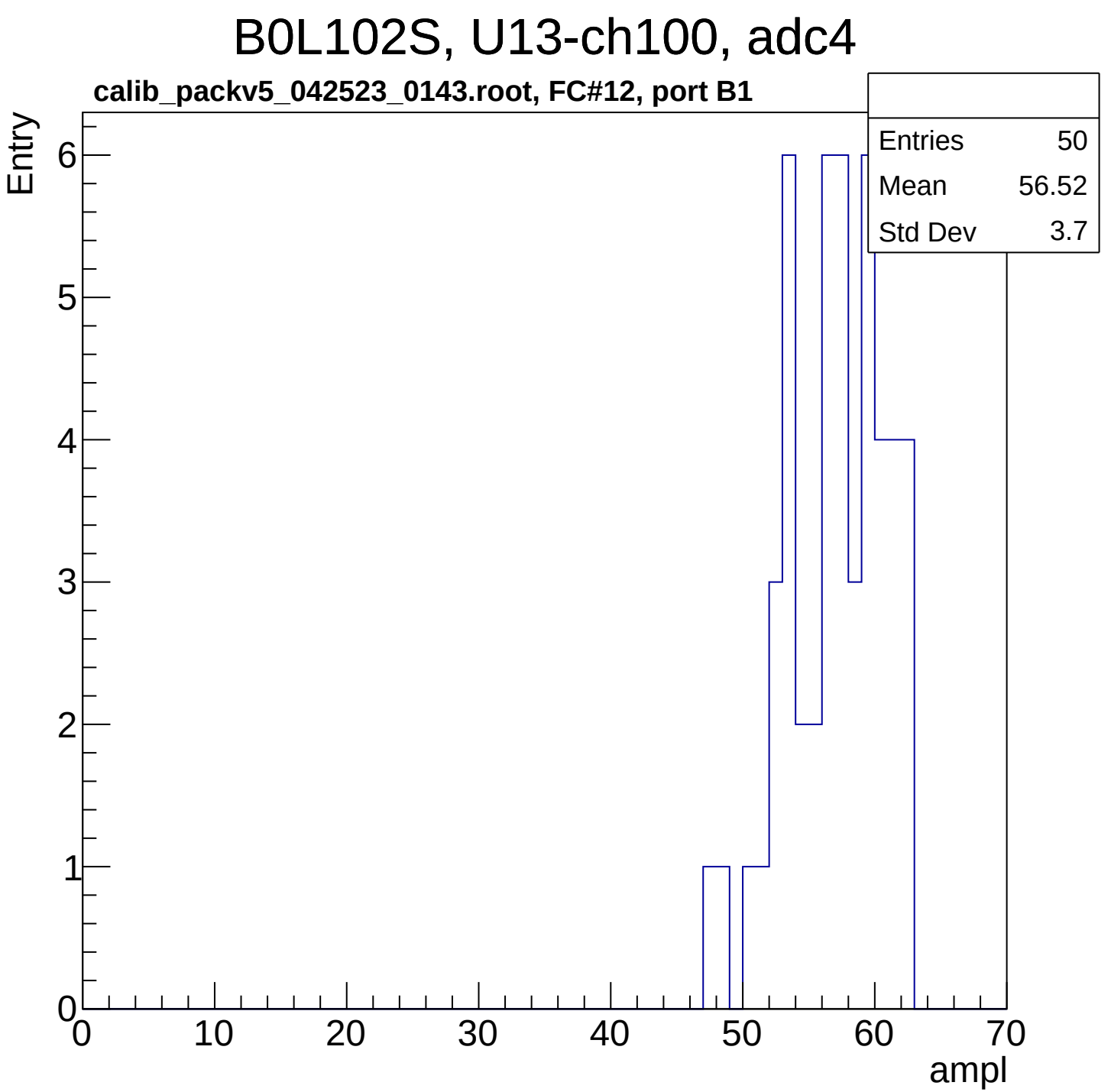
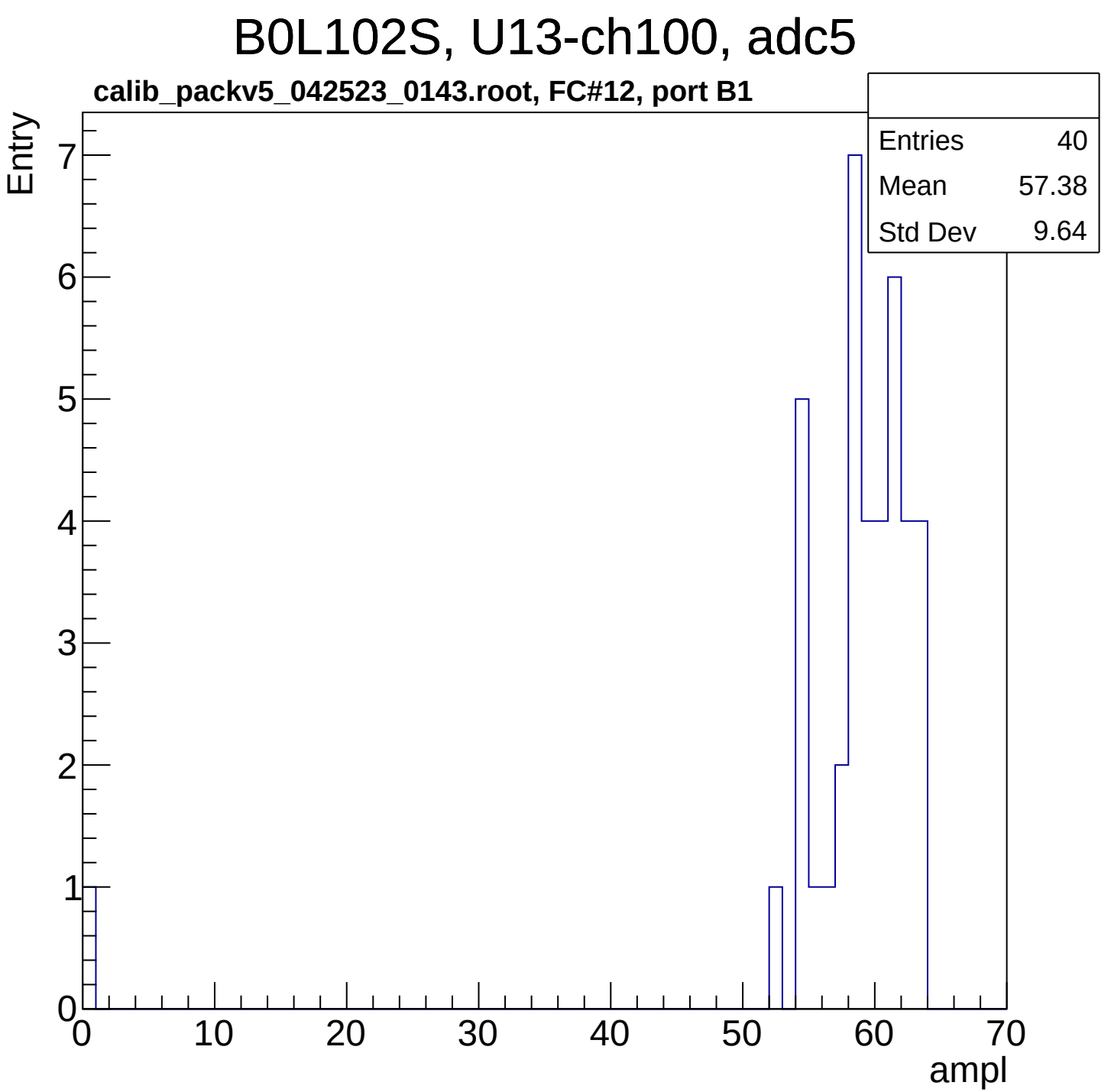
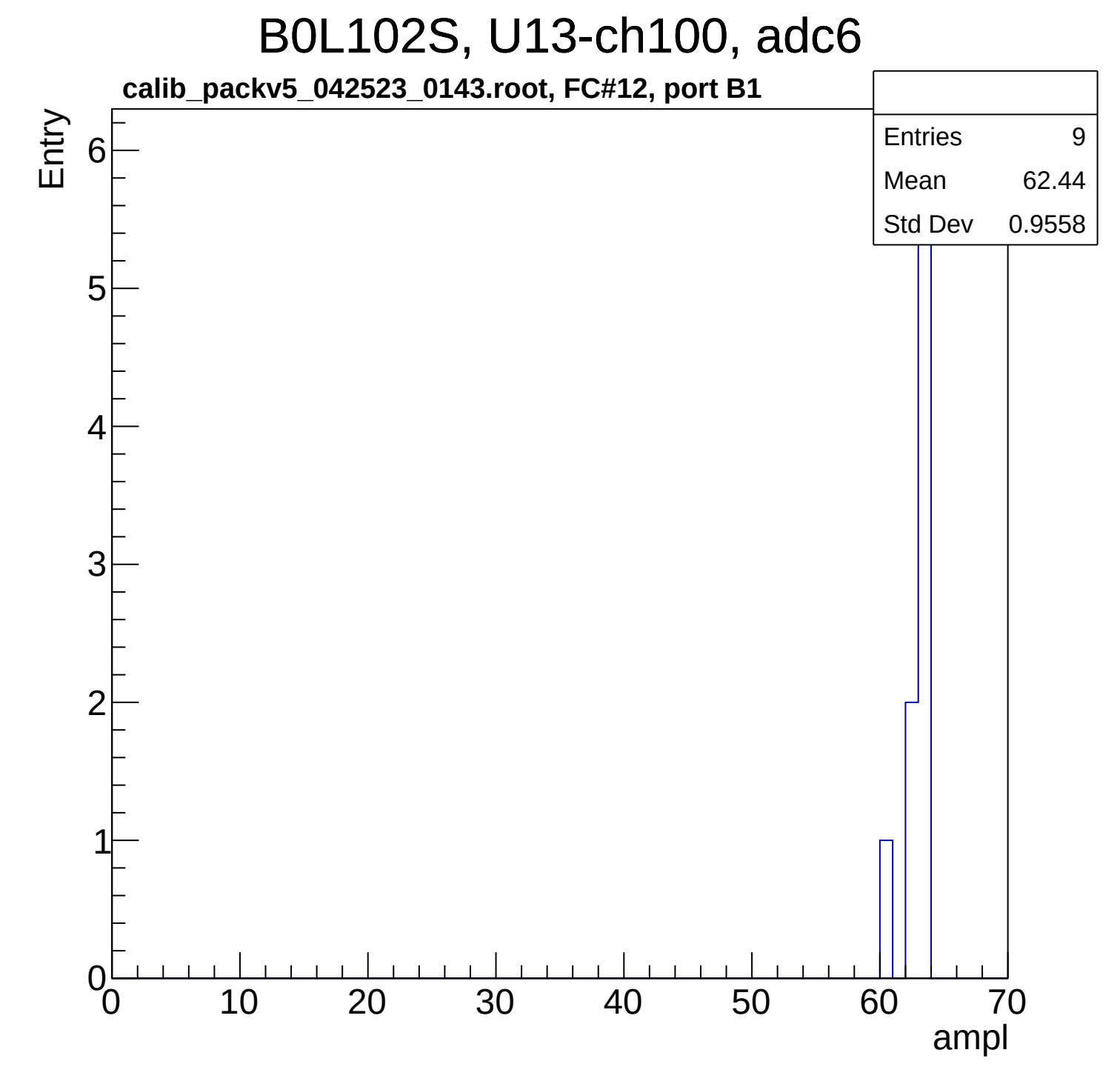
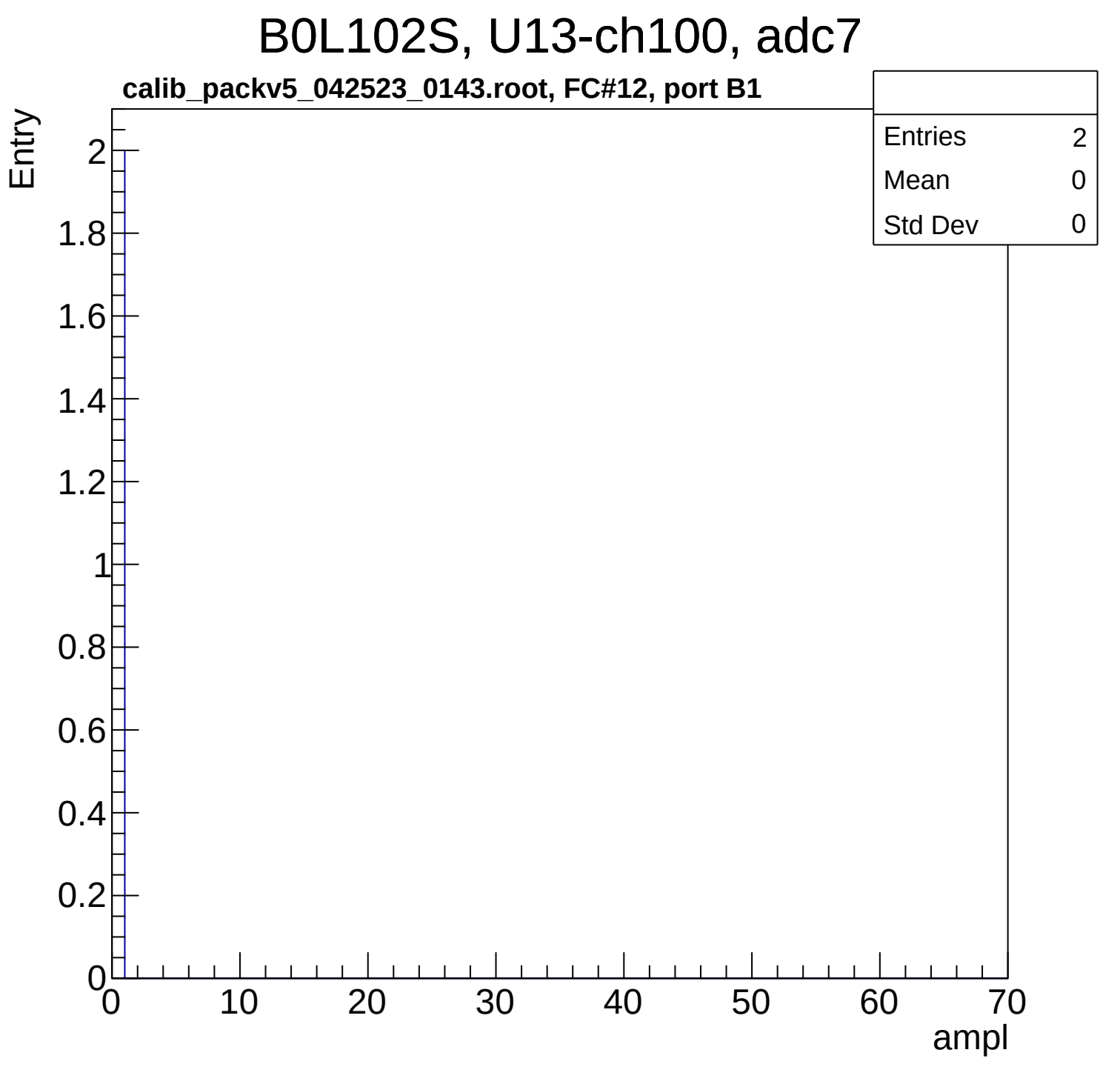
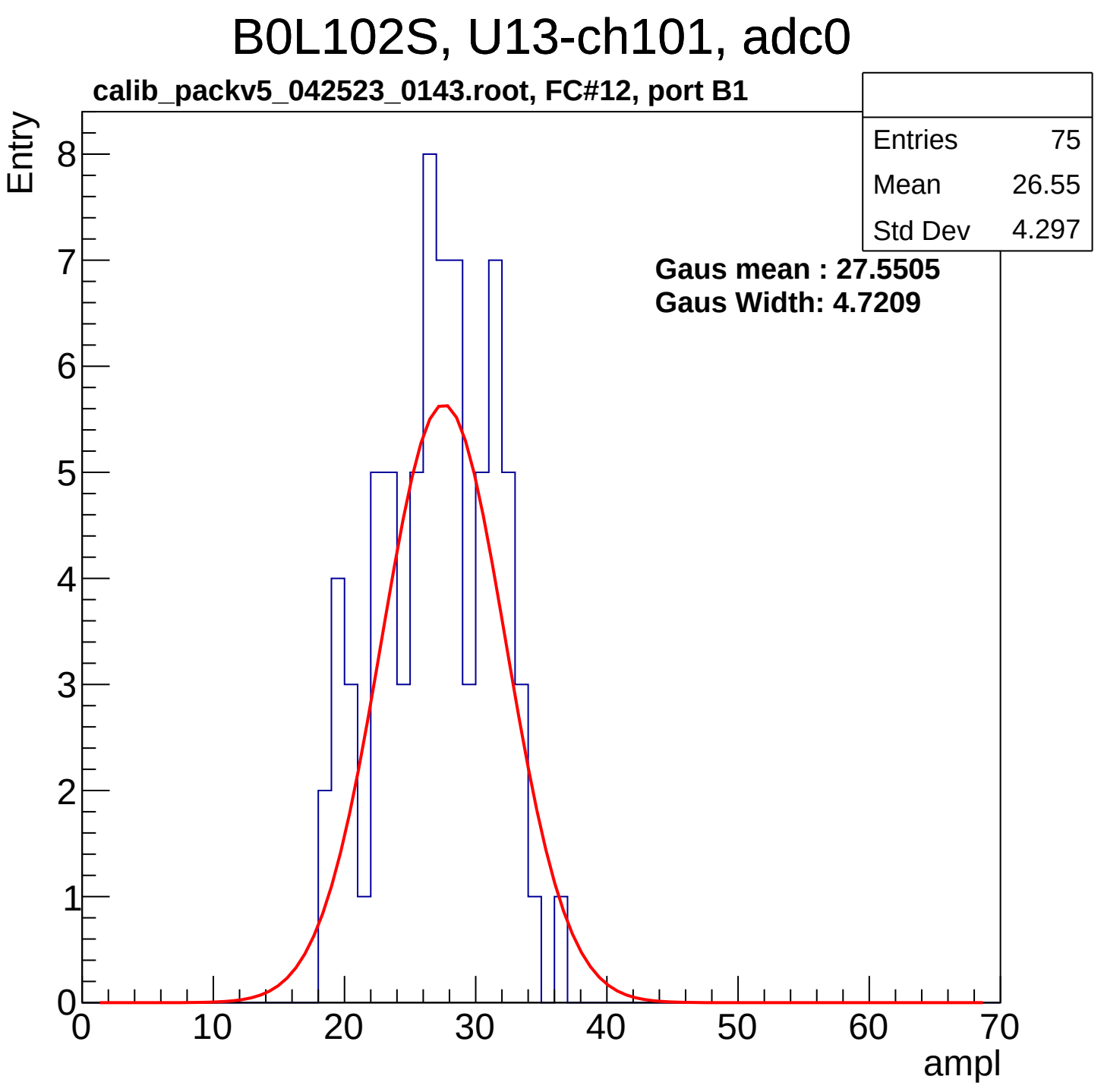
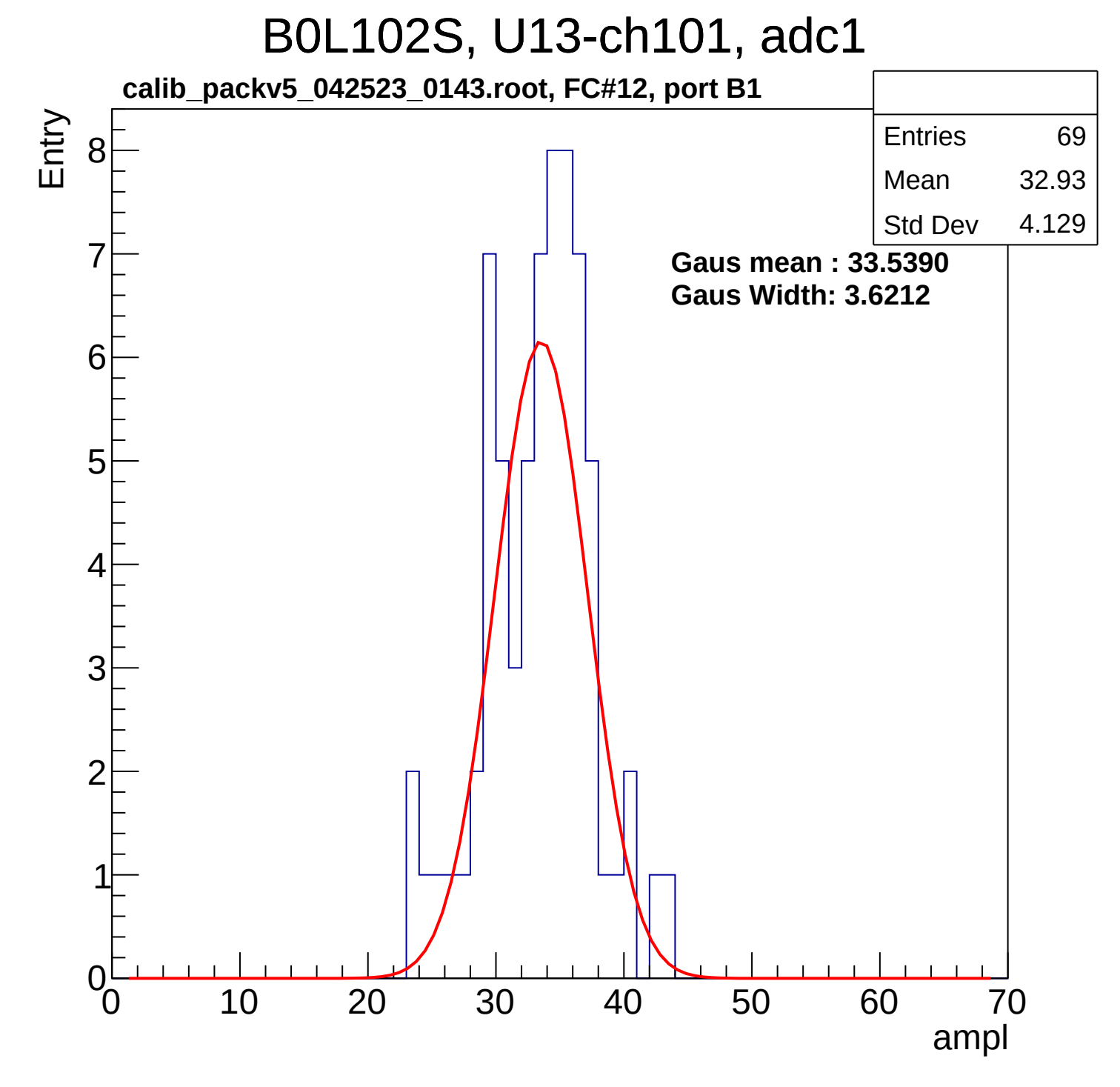

B0L102S, U13-ch100, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 56.52
Std Dev 3.7
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

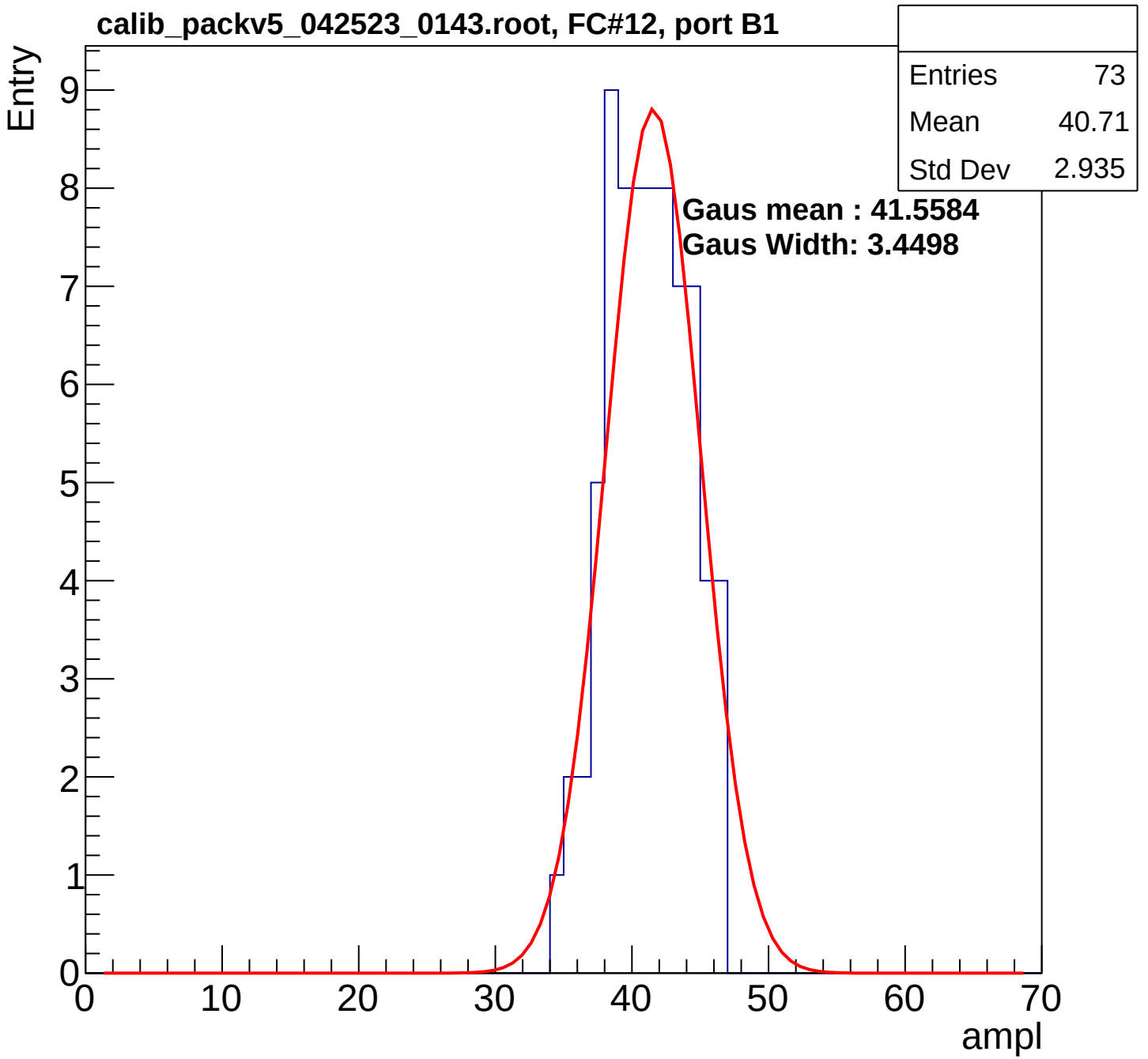

B0L102S, U13-ch100, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 57.38
Std Dev 9.64
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

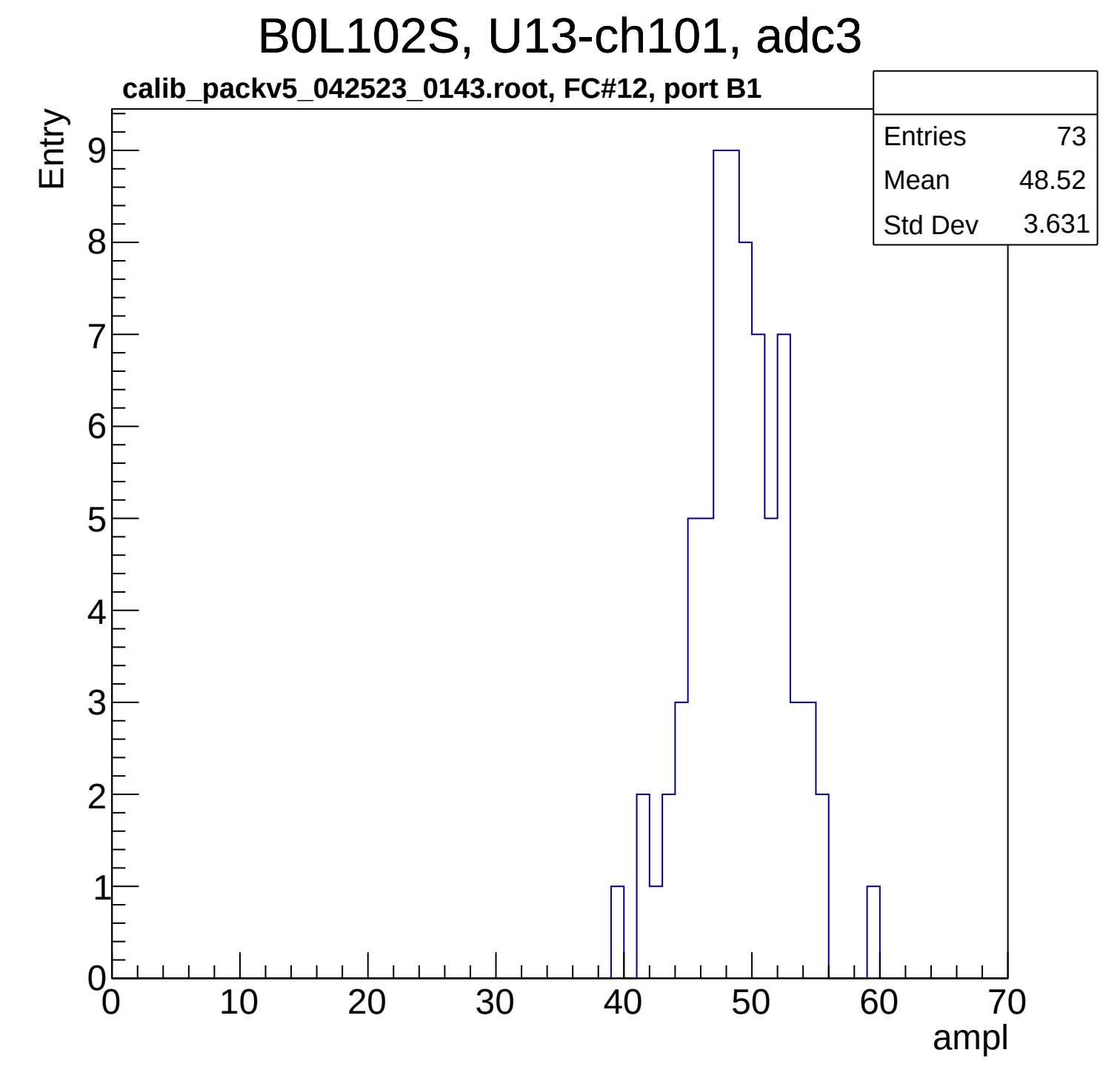

B0L102S, U13-ch100, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62.44
Std Dev 0.9558
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

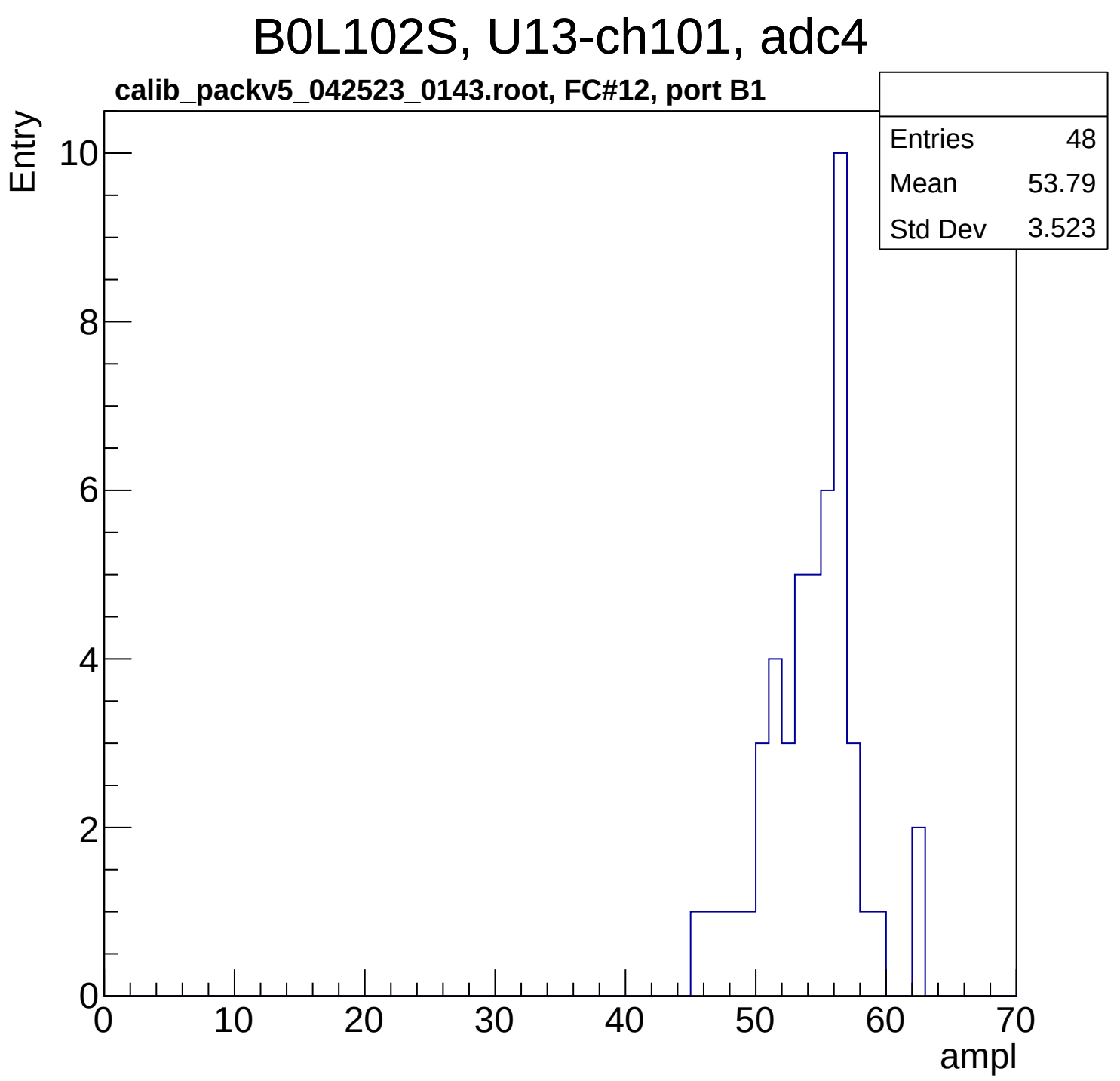

B0L102S, U13-ch100, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

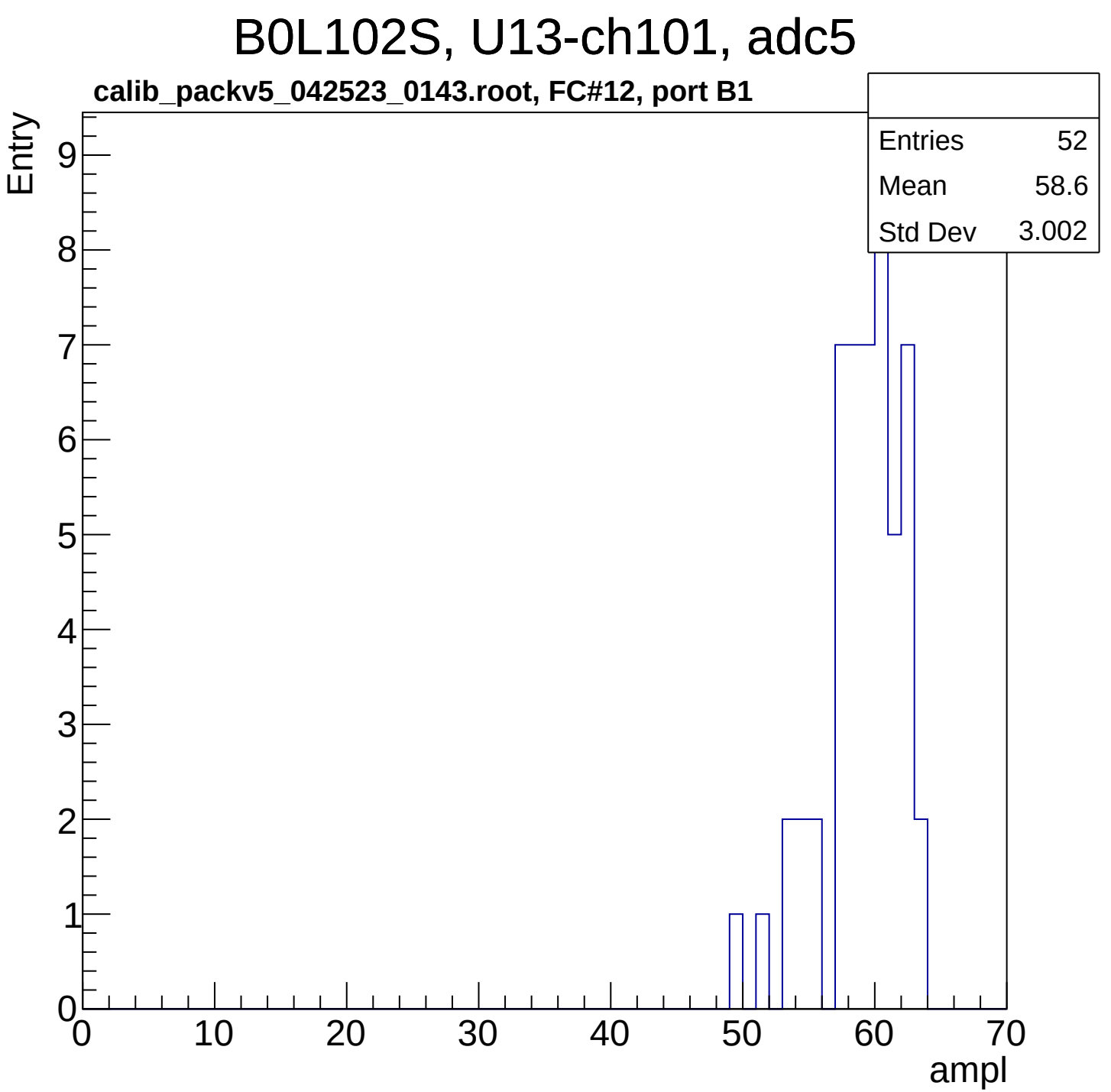

B0L102S, U13-ch101, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 26.55
Std Dev 4.297
Gaus mean : 27.5505
Gaus Width: 4.7209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

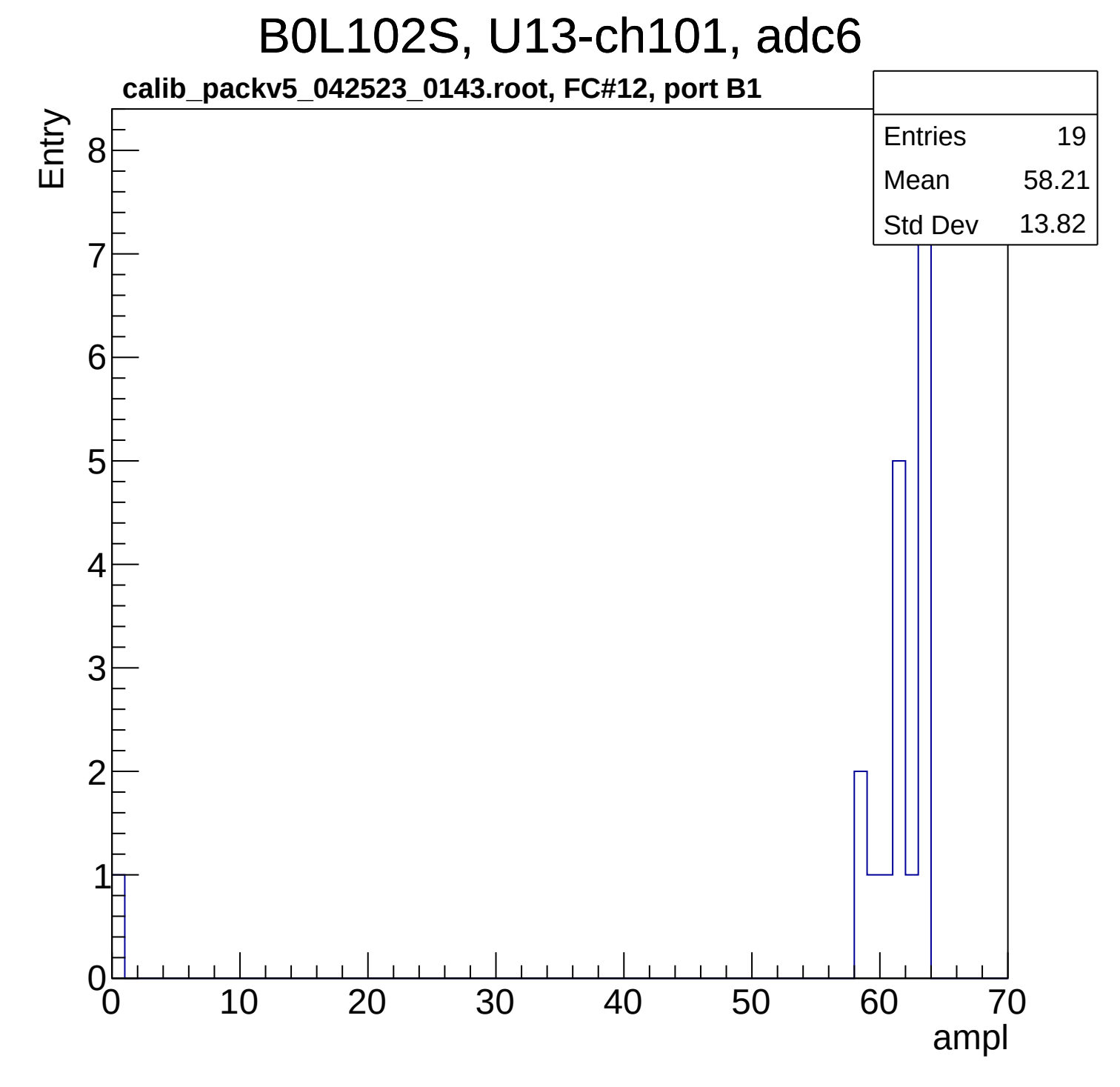

B0L102S, U13-ch101, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 32.93
Std Dev 4.129
Gaus mean : 33.5390
Gaus Width: 3.6212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

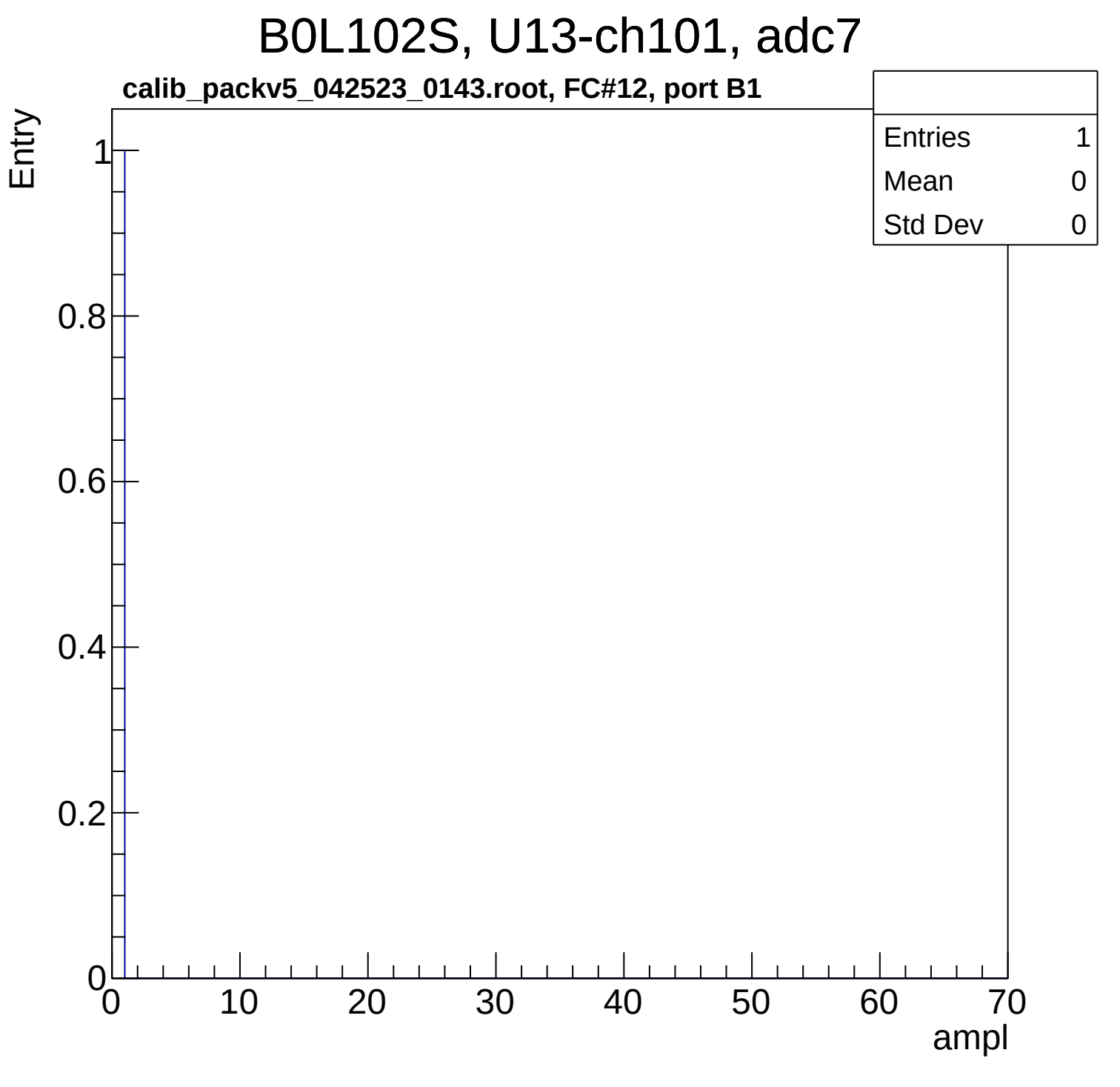

B0L102S, U13-ch101, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 40.71
Std Dev 2.935
Gaus mean : 41.5584
Gaus Width: 3.4498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

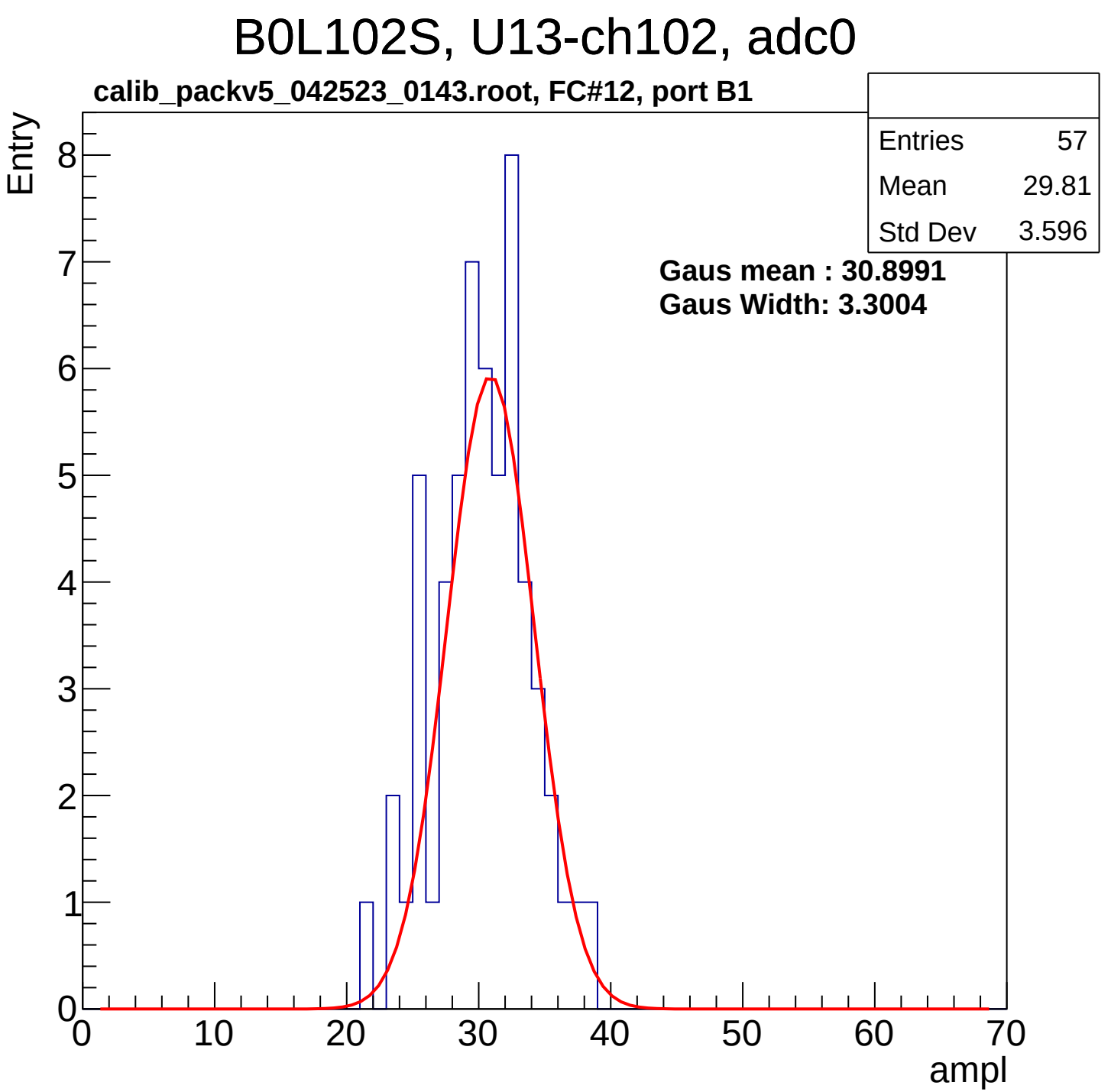

B0L102S, U13-ch101, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 48.52
Std Dev 3.631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

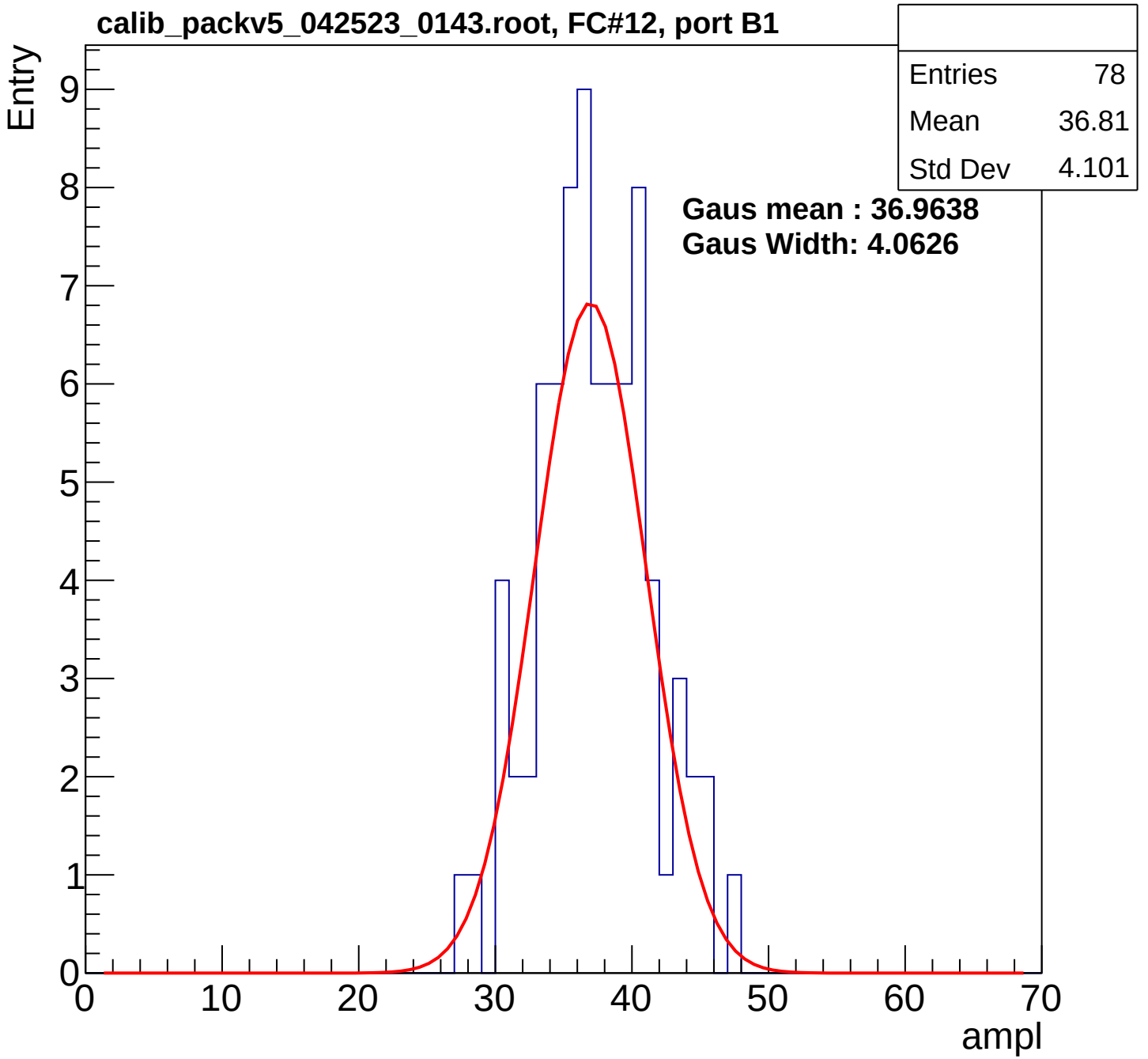

B0L102S, U13-ch101, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 53.79
Std Dev 3.523
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

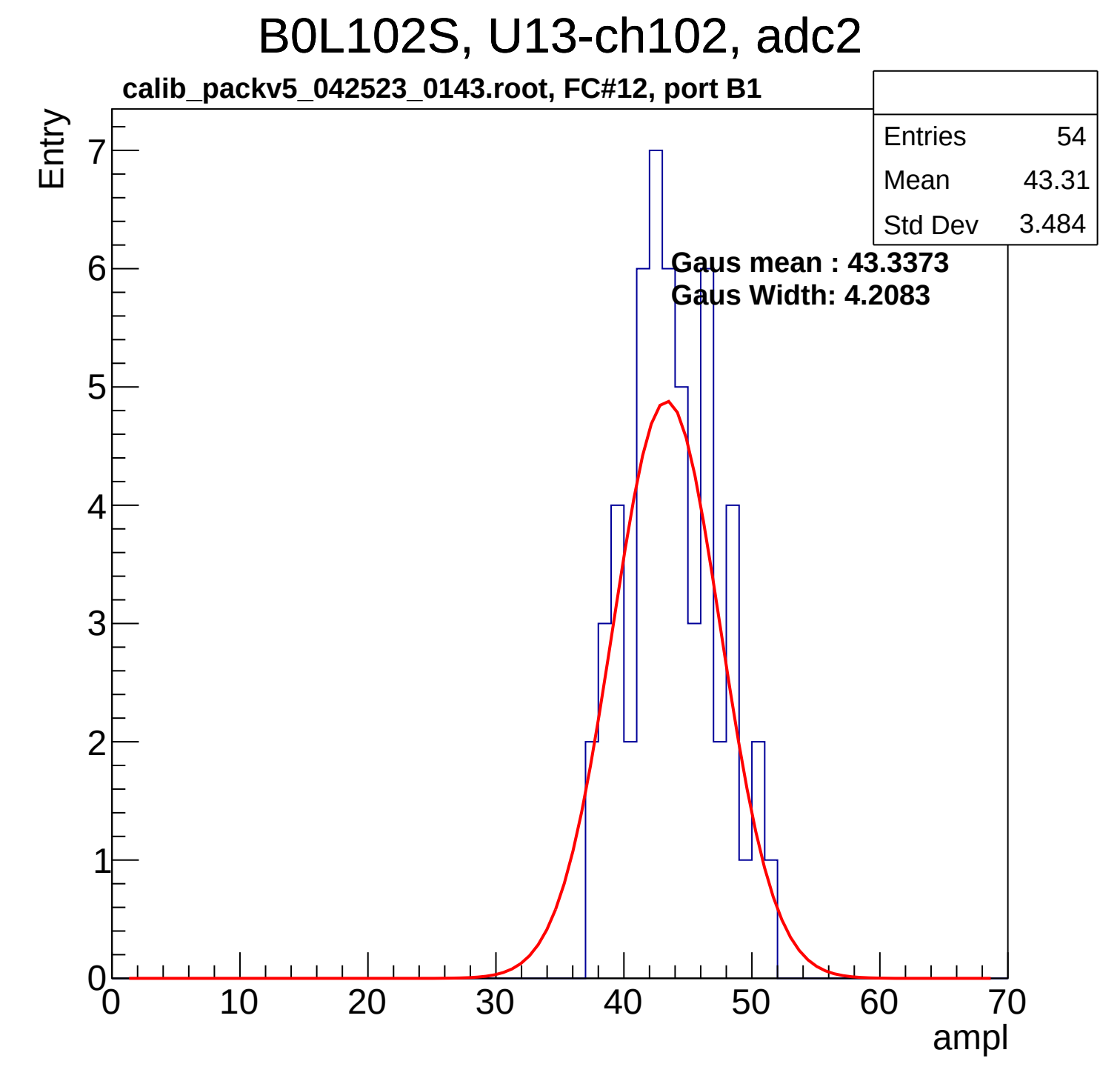

B0L102S, U13-ch101, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.6
Std Dev 3.002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

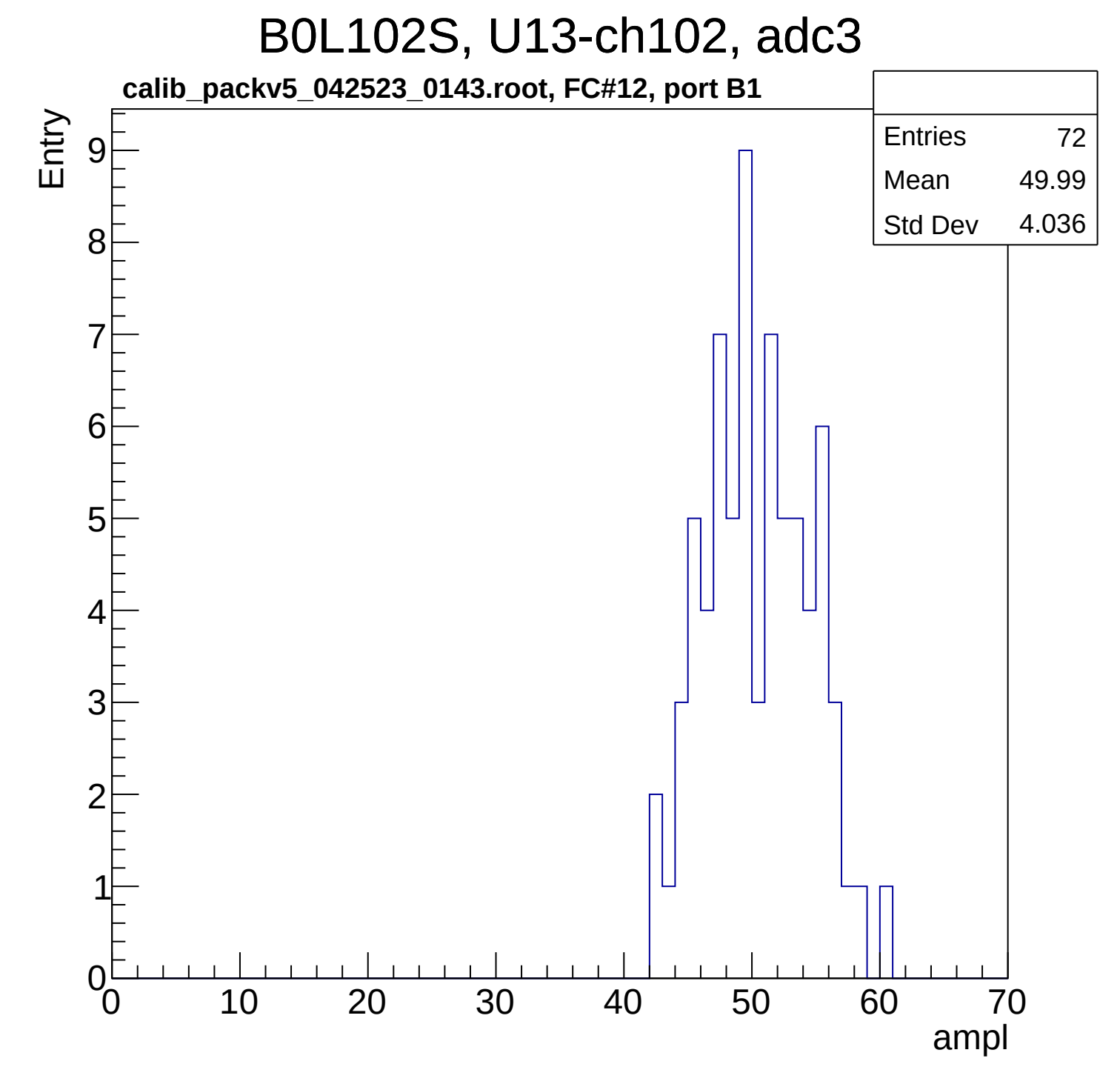

B0L102S, U13-ch101, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 19
Mean 58.21
Std Dev 13.82
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

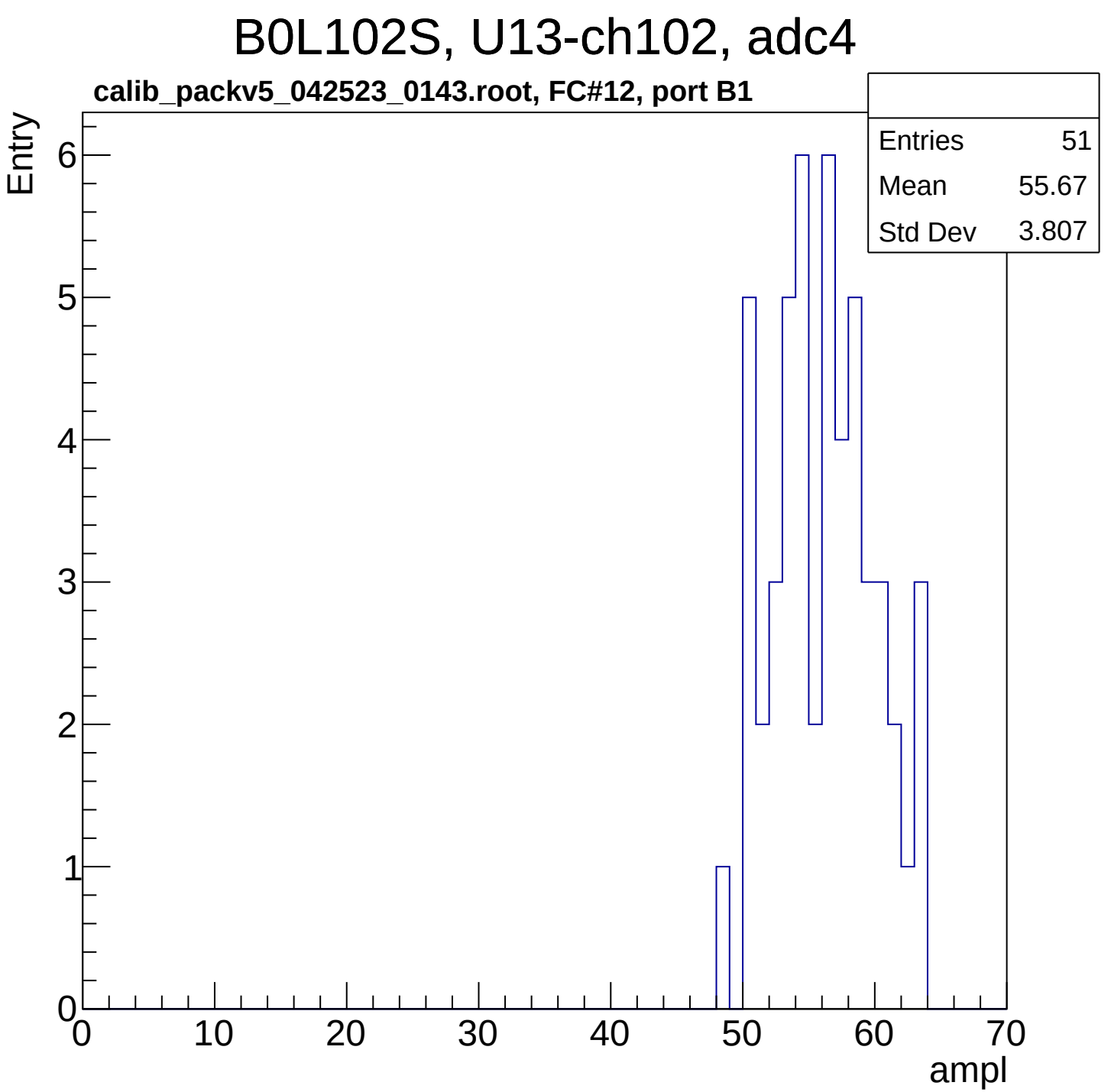

B0L102S, U13-ch101, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

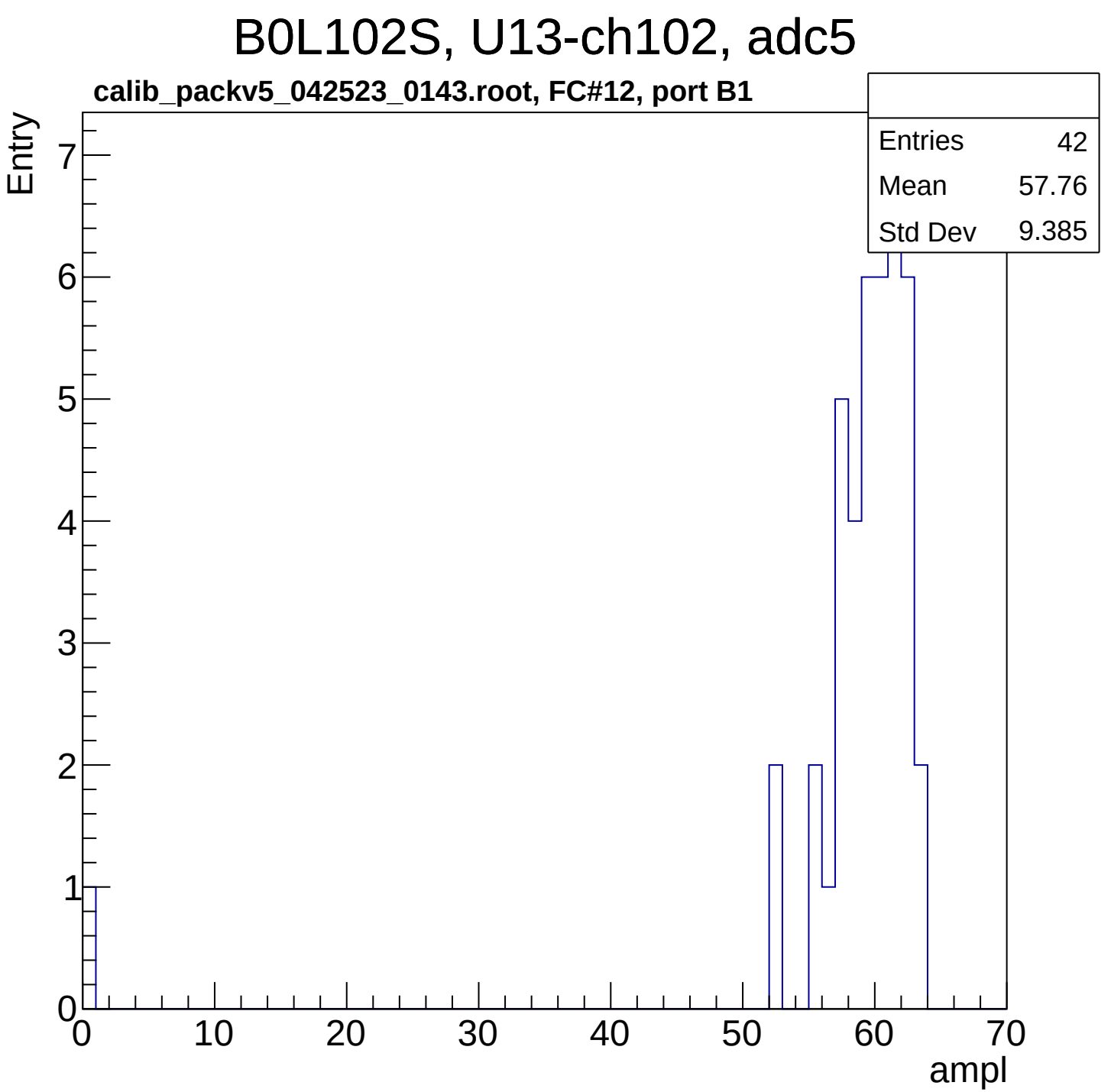

B0L102S, U13-ch102, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 29.81
Std Dev 3.596
Gaus mean : 30.8991
Gaus Width: 3.3004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch102, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 36.81
Std Dev 4.101
Gaus mean : 36.9638
Gaus Width: 4.0626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch102, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.31
Std Dev 3.484
Gaus mean : 43.3373
Gaus Width: 4.2083
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch102, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.99
Std Dev 4.036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch102, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.67
Std Dev 3.807
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch102, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 57.76
Std Dev 9.385
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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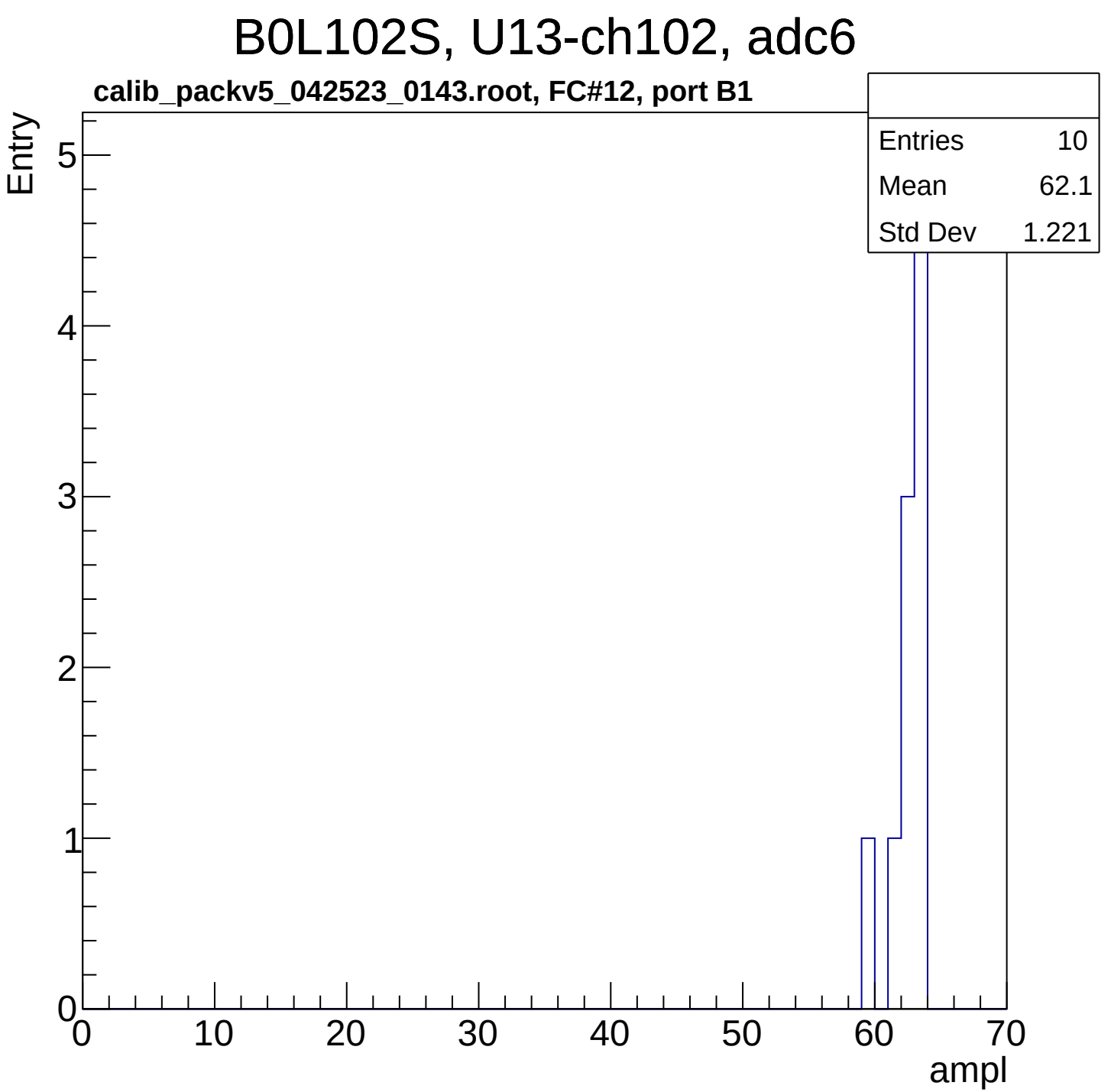
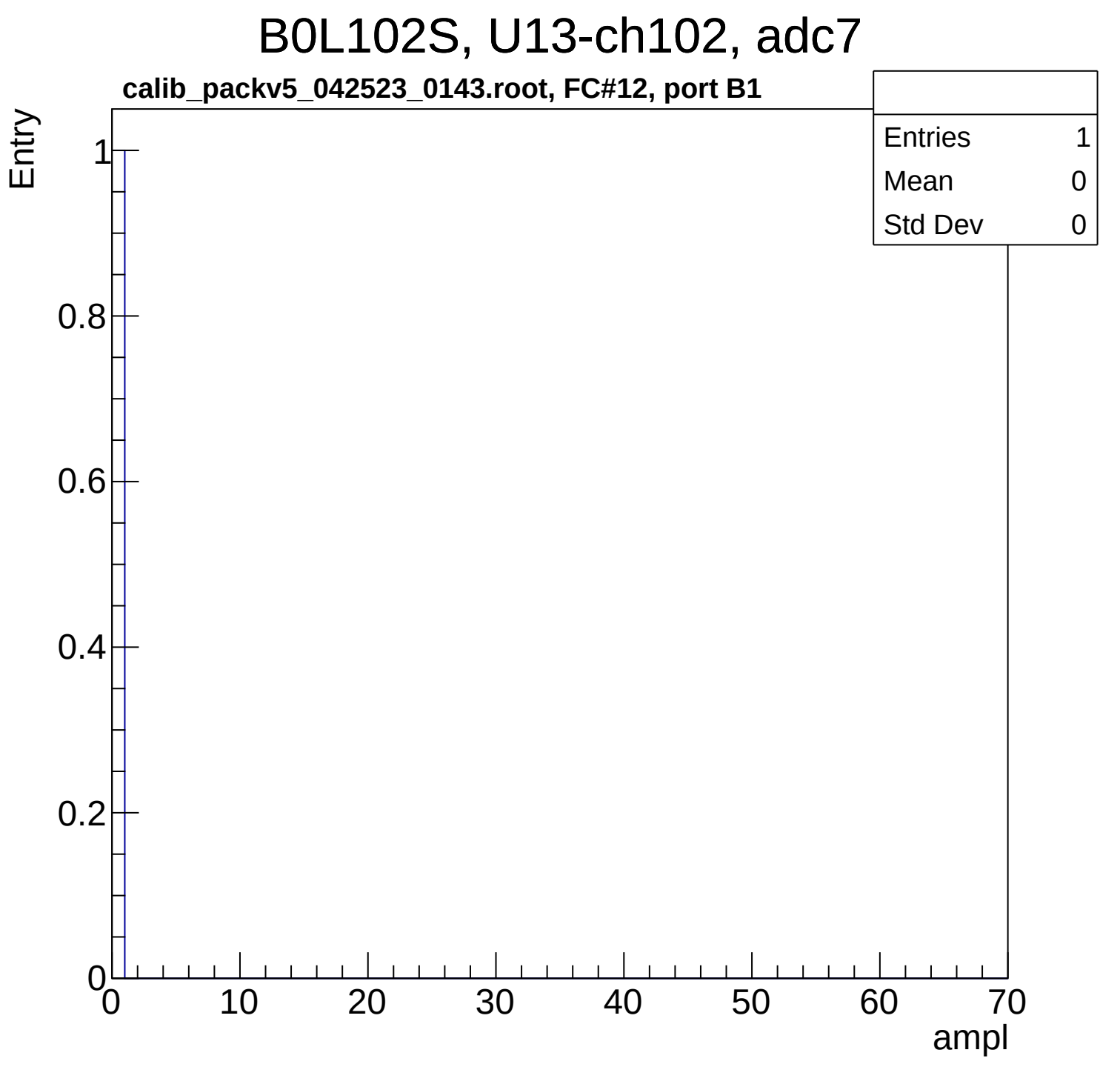
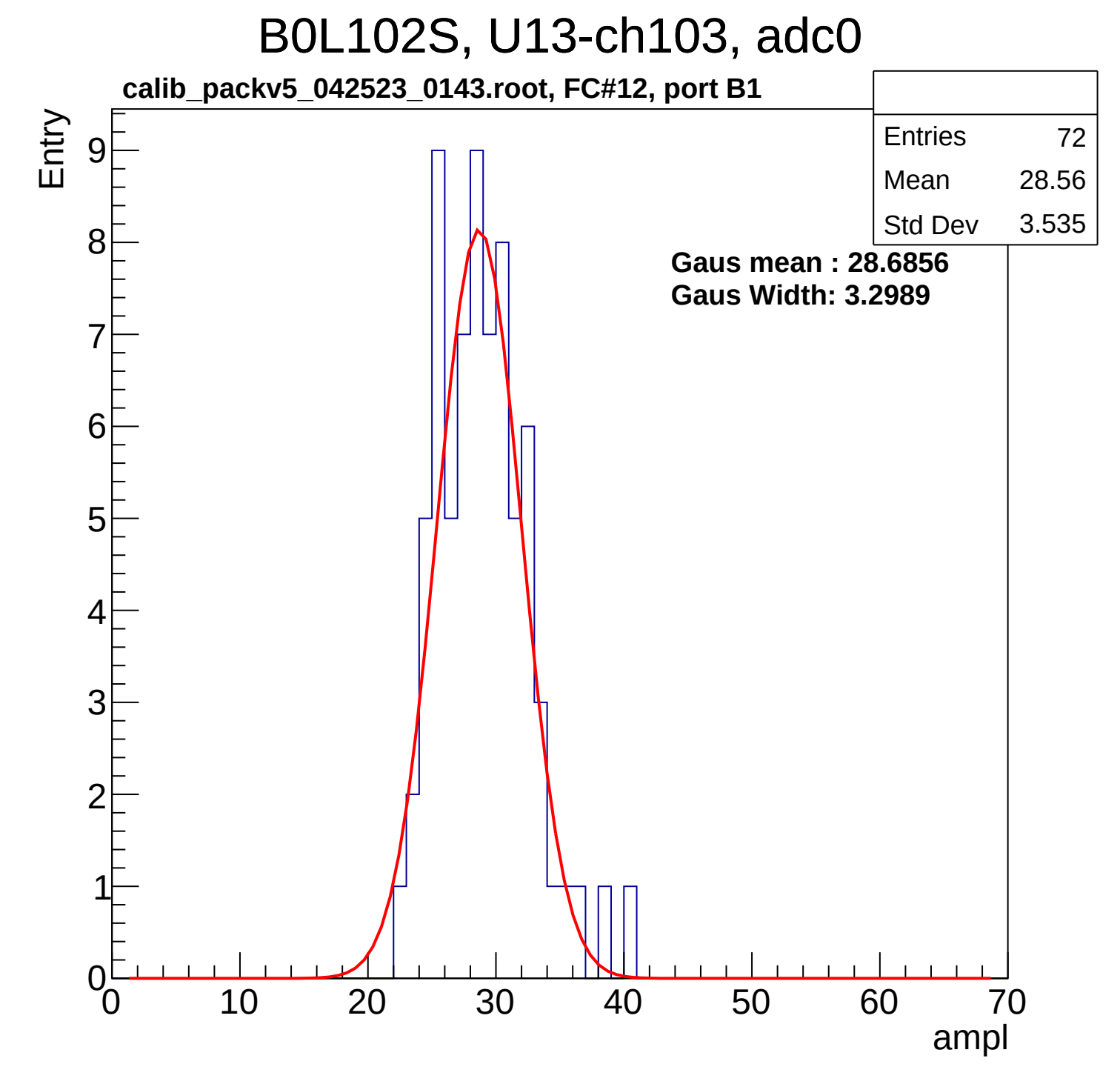
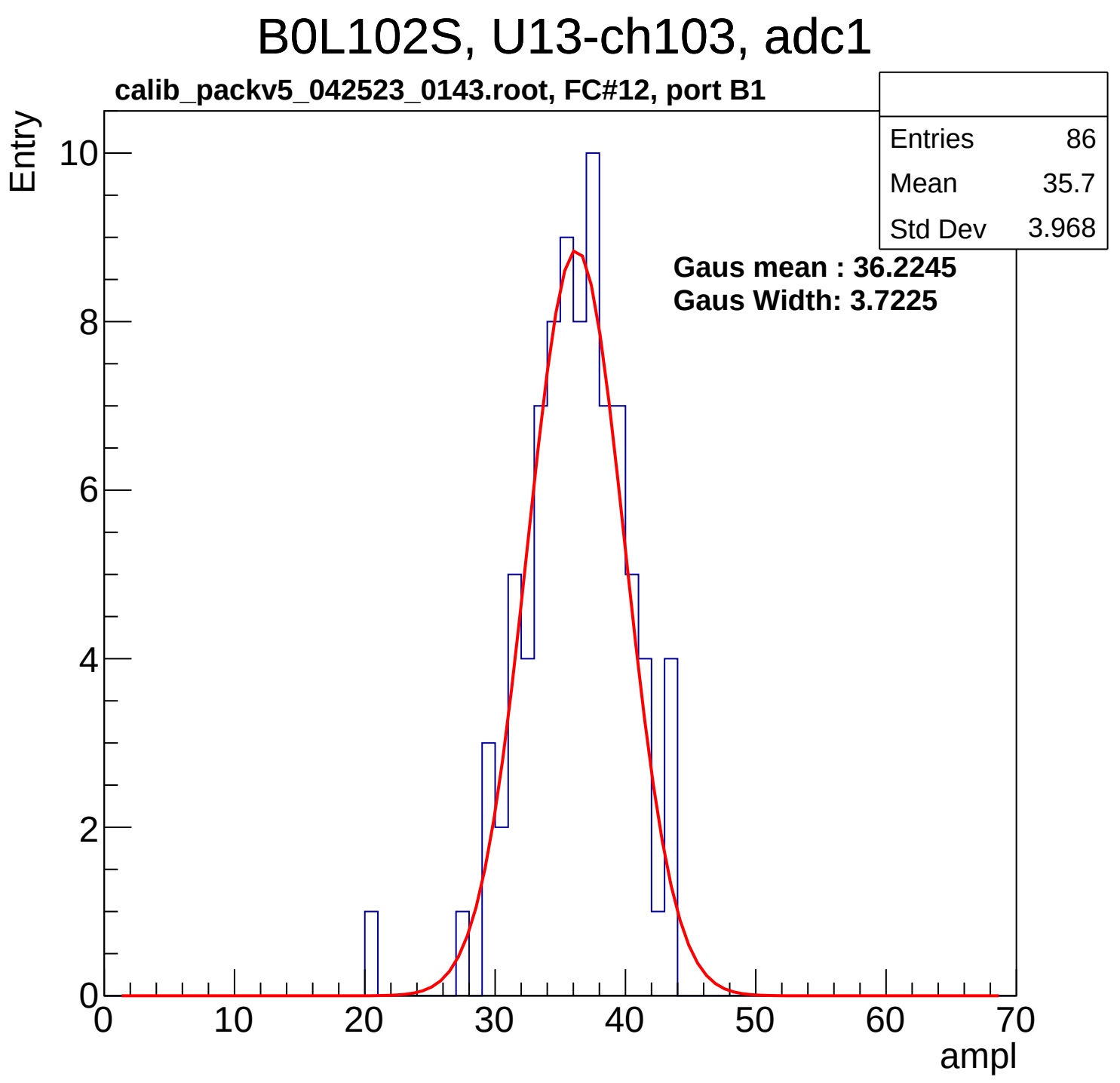
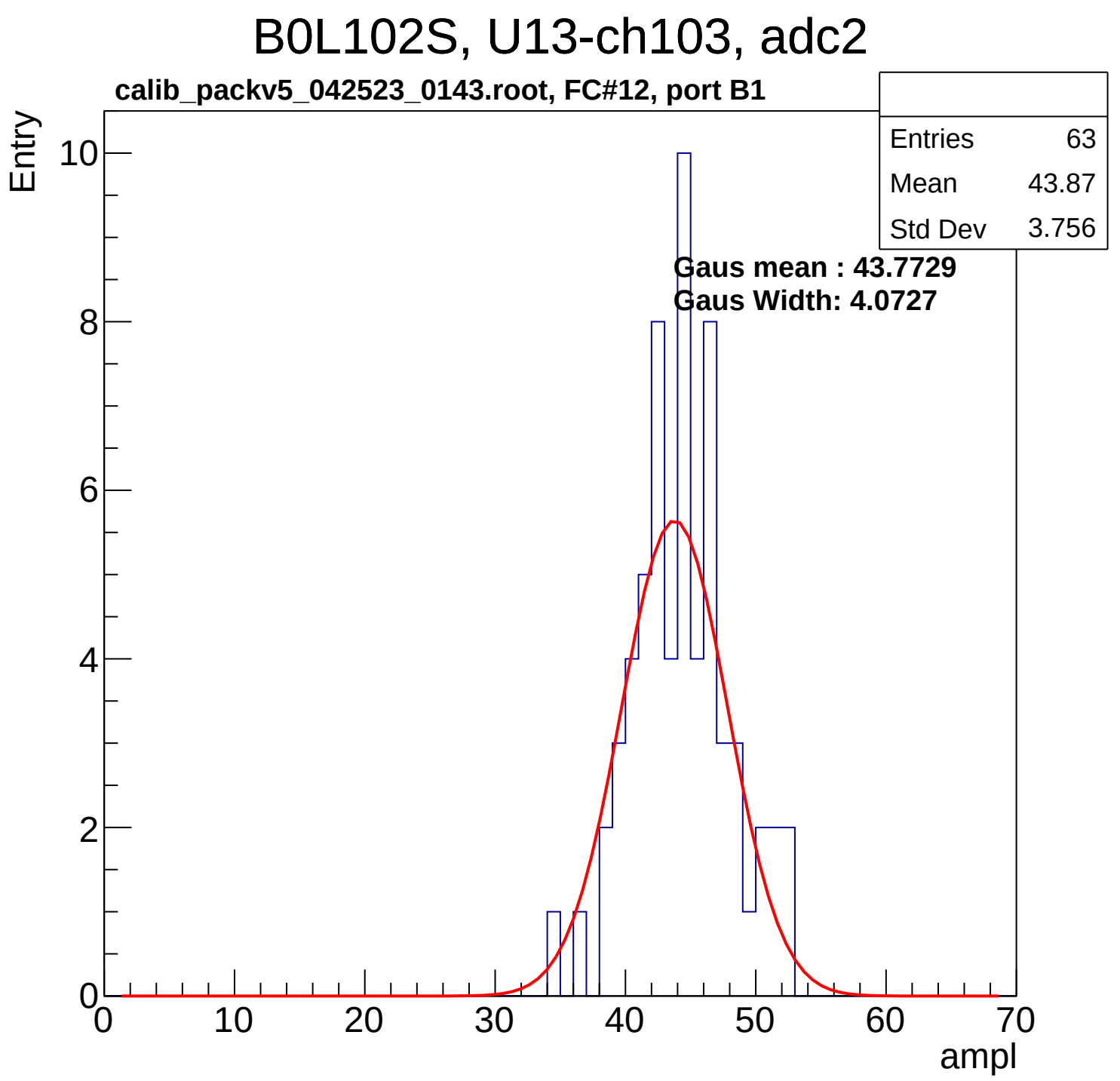
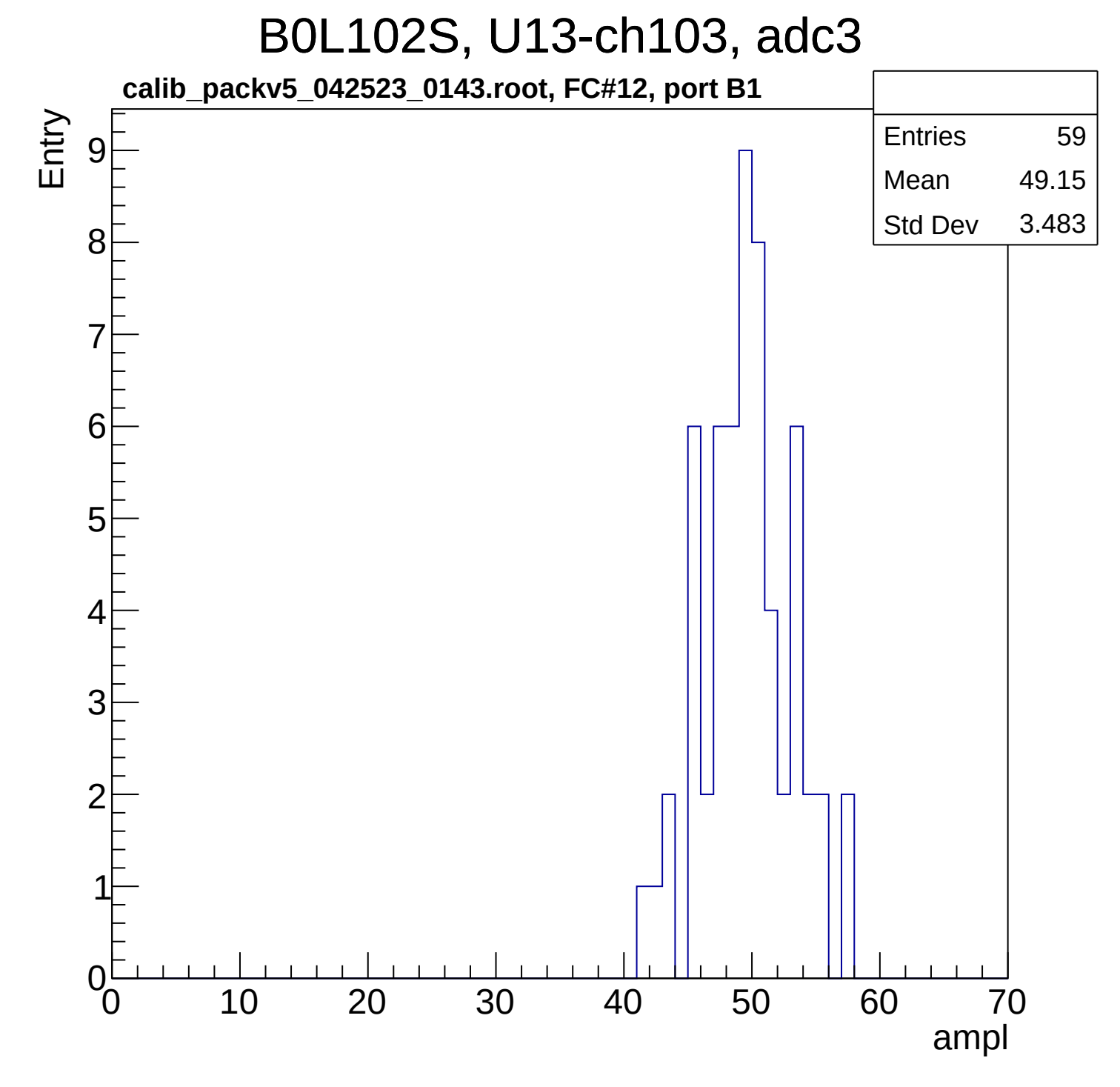

B0L102S, U13-ch102, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62.1
Std Dev 1.221
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch102, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch103, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 28.56
Std Dev 3.535
Gaus mean : 28.6856
Gaus Width: 3.2989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 35.7
Std Dev 3.968
Gaus mean : 36.2245
Gaus Width: 3.7225
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.87
Std Dev 3.756
Gaus mean : 43.7729
Gaus Width: 4.0727
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 49.15
Std Dev 3.483
Entry
ampl
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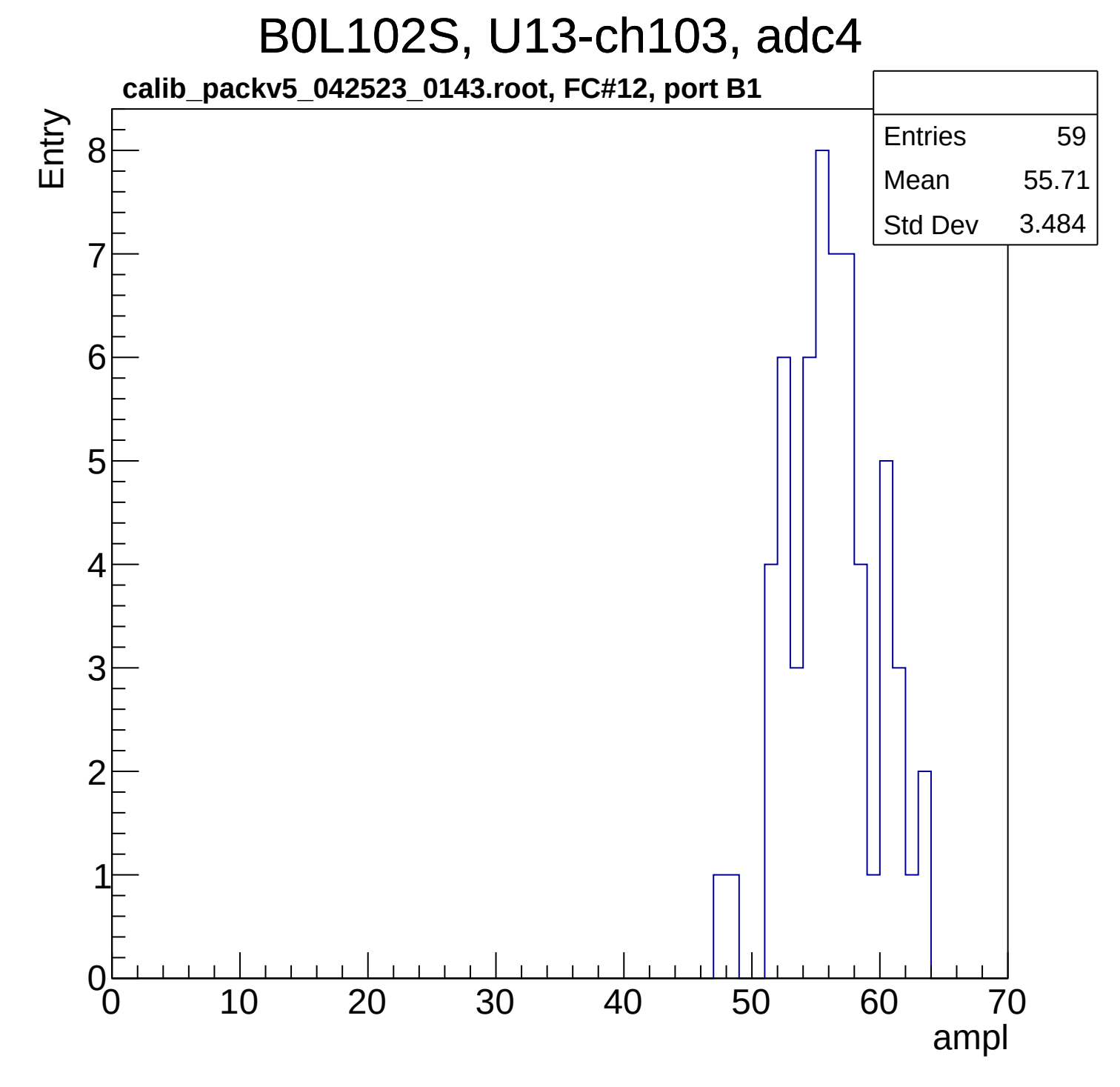
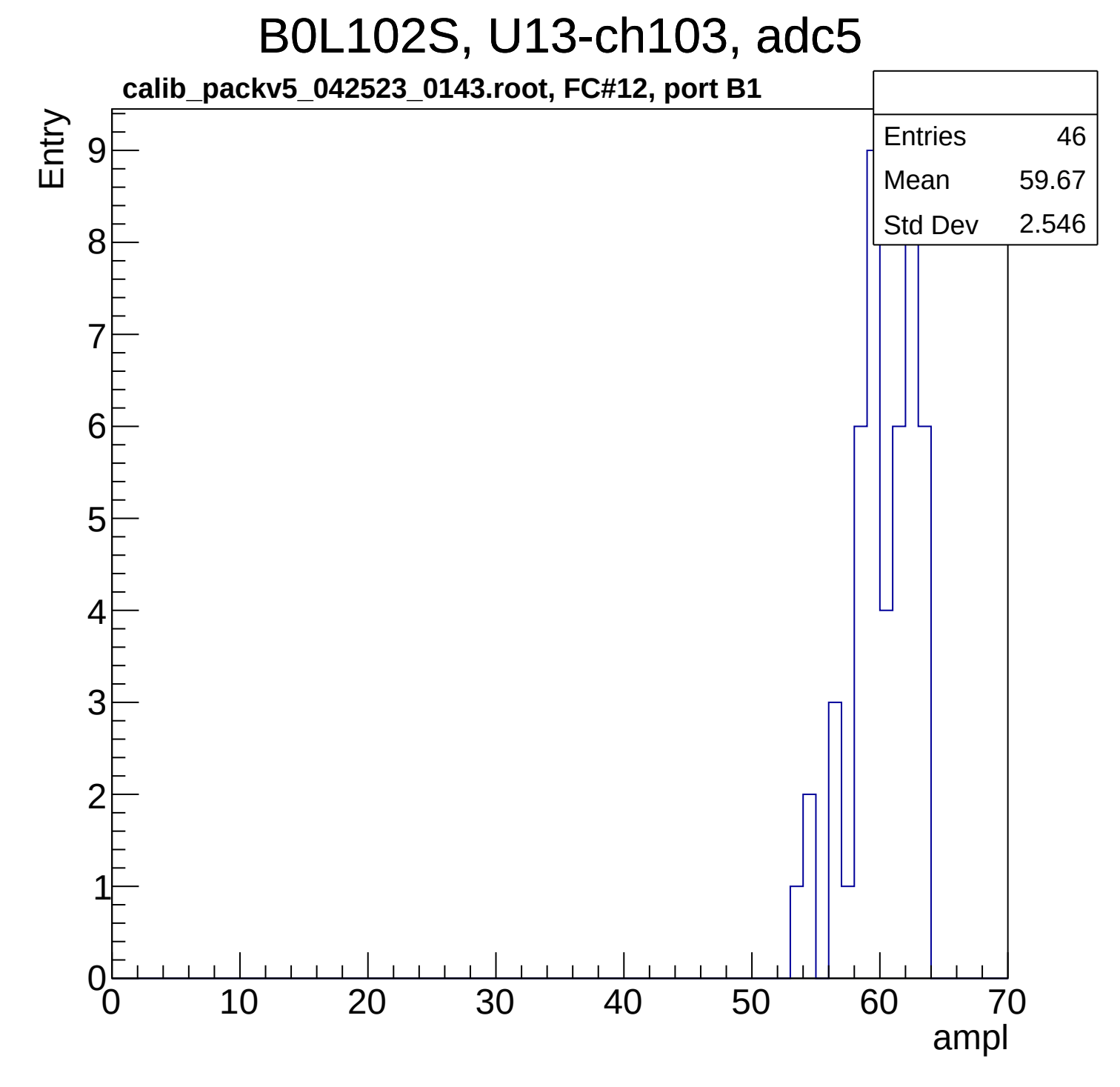
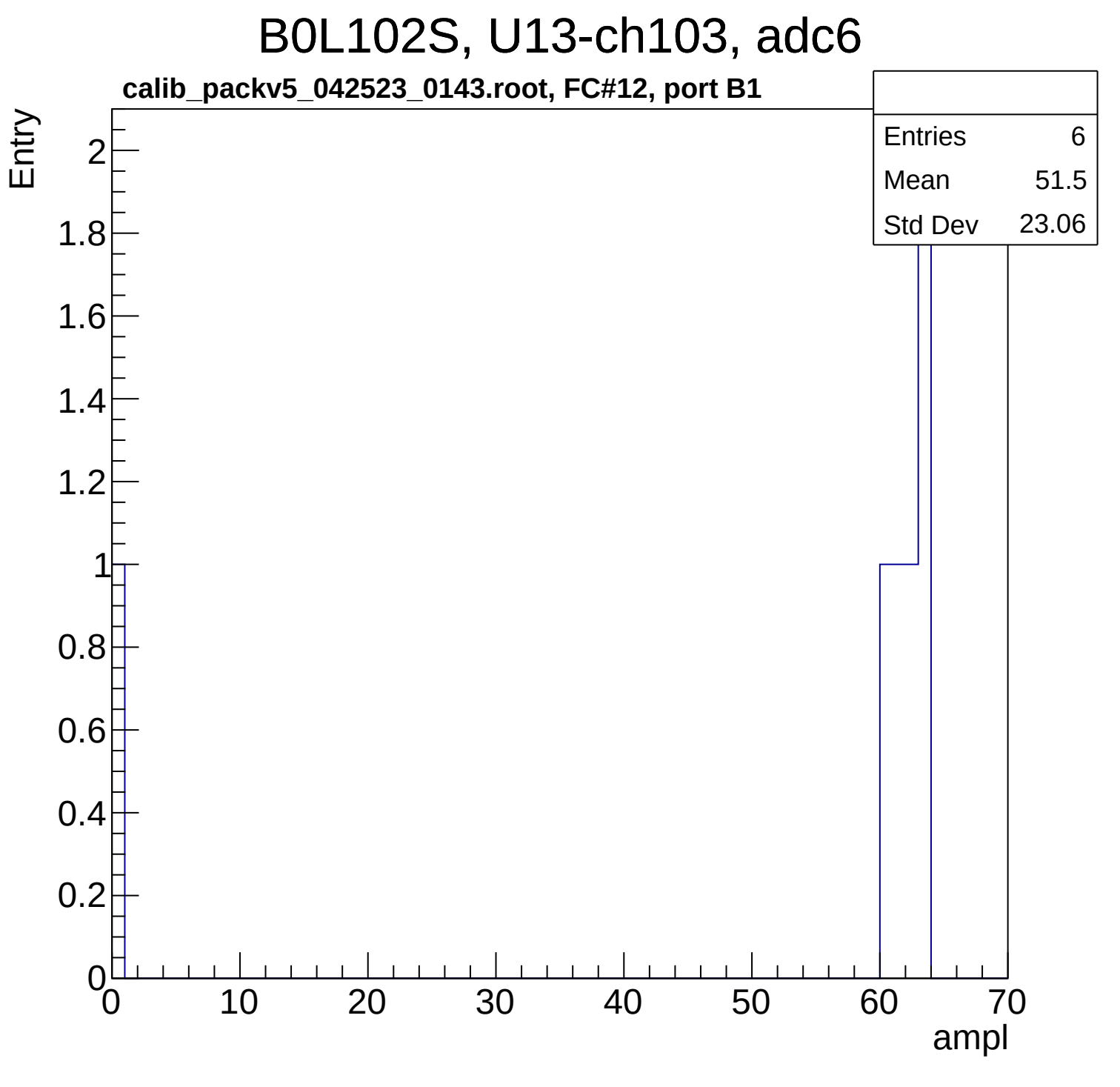
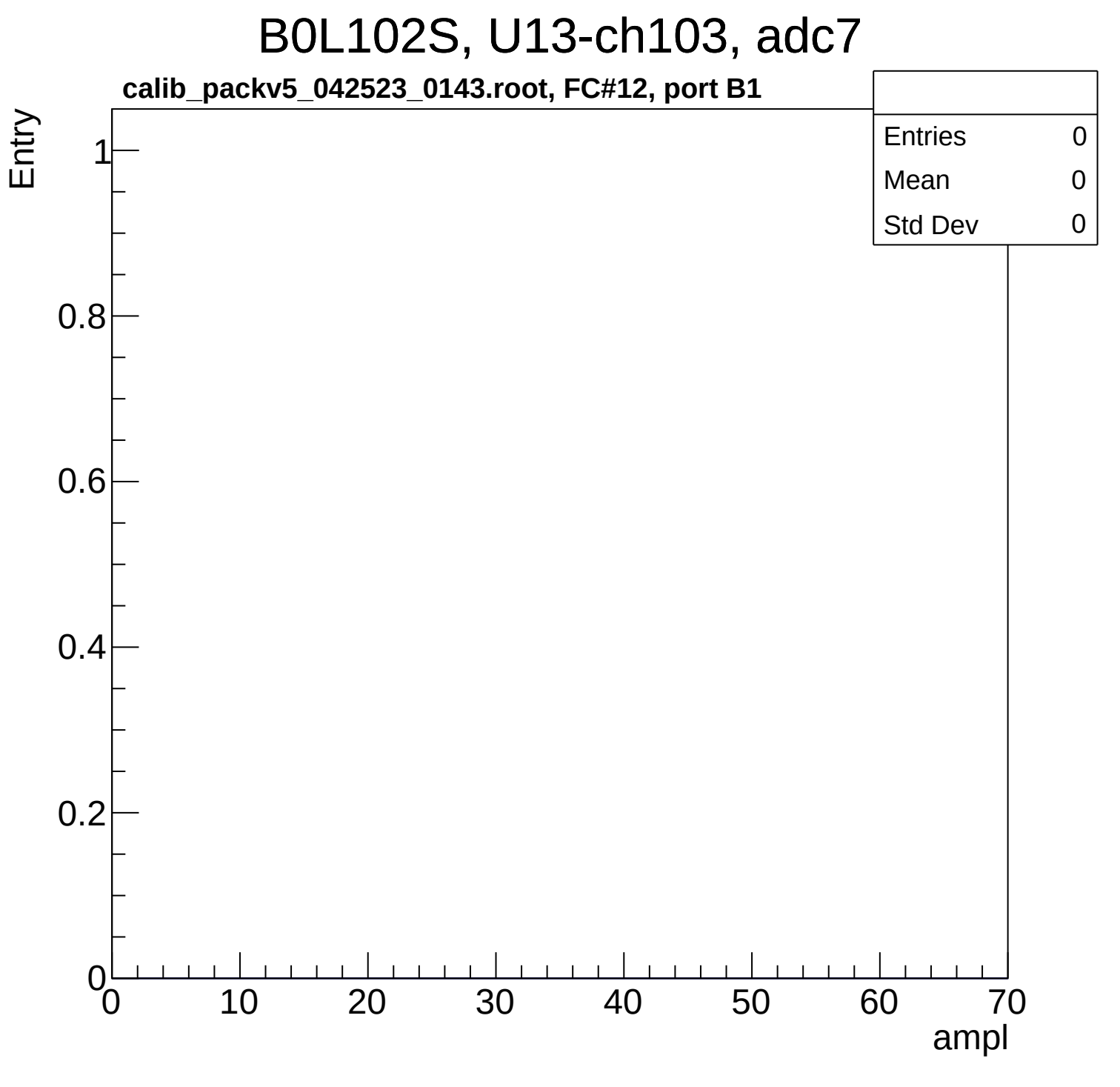
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.71
Std Dev 3.484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.67
Std Dev 2.546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch103, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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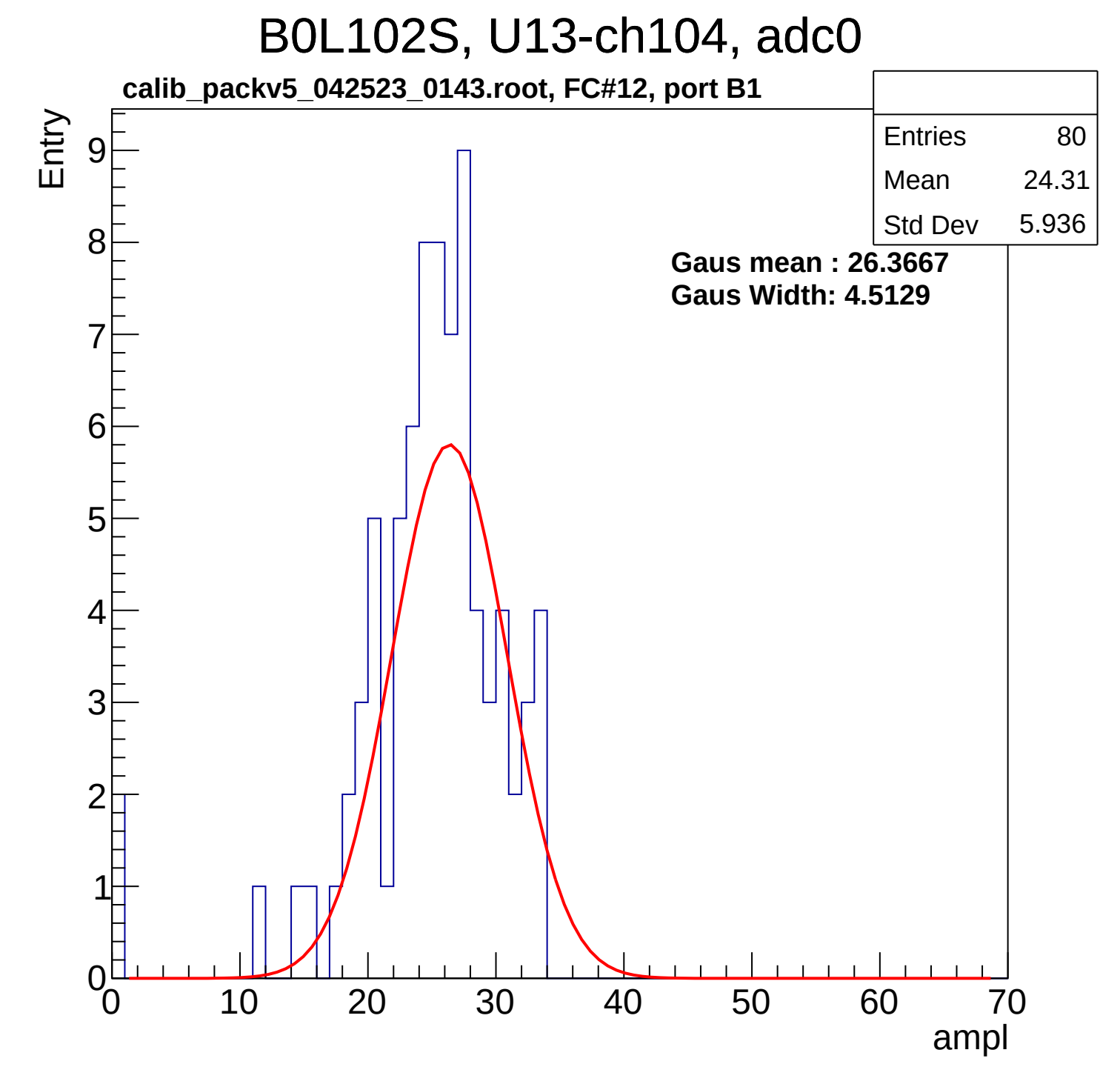
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch104, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 24.31
Std Dev 5.936
Gaus mean : 26.3667
Gaus Width: 4.5129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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50
60
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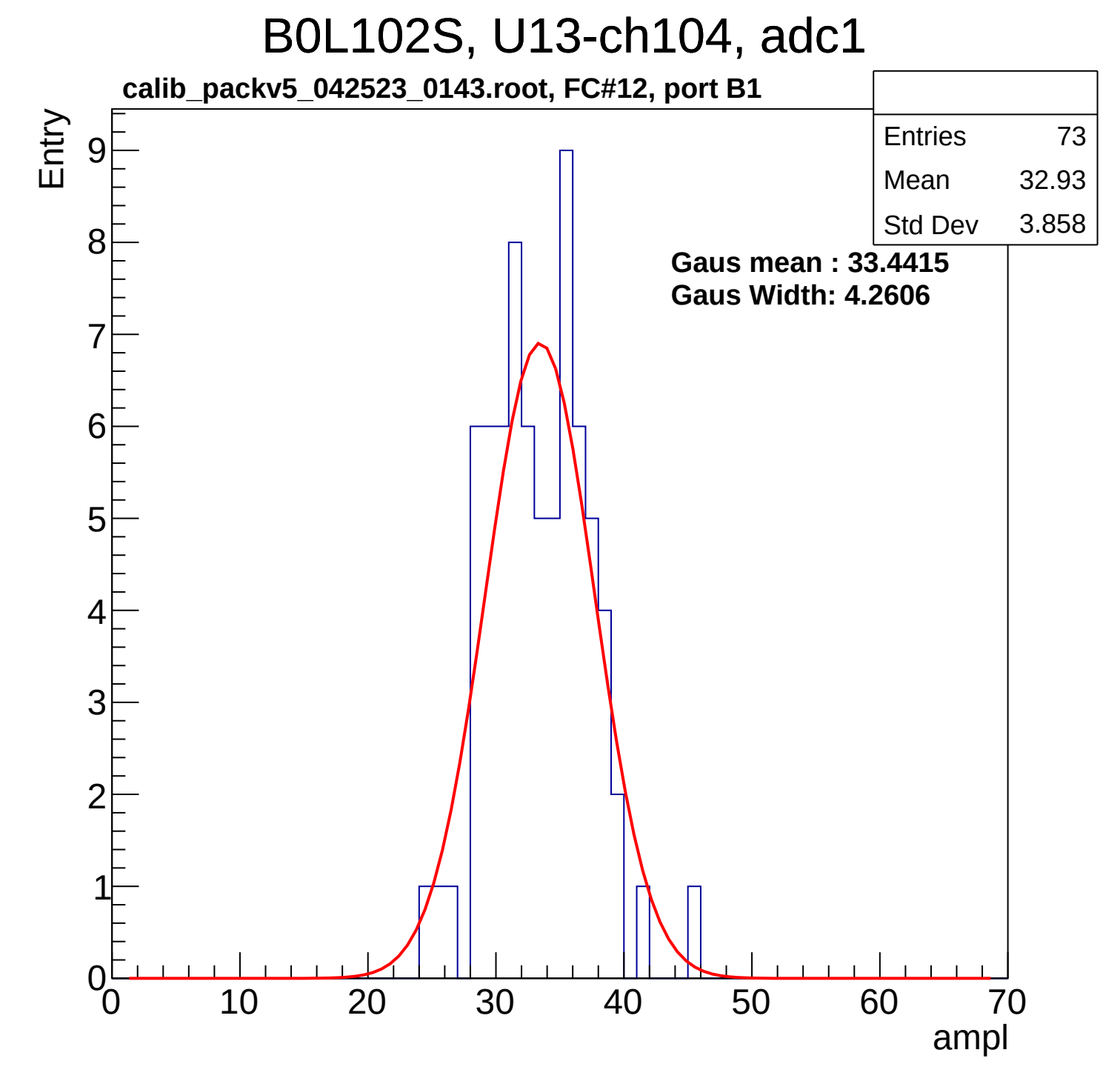
B0L102S, U13-ch104, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 32.93
Std Dev 3.858
Gaus mean : 33.4415
Gaus Width: 4.2606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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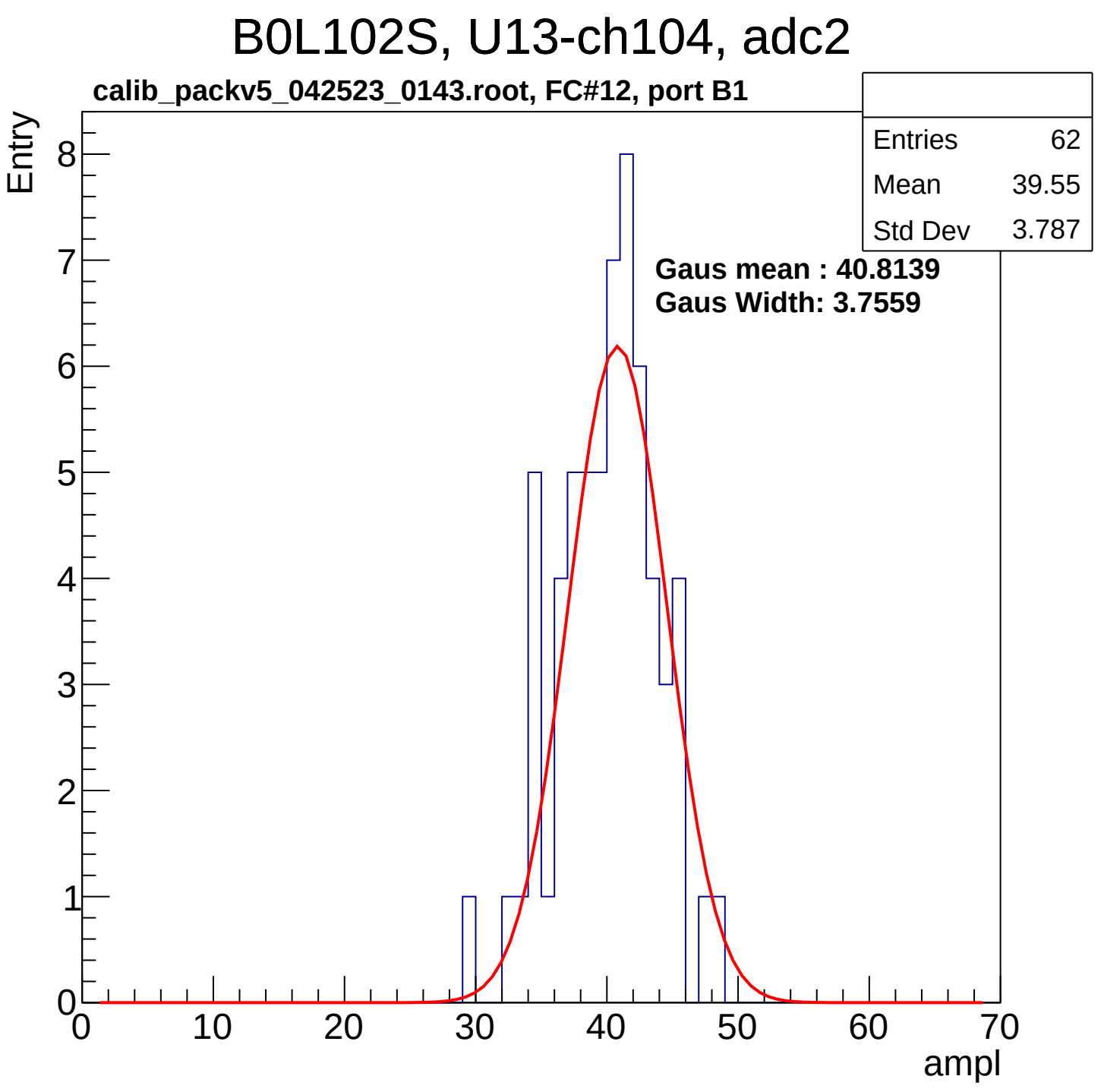
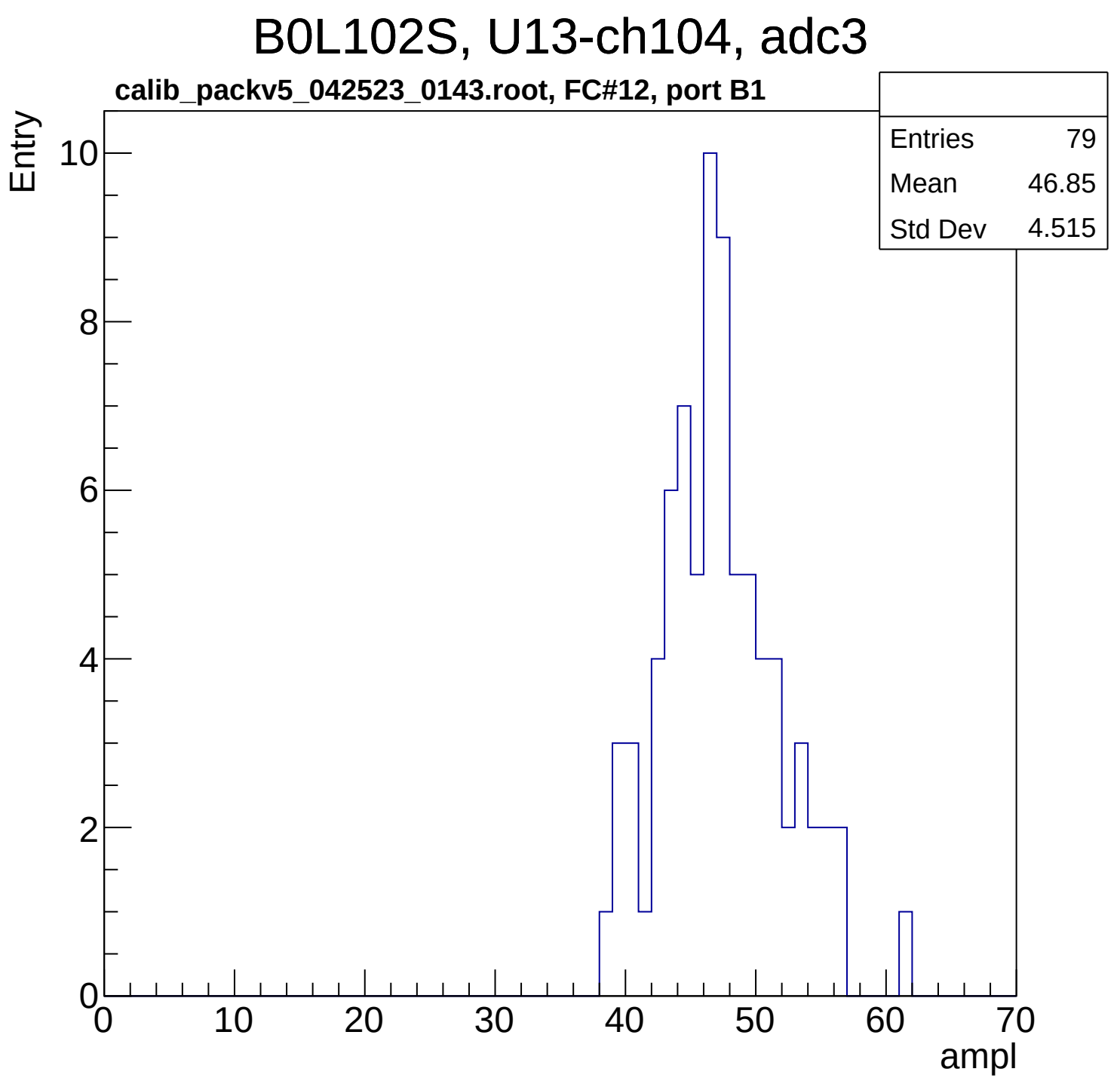
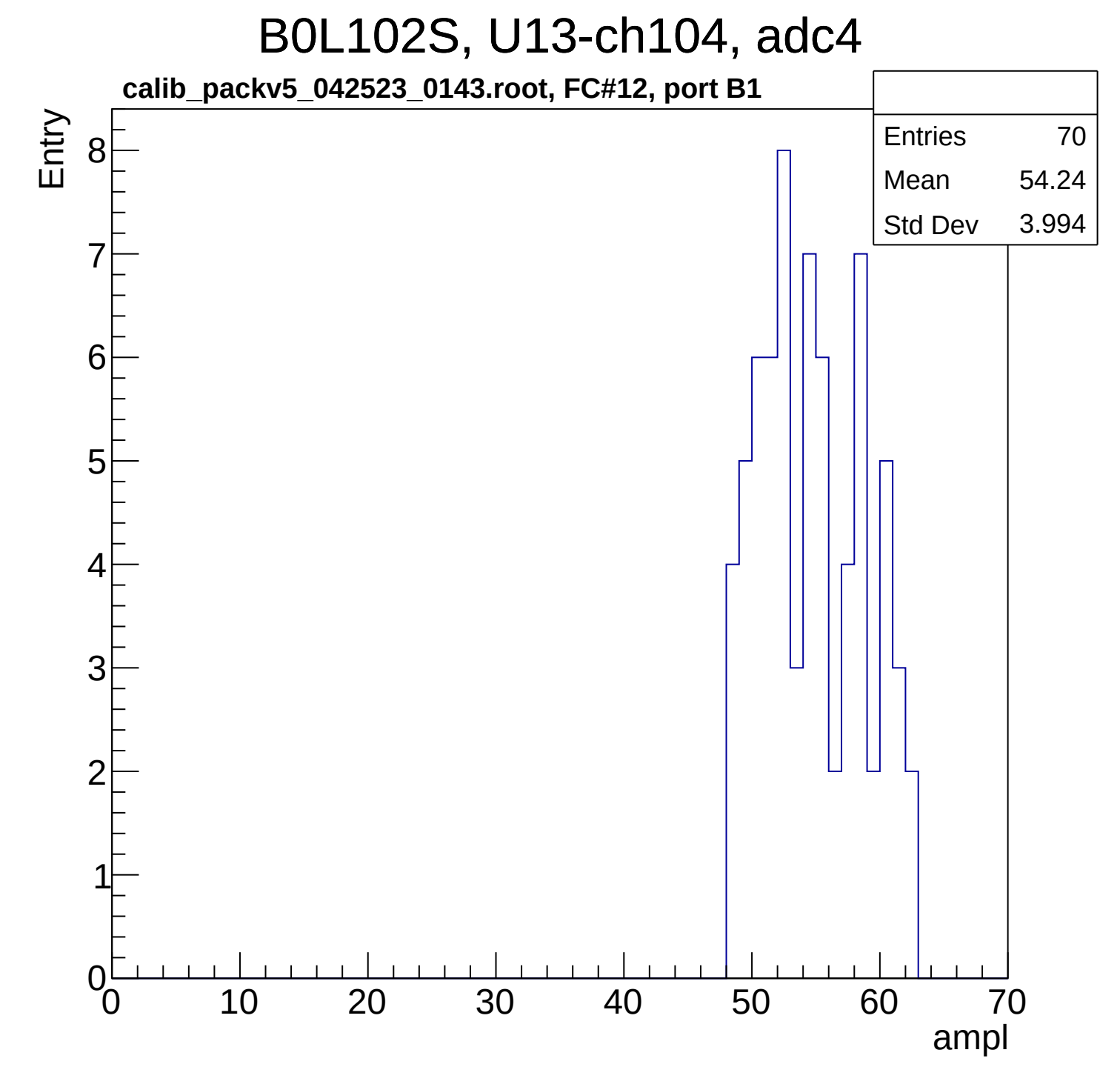
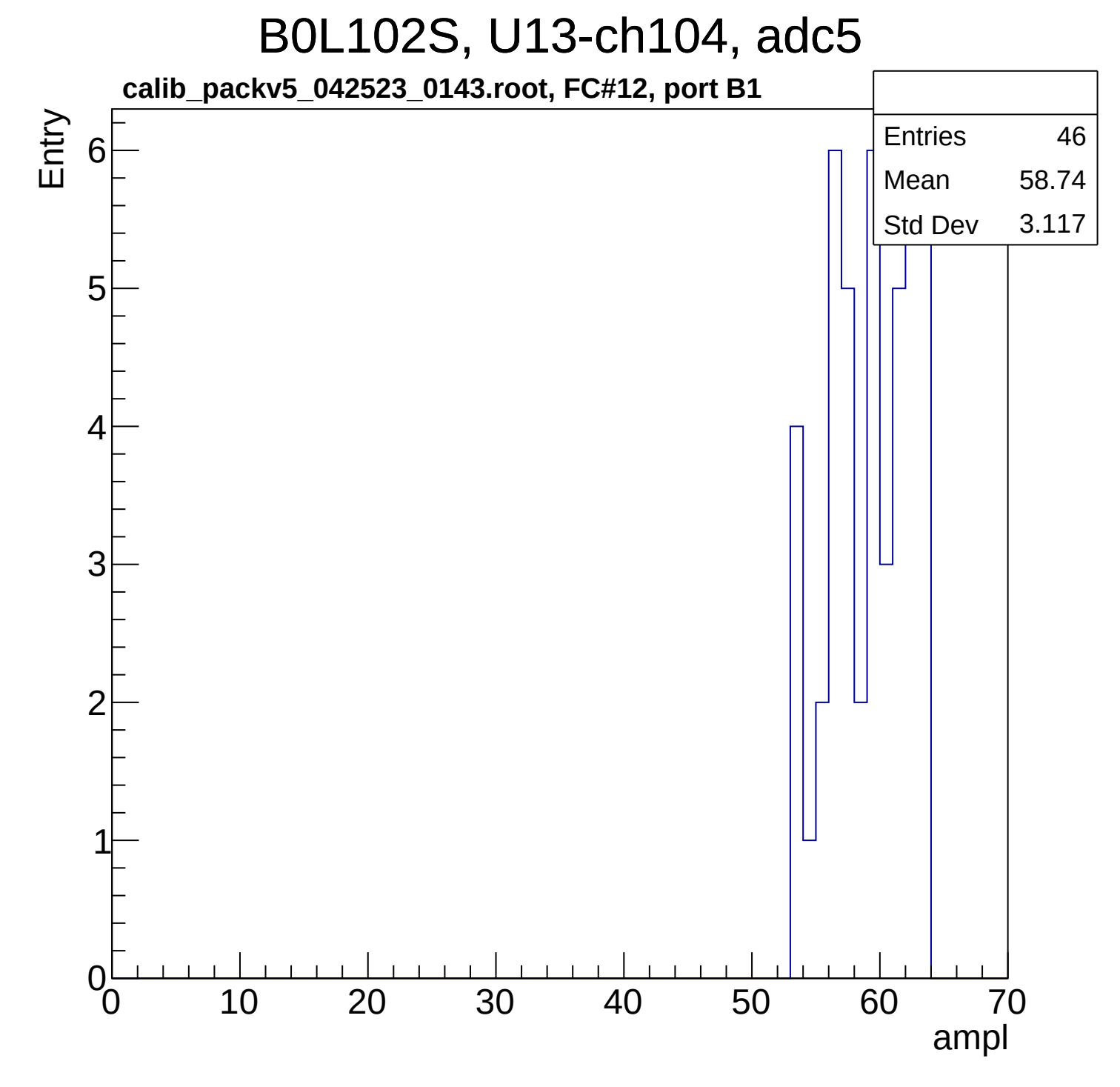
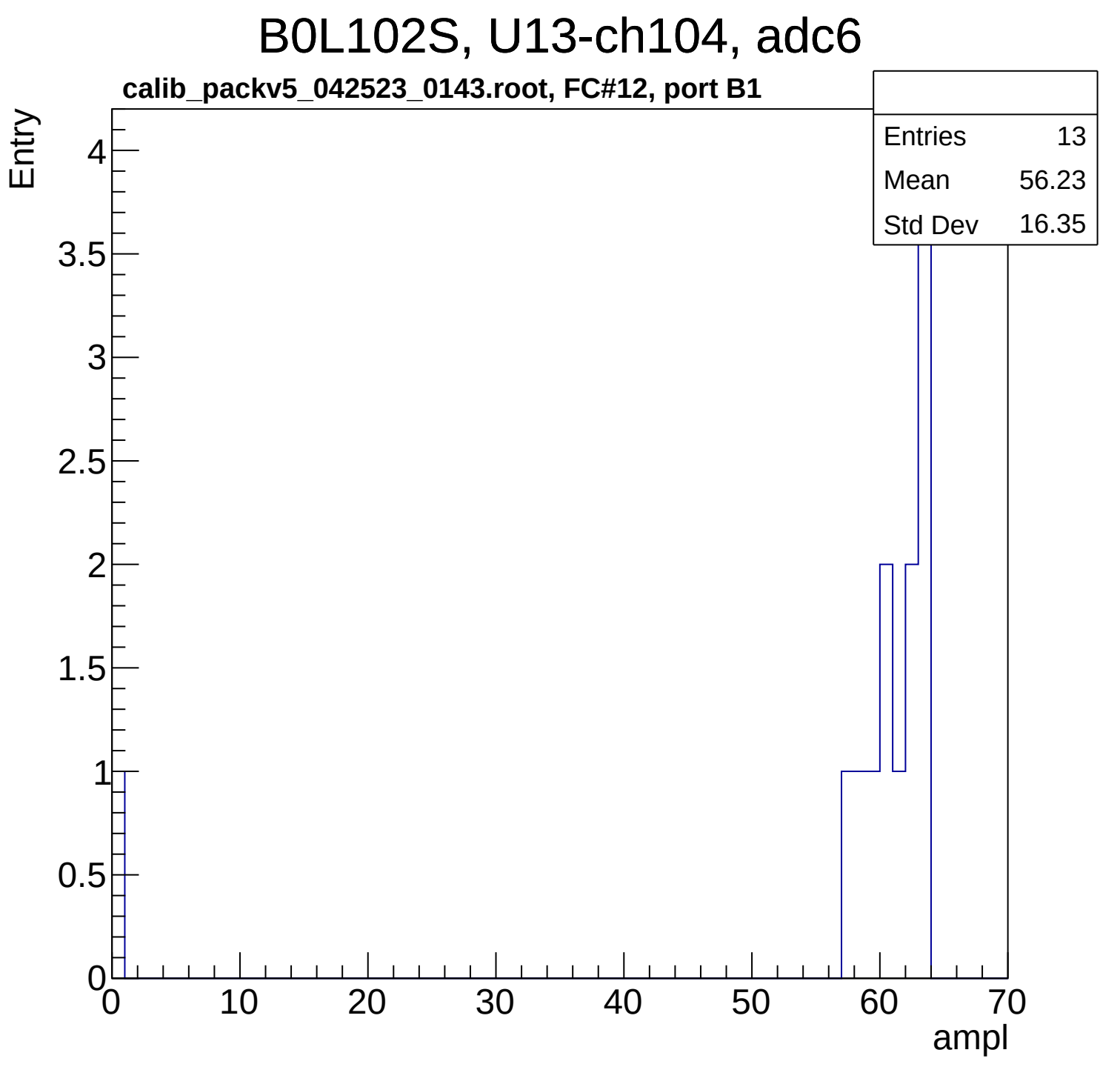
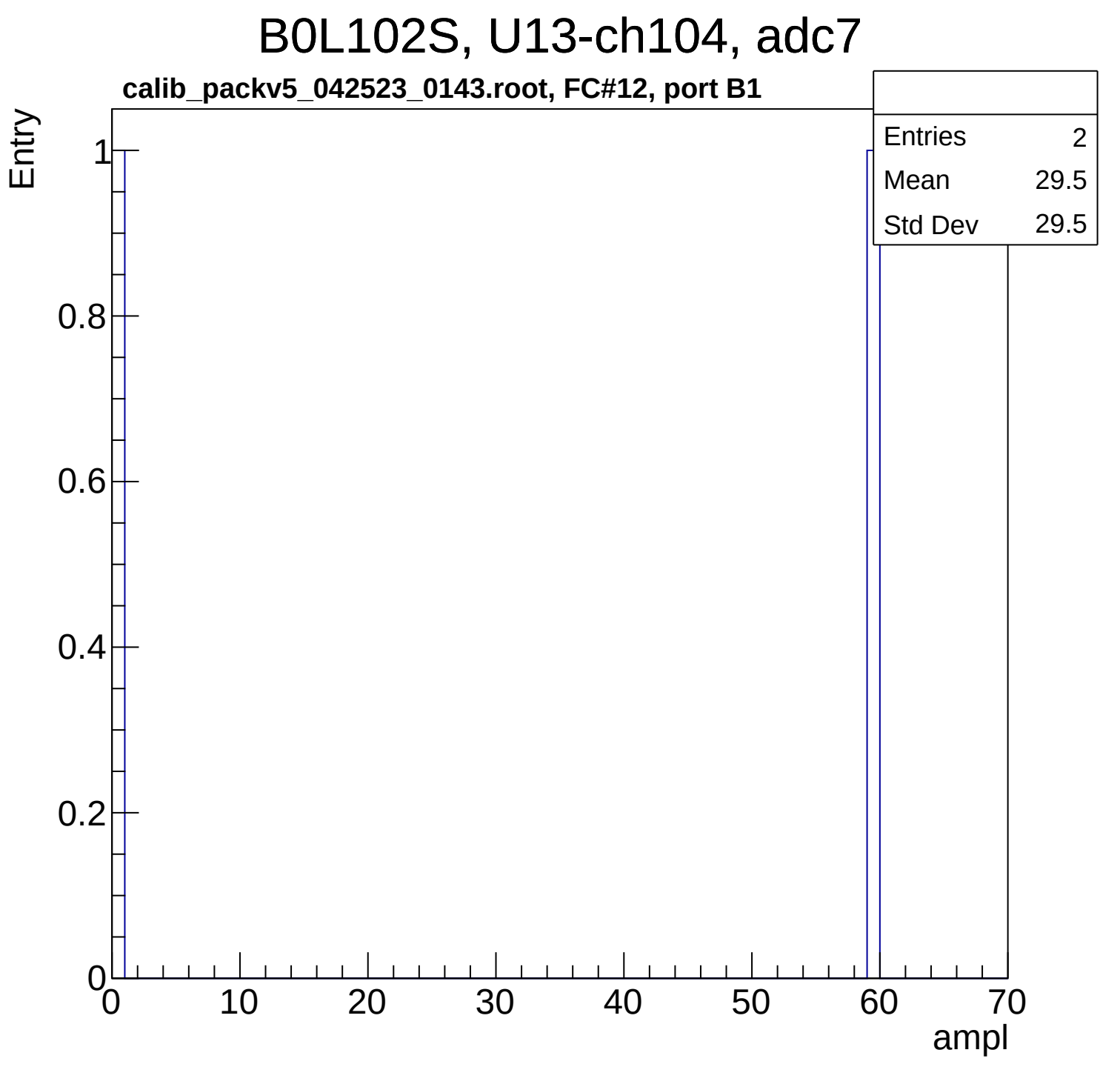

B0L102S, U13-ch104, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 39.55
Std Dev 3.787
Gaus mean : 40.8139
Gaus Width: 3.7559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

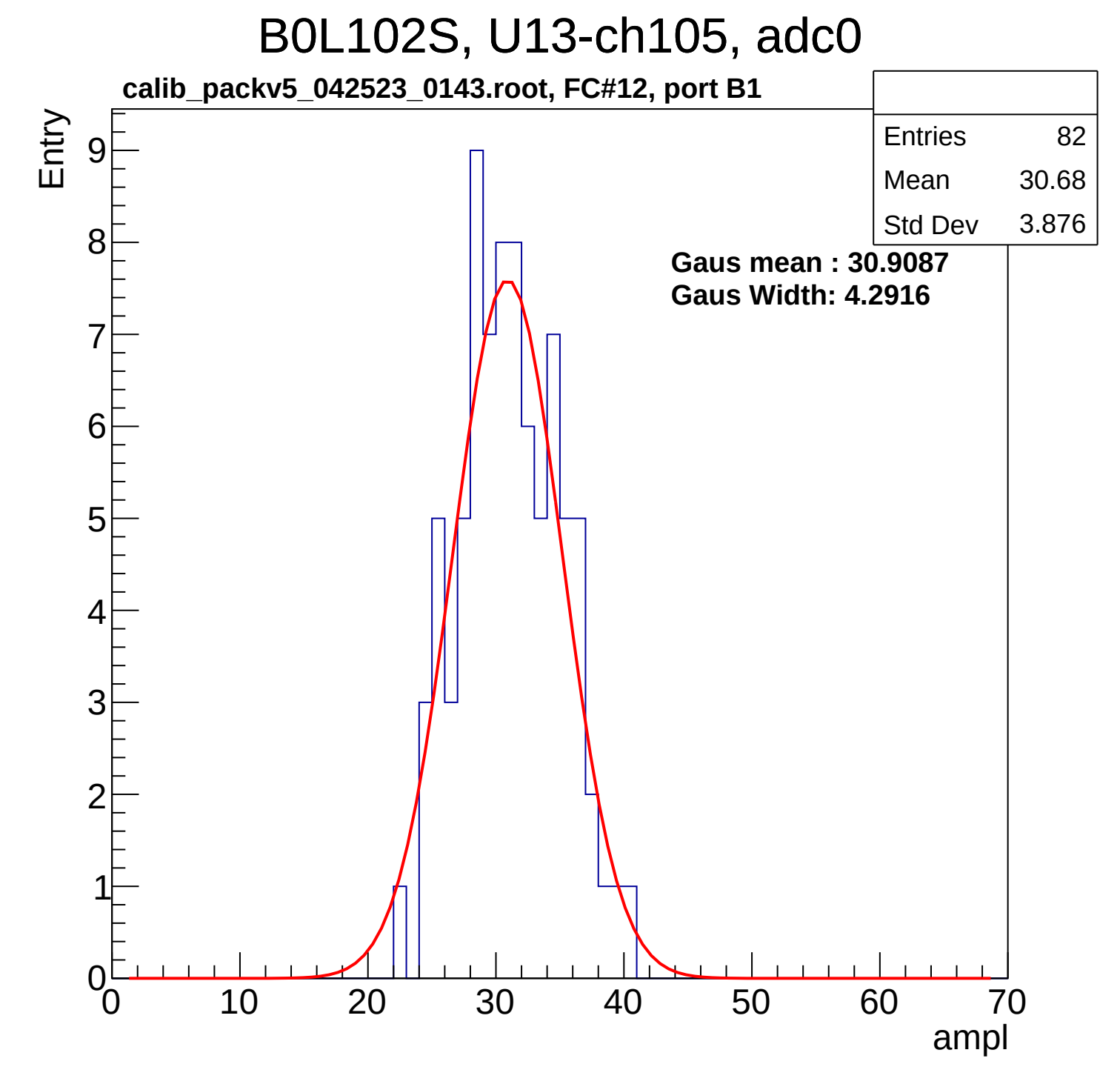

B0L102S, U13-ch104, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 46.85
Std Dev 4.515
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

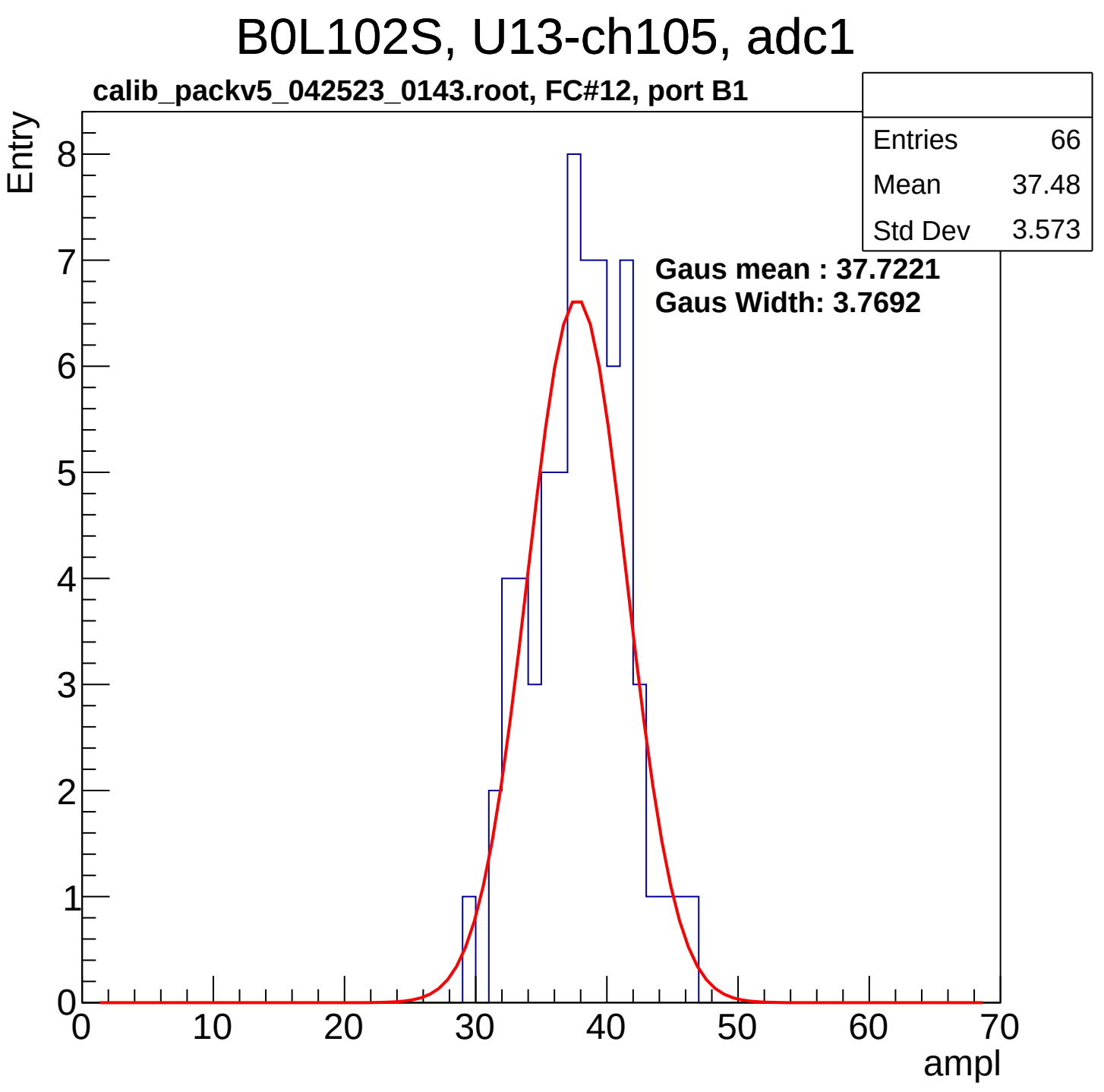

B0L102S, U13-ch104, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 54.24
Std Dev 3.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

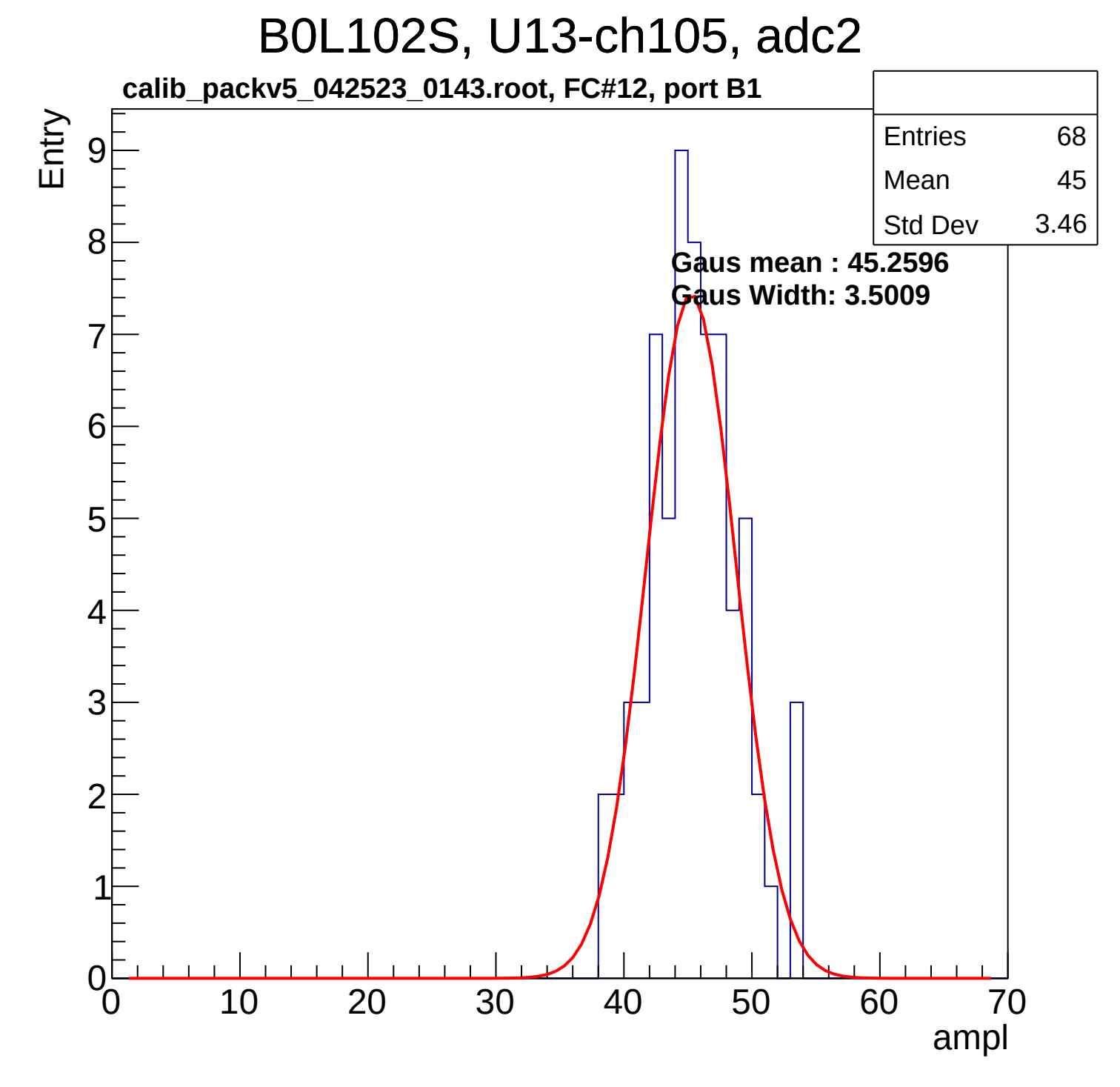

B0L102S, U13-ch104, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.74
Std Dev 3.117
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

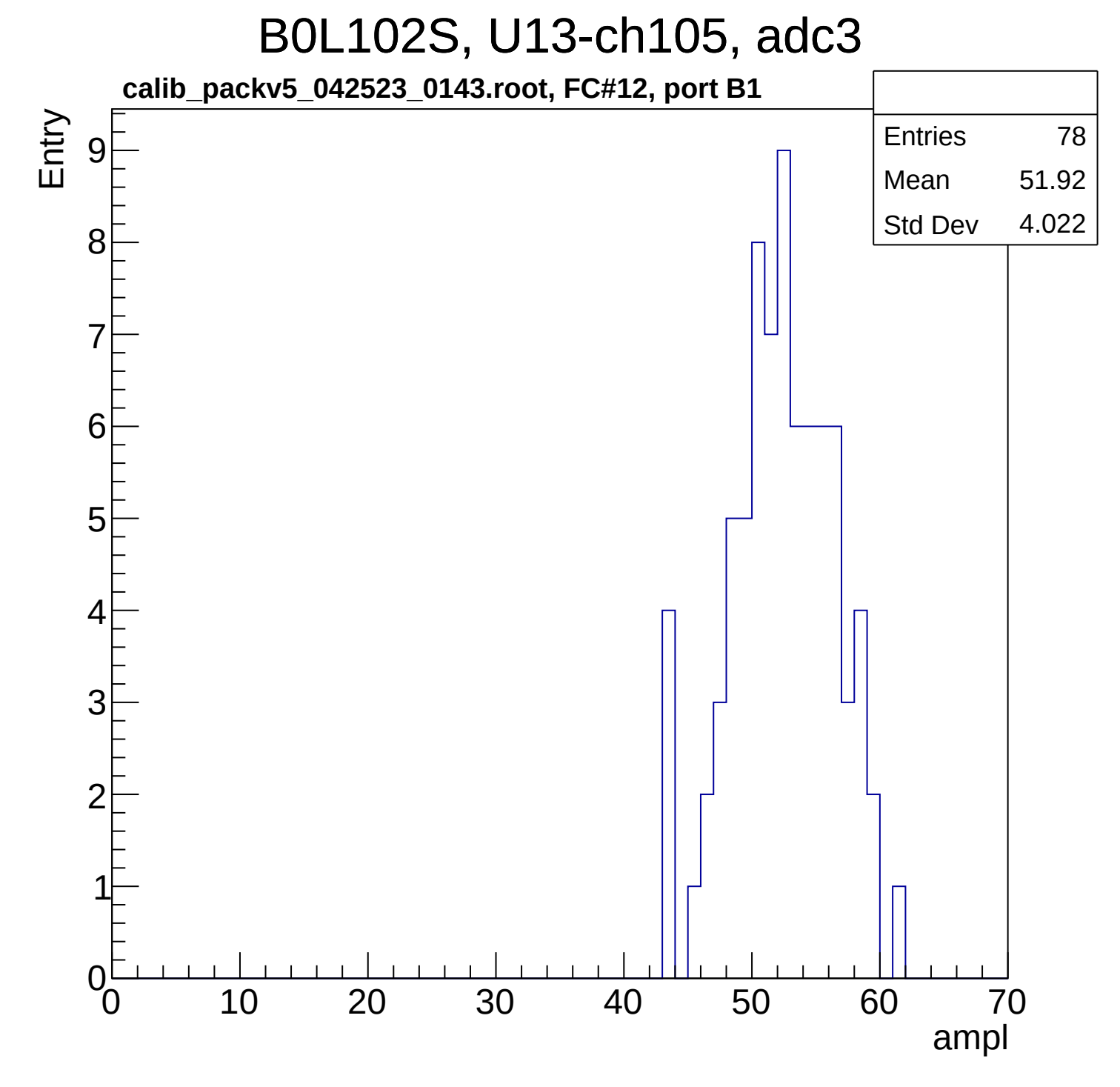

B0L102S, U13-ch104, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.23
Std Dev 16.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch104, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 29.5
Std Dev 29.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch105, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 30.68
Std Dev 3.876
Gaus mean : 30.9087
Gaus Width: 4.2916
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch105, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.48
Std Dev 3.573
Gaus mean : 37.7221
Gaus Width: 3.7692
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch105, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 45
Std Dev 3.46
Gaus mean : 45.2596
Gaus Width: 3.5009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch105, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 51.92
Std Dev 4.022
Entry
ampl
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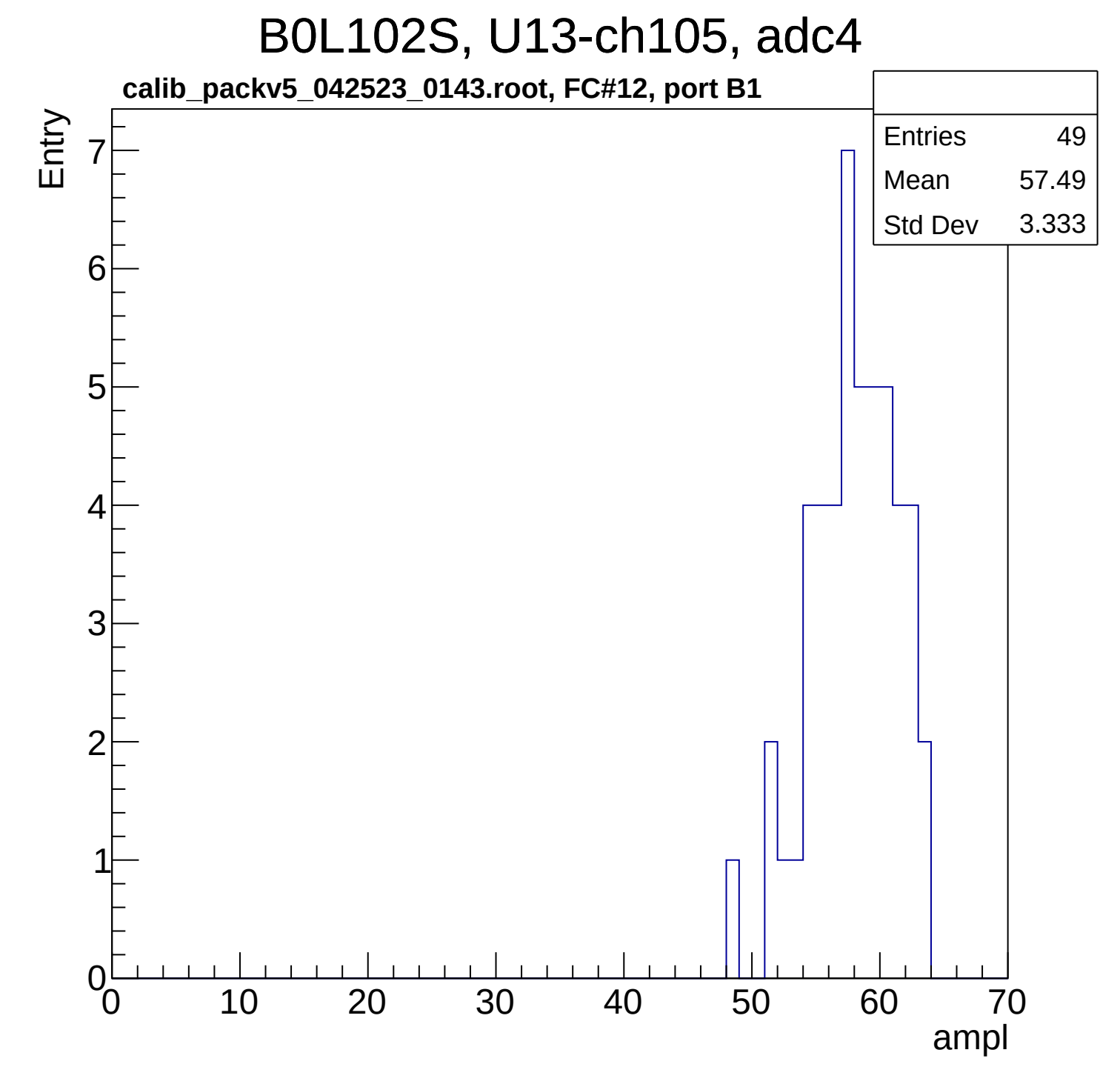
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch105, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 57.49
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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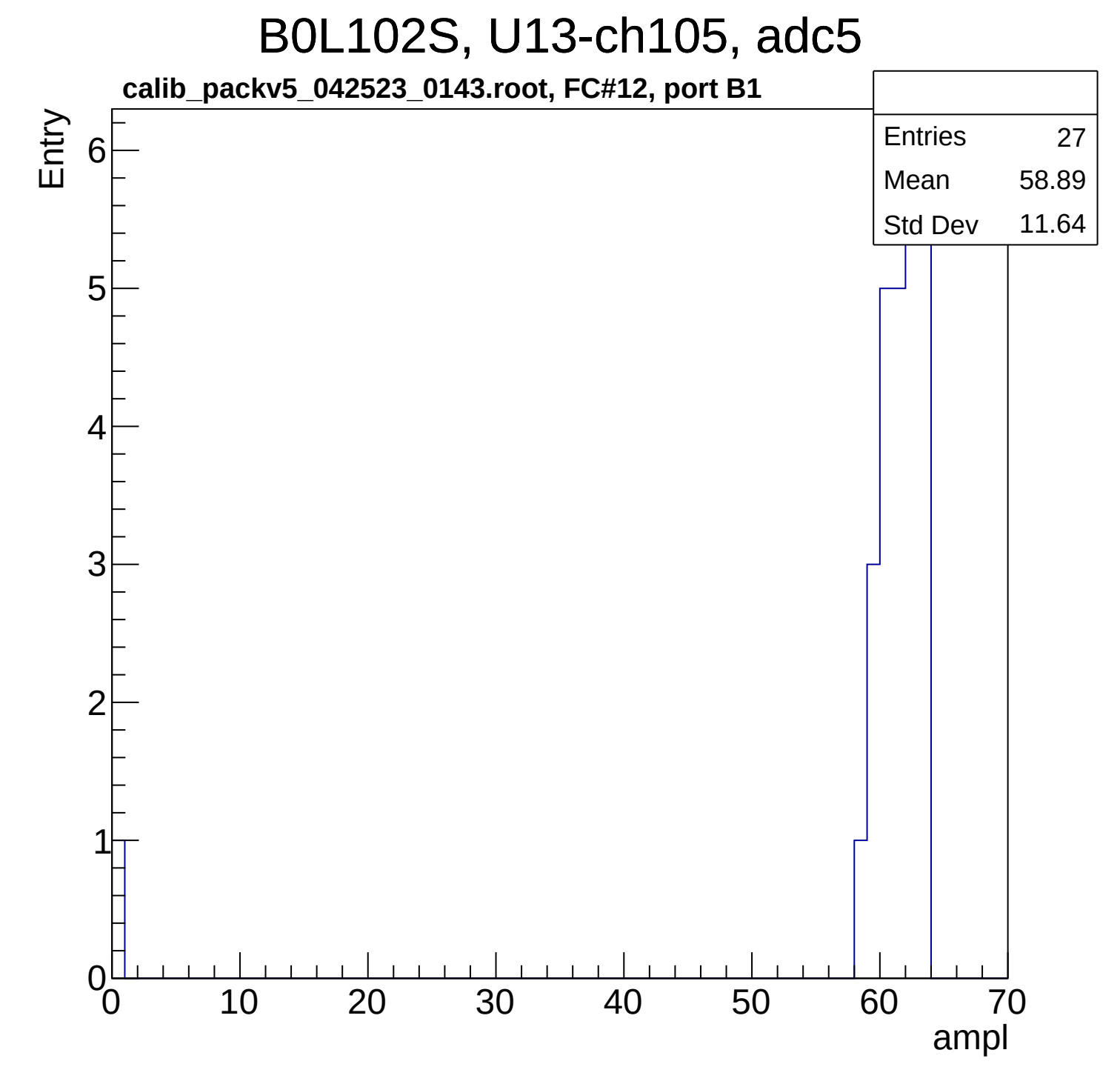
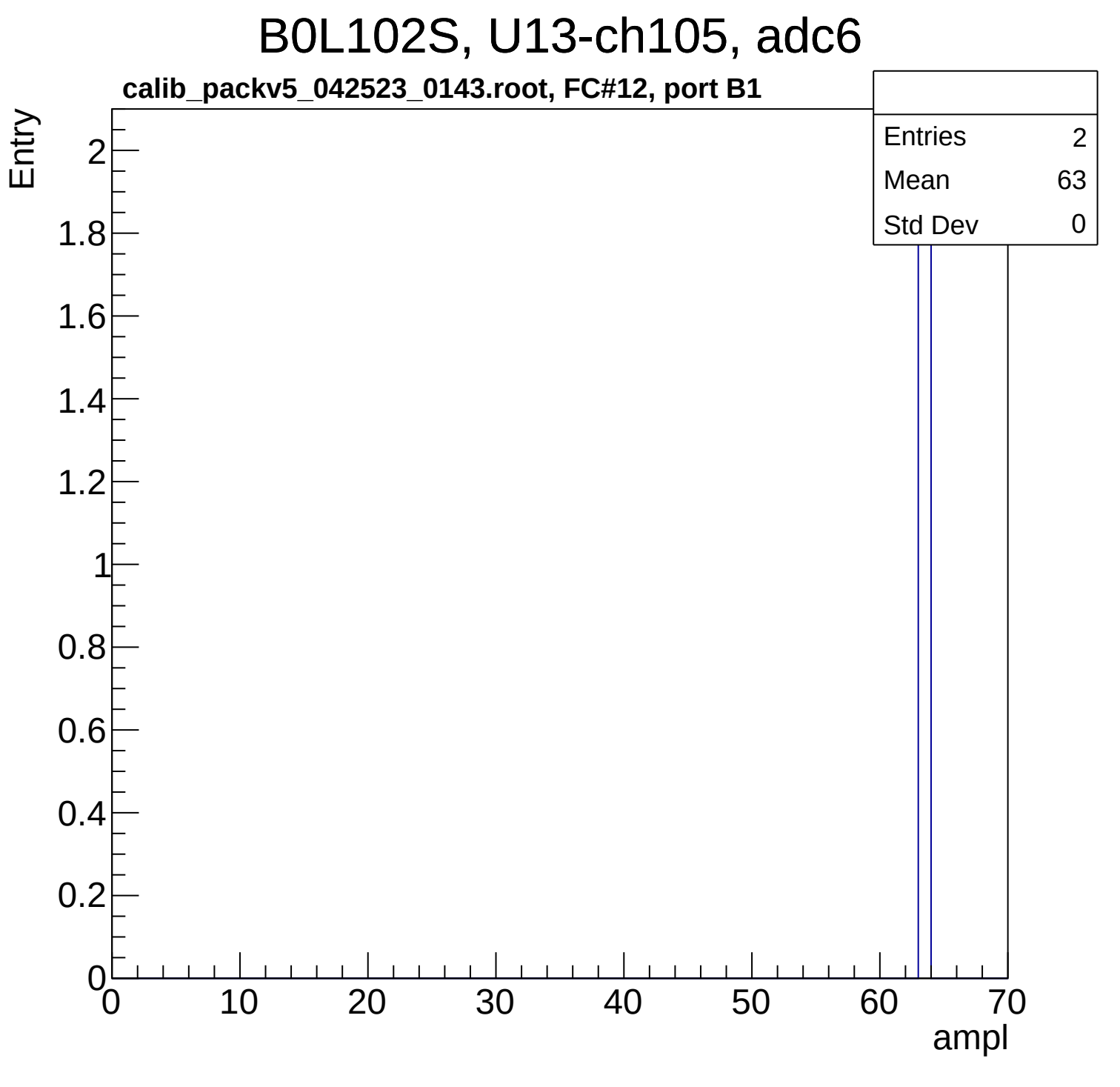
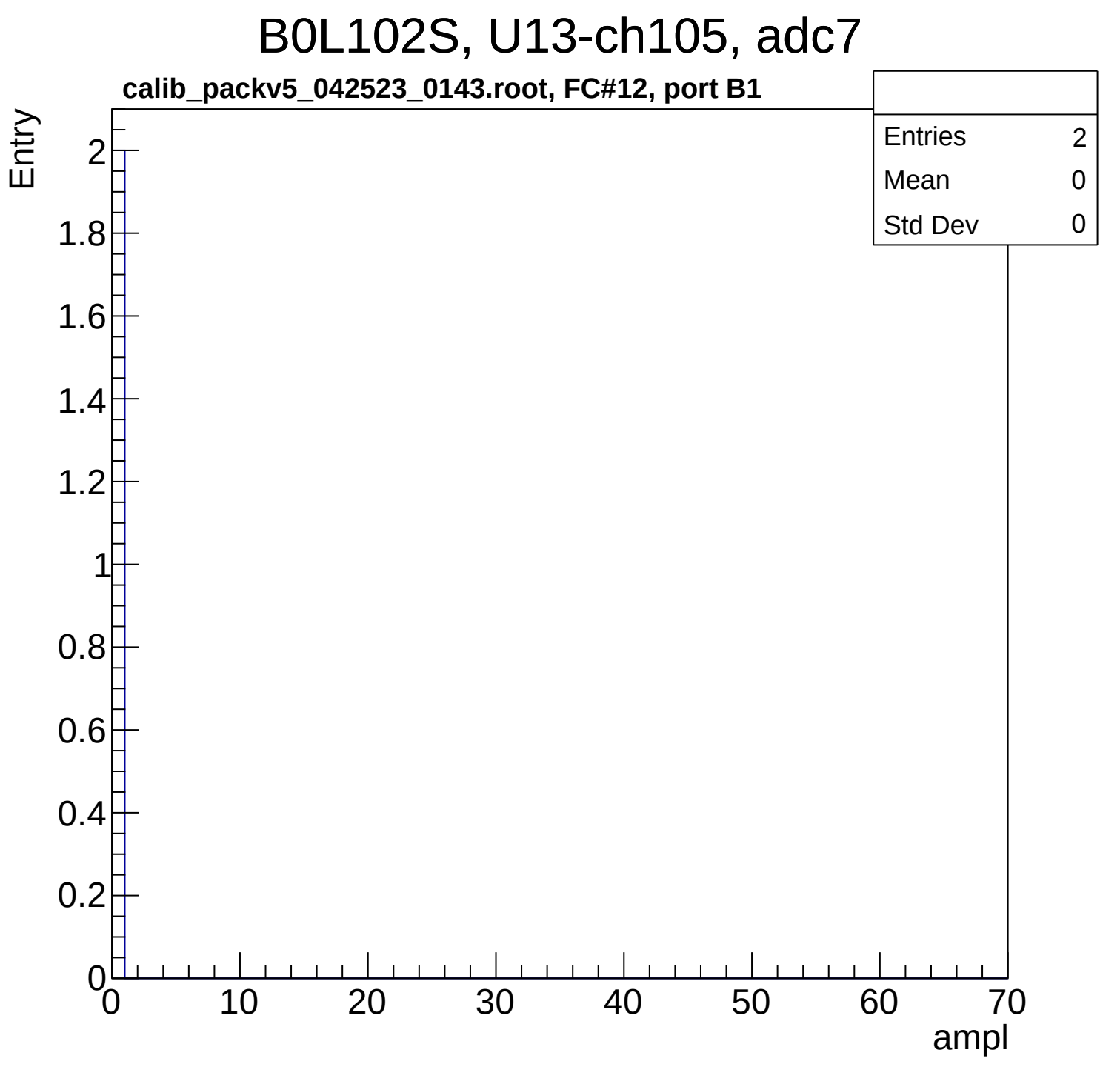
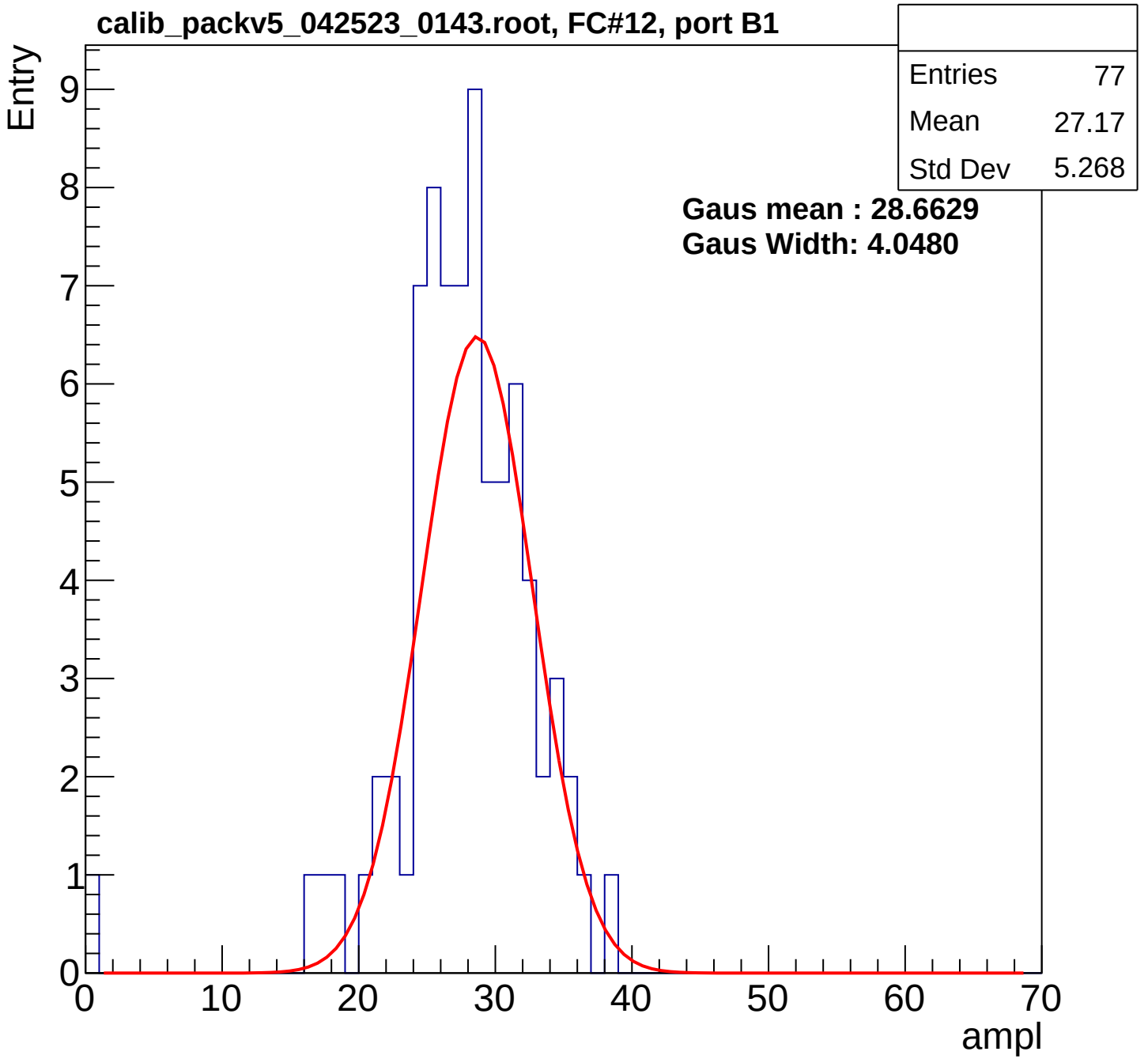
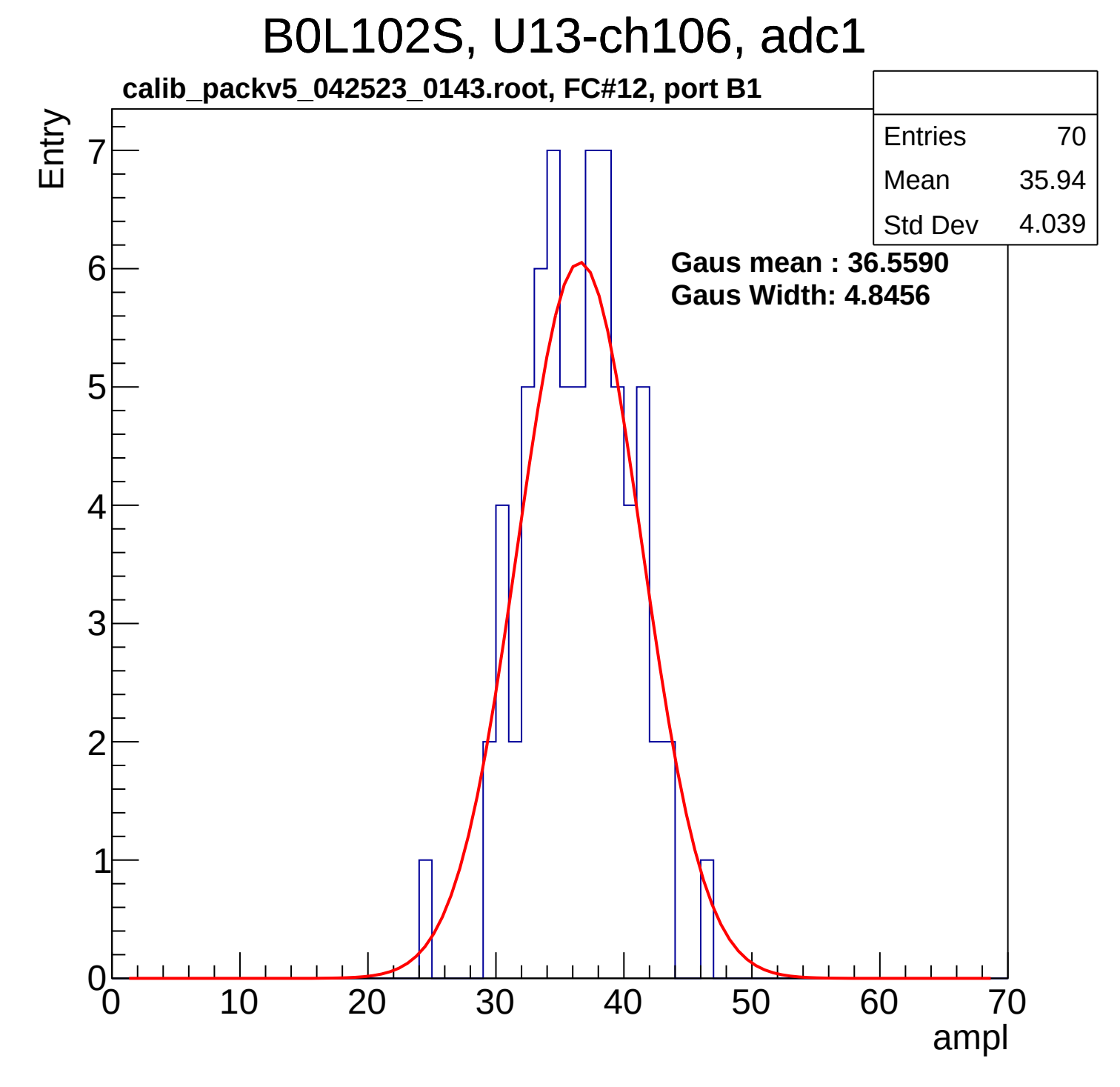
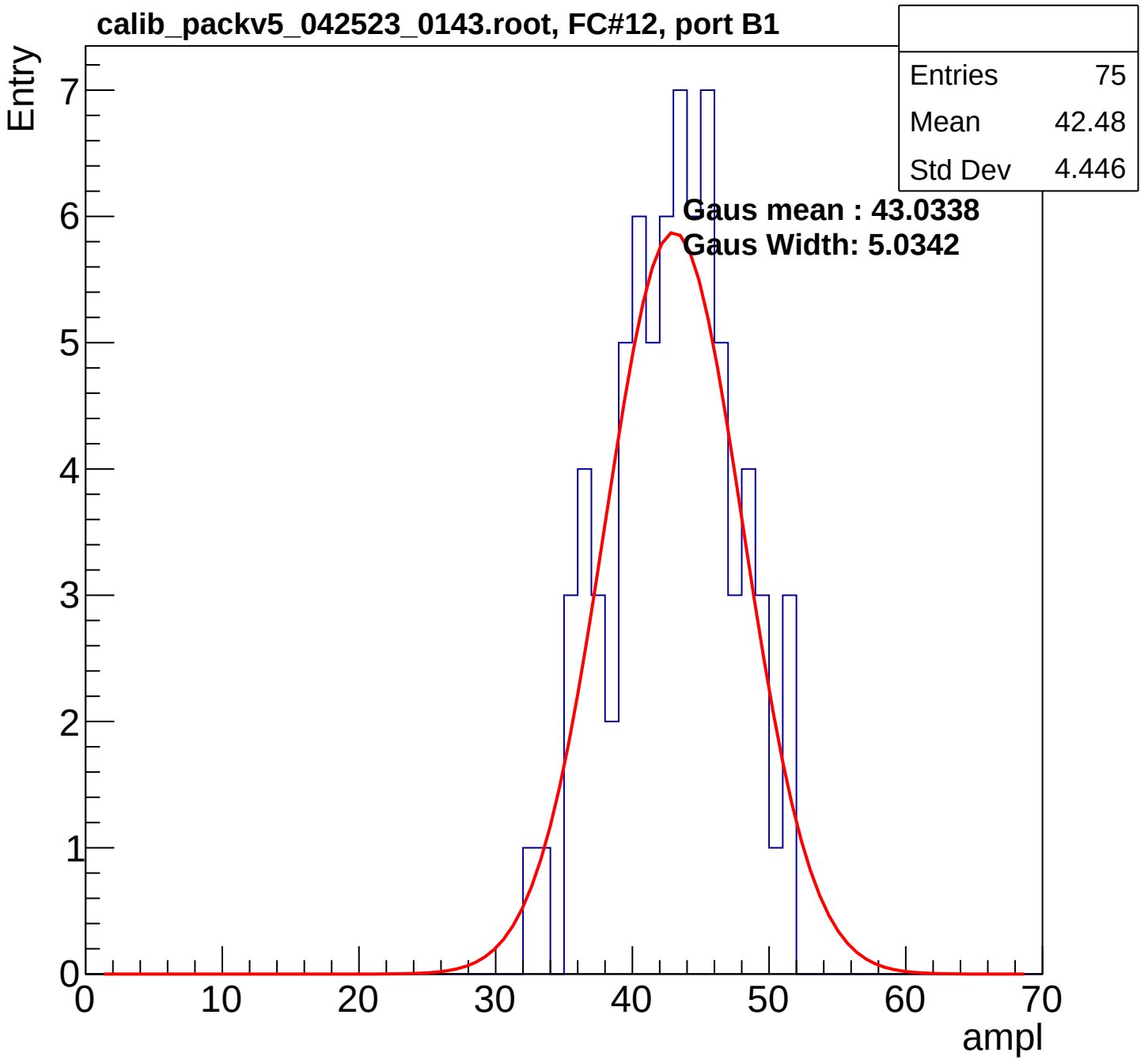

B0L102S, U13-ch105, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.89
Std Dev 11.64
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch105, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch105, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch106, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 27.17
Std Dev 5.268
Gaus mean : 28.6629
Gaus Width: 4.0480
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch106, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 35.94
Std Dev 4.039
Gaus mean : 36.5590
Gaus Width: 4.8456
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch106, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 42.48
Std Dev 4.446
Gaus mean : 43.0338
Gaus Width: 5.0342
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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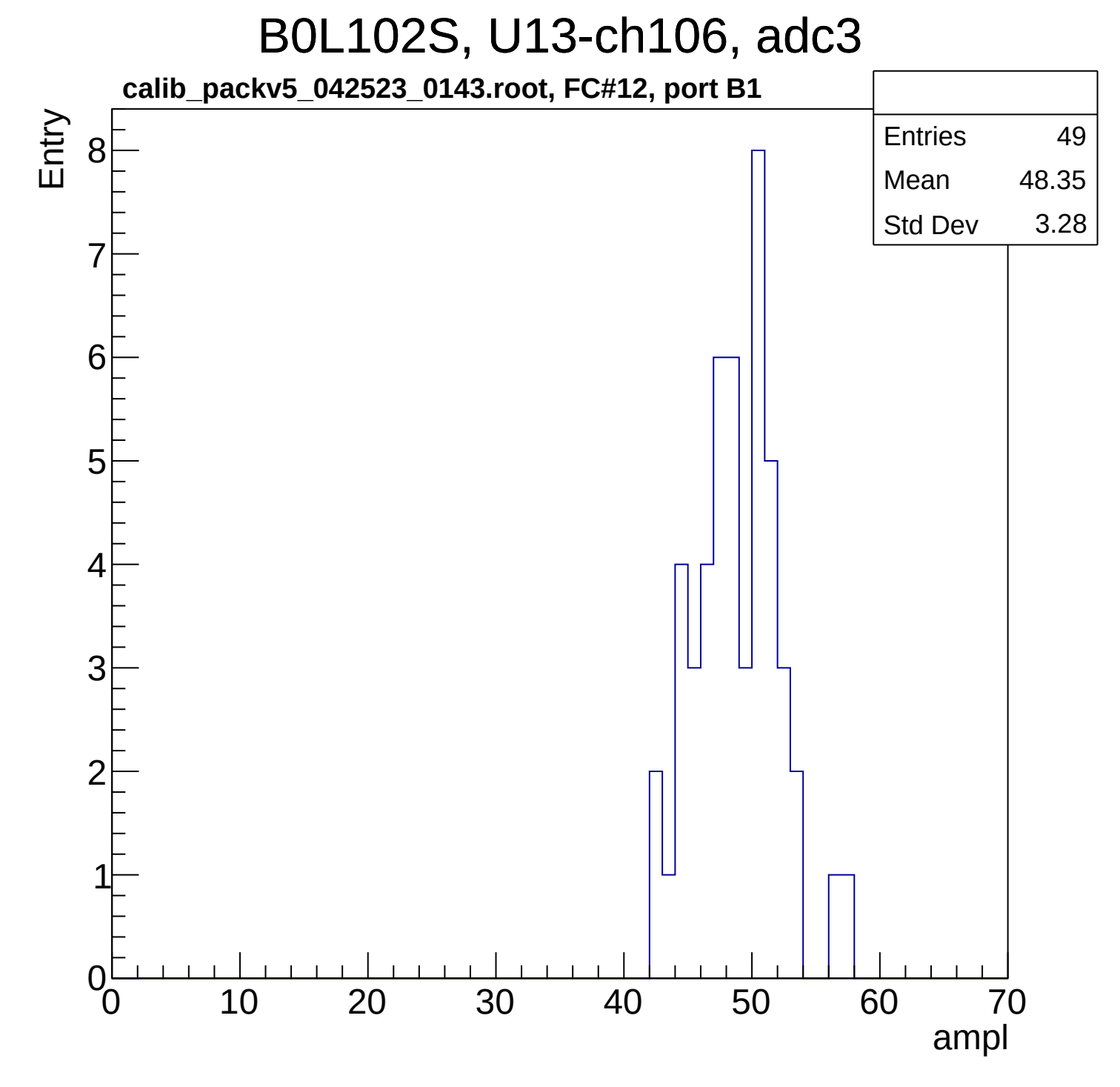
B0L102S, U13-ch106, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 48.35
Std Dev 3.28
Entry
ampl
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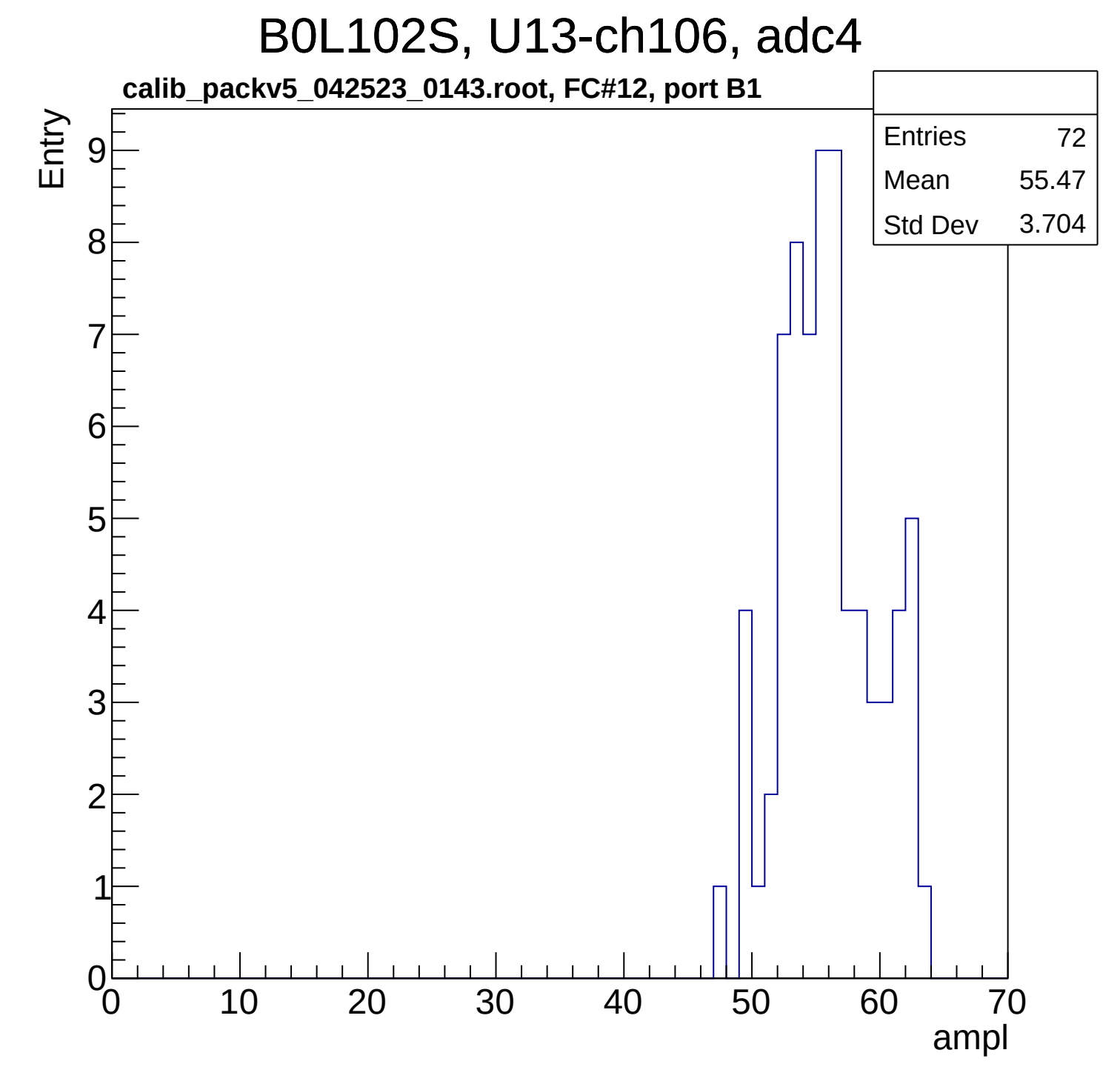
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch106, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 55.47
Std Dev 3.704
Entry
ampl
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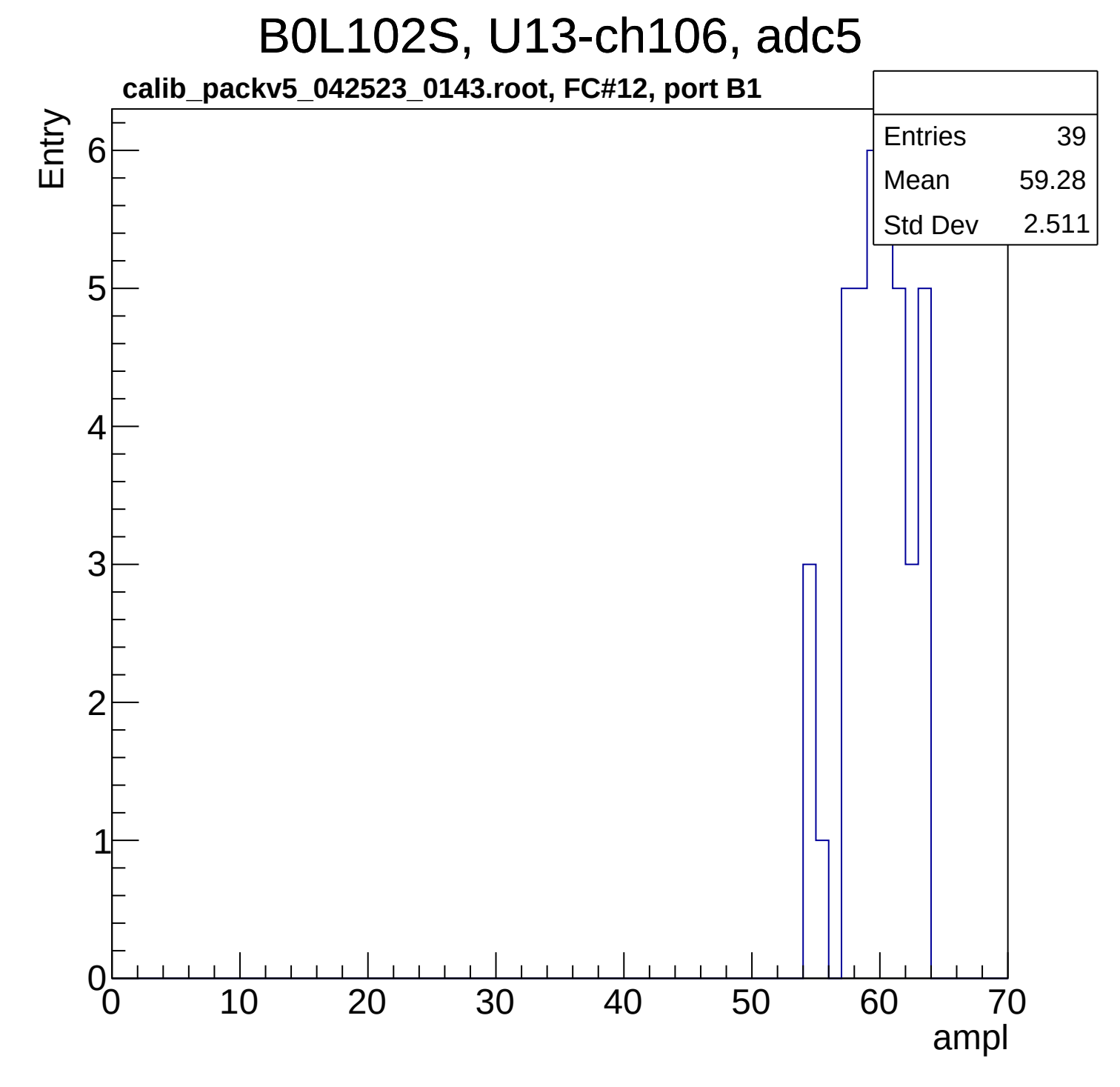
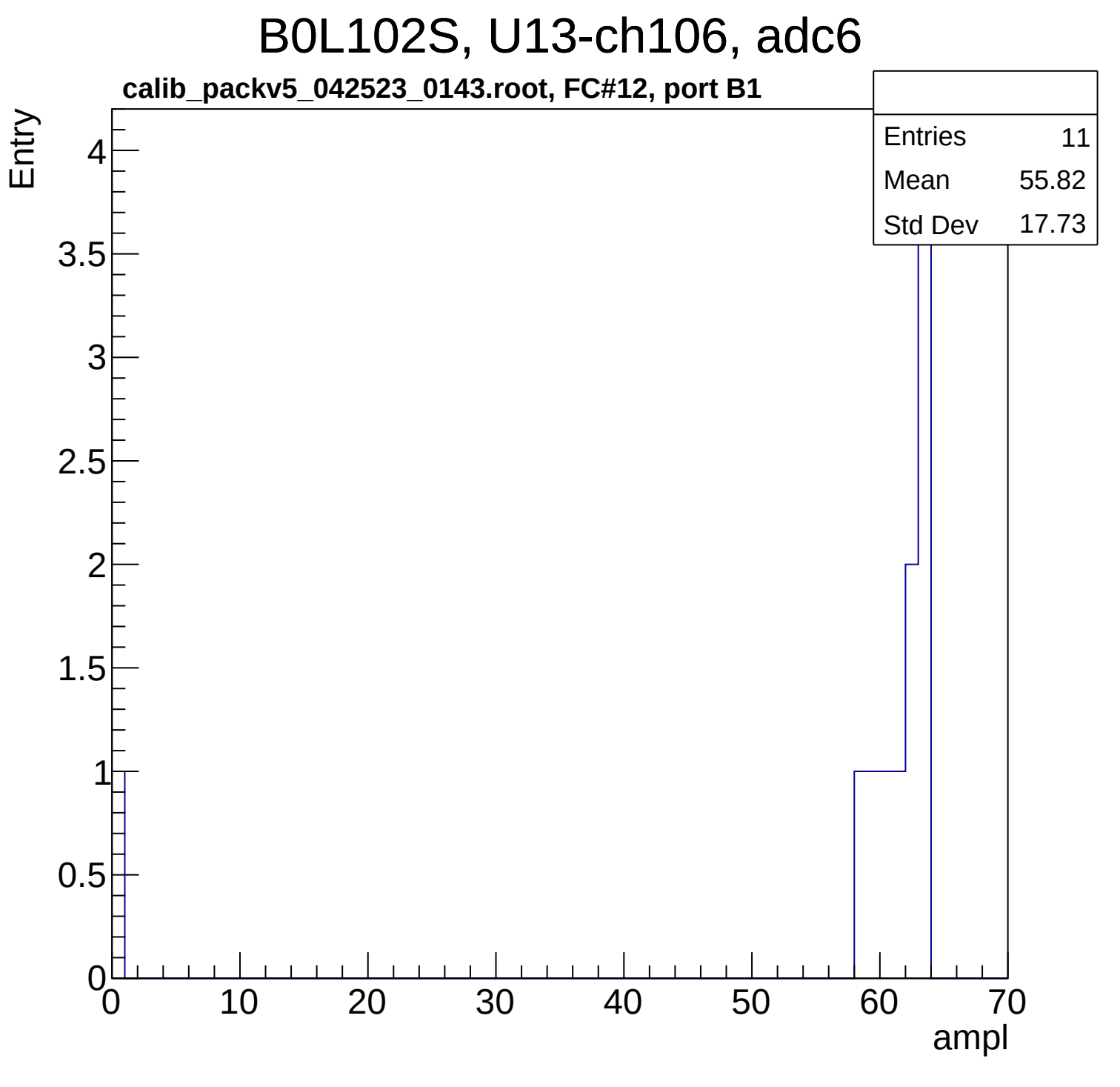
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch106, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 59.28
Std Dev 2.511
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch106, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 55.82
Std Dev 17.73
Entry
ampl
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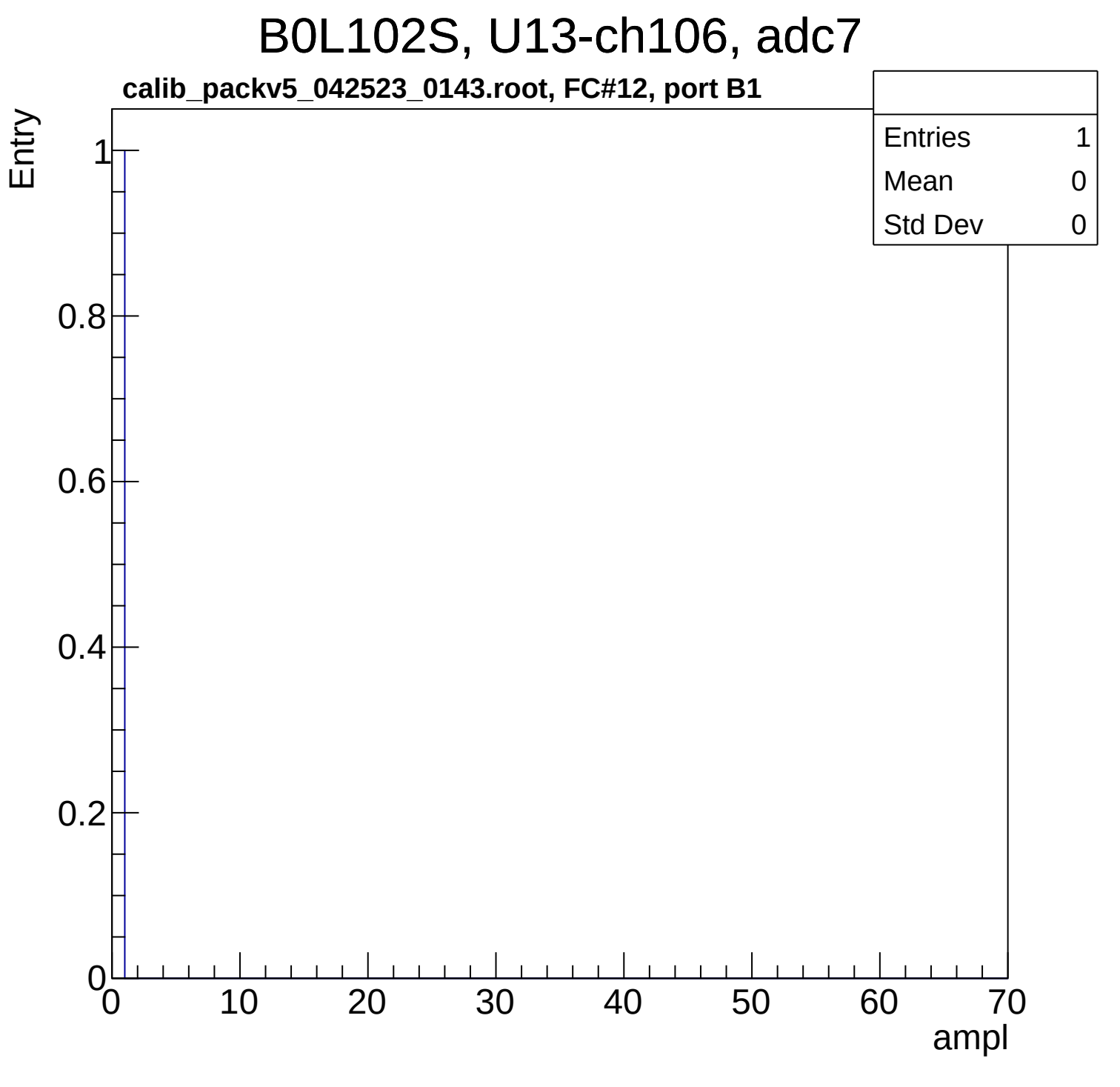
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U13-ch106, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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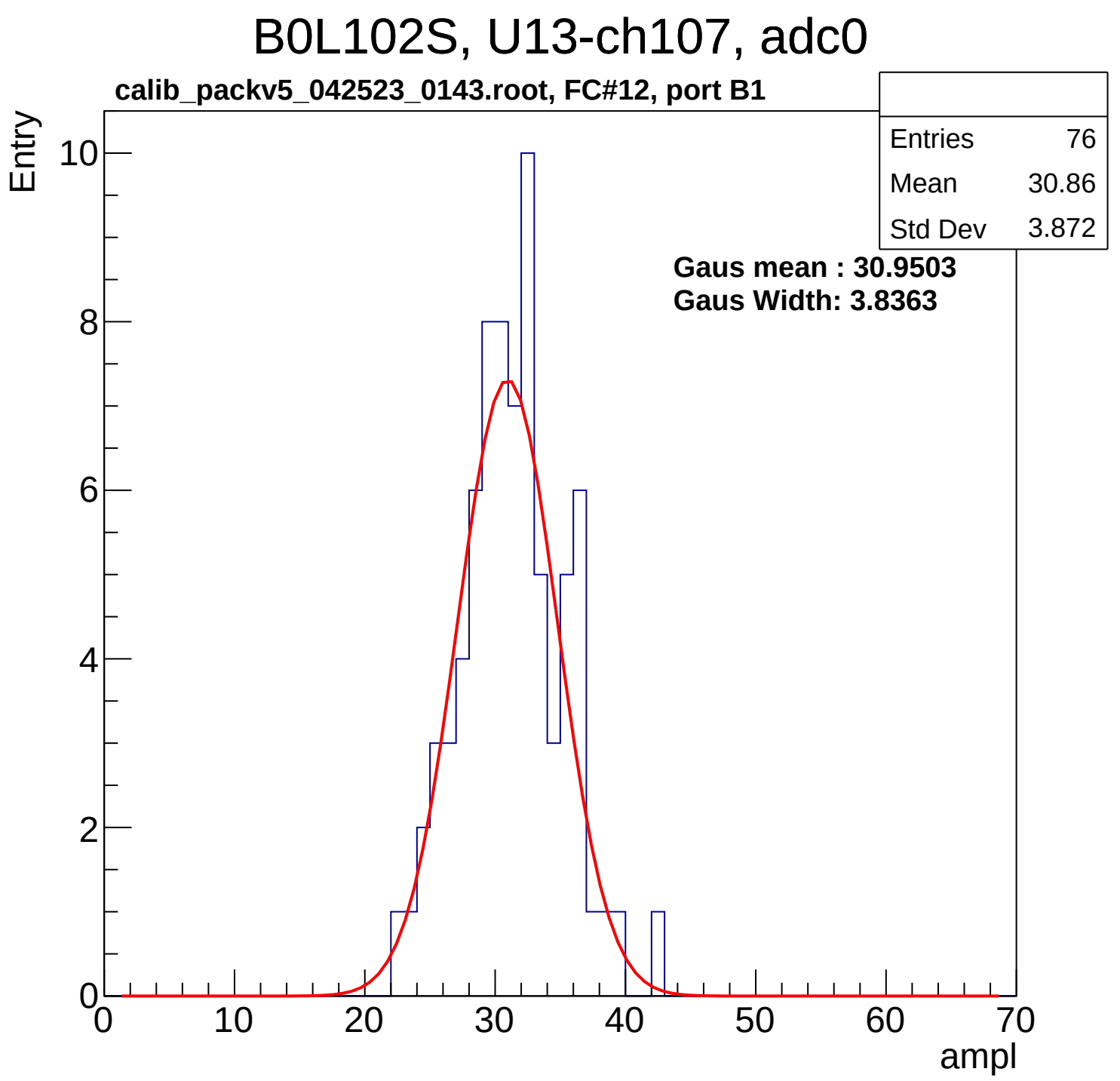
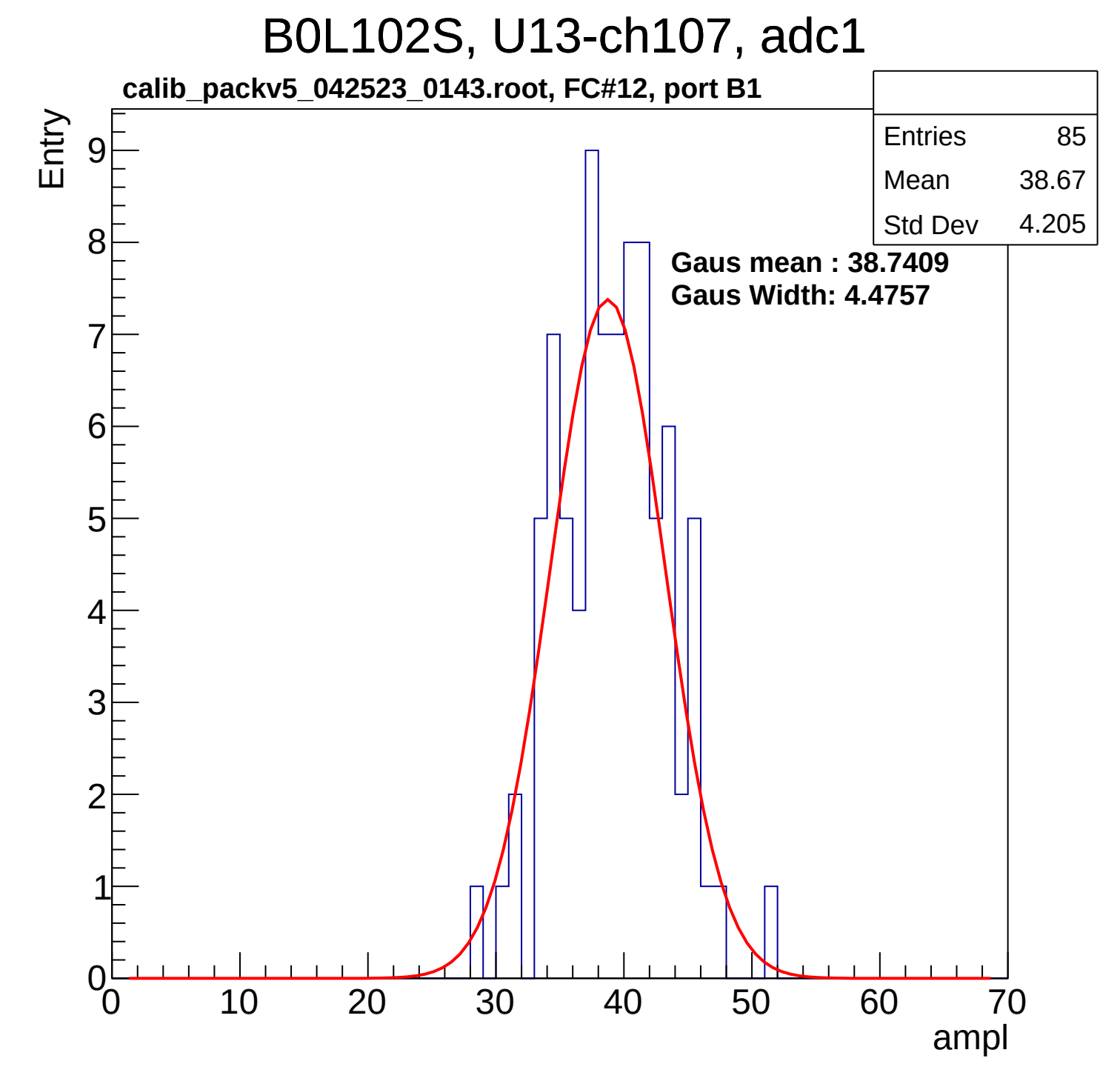
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch107, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 30.86
Std Dev 3.872
Gaus mean : 30.9503
Gaus Width: 3.8363
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch107, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 38.67
Std Dev 4.205
Gaus mean : 38.7409
Gaus Width: 4.4757
Entry
ampl
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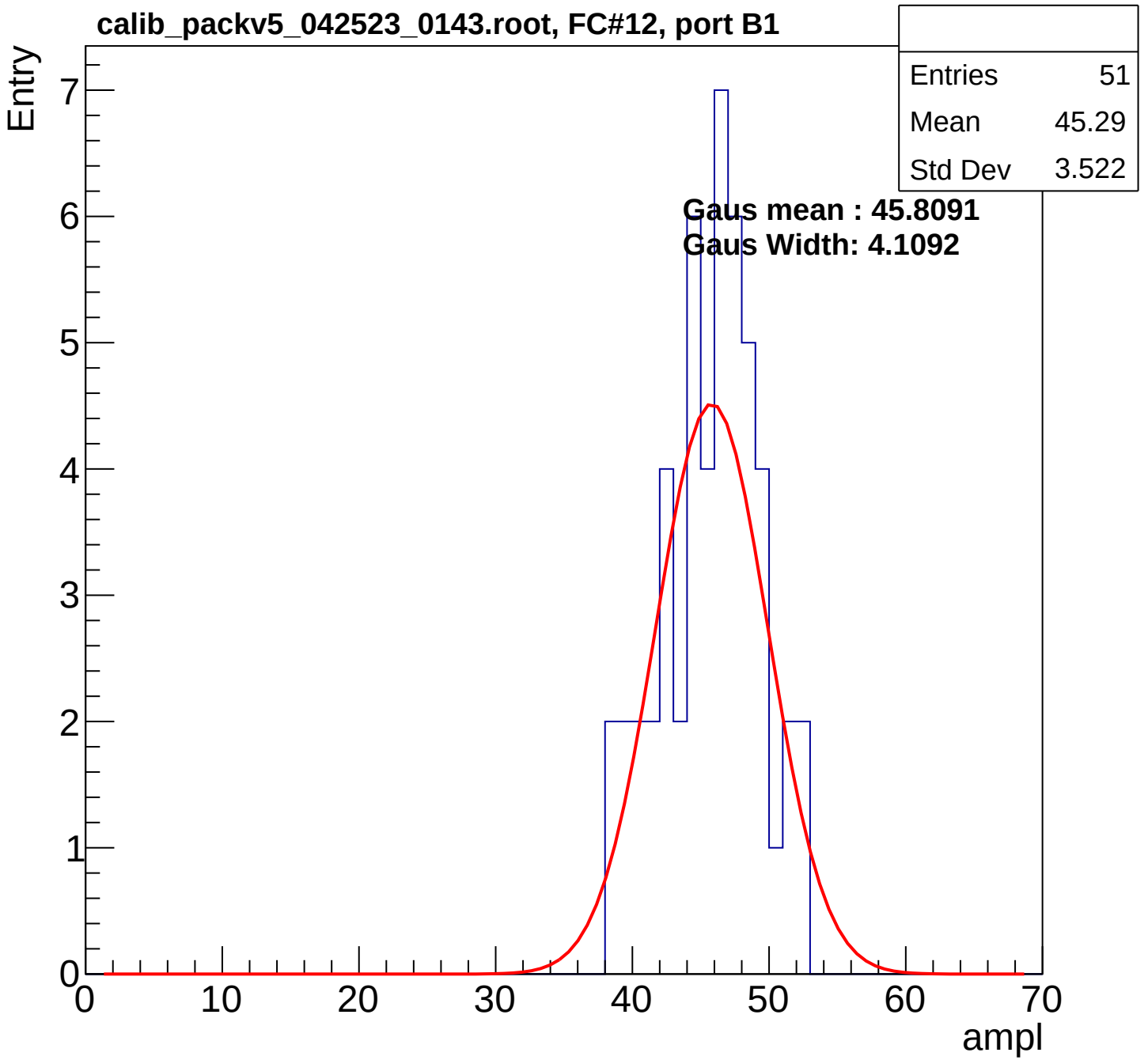
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch107, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 45.29
Std Dev 3.522
Gaus mean : 45.8091
Gaus Width: 4.1092
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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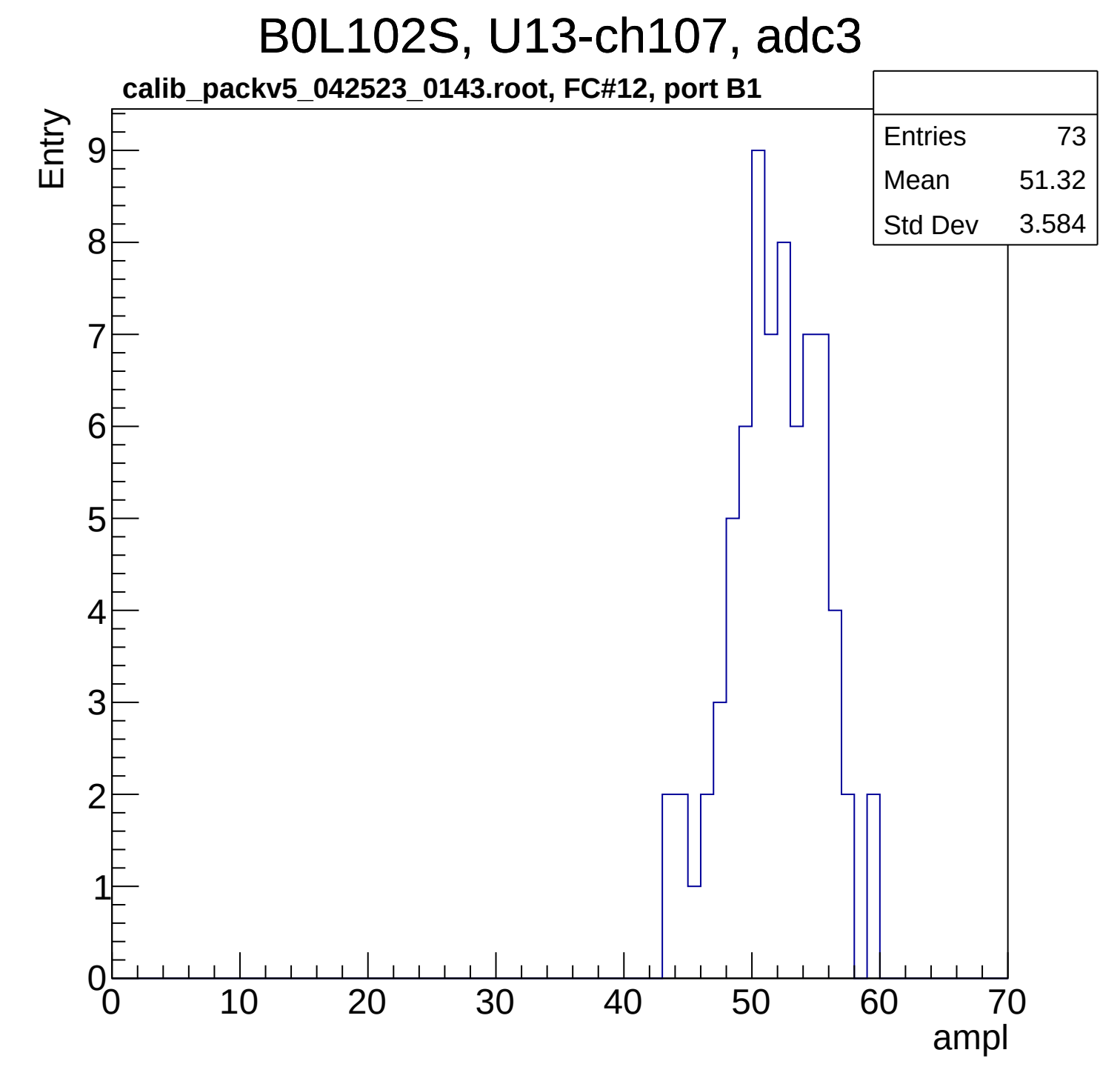
B0L102S, U13-ch107, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 51.32
Std Dev 3.584
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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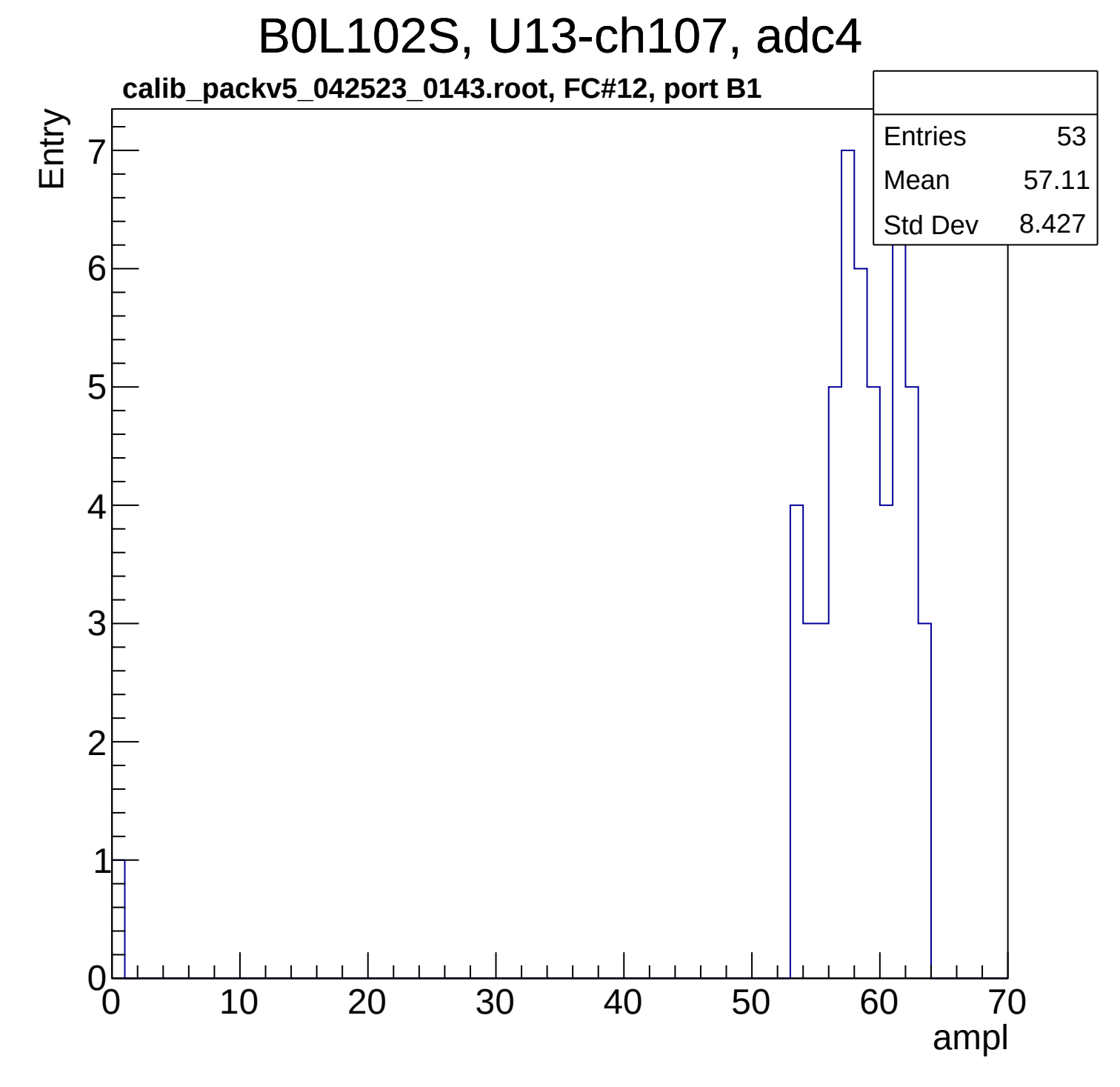
B0L102S, U13-ch107, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.11
Std Dev 8.427
Entry
ampl
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2
3
4
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0
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40
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60
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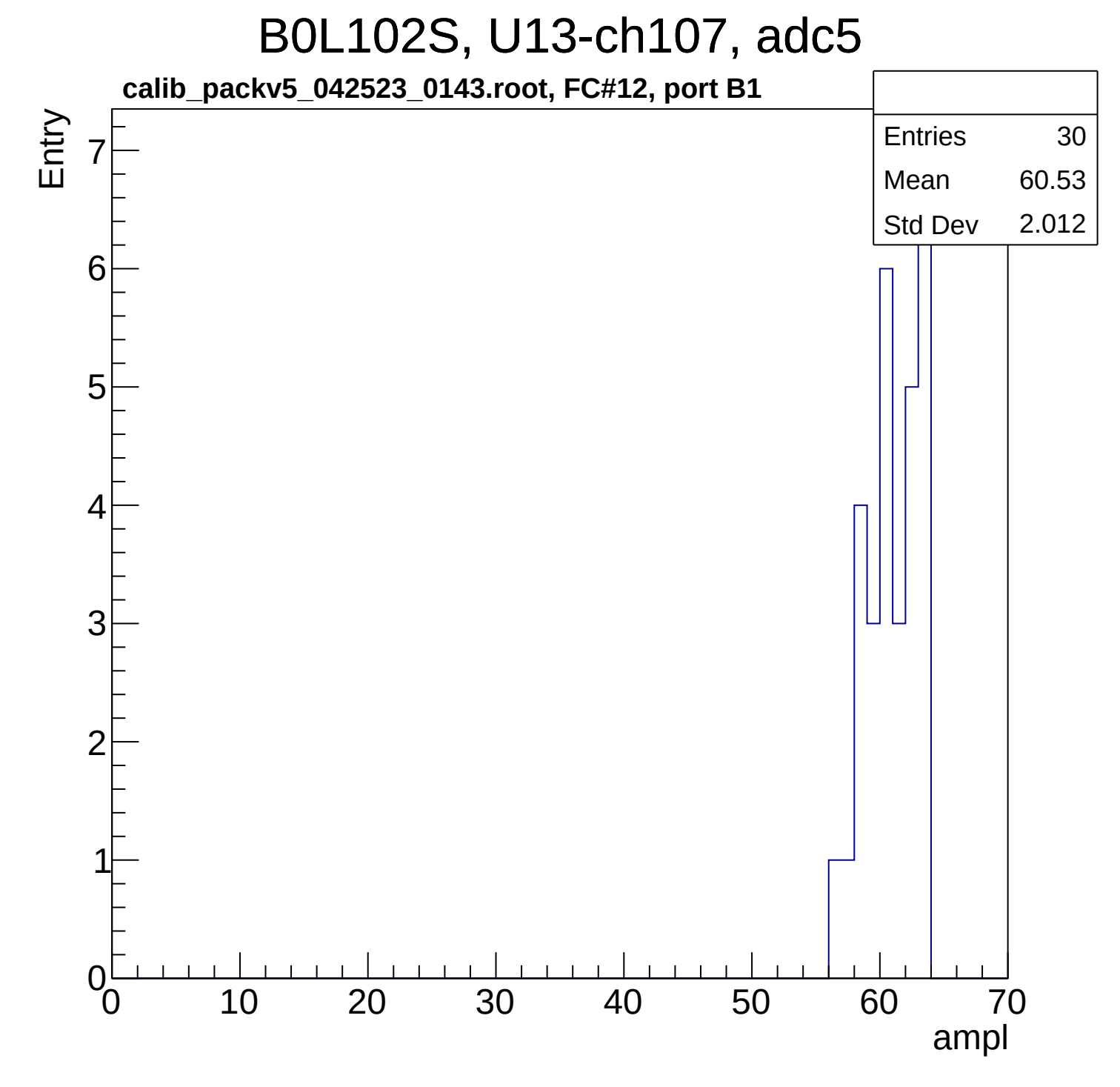
B0L102S, U13-ch107, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 60.53
Std Dev 2.012
Entry
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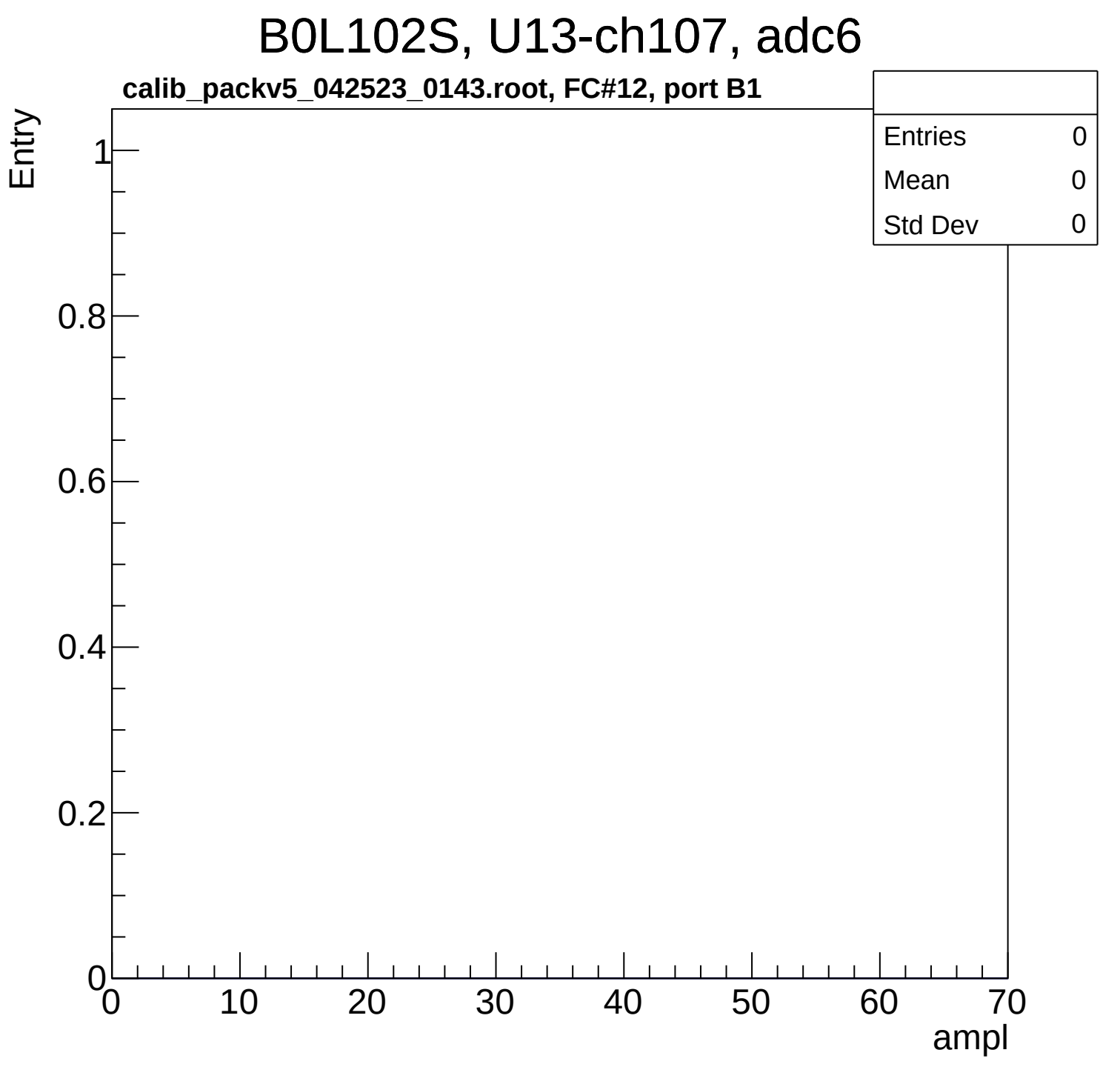
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch107, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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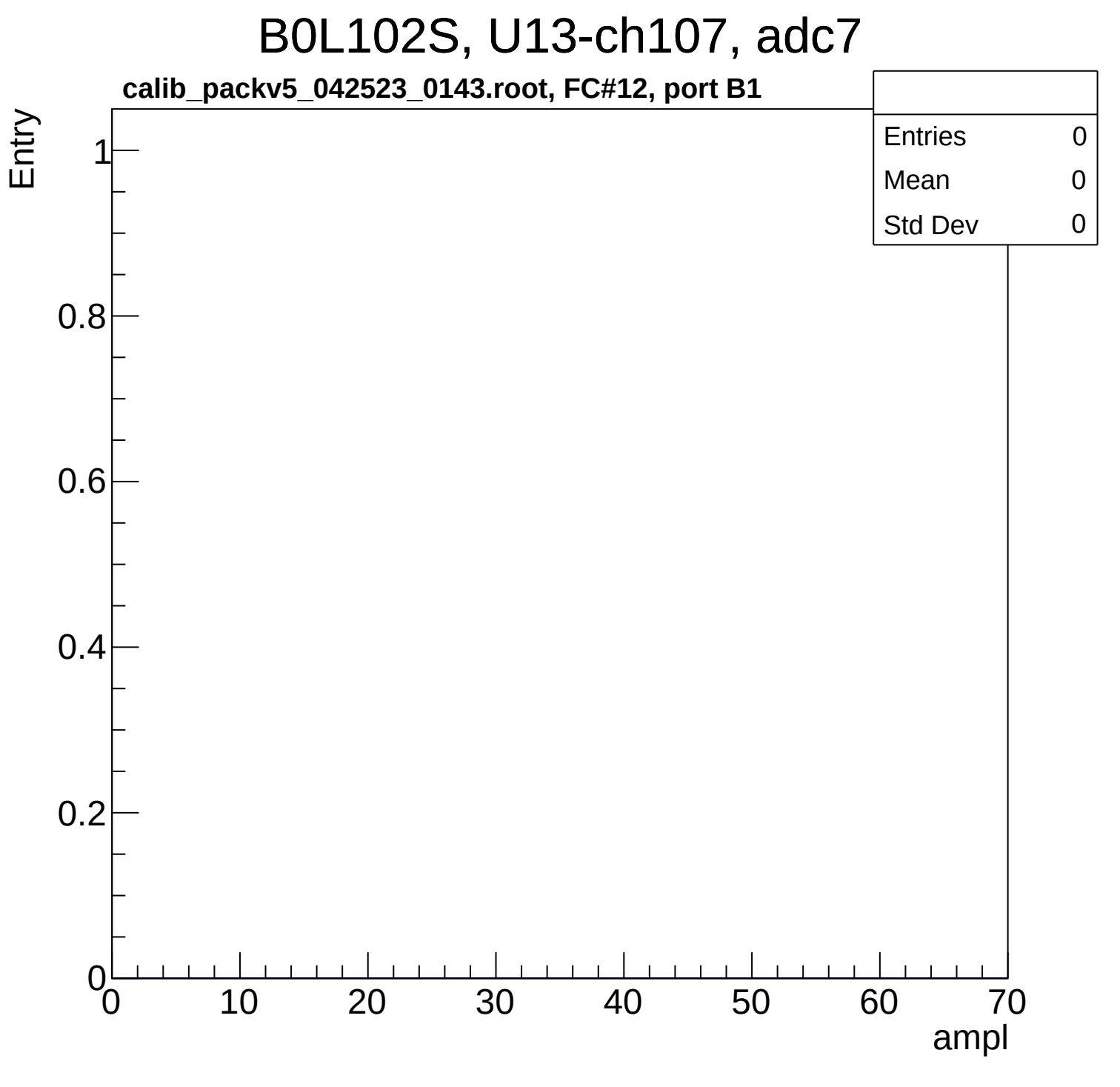
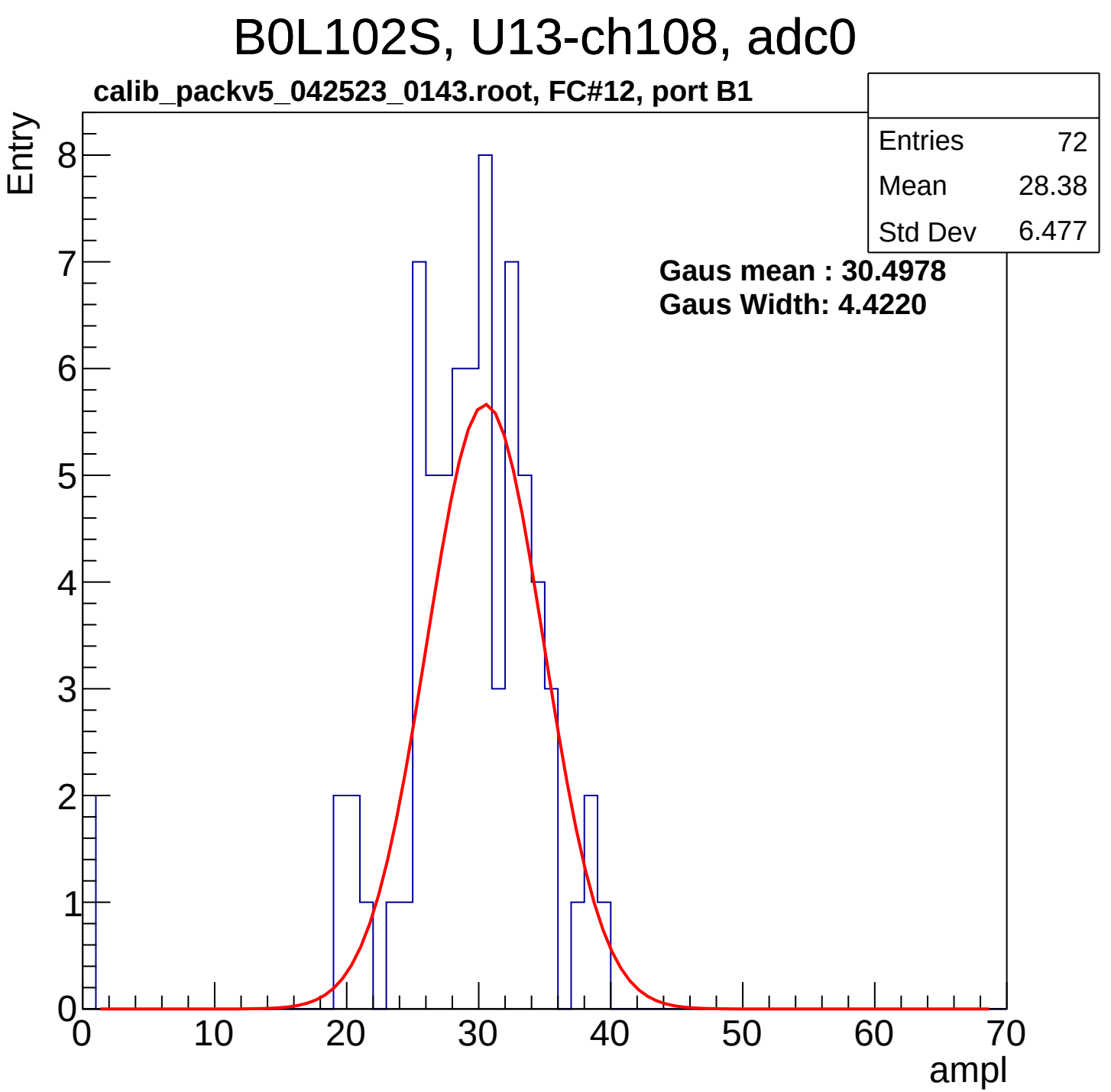
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch107, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch108, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 28.38
Std Dev 6.477
Gaus mean : 30.4978
Gaus Width: 4.4220
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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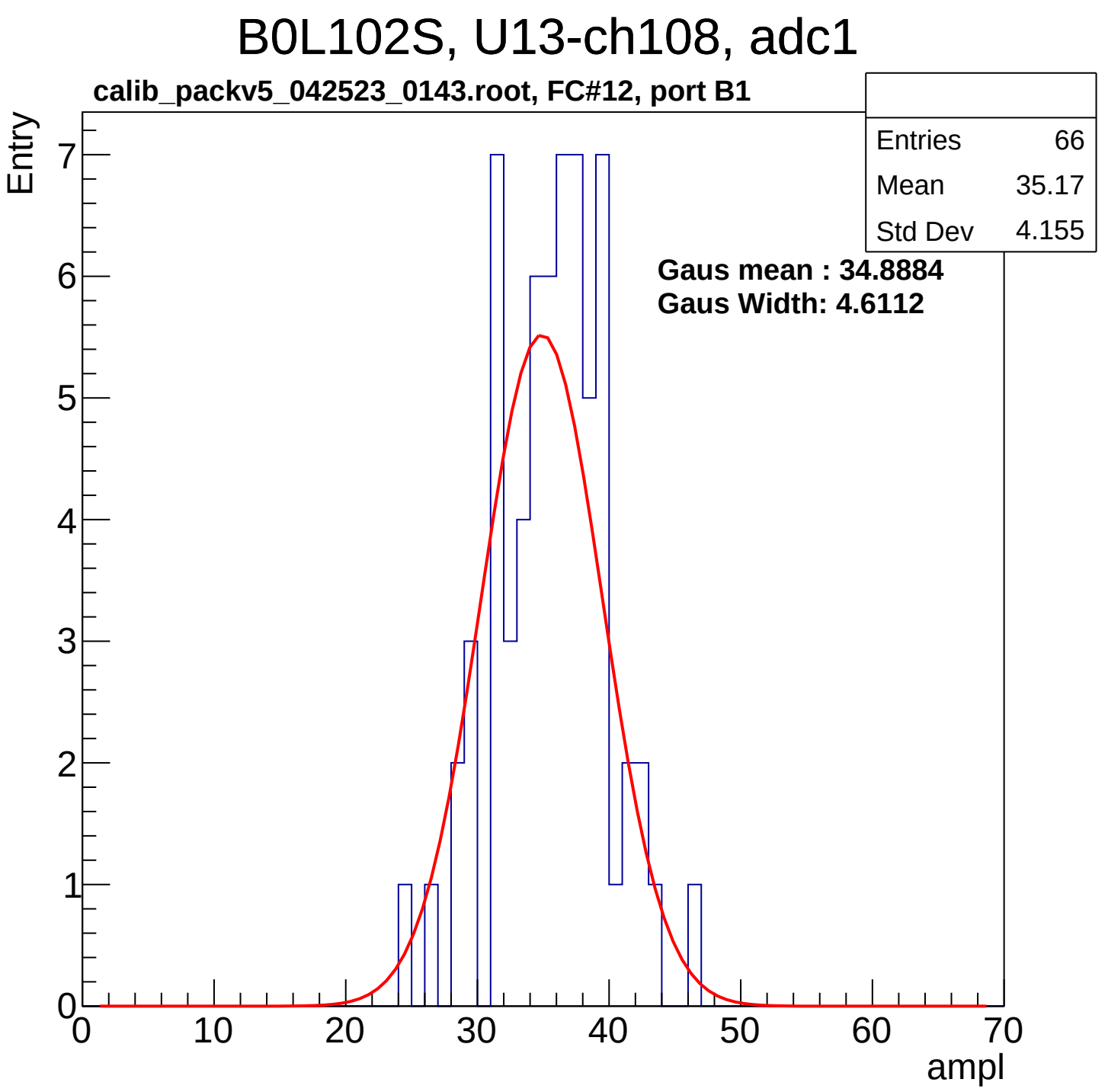
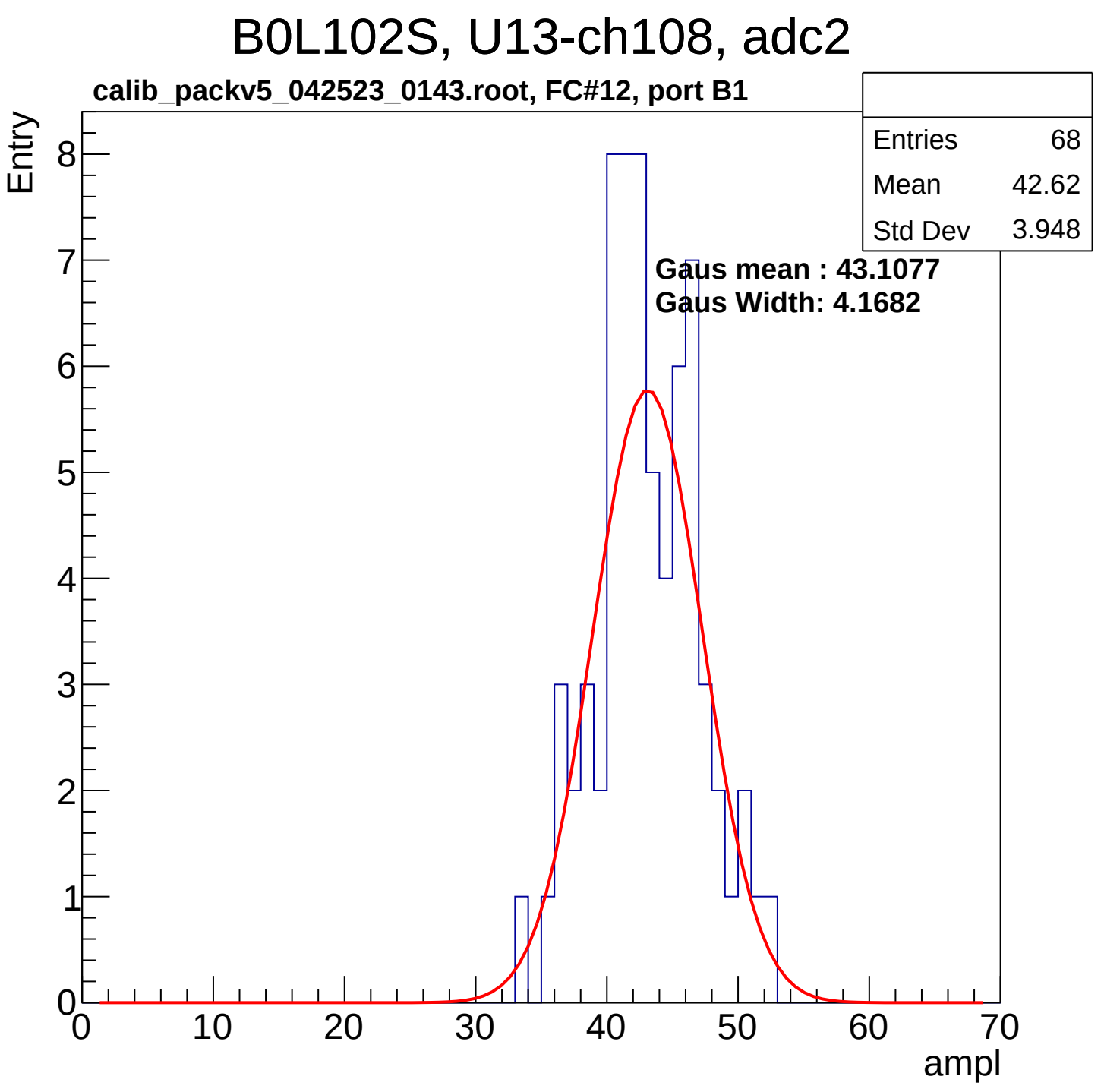

B0L102S, U13-ch108, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 35.17
Std Dev 4.155
Gaus mean : 34.8884
Gaus Width: 4.6112
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch108, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.62
Std Dev 3.948
Gaus mean : 43.1077
Gaus Width: 4.1682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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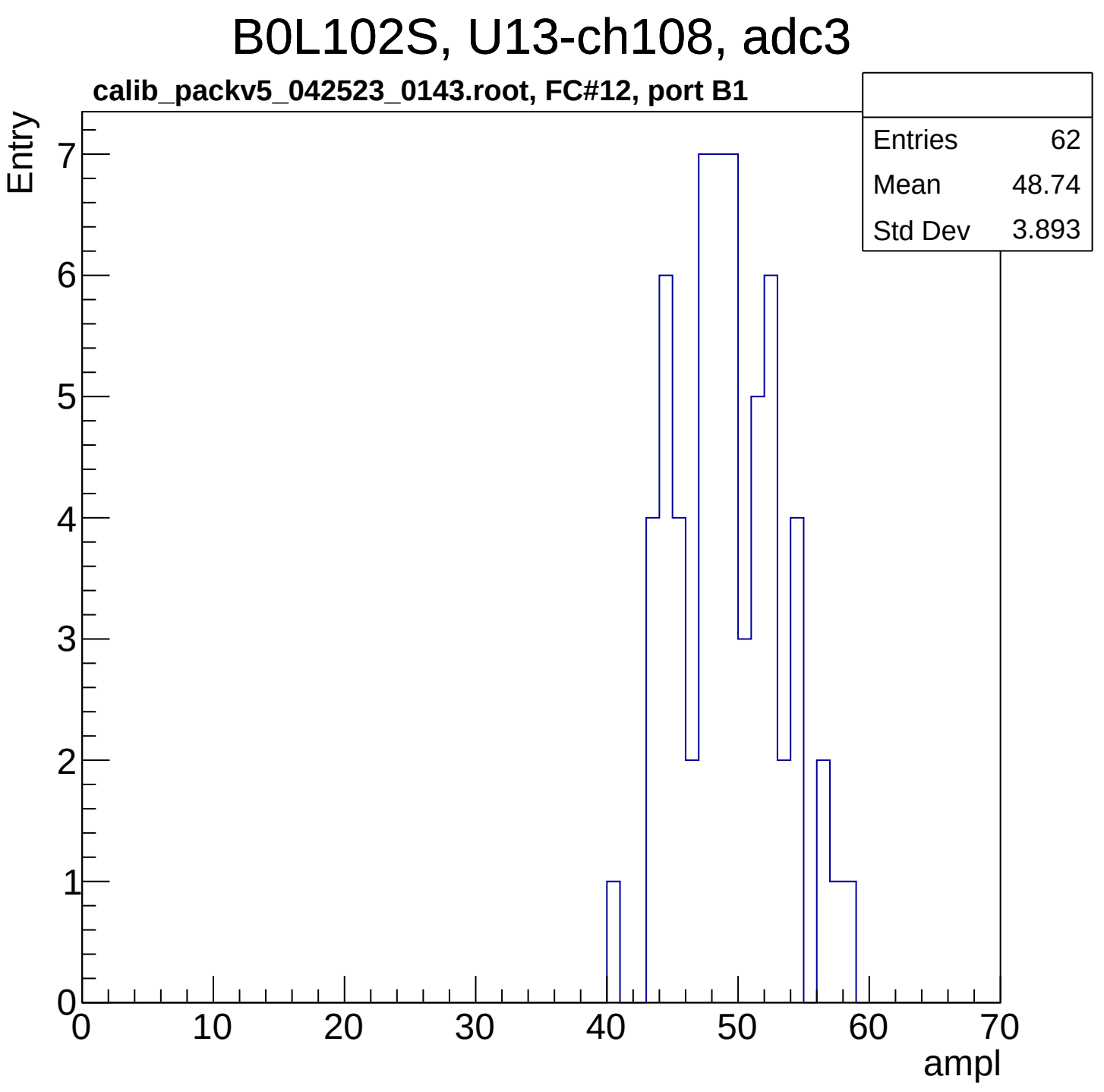
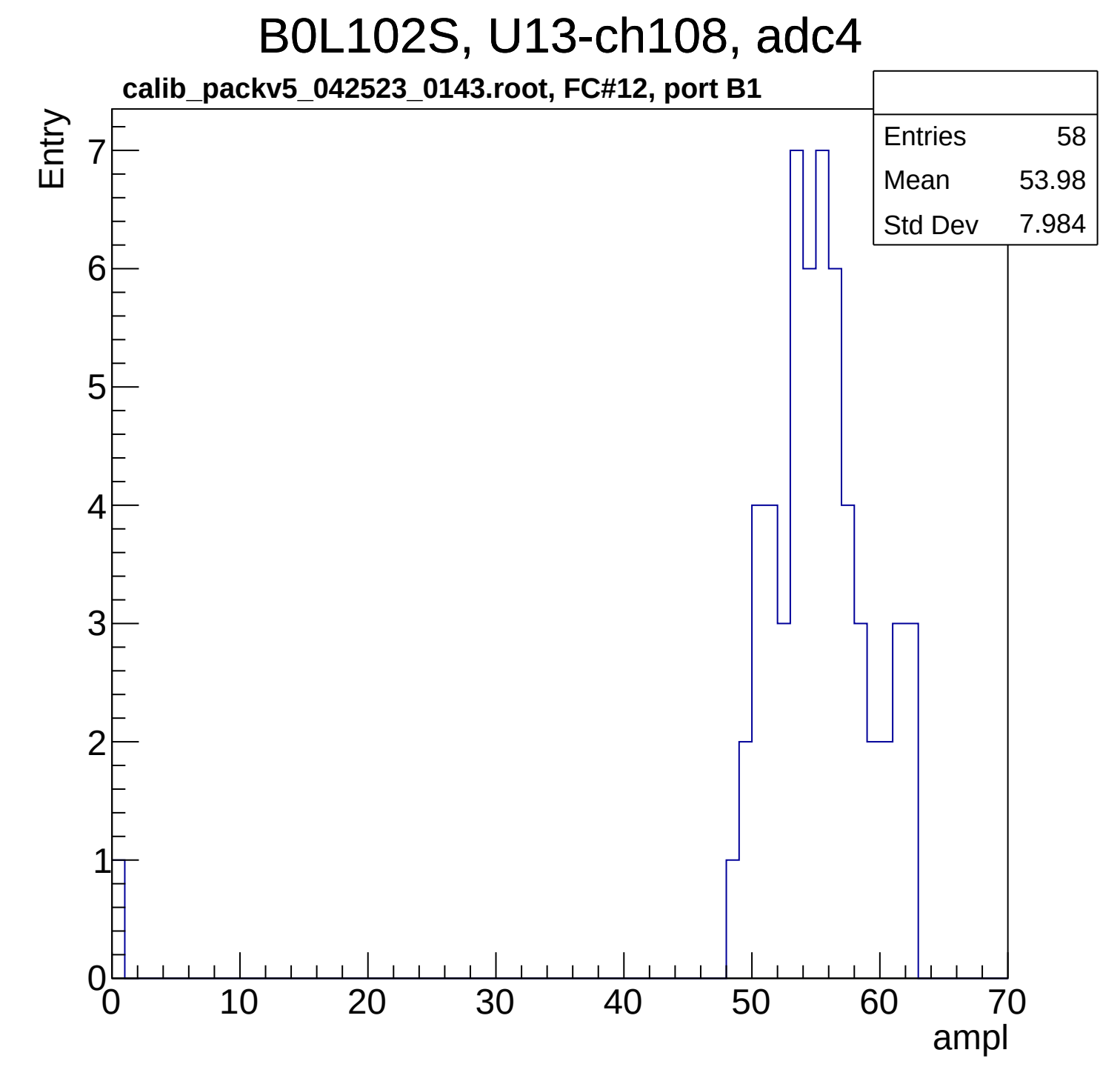
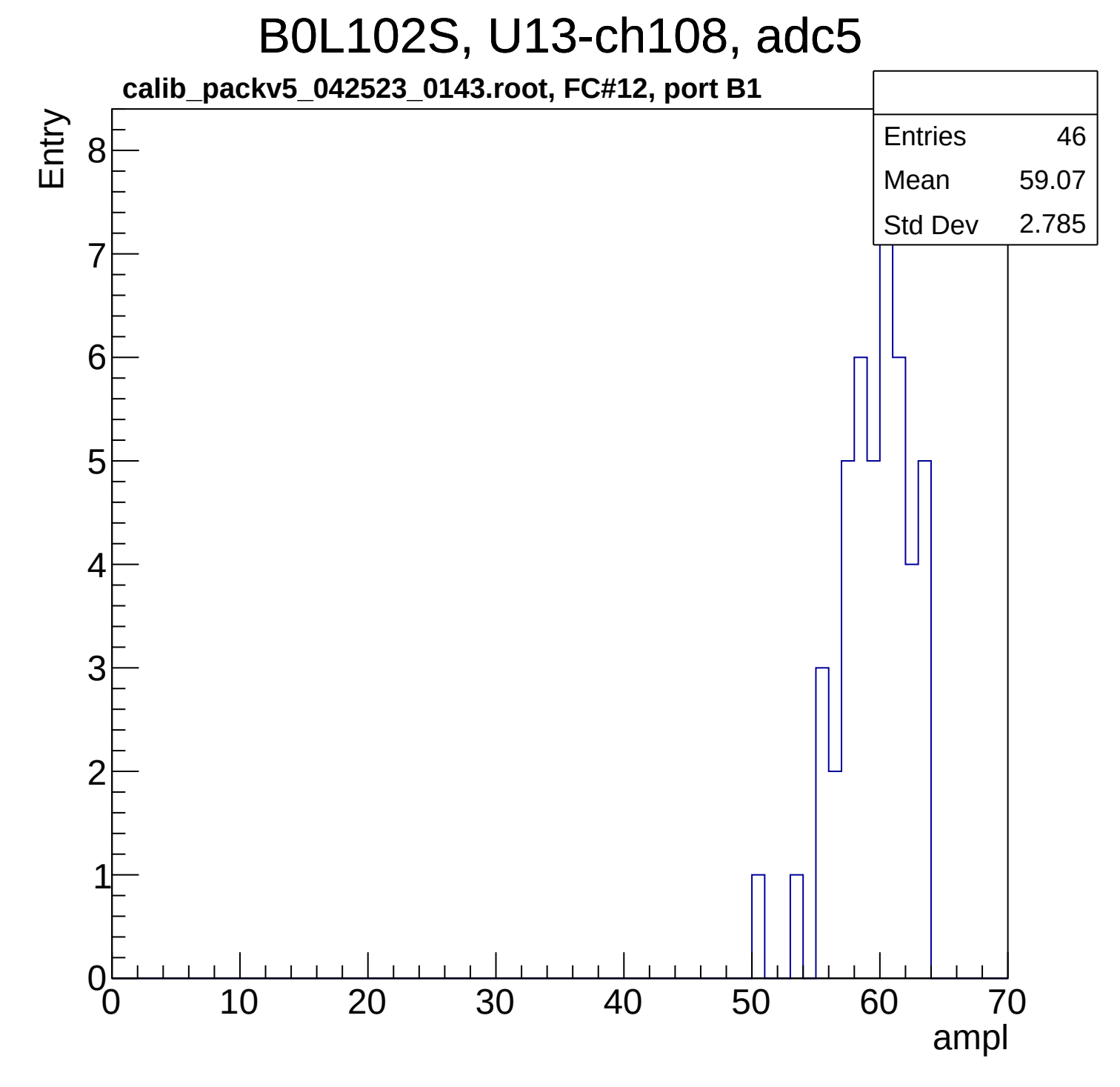

B0L102S, U13-ch108, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 48.74
Std Dev 3.893
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch108, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 53.98
Std Dev 7.984
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch108, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.07
Std Dev 2.785
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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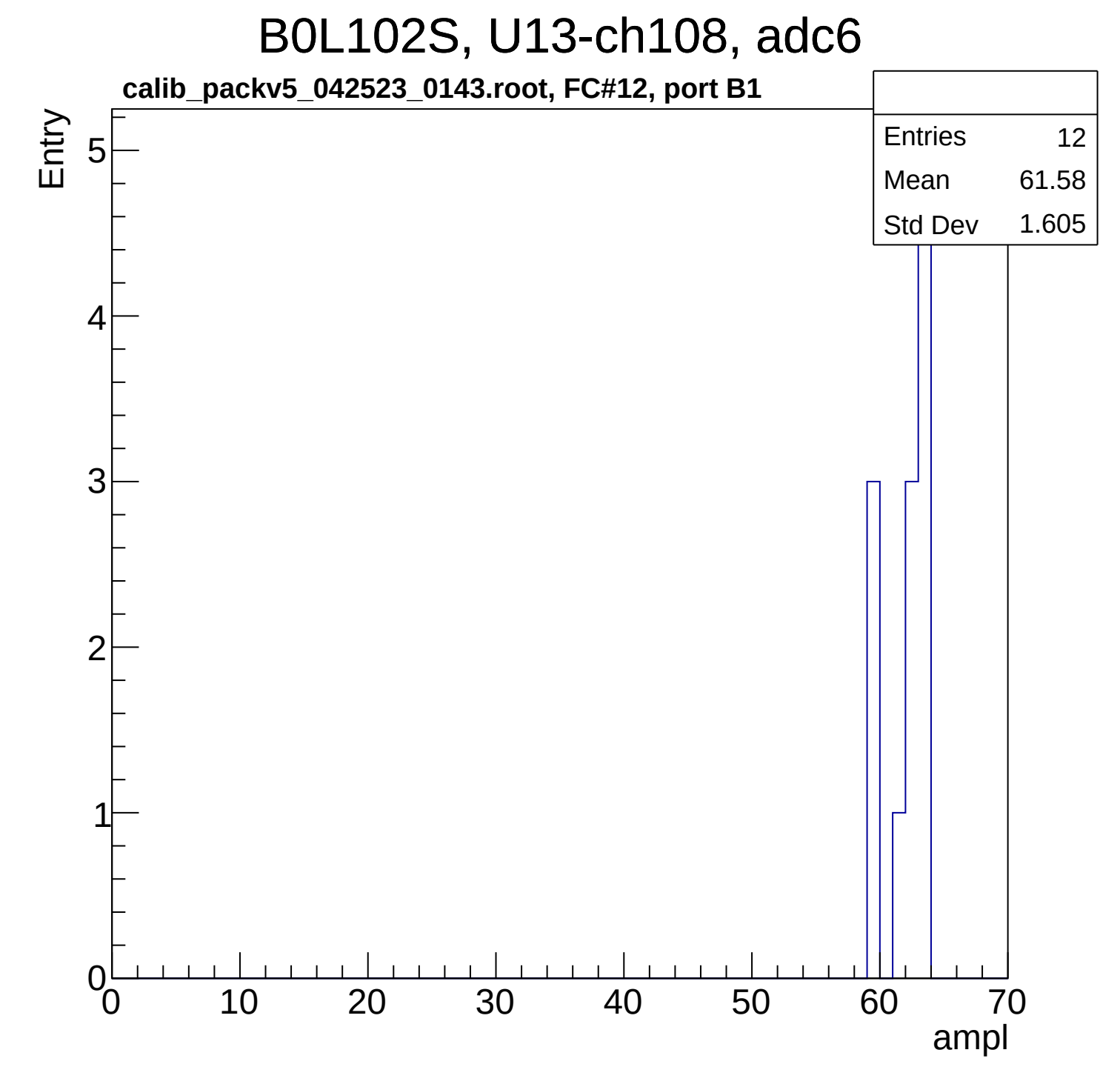
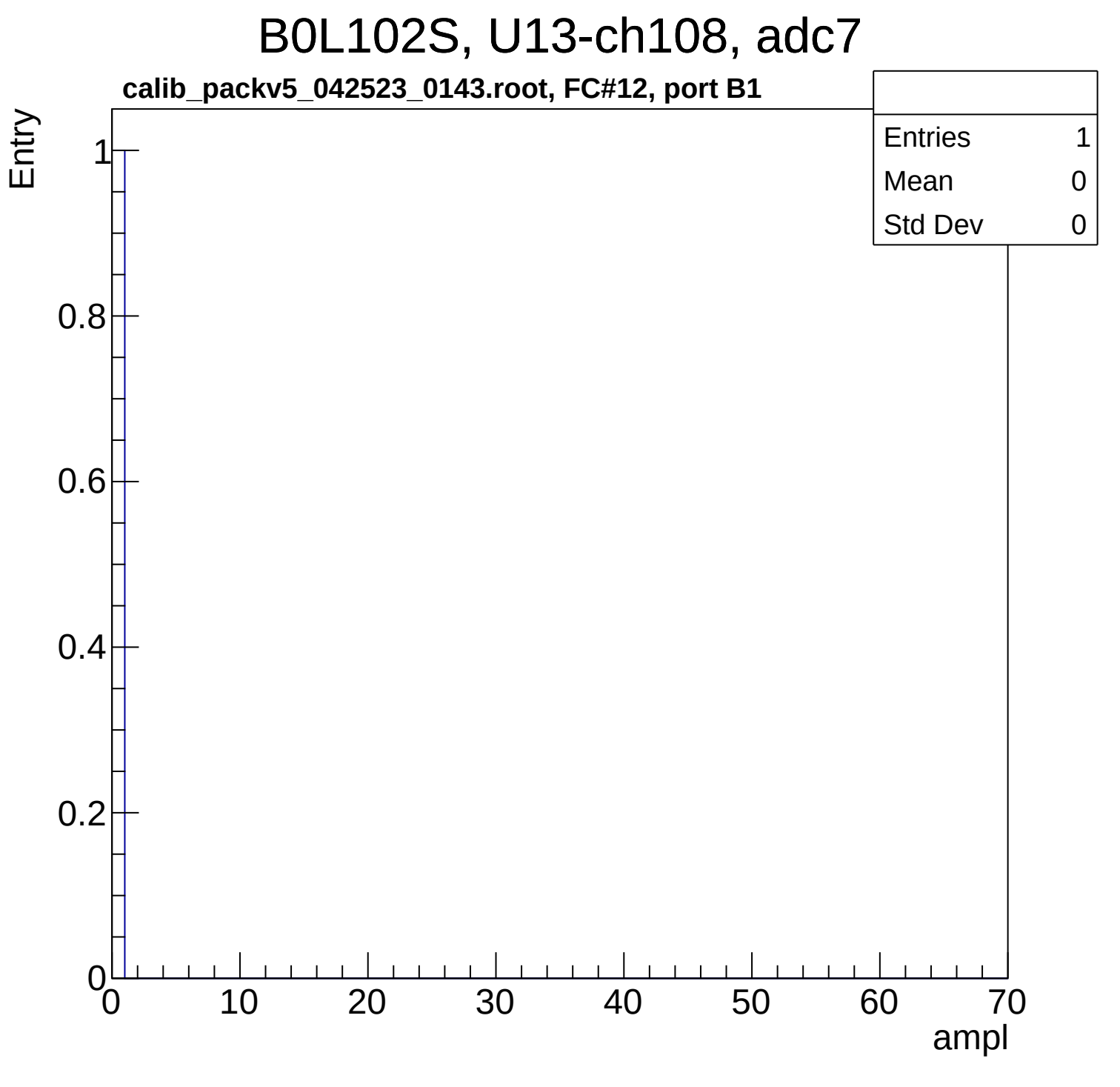
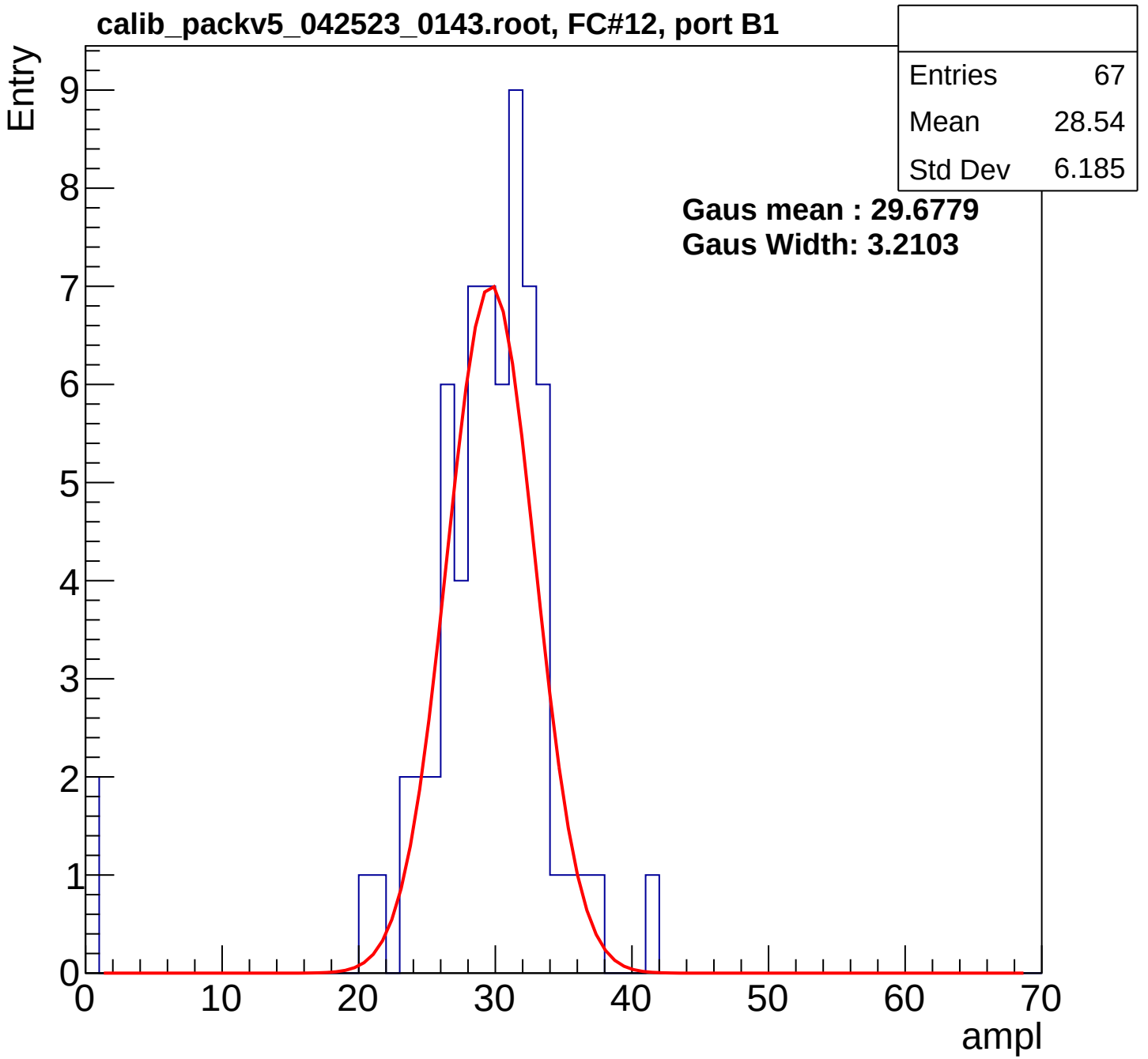
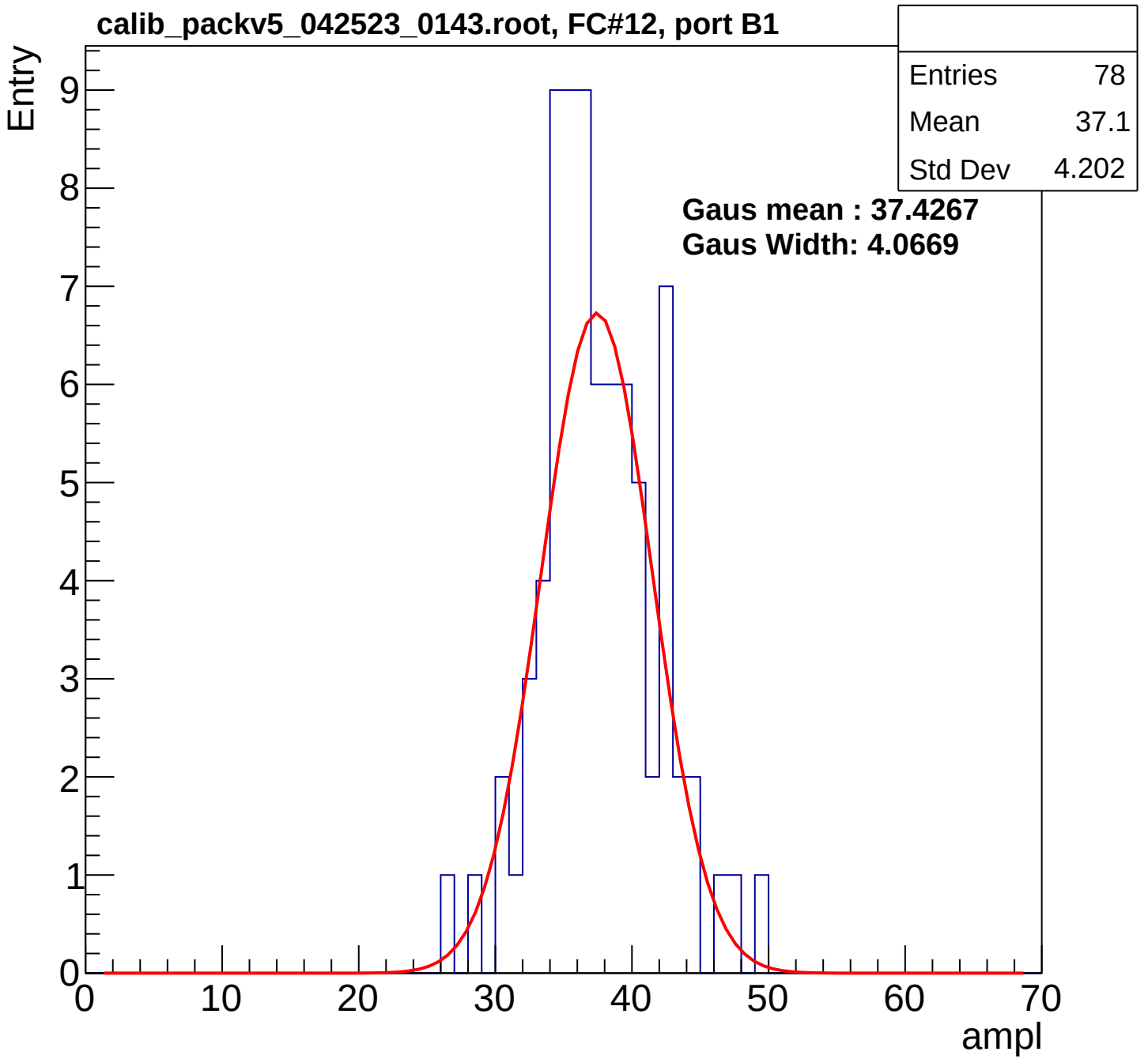
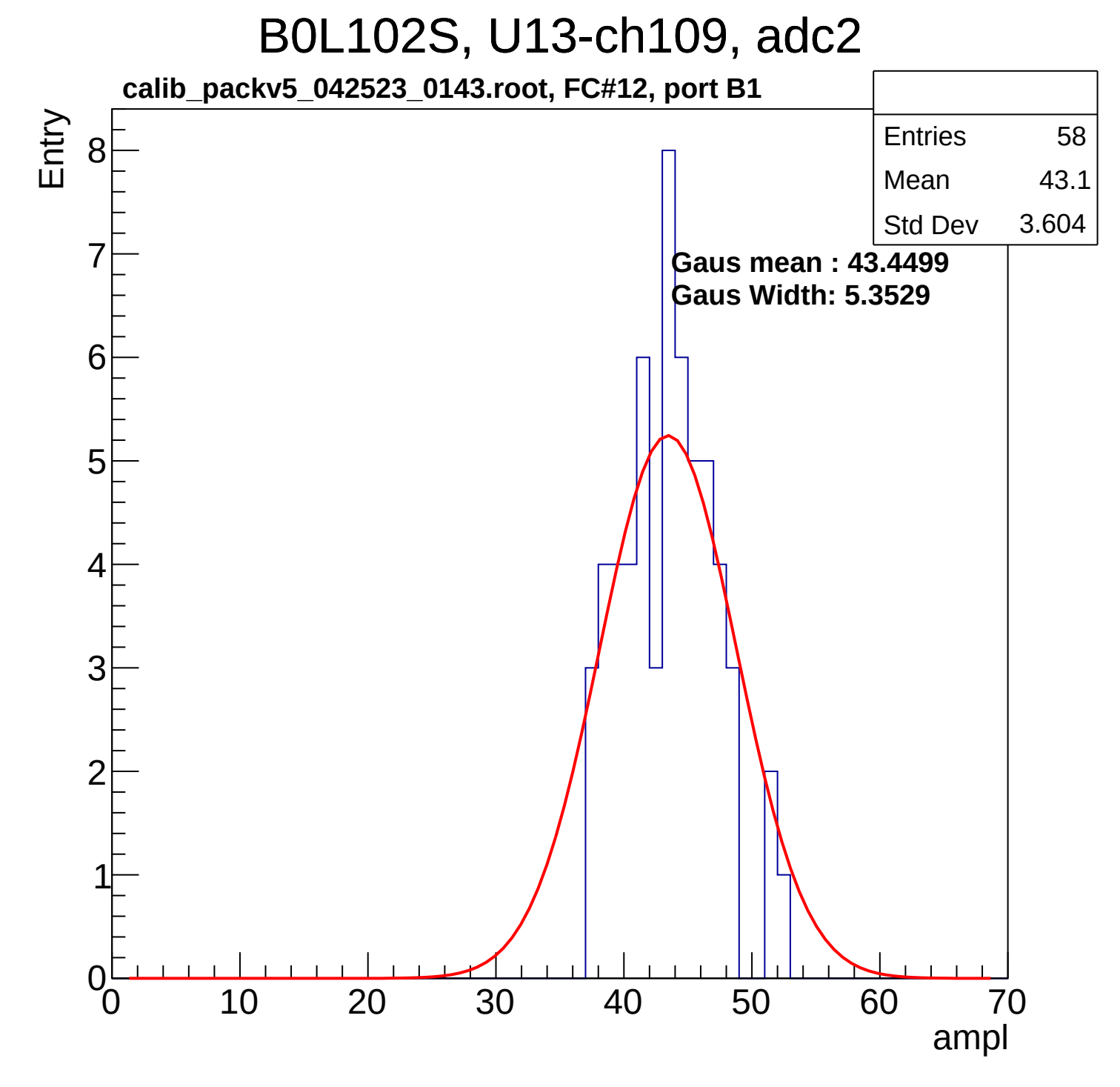
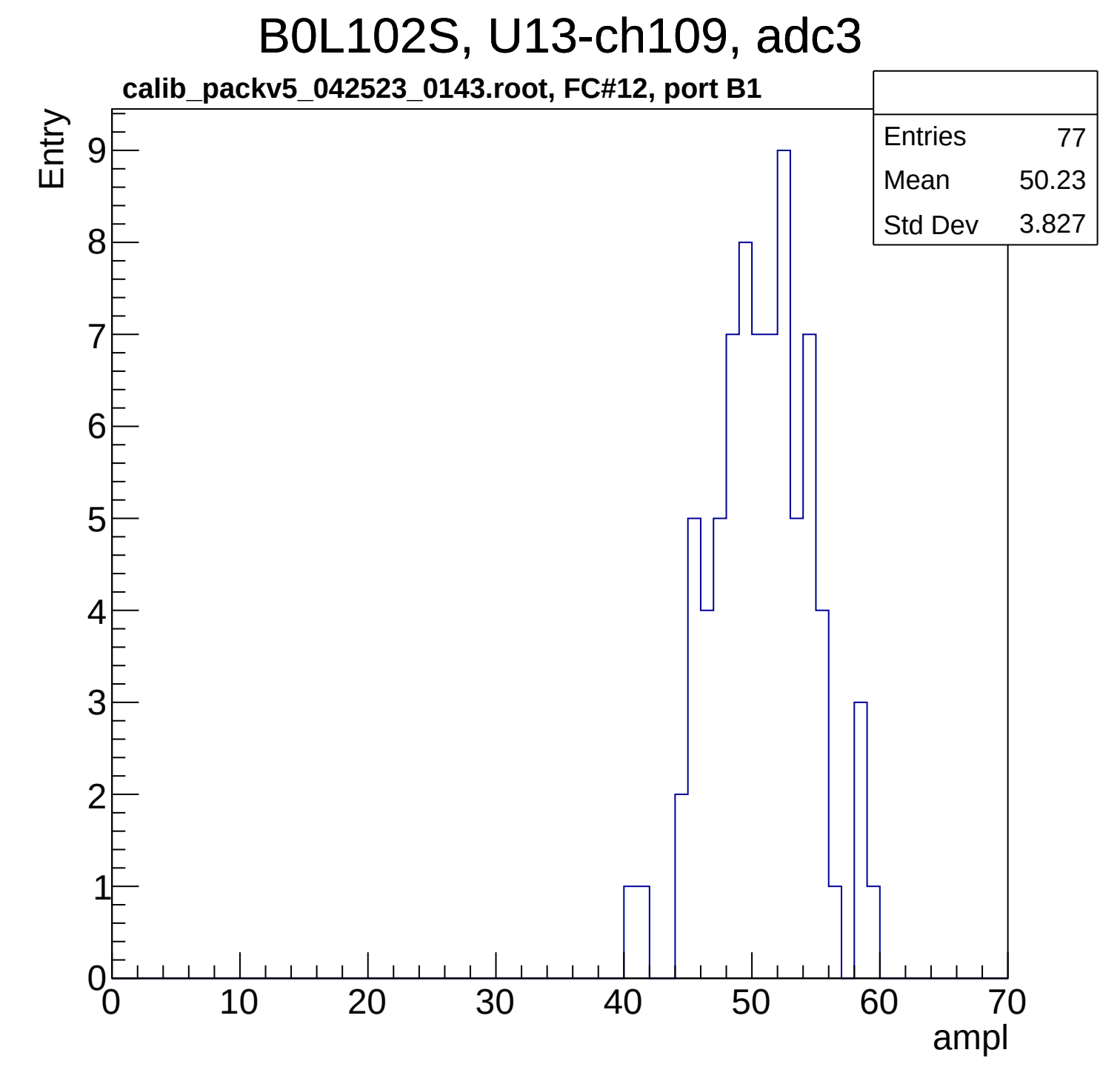

B0L102S, U13-ch108, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.58
Std Dev 1.605
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

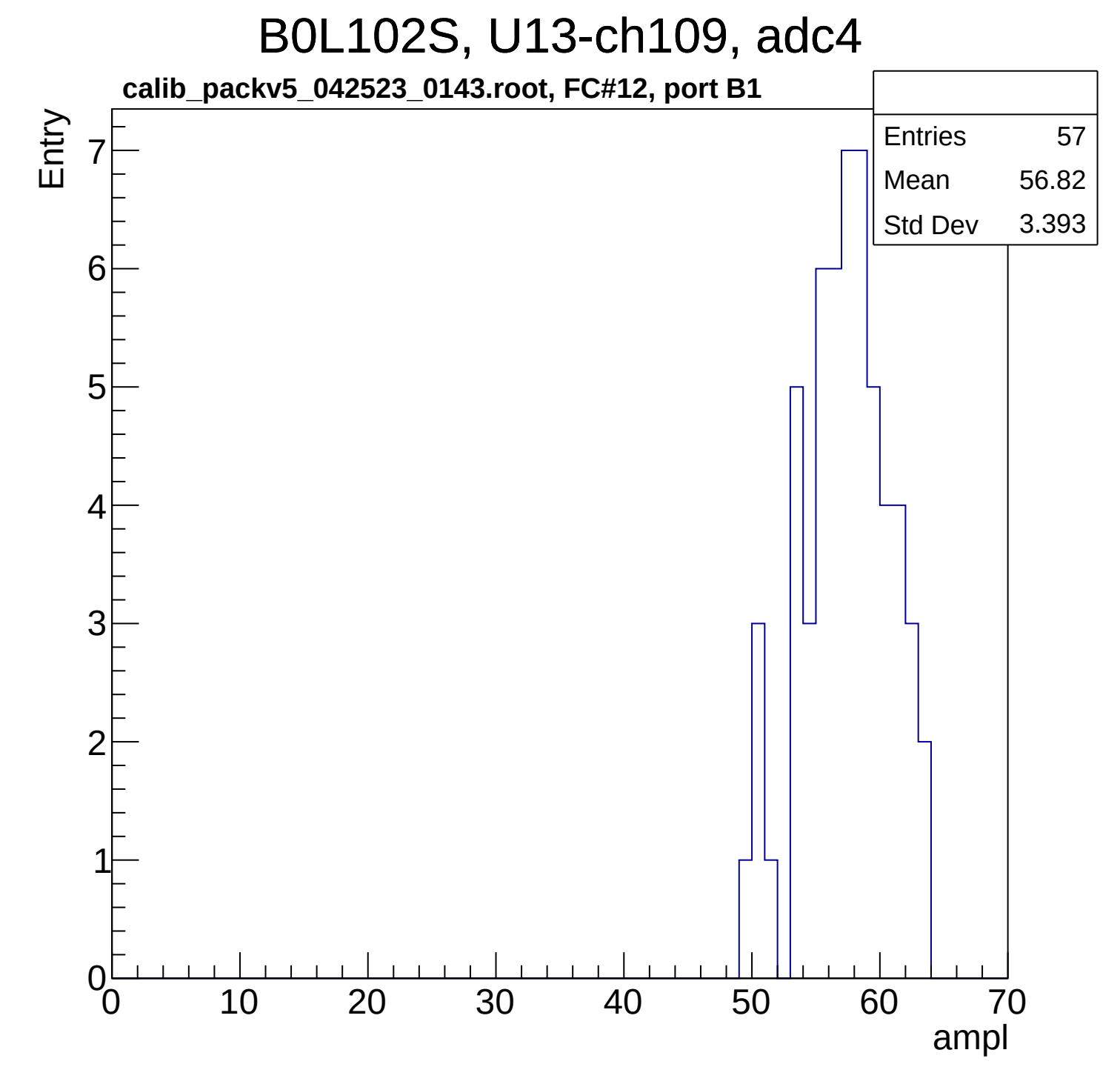

B0L102S, U13-ch108, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

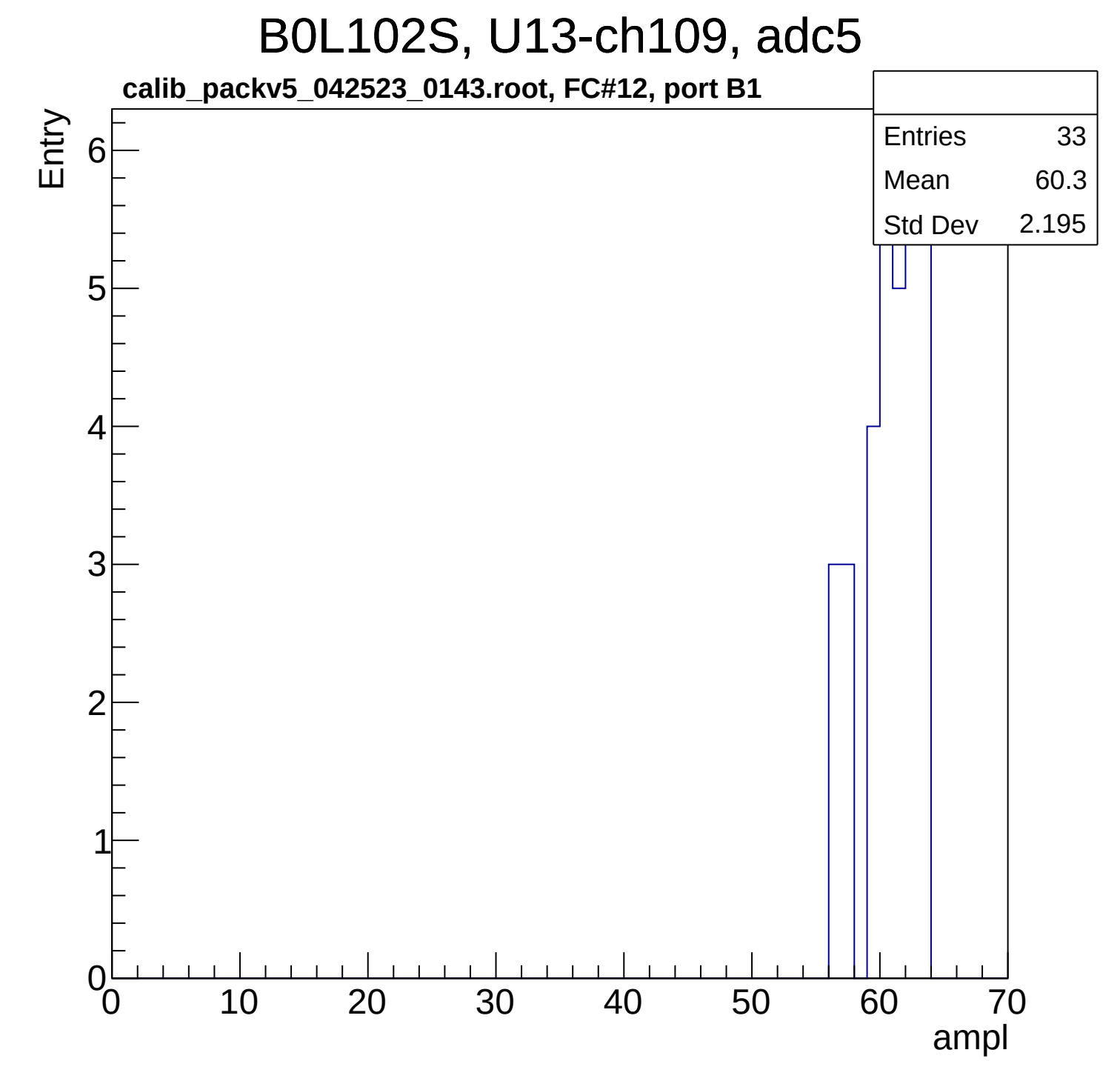

B0L102S, U13-ch109, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.54
Std Dev 6.185
Gaus mean : 29.6779
Gaus Width: 3.2103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

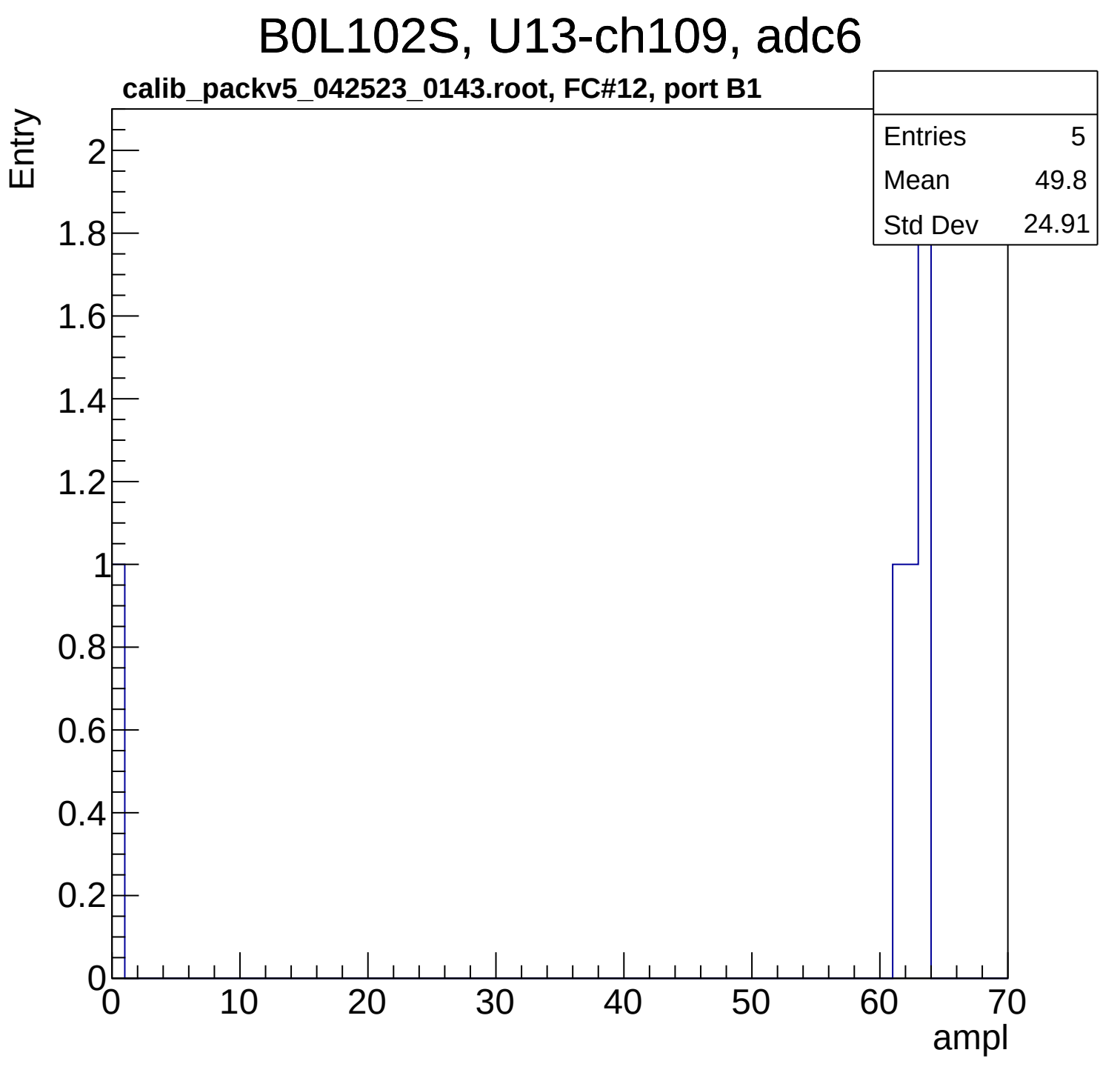

B0L102S, U13-ch109, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 37.1
Std Dev 4.202
Gaus mean : 37.4267
Gaus Width: 4.0669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

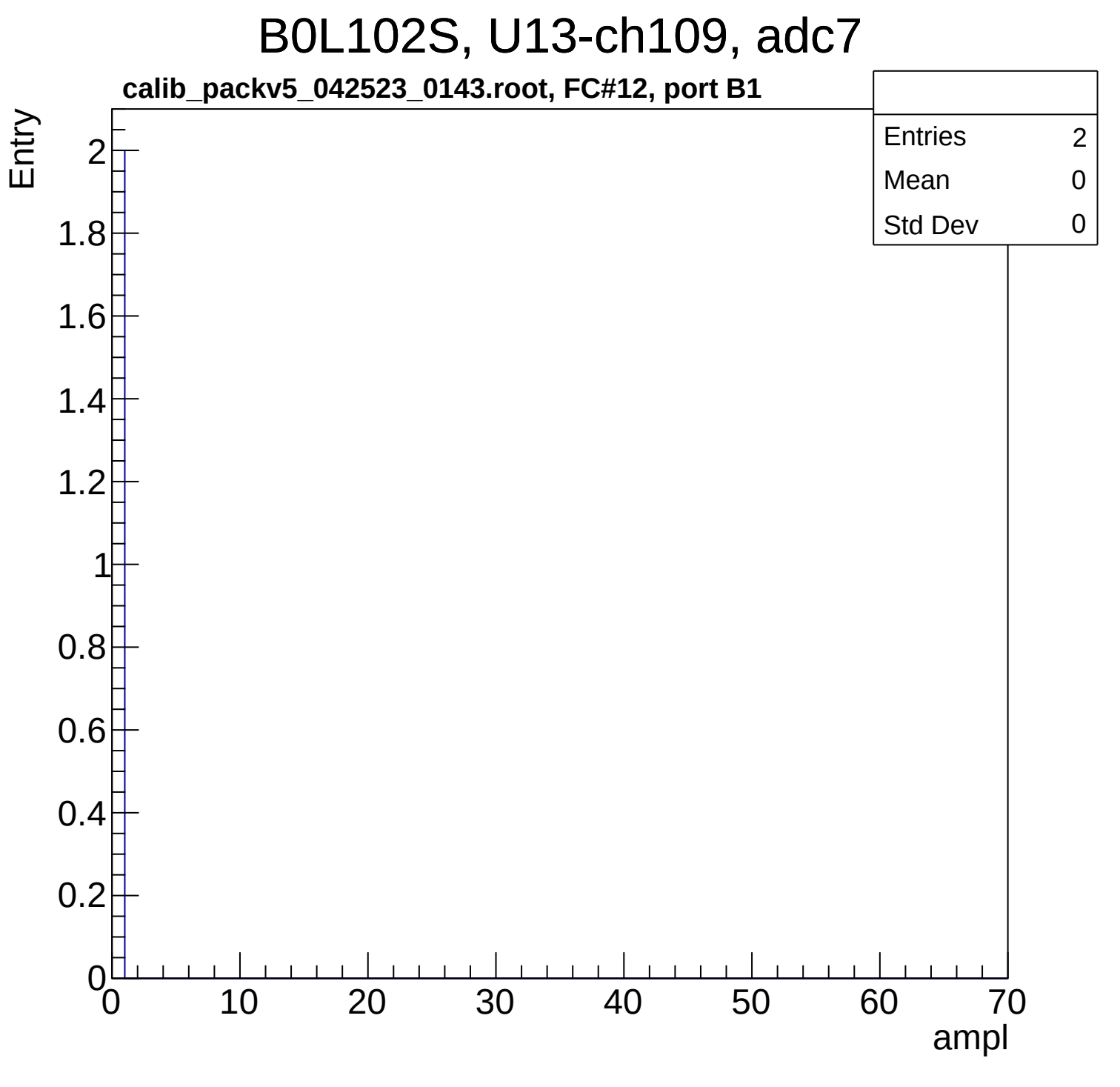

B0L102S, U13-ch109, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 43.1
Std Dev 3.604
Gaus mean : 43.4499
Gaus Width: 5.3529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

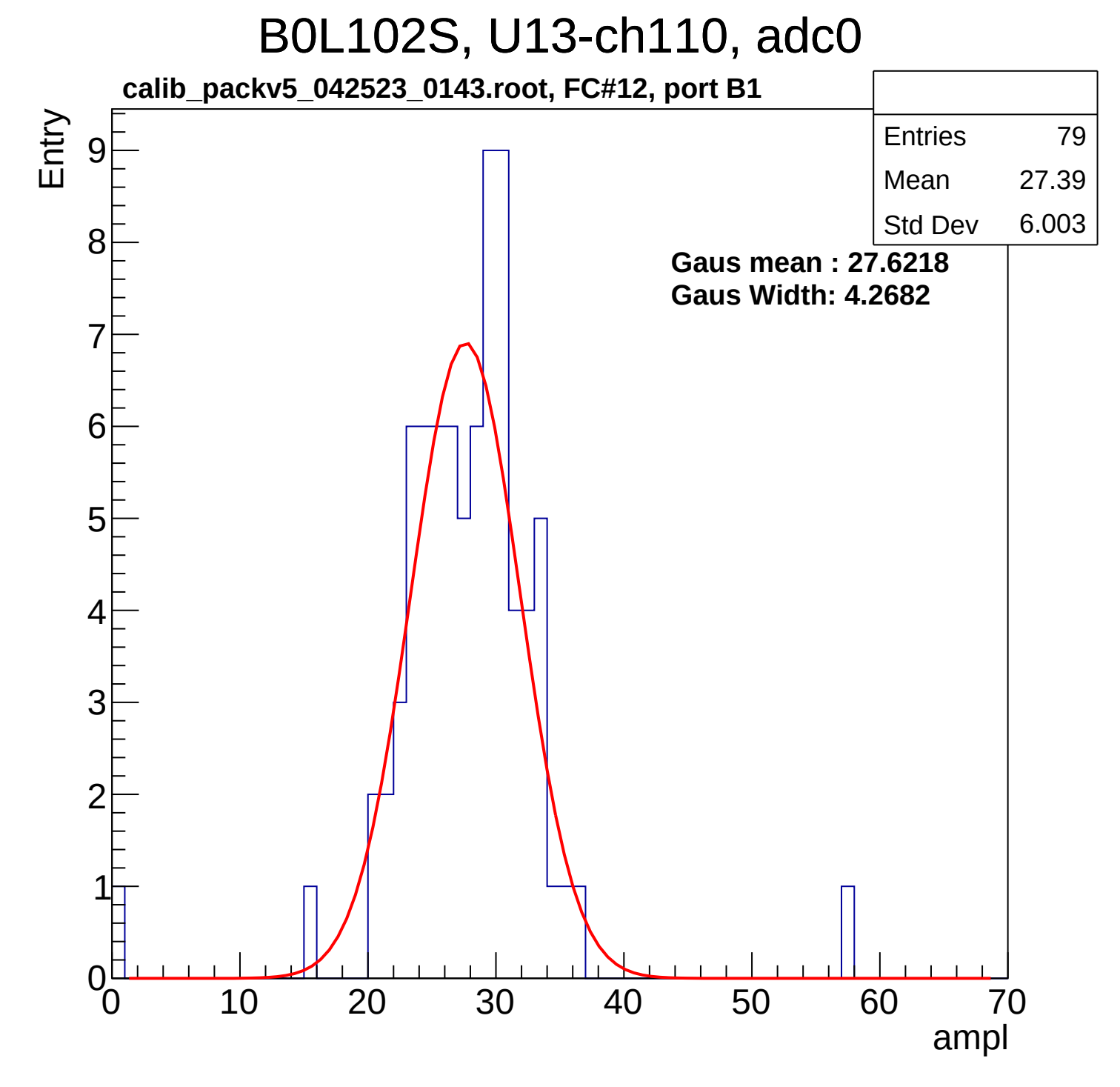

B0L102S, U13-ch109, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 50.23
Std Dev 3.827
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

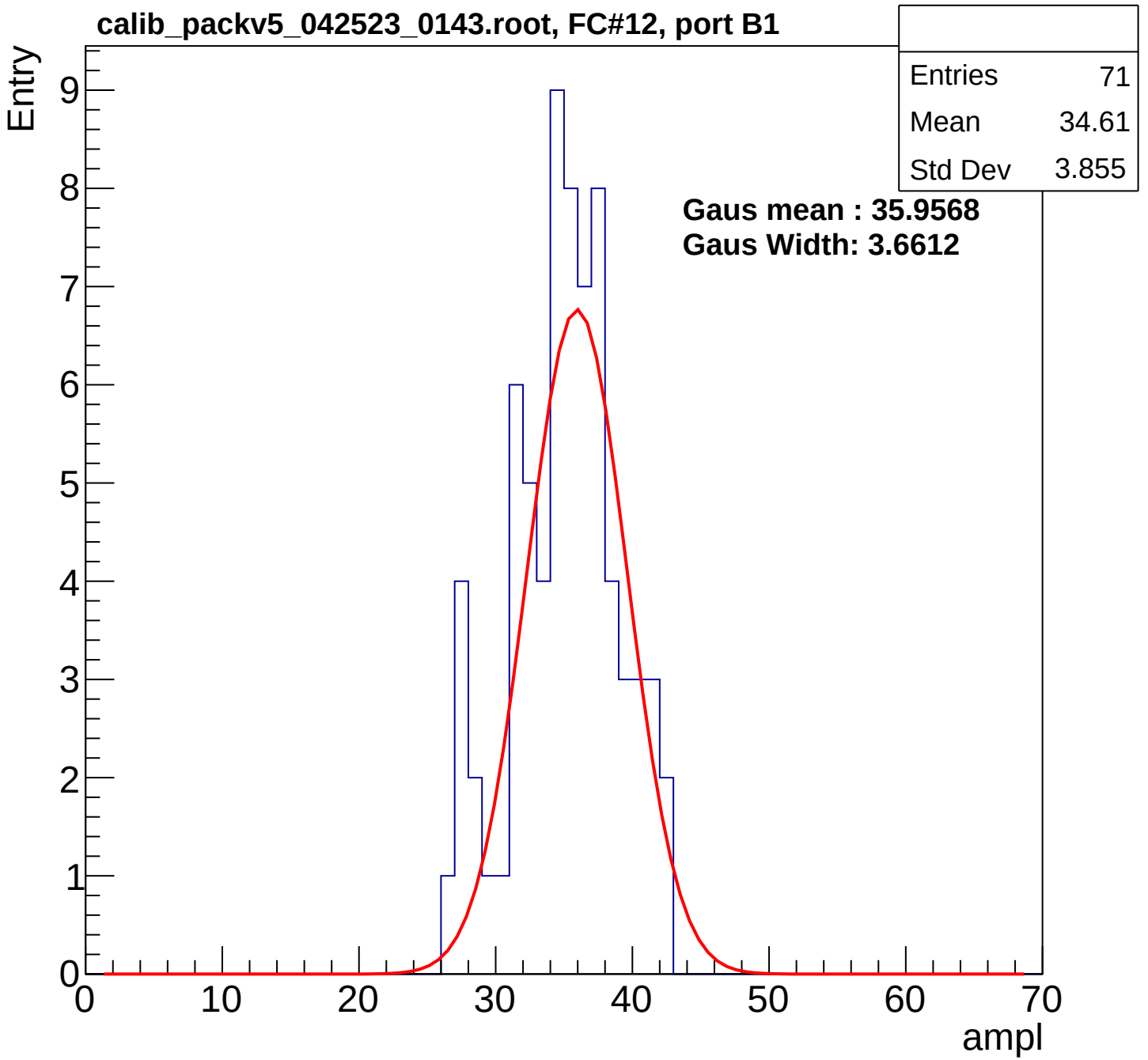

B0L102S, U13-ch109, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 56.82
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

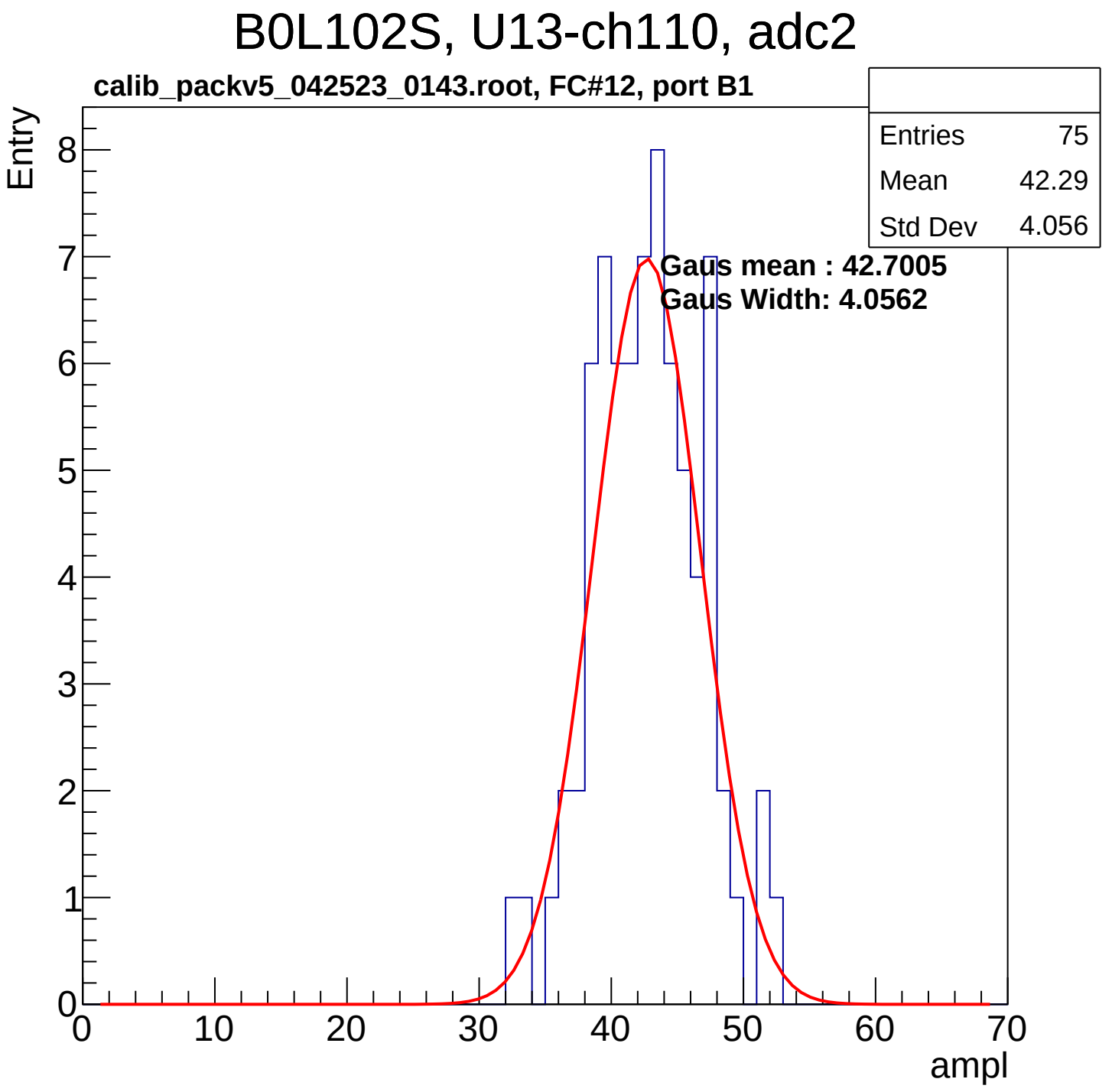

B0L102S, U13-ch109, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.3
Std Dev 2.195
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch109, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch109, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch110, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 27.39
Std Dev 6.003
Gaus mean : 27.6218
Gaus Width: 4.2682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch110, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 34.61
Std Dev 3.855
Gaus mean : 35.9568
Gaus Width: 3.6612
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U13-ch110, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 42.29
Std Dev 4.056
Gaus mean : 42.7005
Gaus Width: 4.0562
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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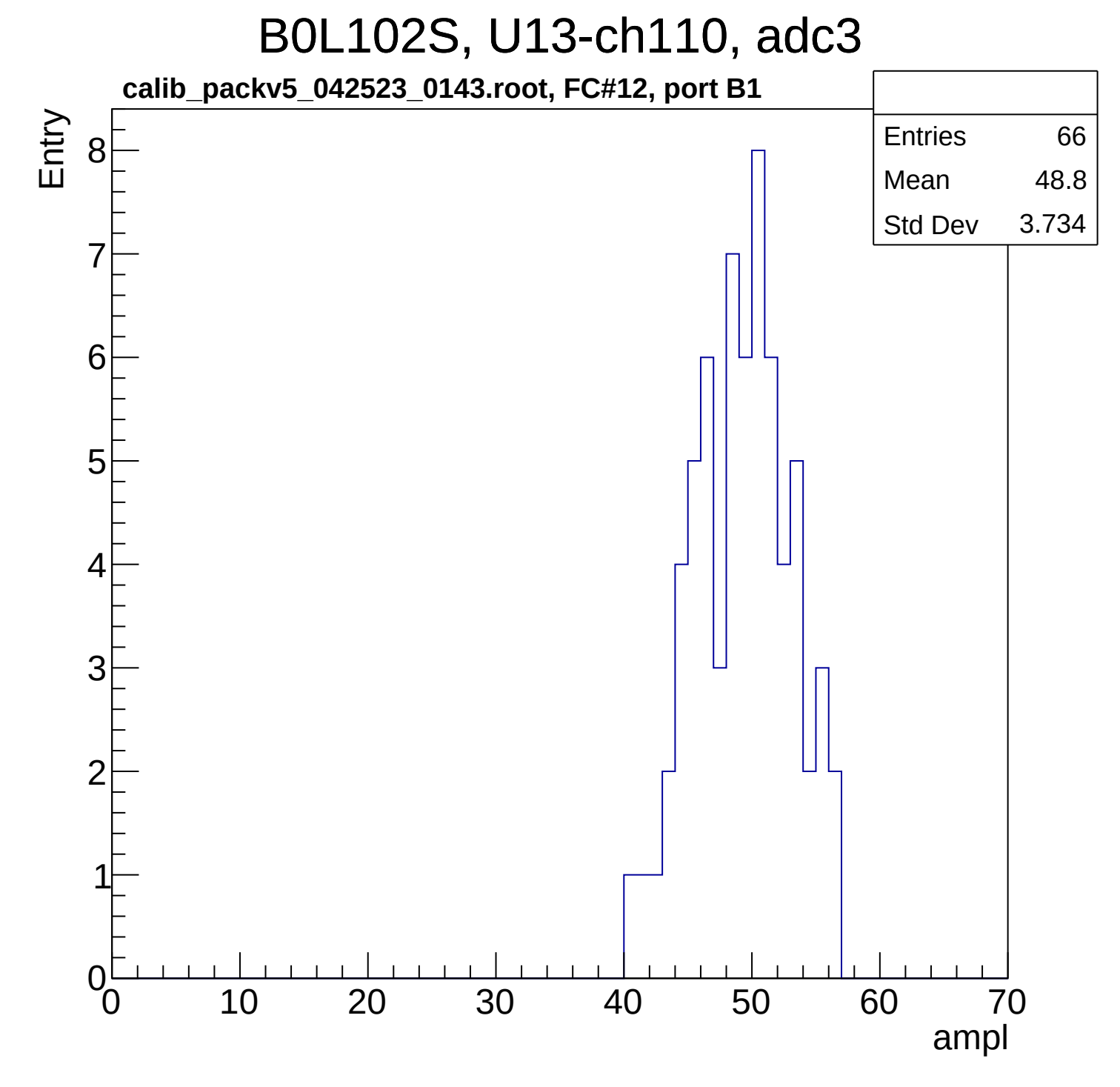
B0L102S, U13-ch110, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 48.8
Std Dev 3.734
Entry
ampl
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0
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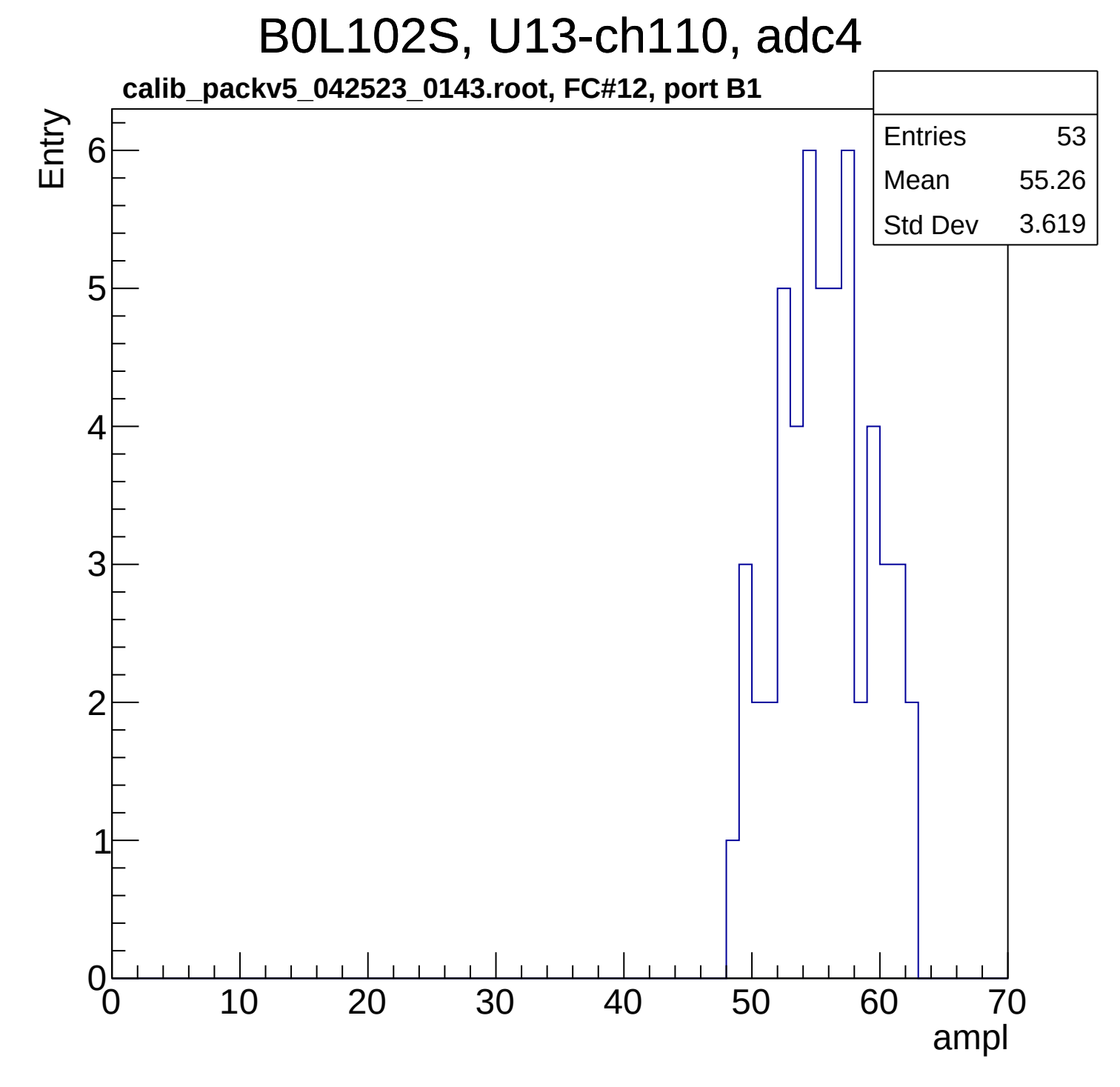
40
50
60
70


B0L102S, U13-ch110, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 55.26
Std Dev 3.619
Entry
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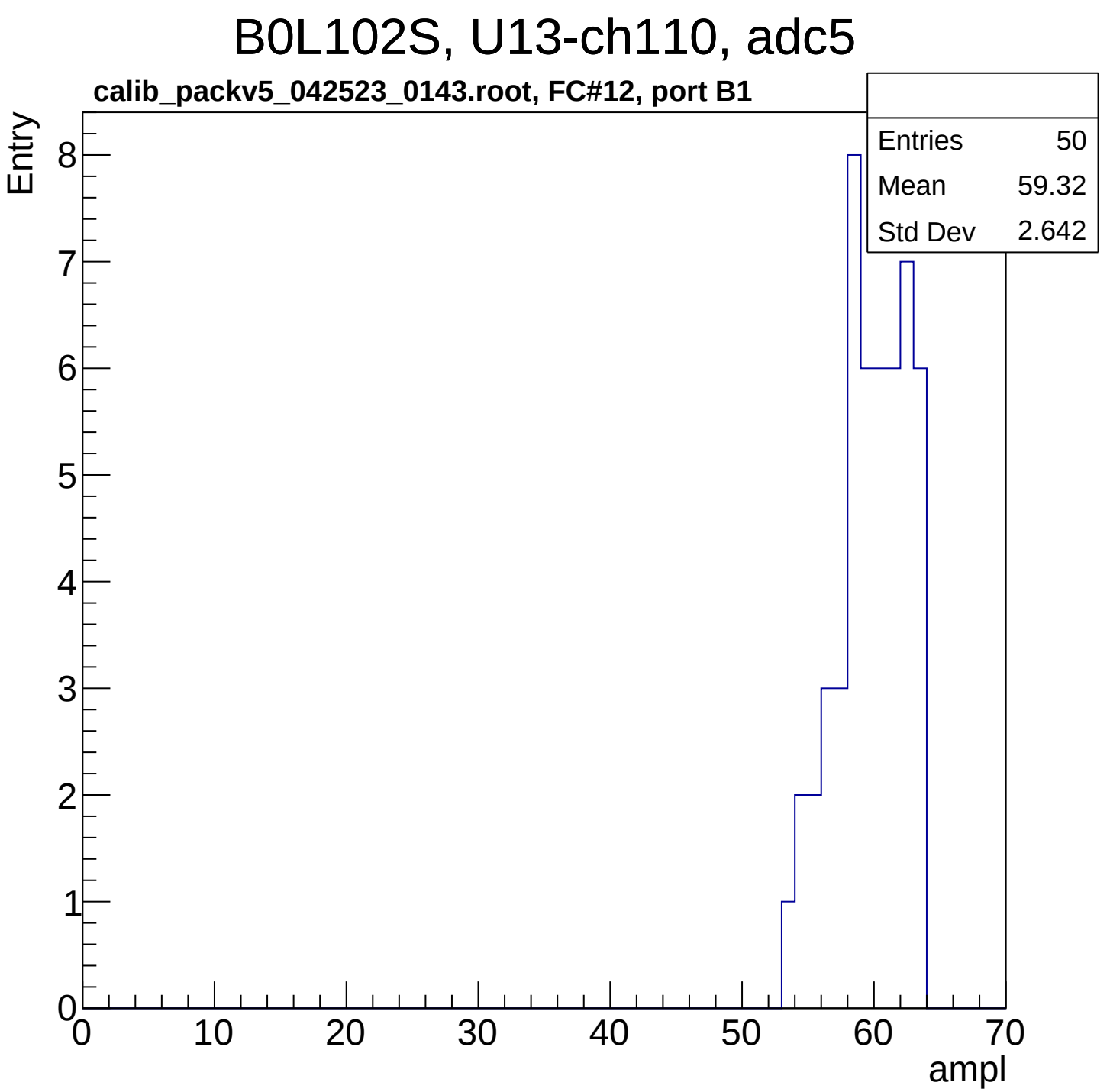
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch110, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 59.32
Std Dev 2.642
Entry
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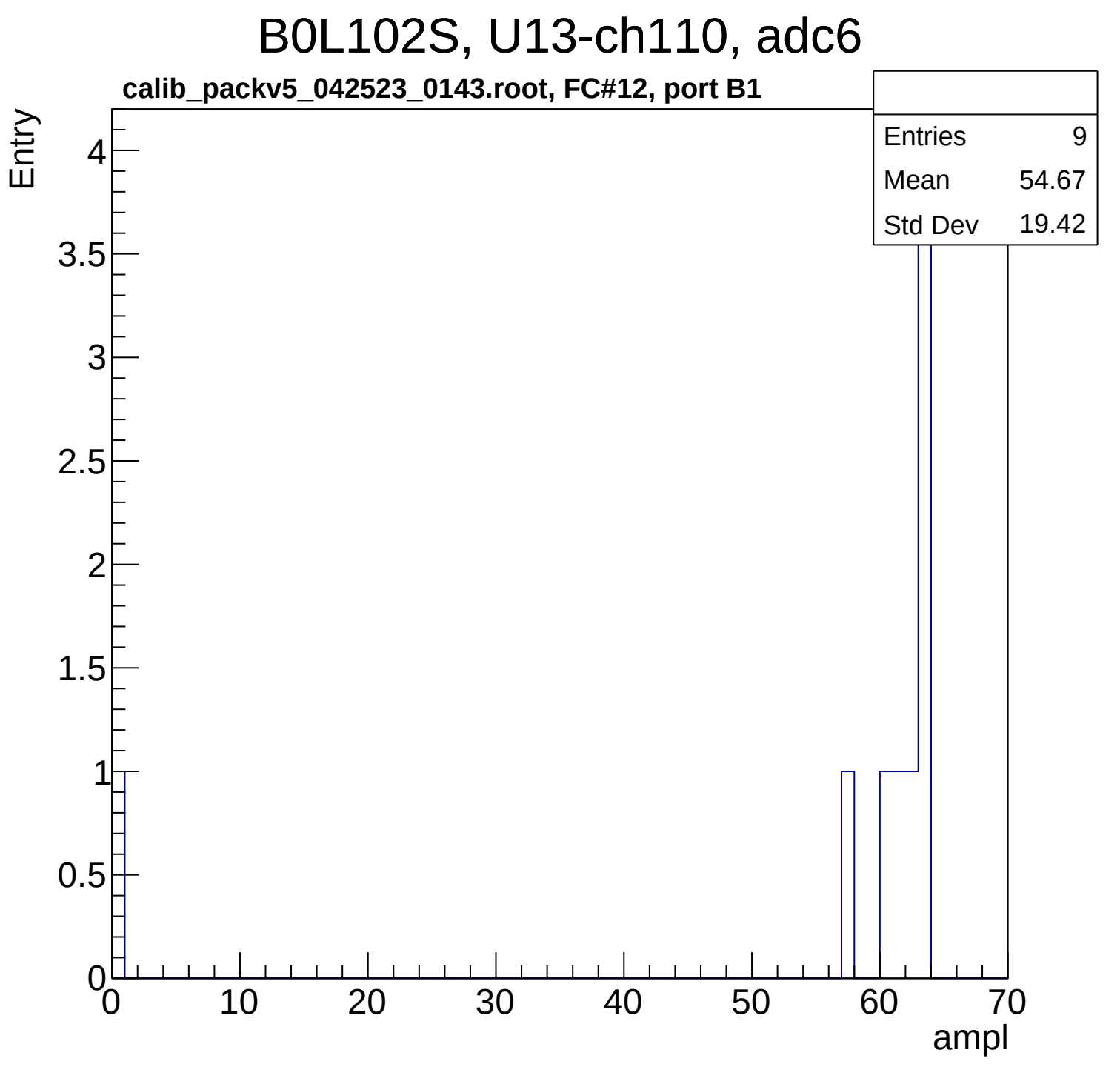
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch110, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.67
Std Dev 19.42
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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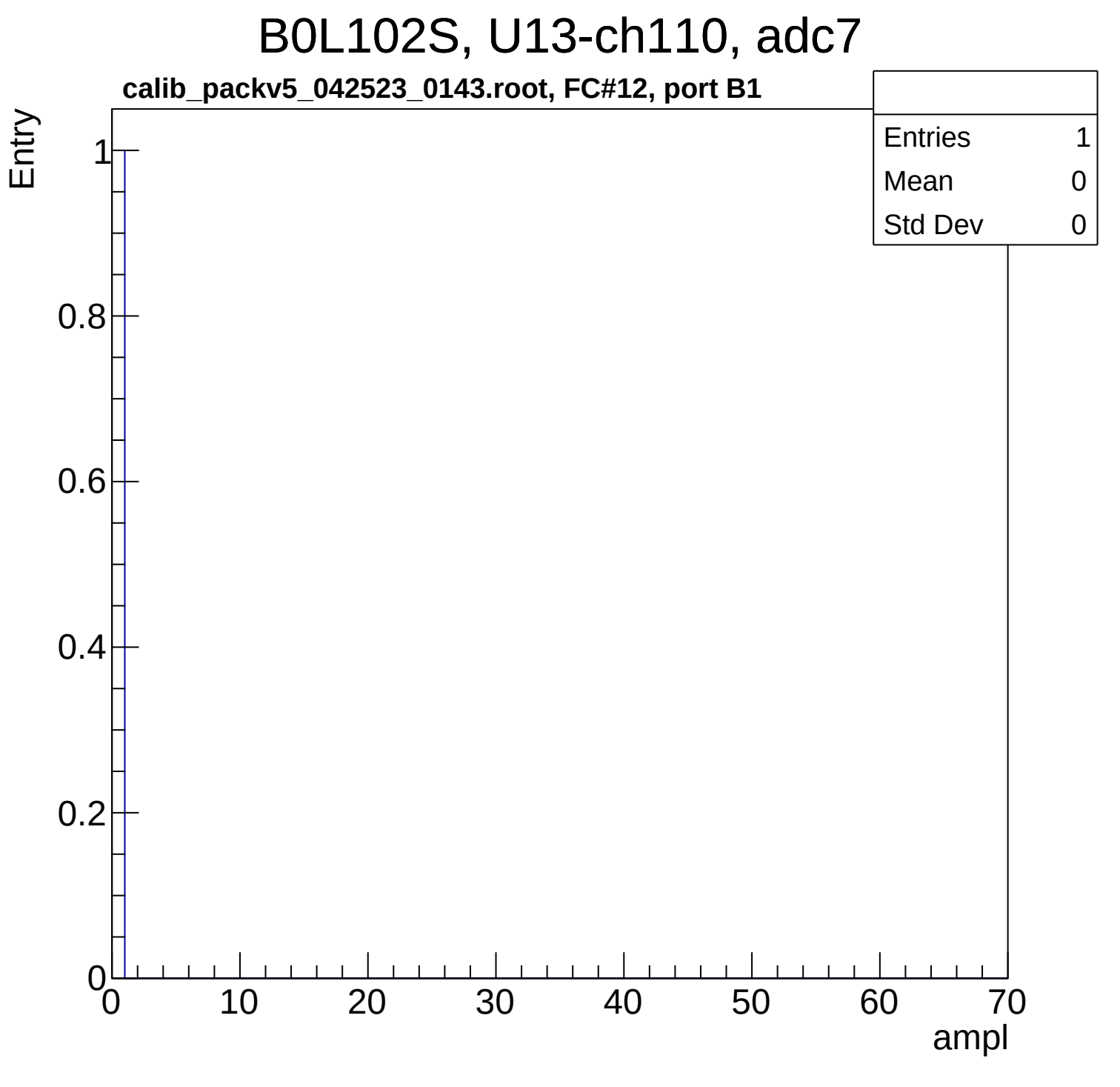
60
70


B0L102S, U13-ch110, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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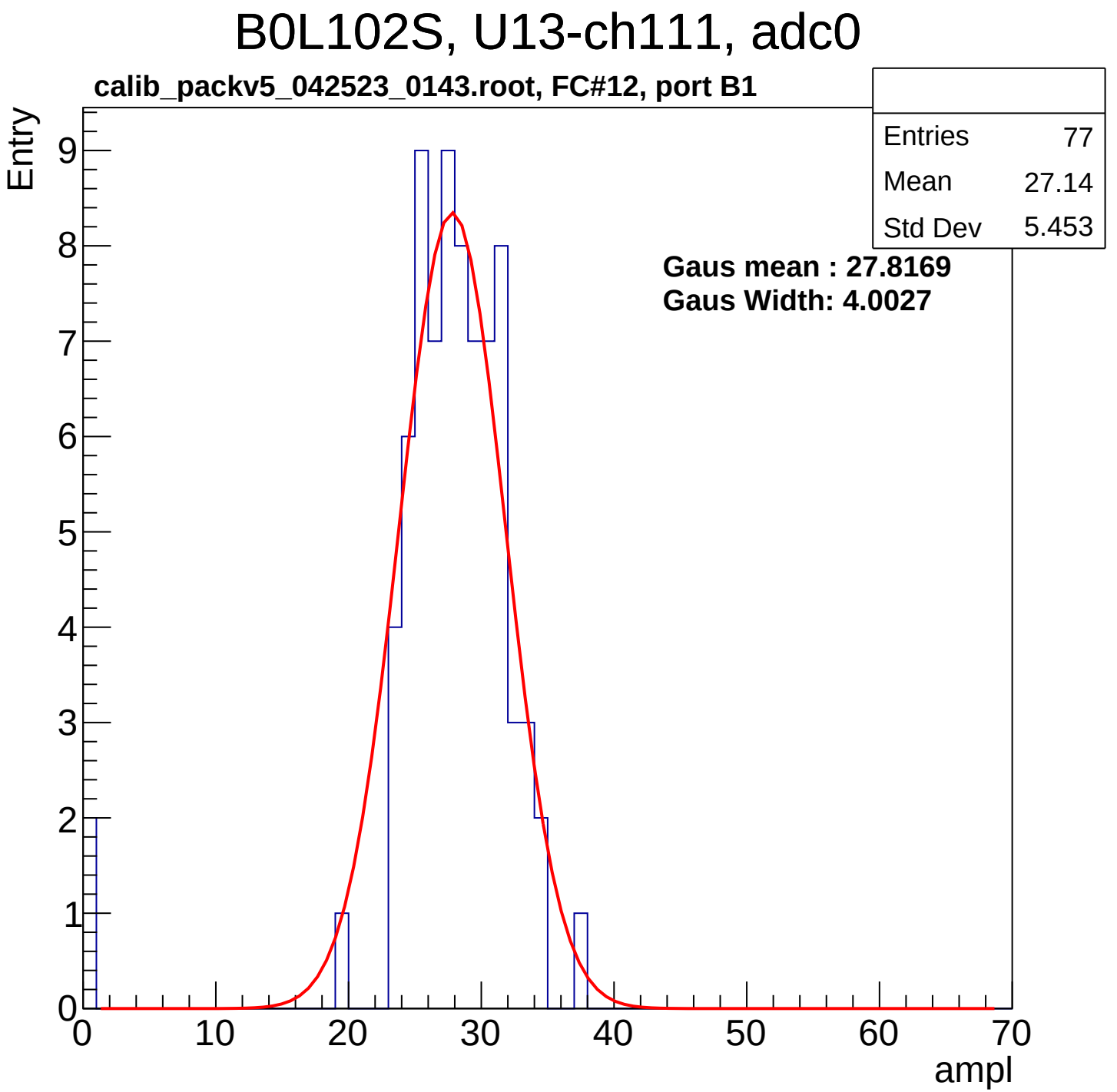
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch111, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 27.14
Std Dev 5.453
Gaus mean : 27.8169
Gaus Width: 4.0027
Entry
ampl
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2
3
4
5
6
7
8
9
0
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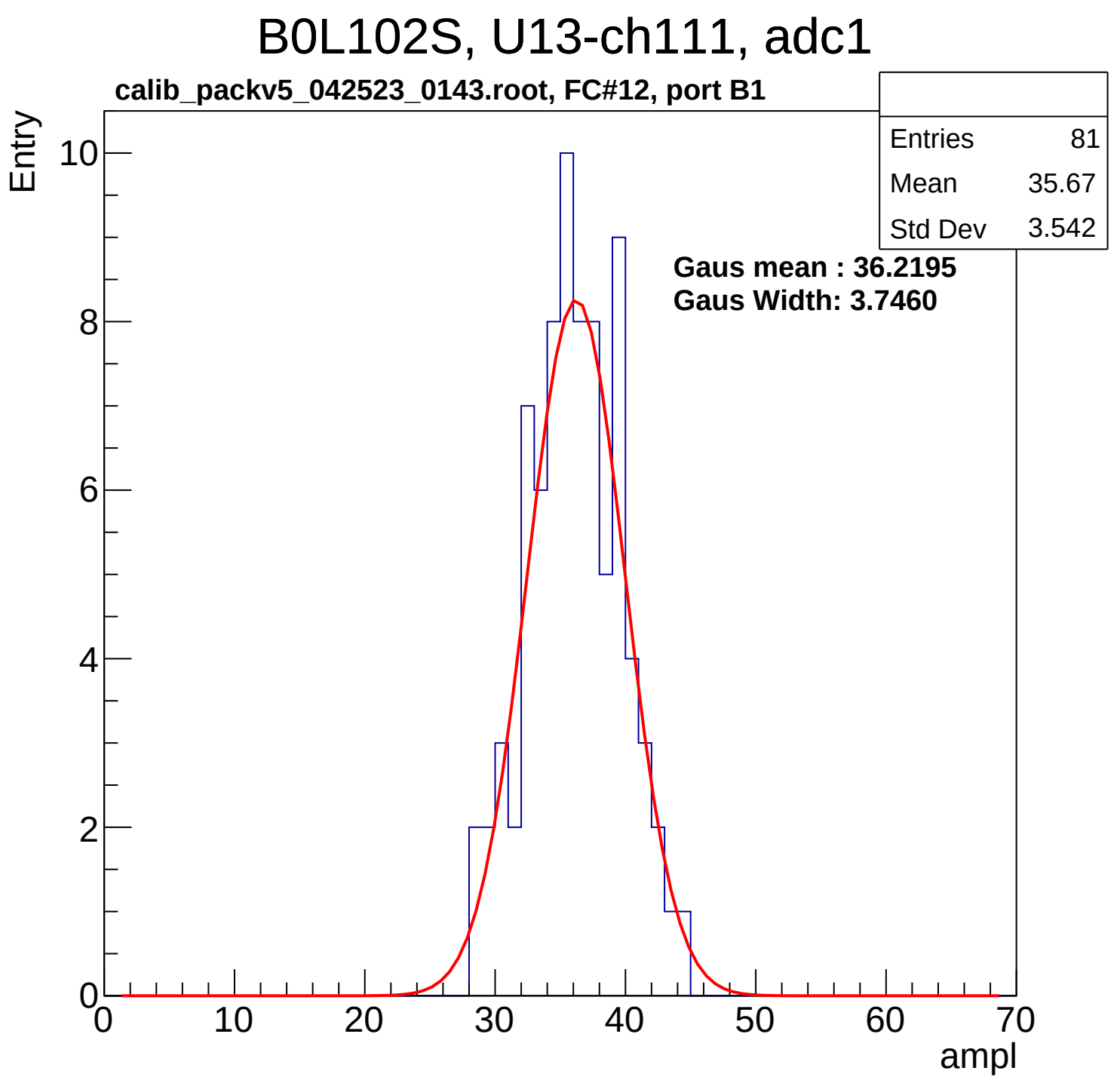
20
30
40
50
60
70


B0L102S, U13-ch111, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 35.67
Std Dev 3.542
Gaus mean : 36.2195
Gaus Width: 3.7460
Entry
ampl
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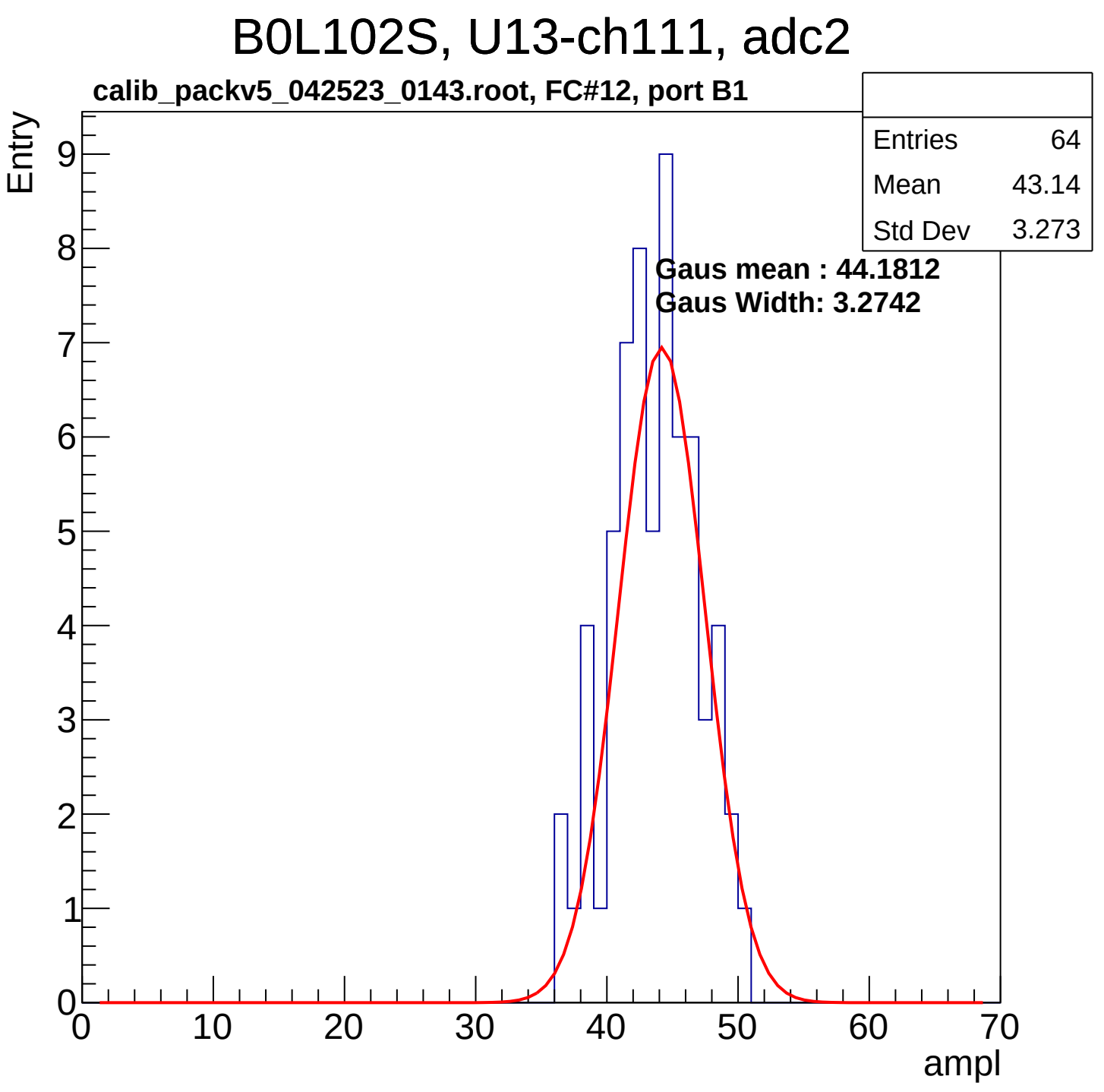
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch111, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.14
Std Dev 3.273
Gaus mean : 44.1812
Gaus Width: 3.2742
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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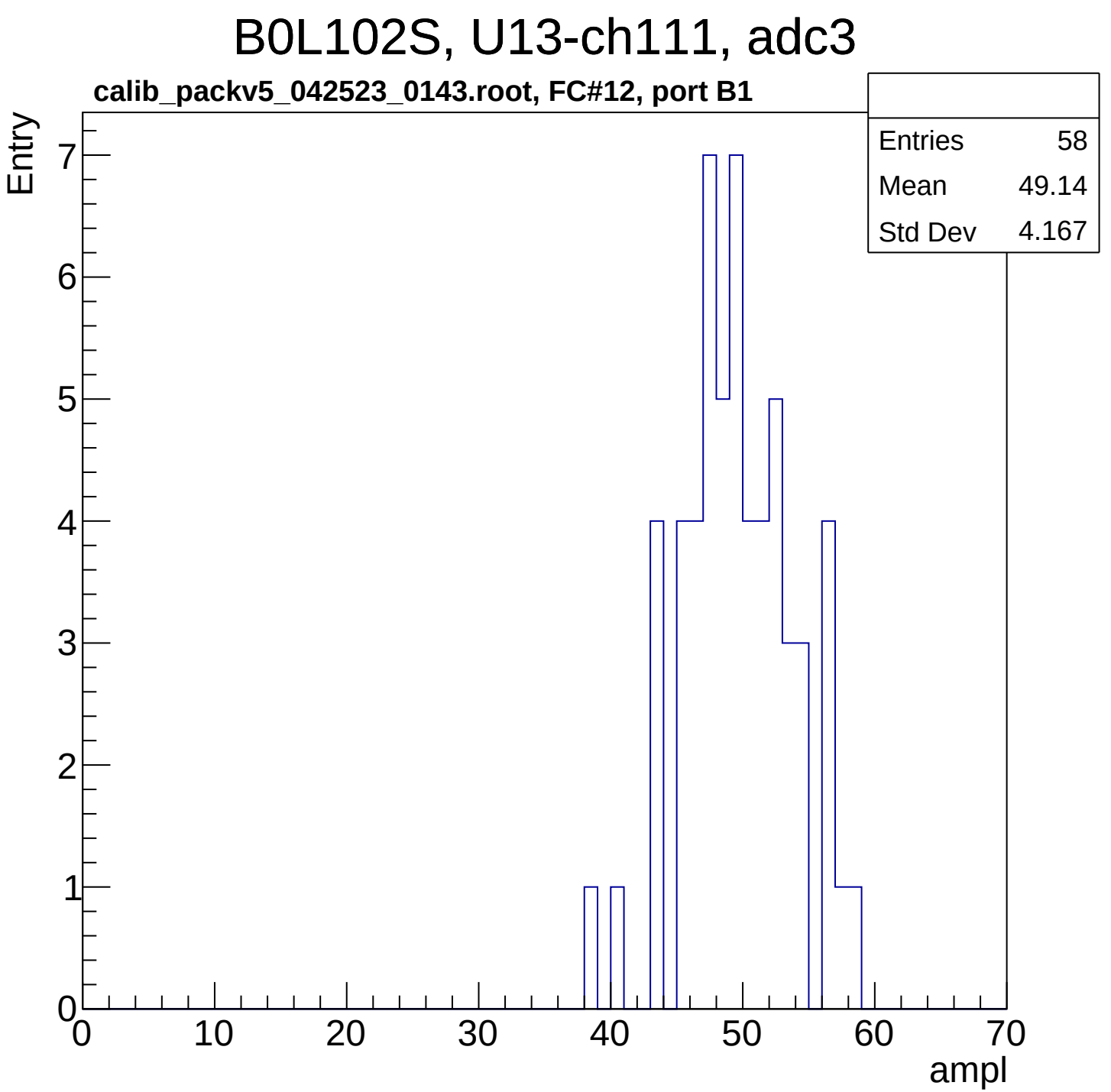
B0L102S, U13-ch111, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 49.14
Std Dev 4.167
Entry
ampl
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60
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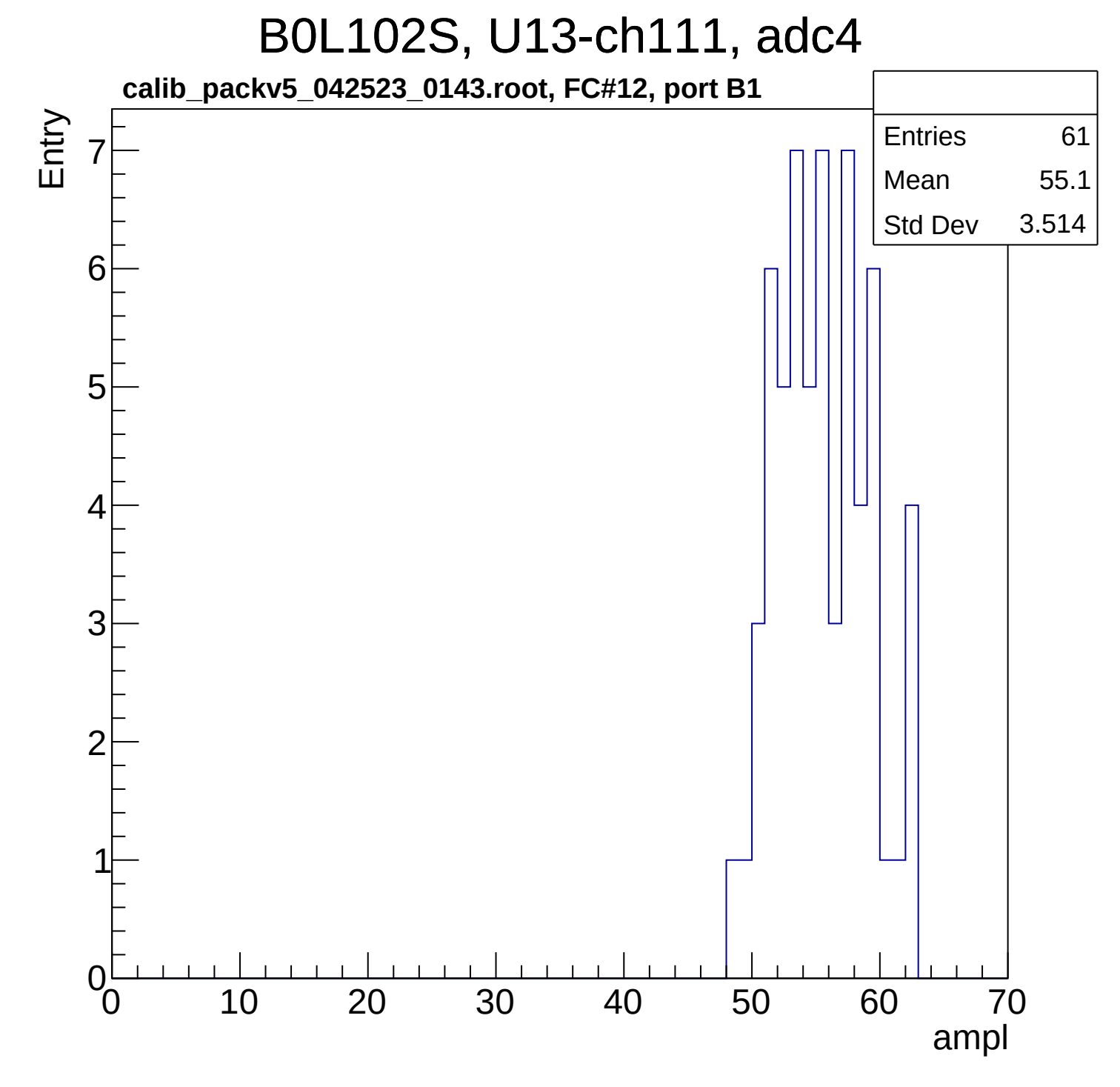
B0L102S, U13-ch111, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.1
Std Dev 3.514
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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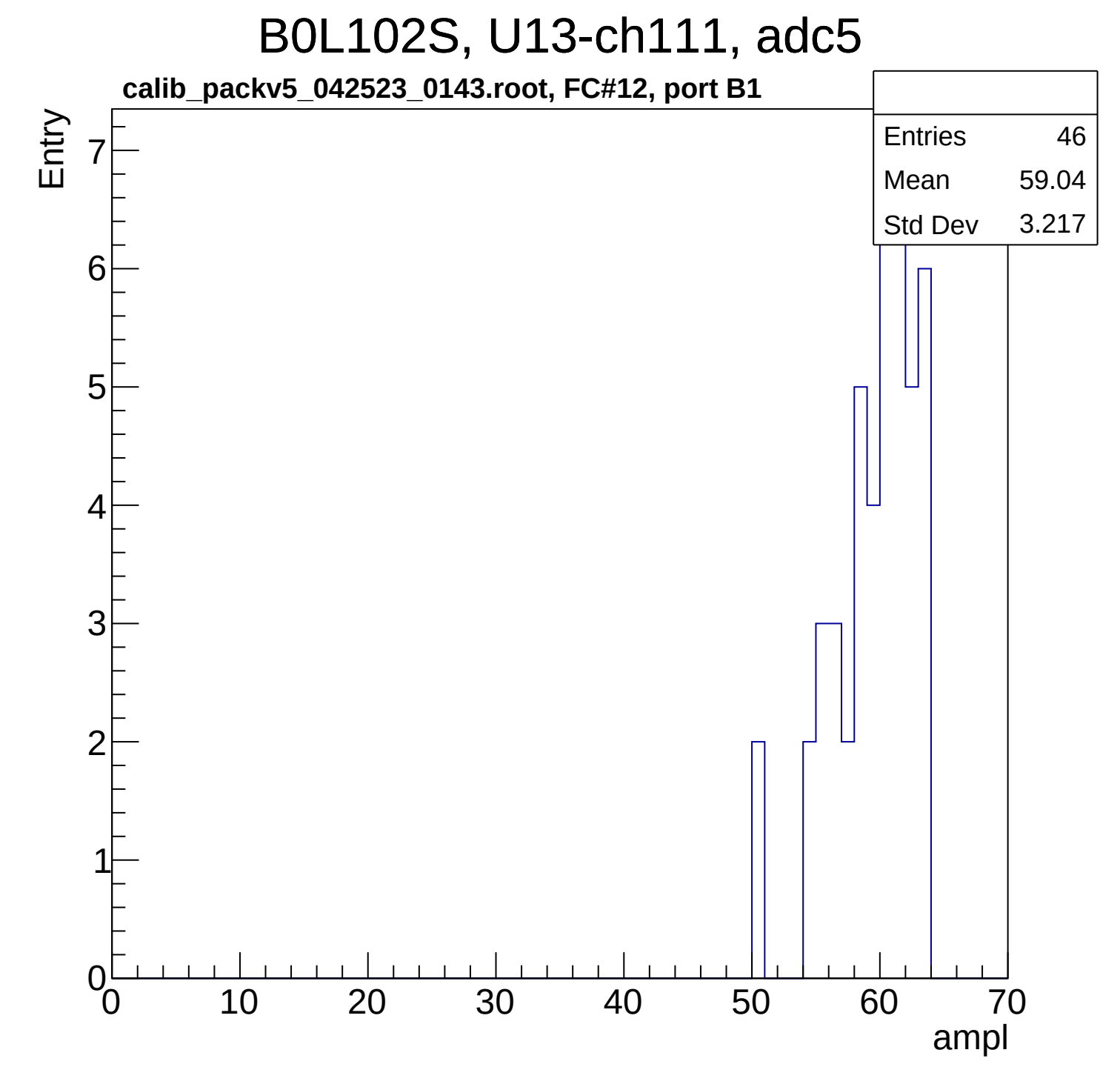
B0L102S, U13-ch111, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.04
Std Dev 3.217
Entry
ampl
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7
0
10
20
30
40
50
60
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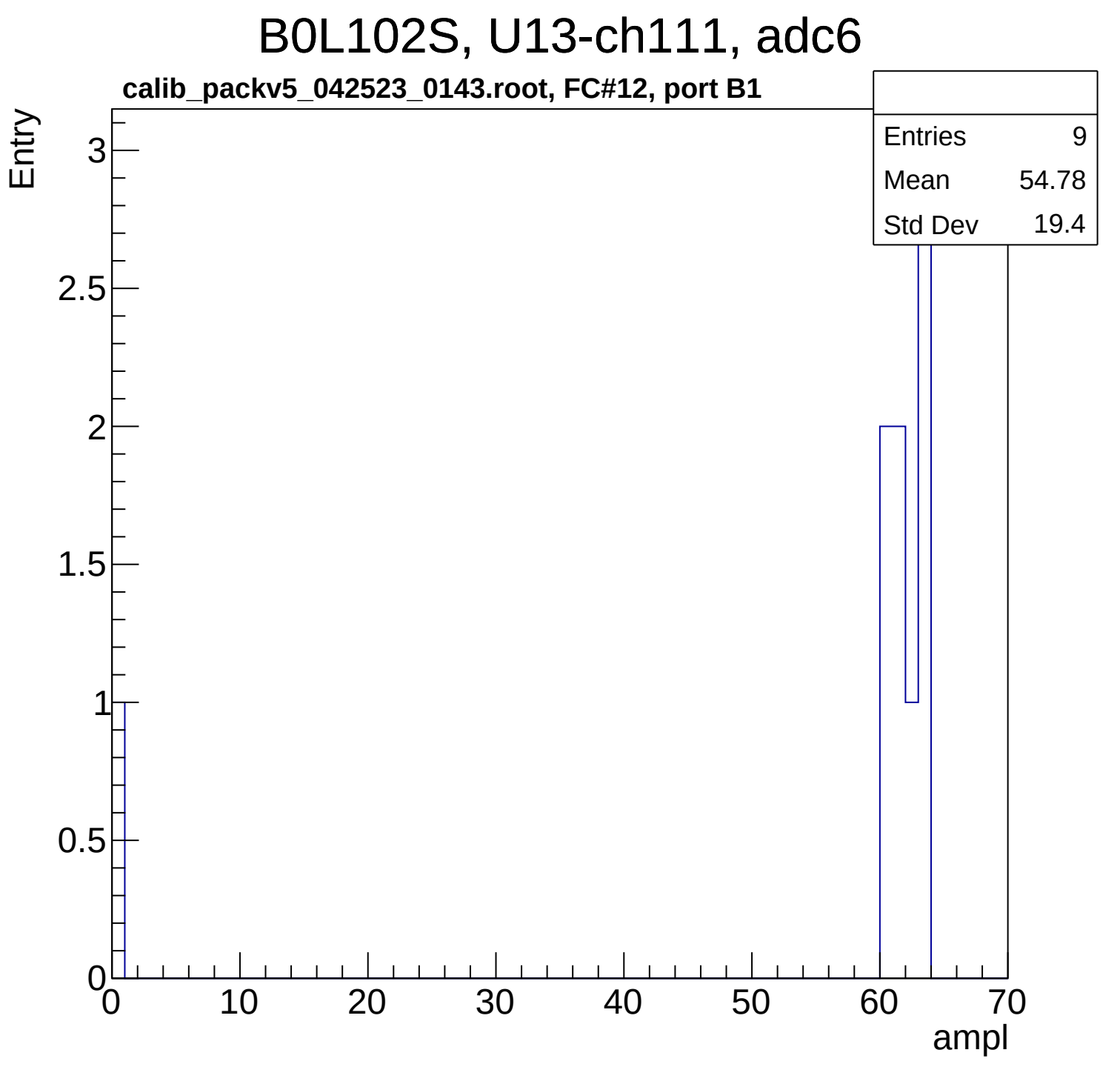
B0L102S, U13-ch111, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.78
Std Dev 19.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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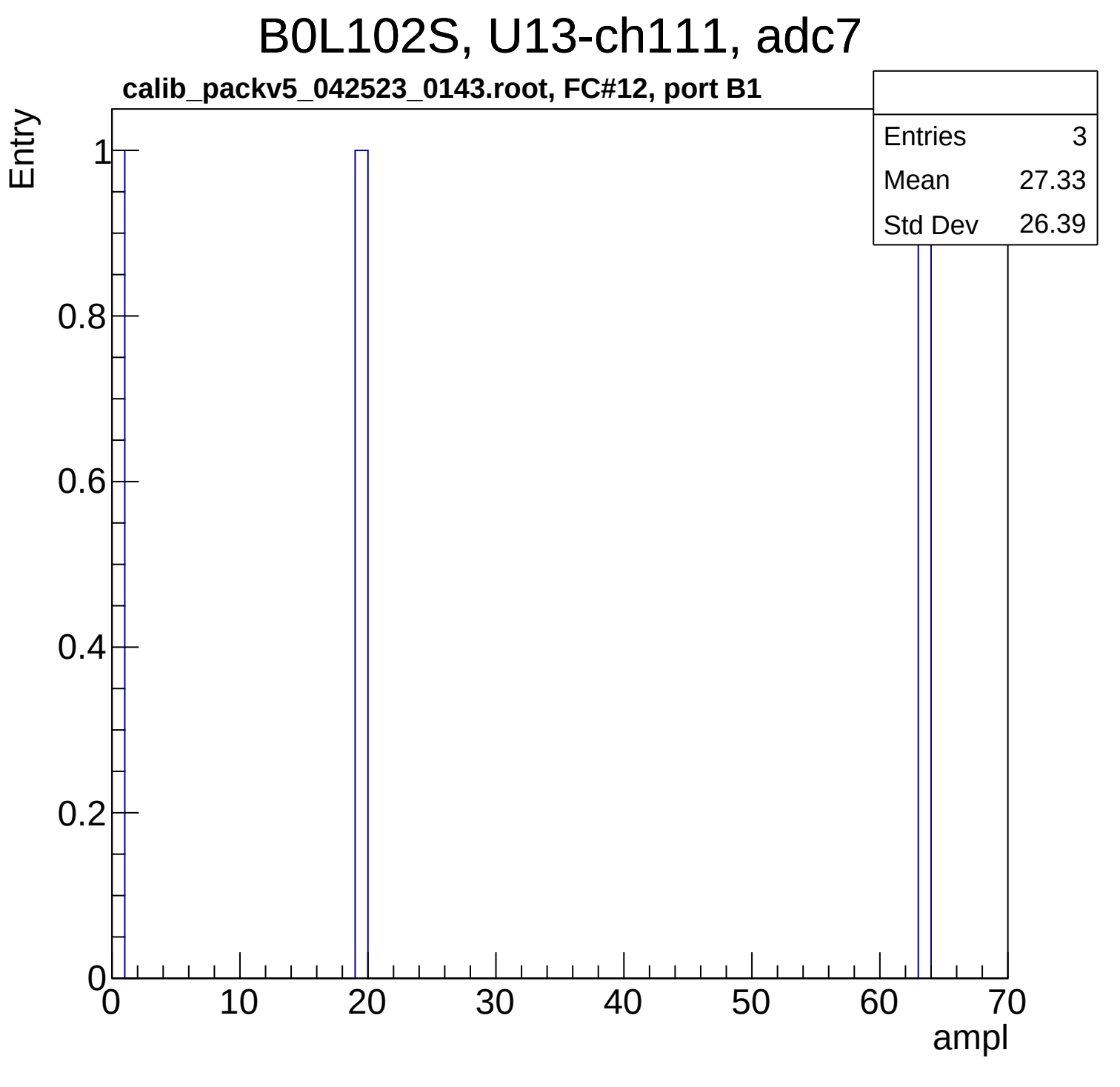
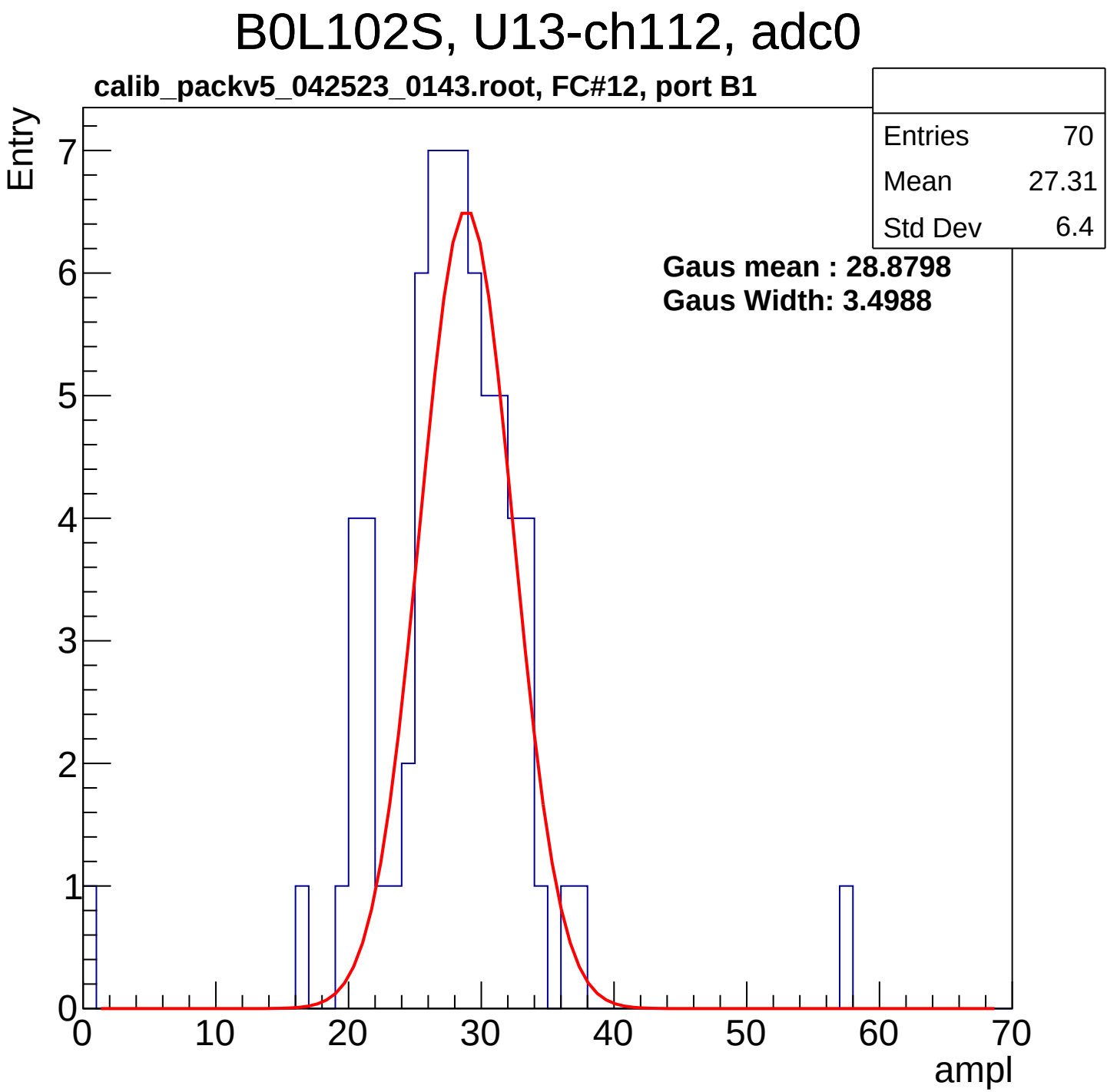
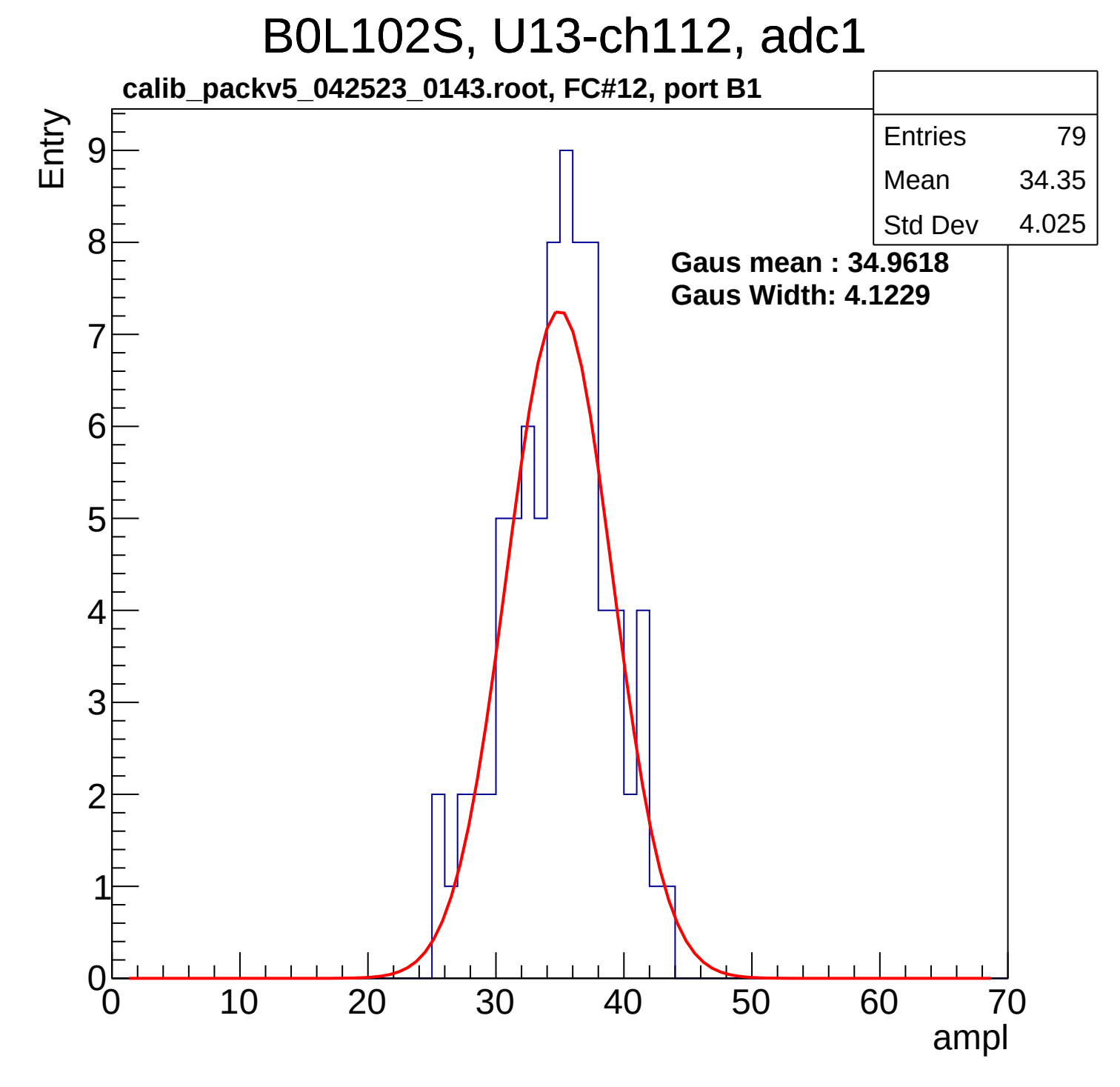
40
50
60
70


B0L102S, U13-ch111, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27.33
Std Dev 26.39
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch112, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.31
Std Dev 6.4
Gaus mean : 28.8798
Gaus Width: 3.4988
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch112, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 34.35
Std Dev 4.025
Gaus mean : 34.9618
Gaus Width: 4.1229
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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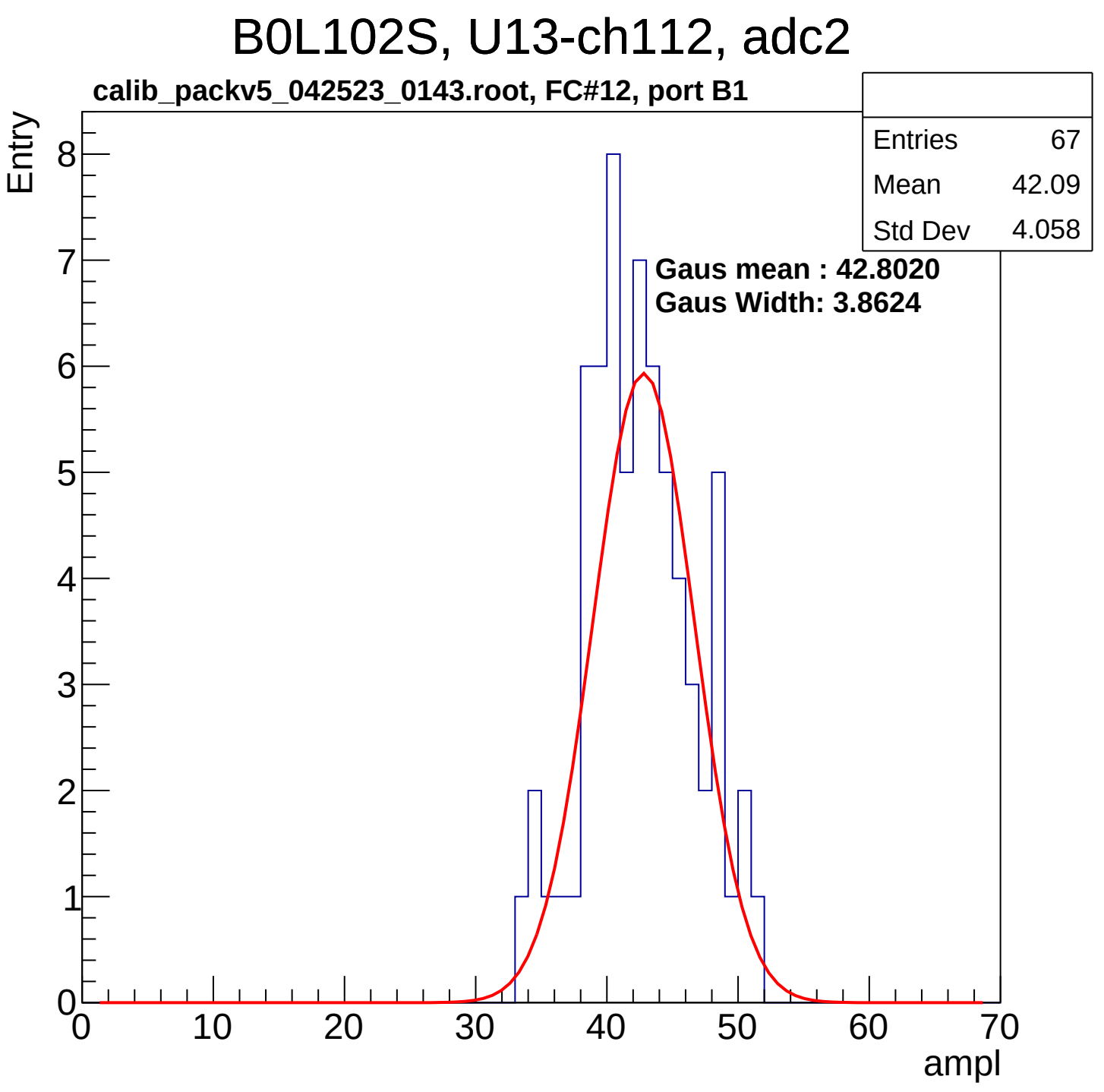
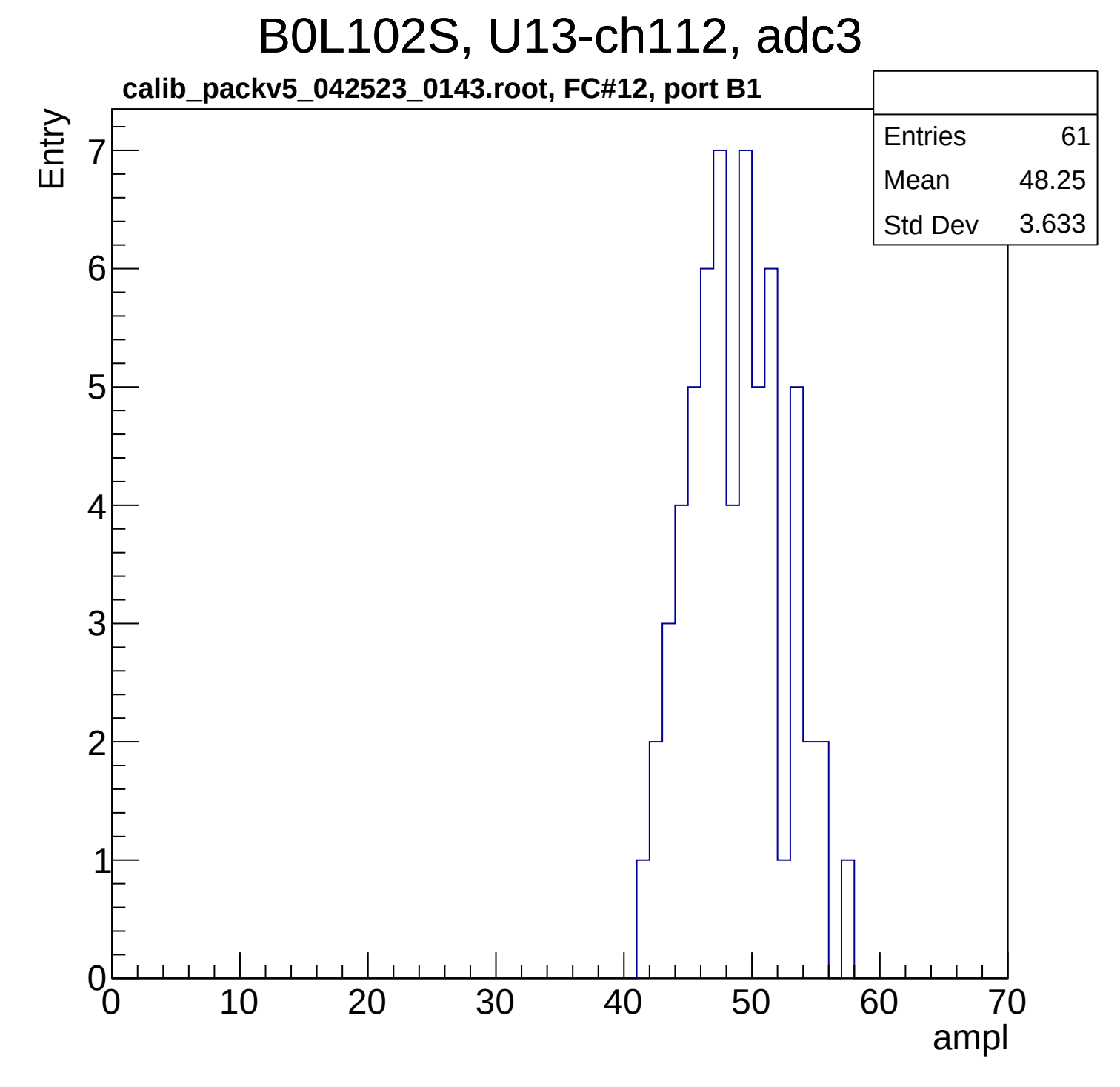

B0L102S, U13-ch112, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.09
Std Dev 4.058
Gaus mean : 42.8020
Gaus Width: 3.8624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch112, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 48.25
Std Dev 3.633
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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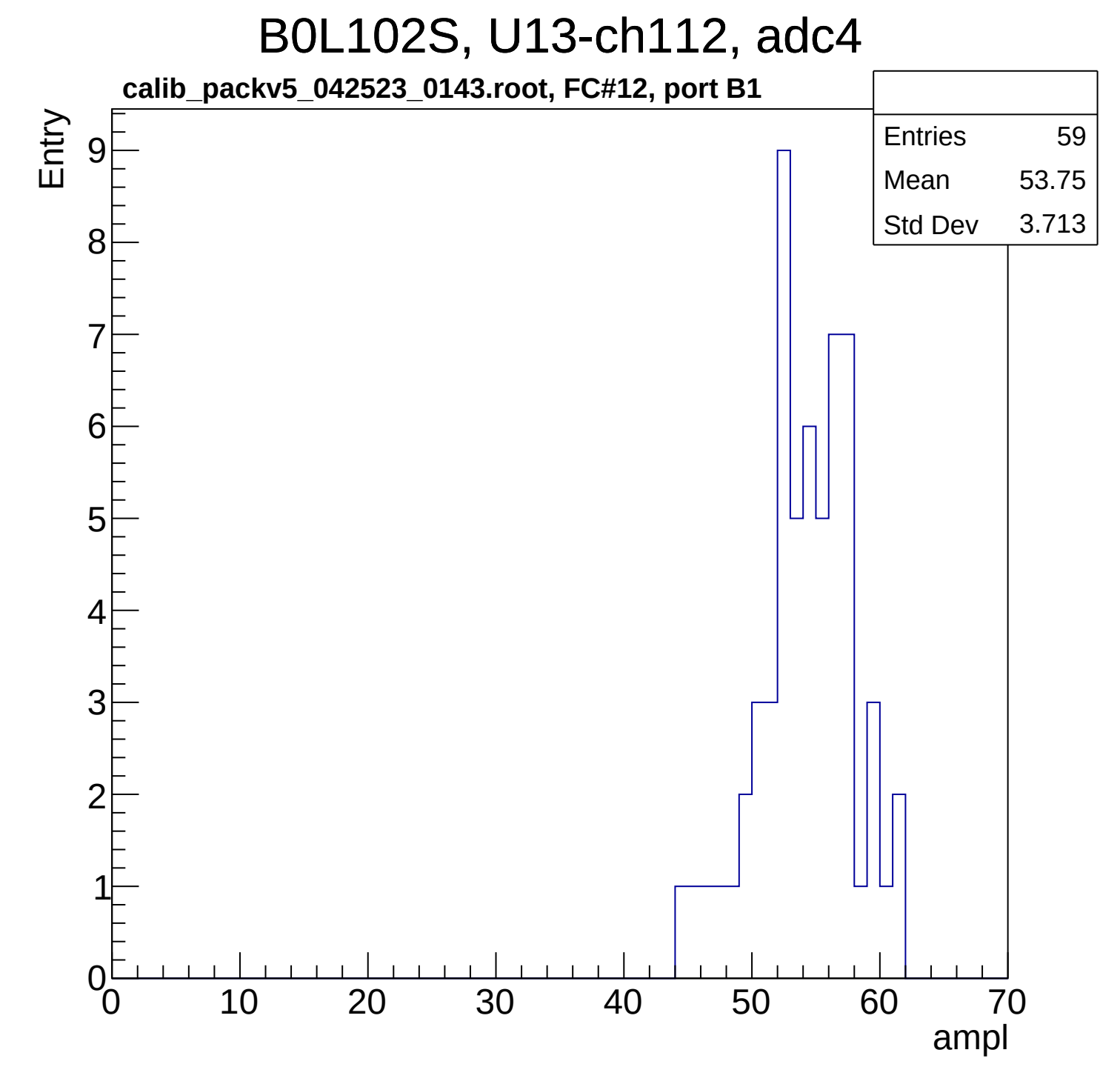
B0L102S, U13-ch112, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 53.75
Std Dev 3.713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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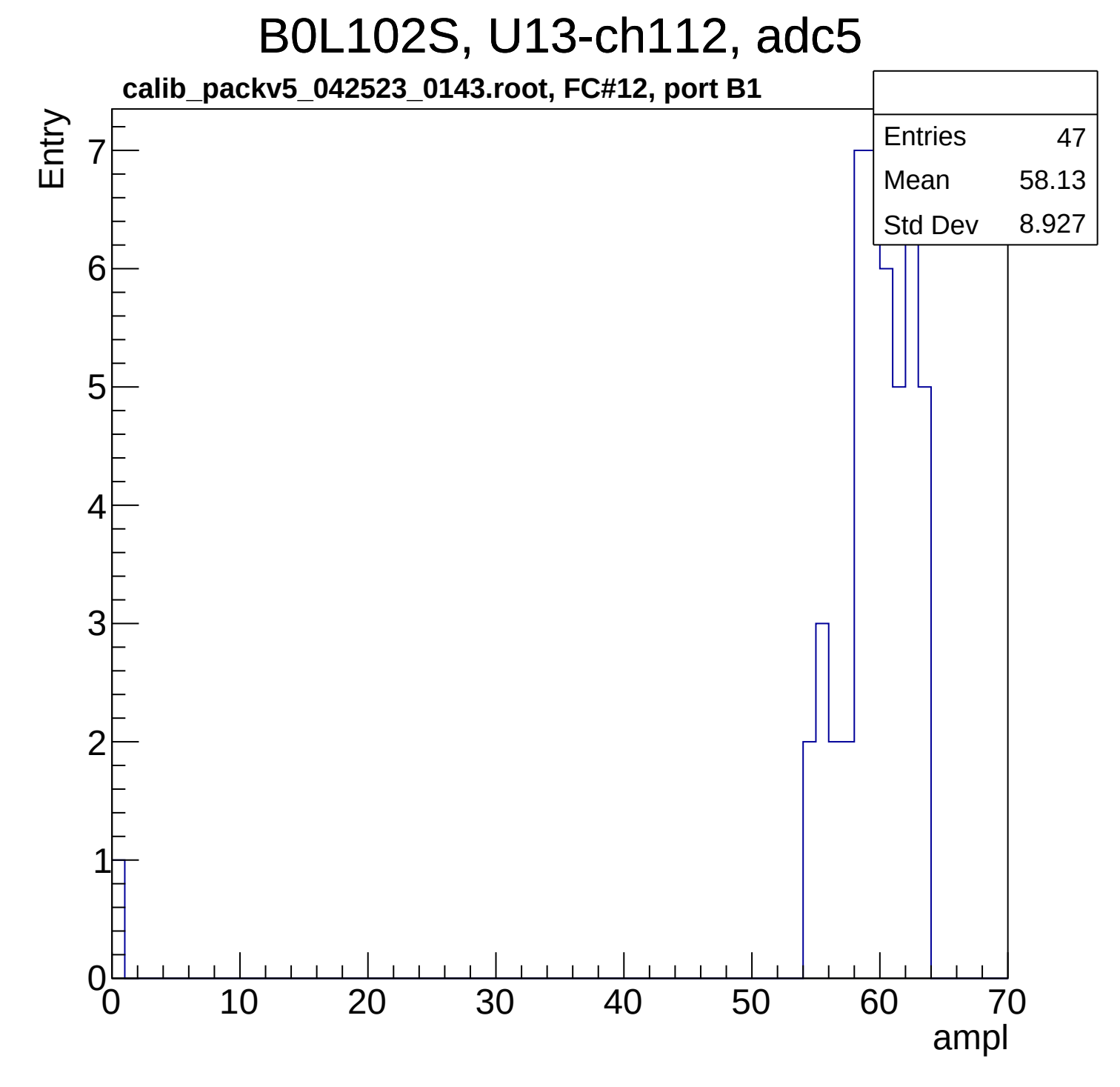
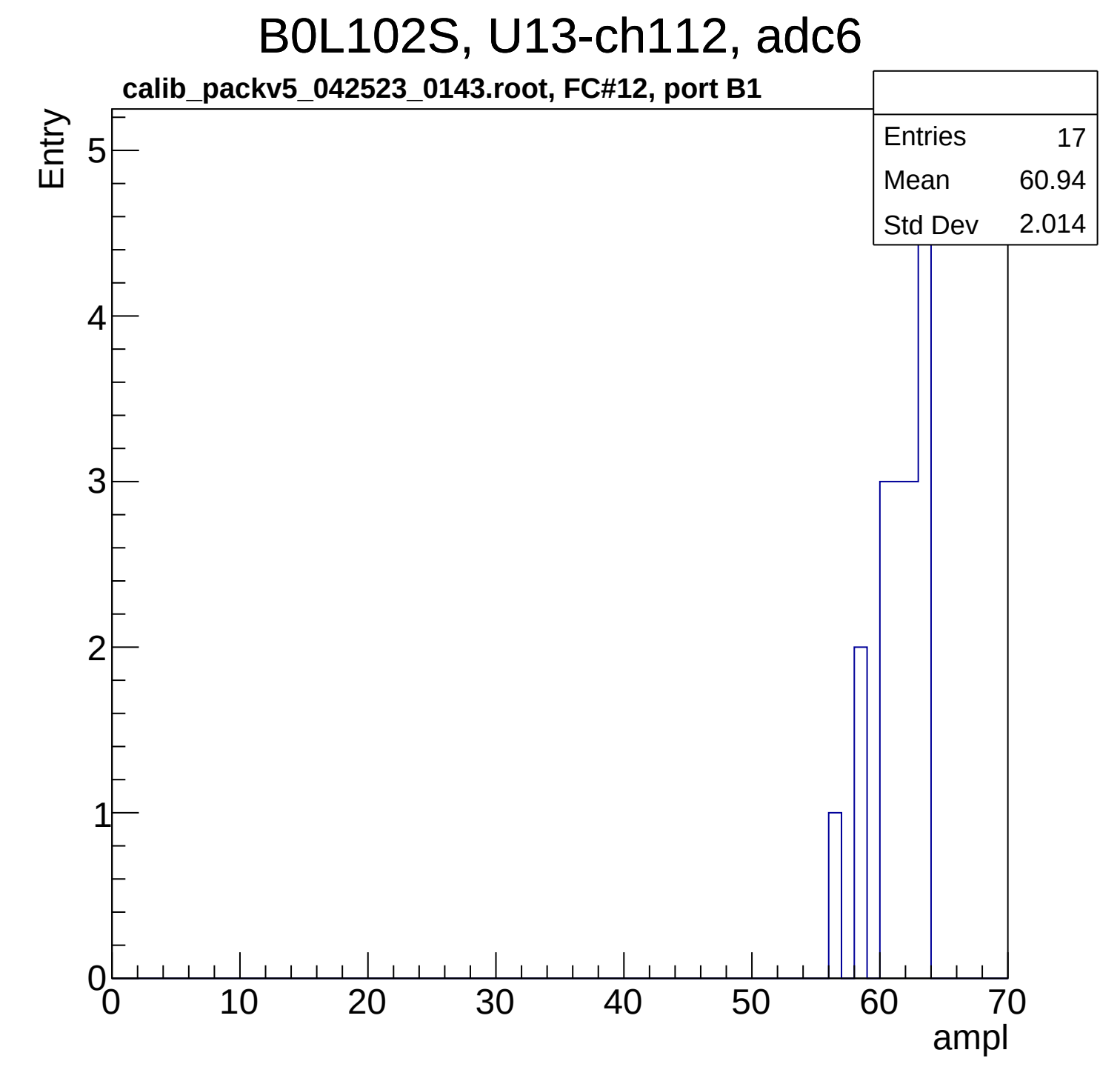
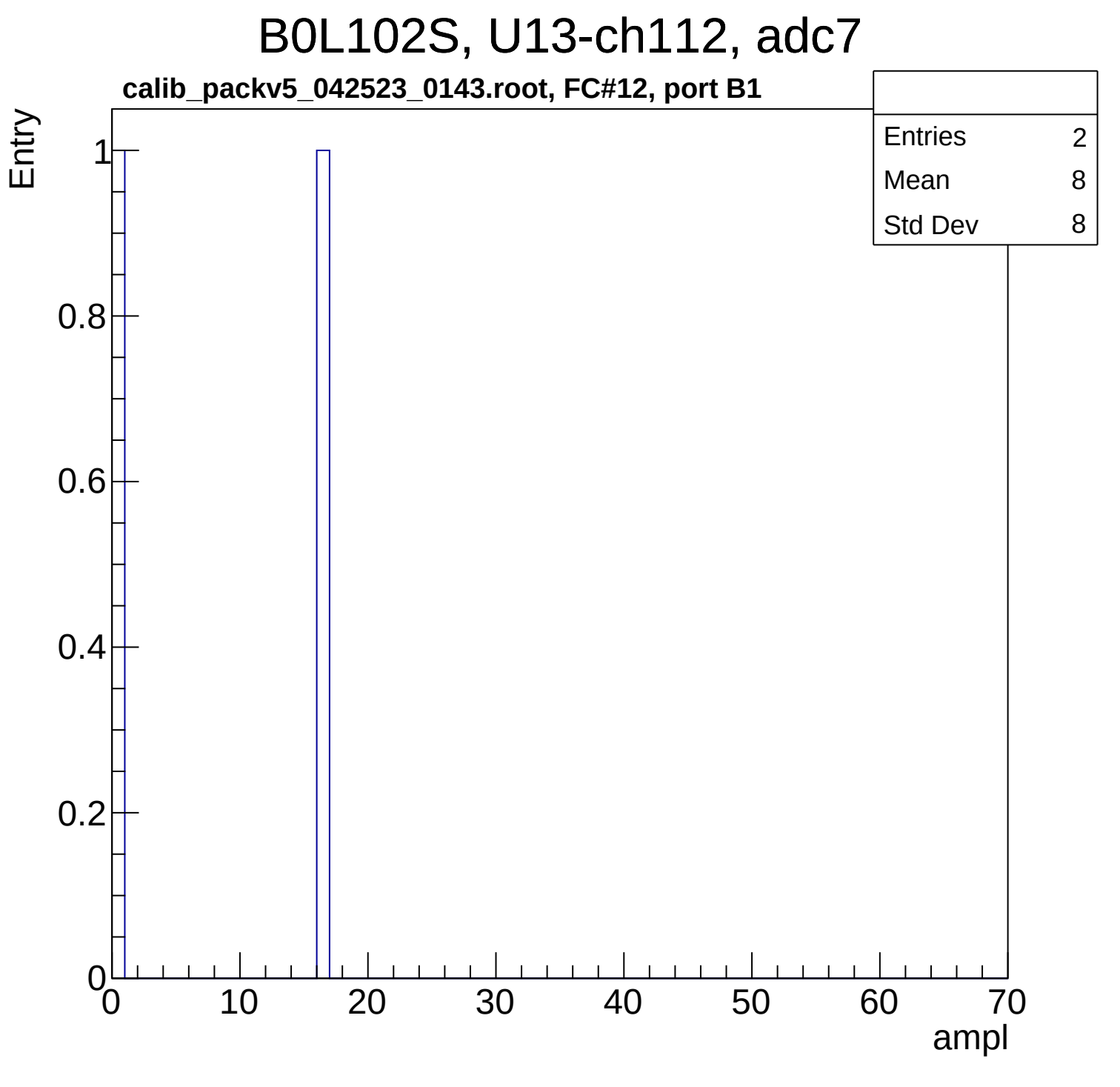

B0L102S, U13-ch112, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.13
Std Dev 8.927
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch112, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 60.94
Std Dev 2.014
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U13-ch112, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 8
Std Dev 8
Entry
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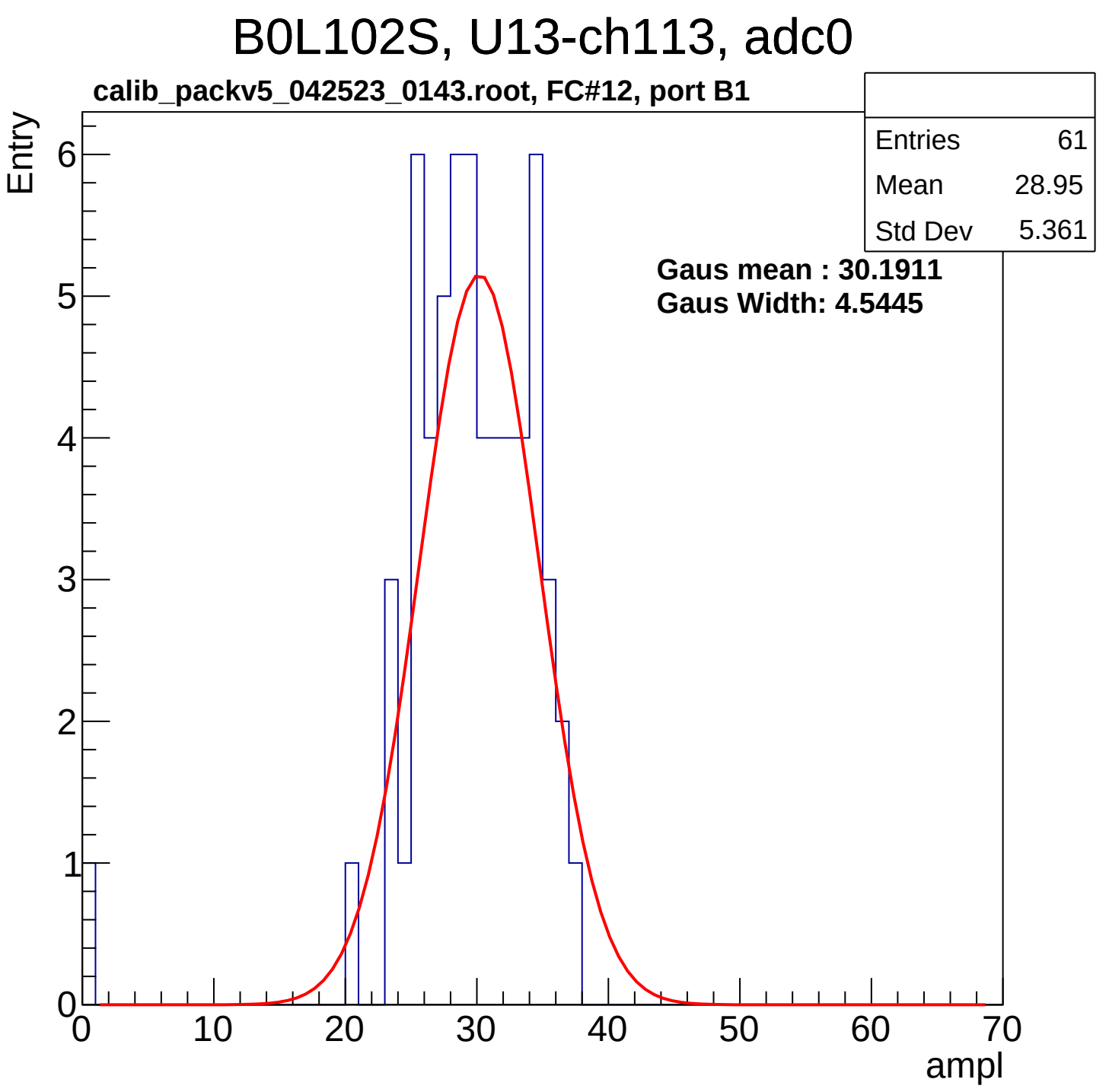
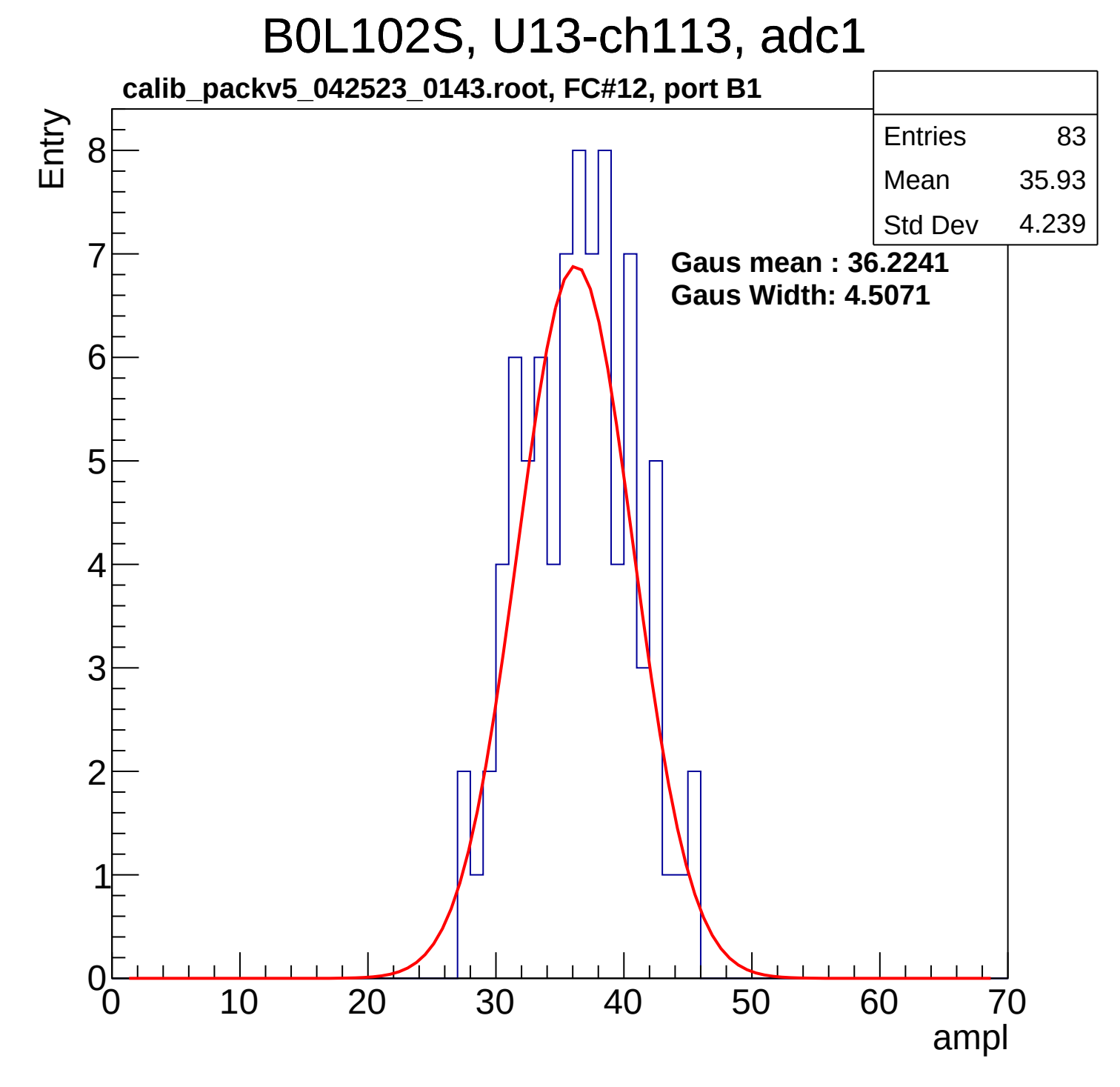
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch113, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 28.95
Std Dev 5.361
Gaus mean : 30.1911
Gaus Width: 4.5445
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch113, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 35.93
Std Dev 4.239
Gaus mean : 36.2241
Gaus Width: 4.5071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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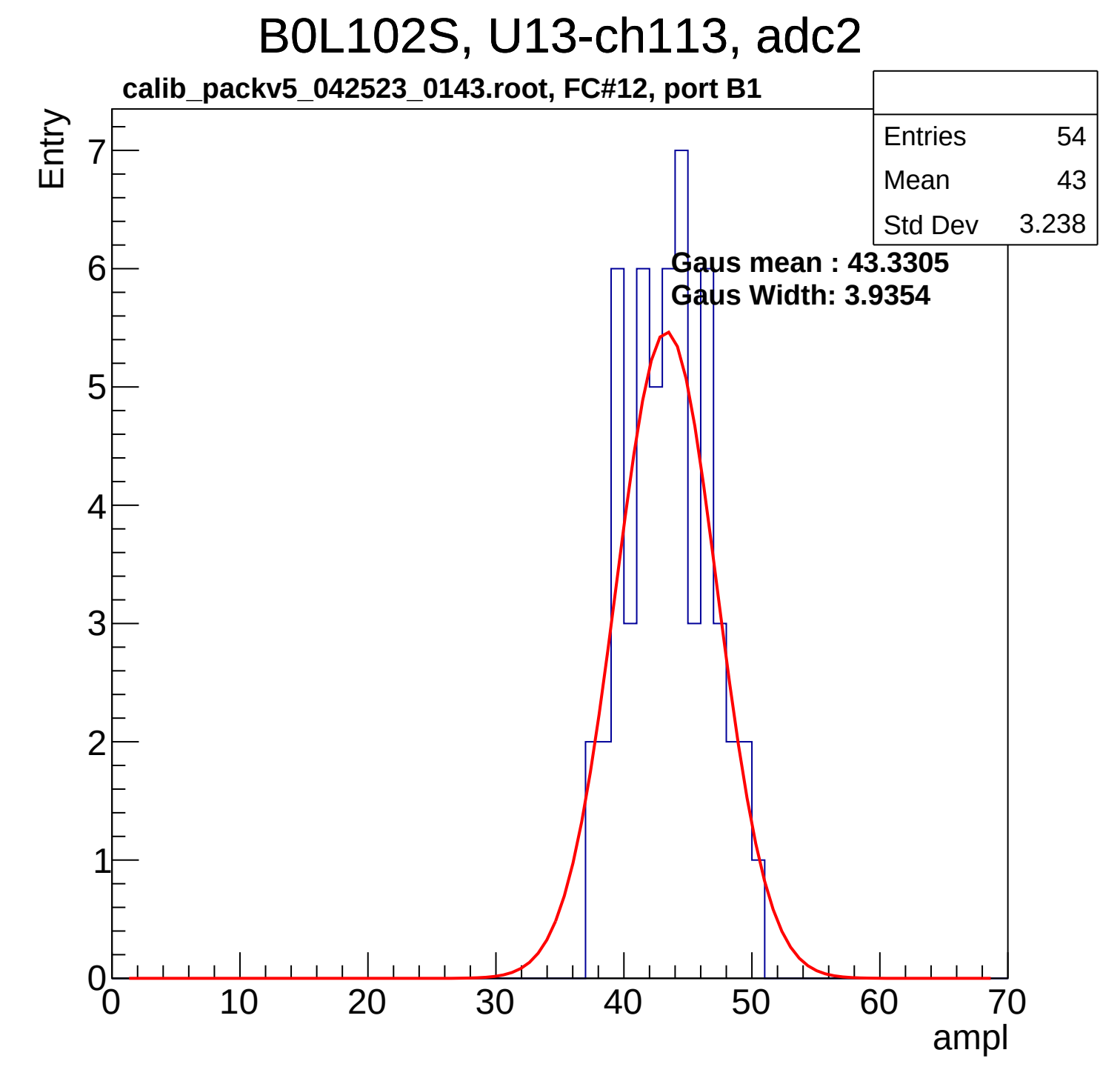
B0L102S, U13-ch113, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43
Std Dev 3.238
Gaus mean : 43.3305
Gaus Width: 3.9354
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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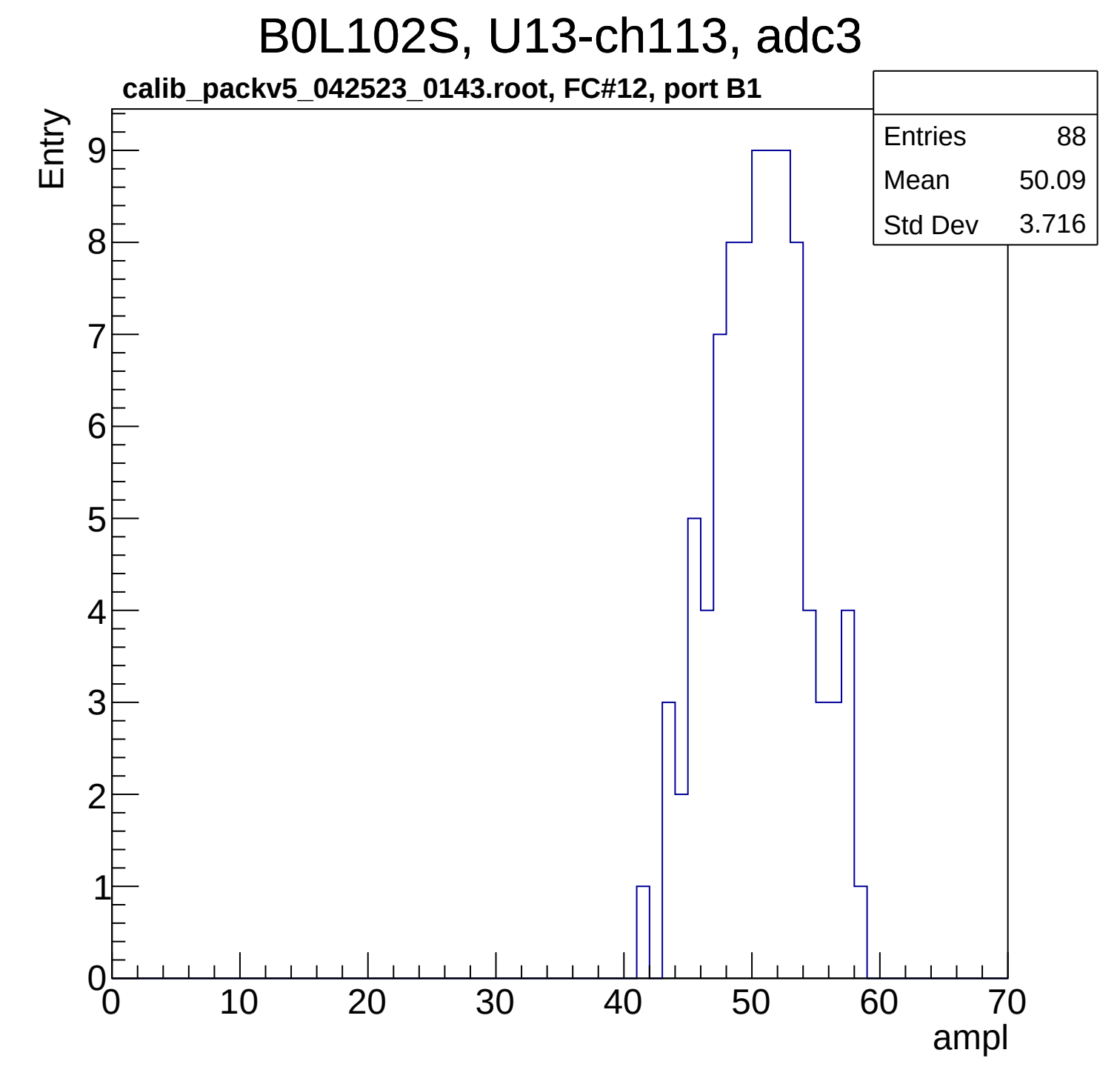
B0L102S, U13-ch113, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 50.09
Std Dev 3.716
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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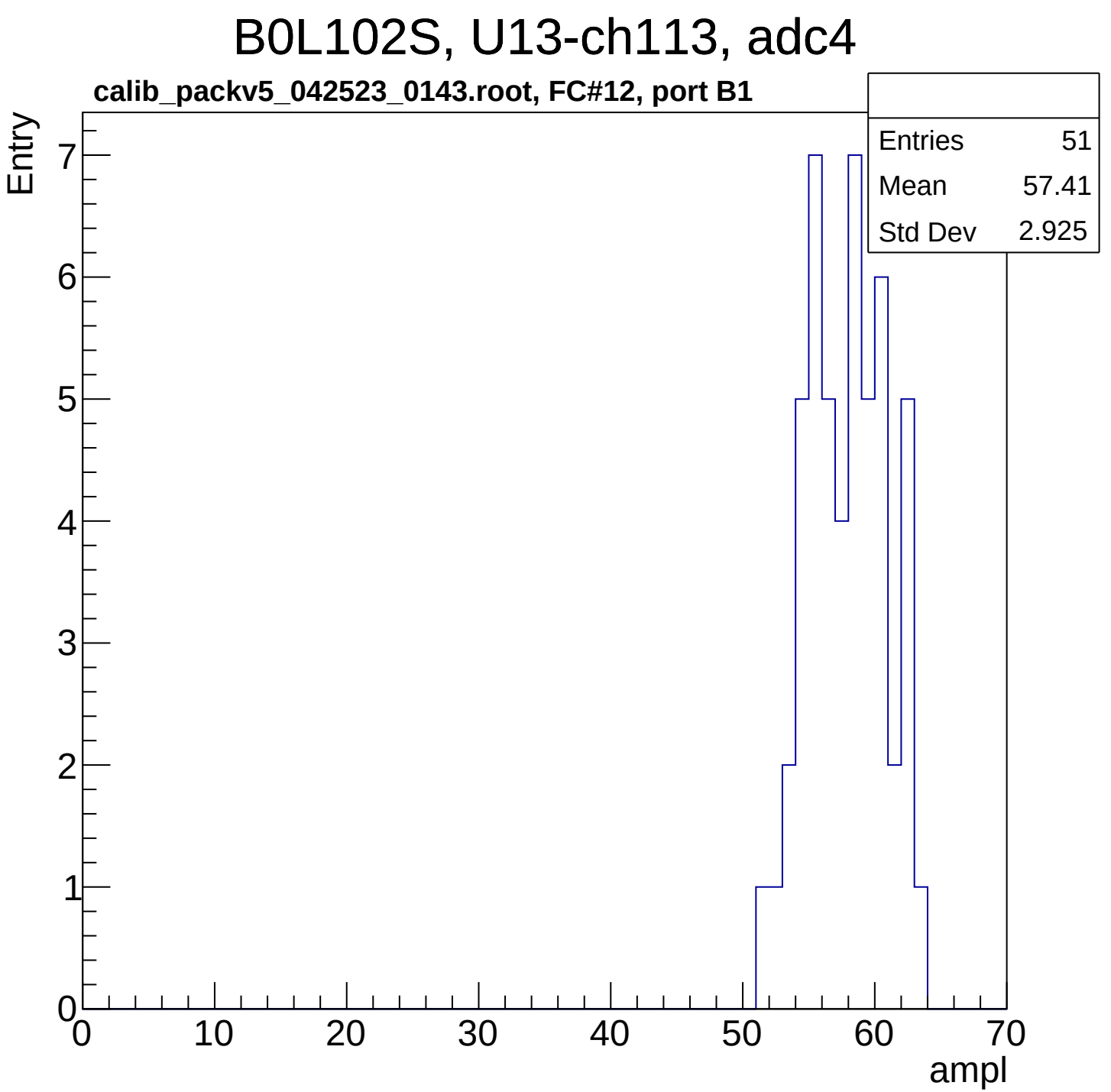
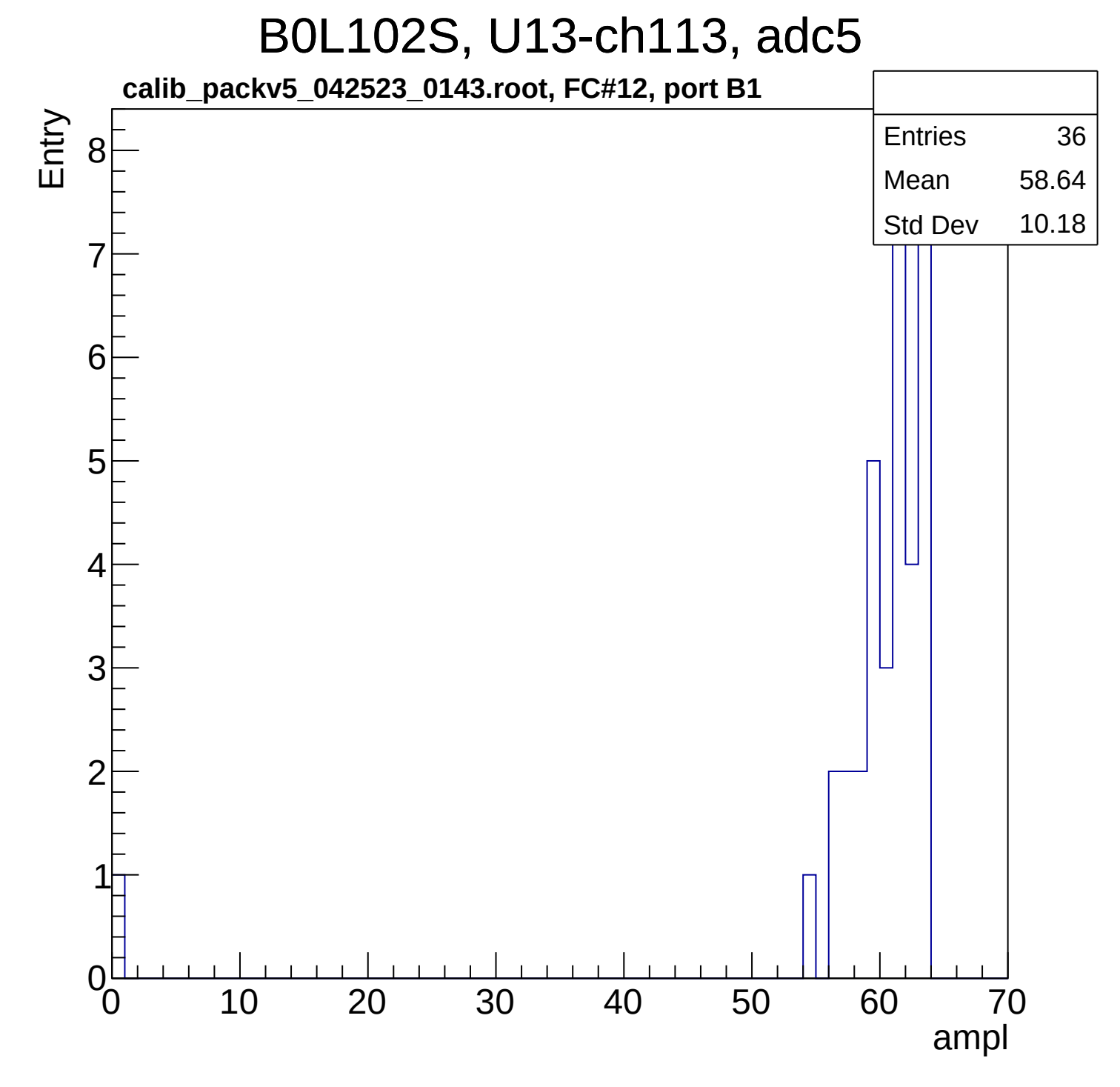
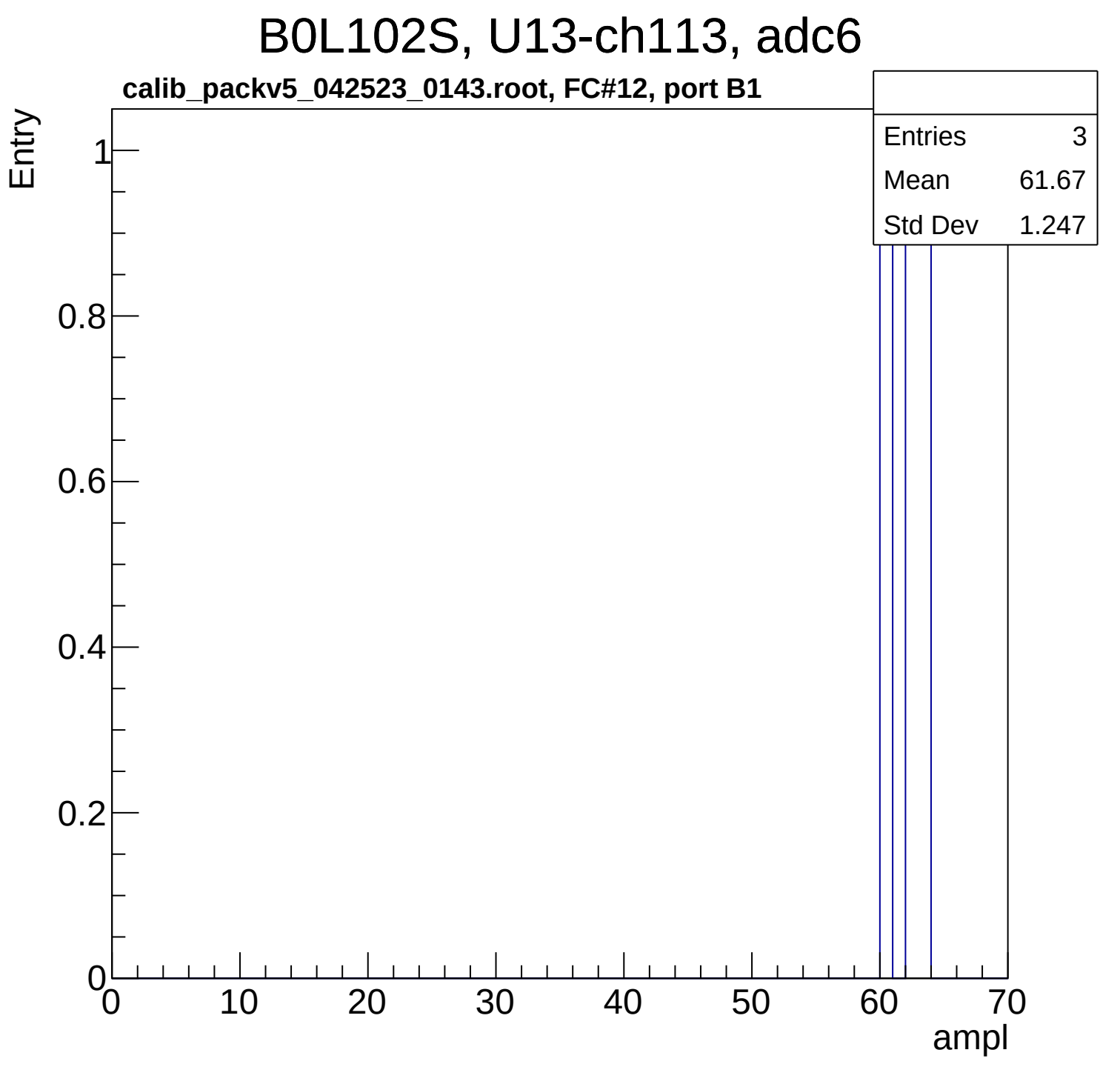
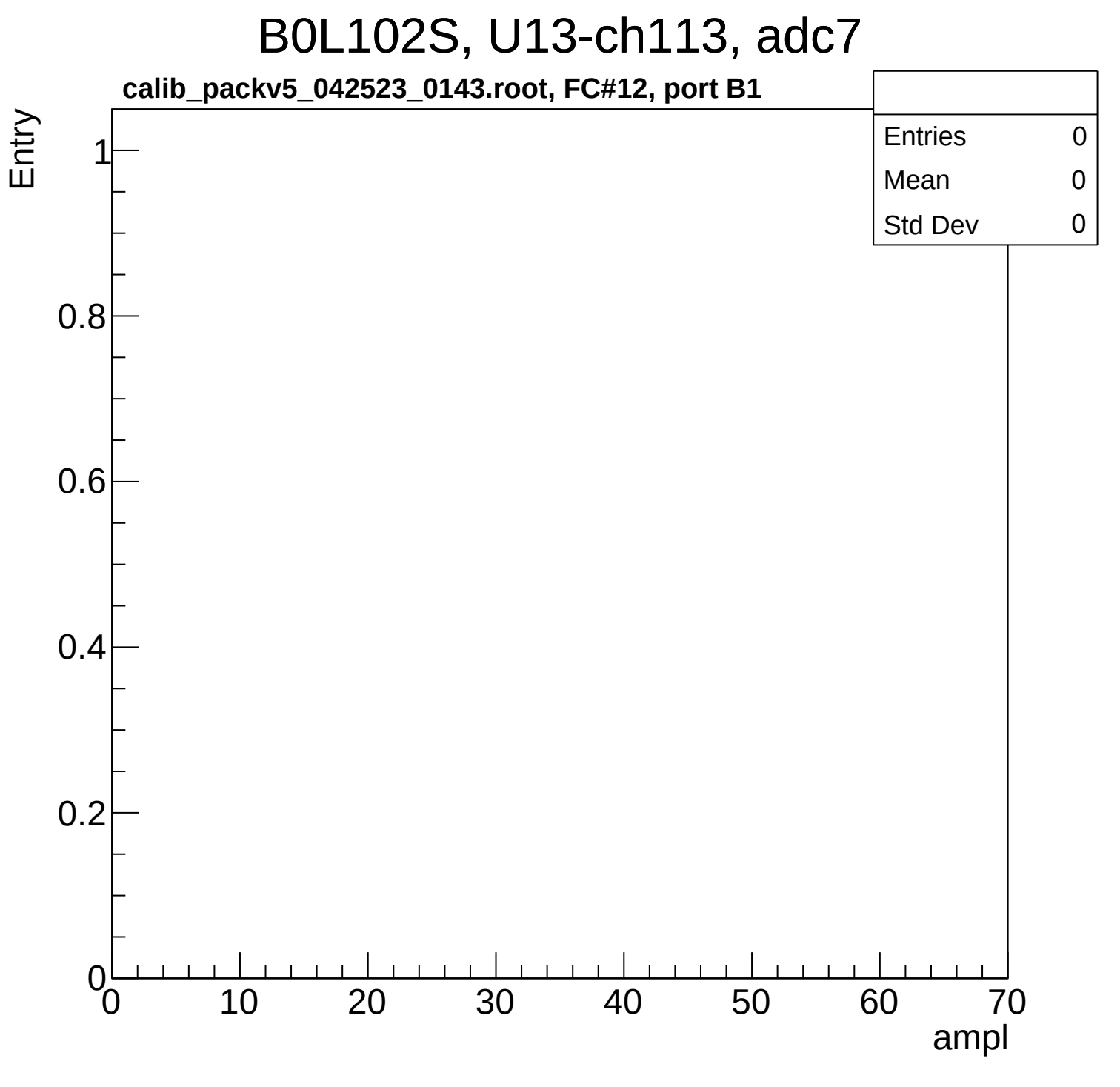
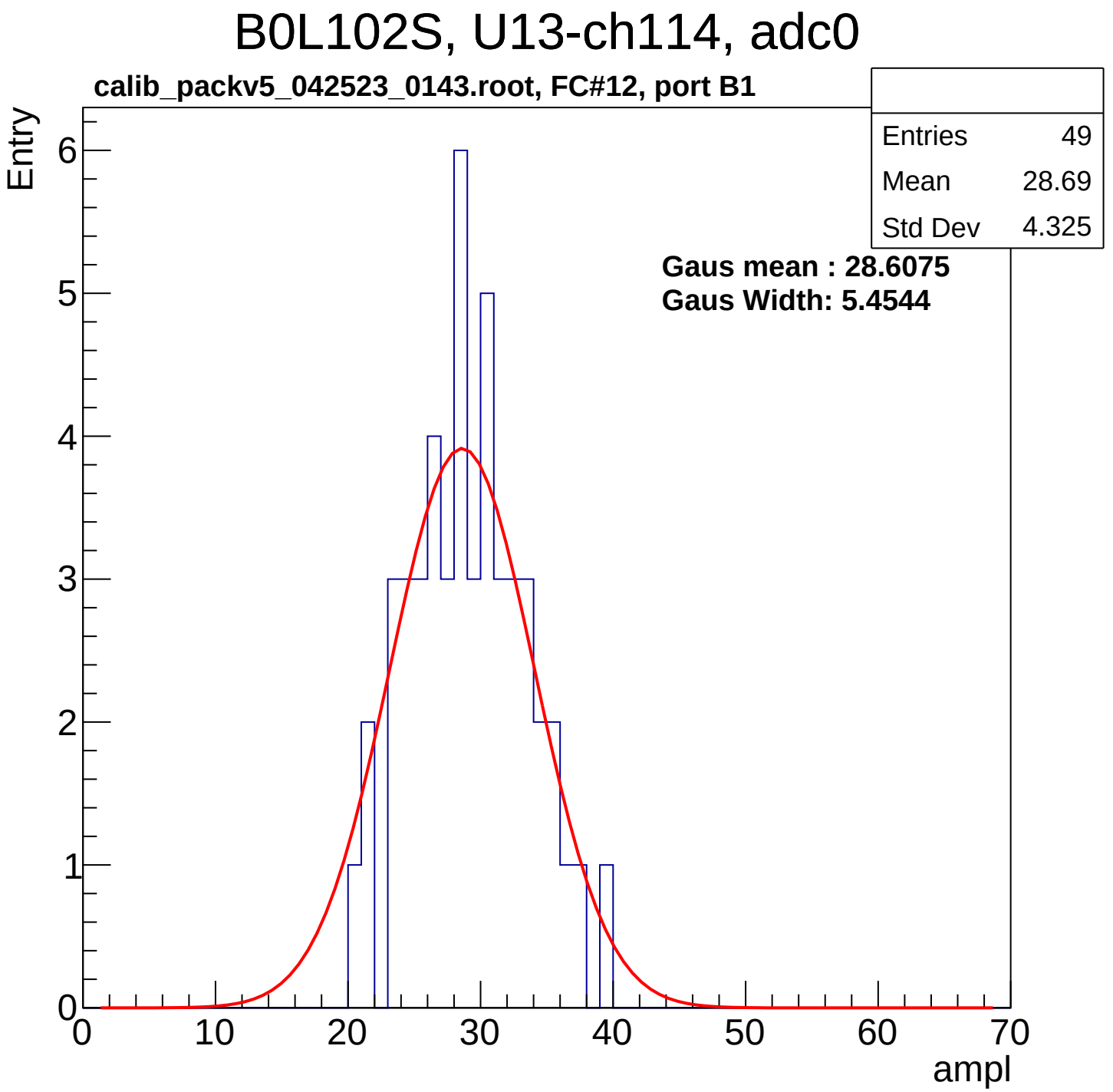
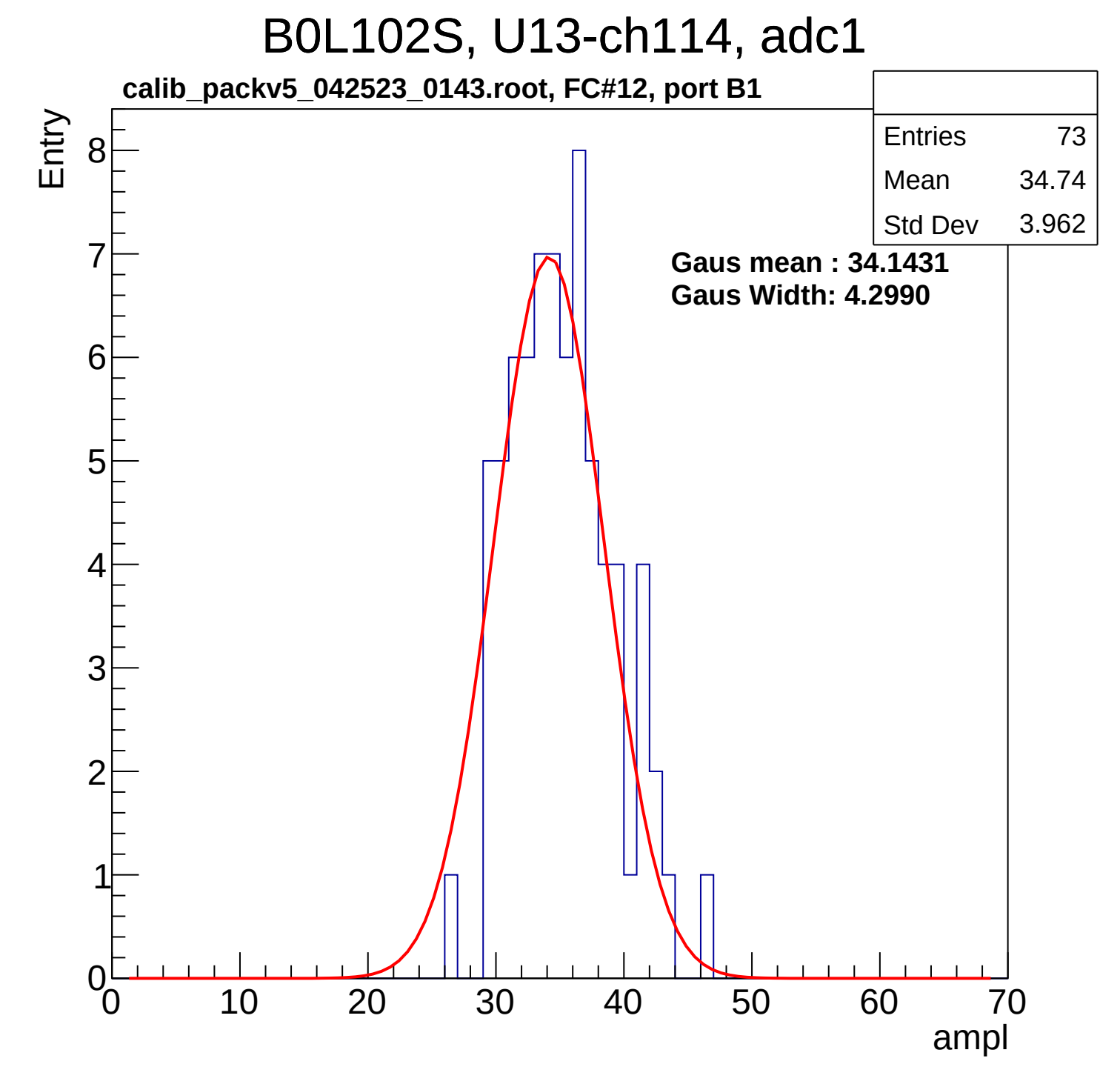

B0L102S, U13-ch113, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 57.41
Std Dev 2.925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch113, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.64
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch113, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U13-ch113, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch114, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 28.69
Std Dev 4.325
Gaus mean : 28.6075
Gaus Width: 5.4544
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch114, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 34.74
Std Dev 3.962
Gaus mean : 34.1431
Gaus Width: 4.2990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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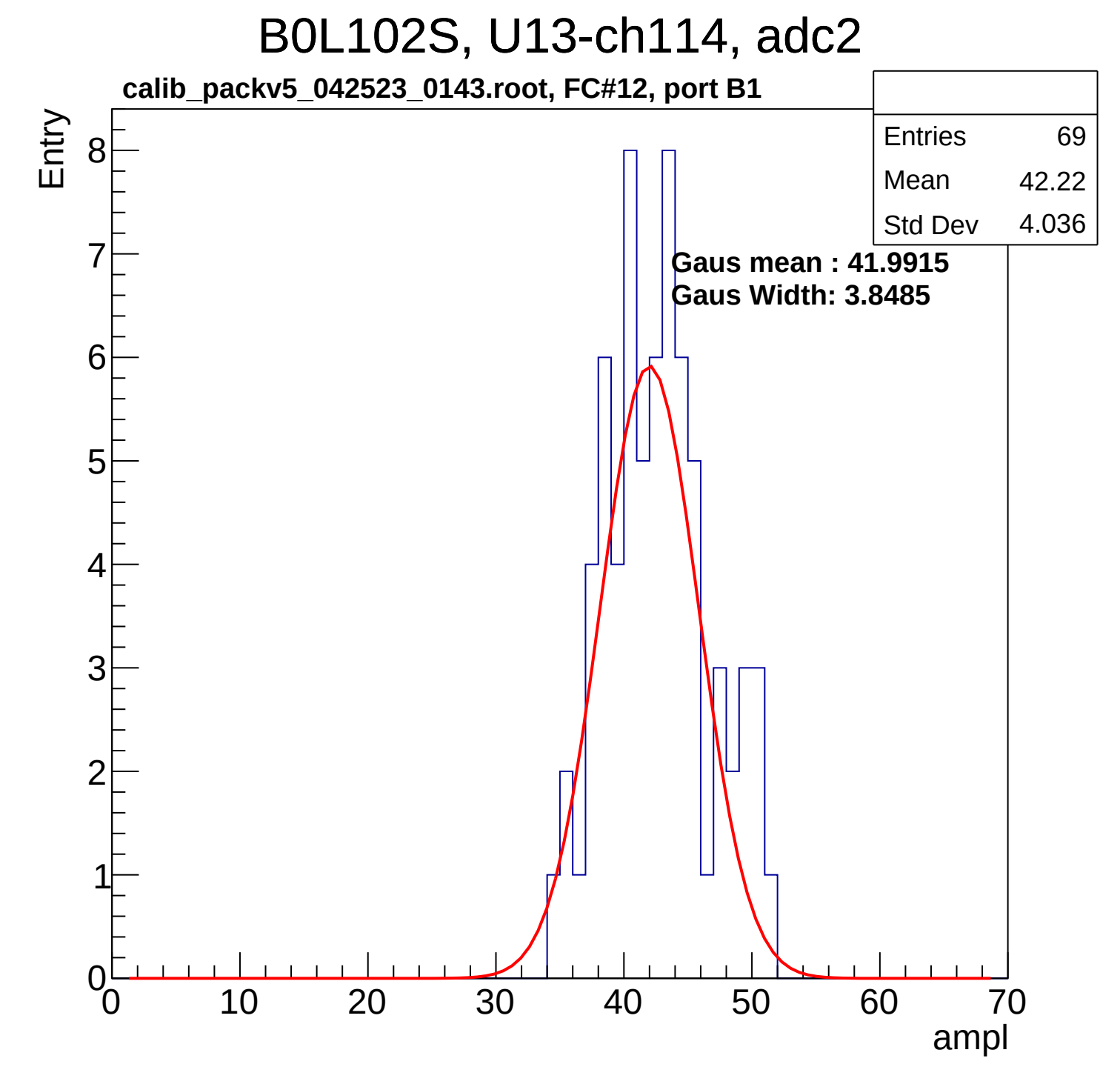
B0L102S, U13-ch114, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.22
Std Dev 4.036
Gaus mean : 41.9915
Gaus Width: 3.8485
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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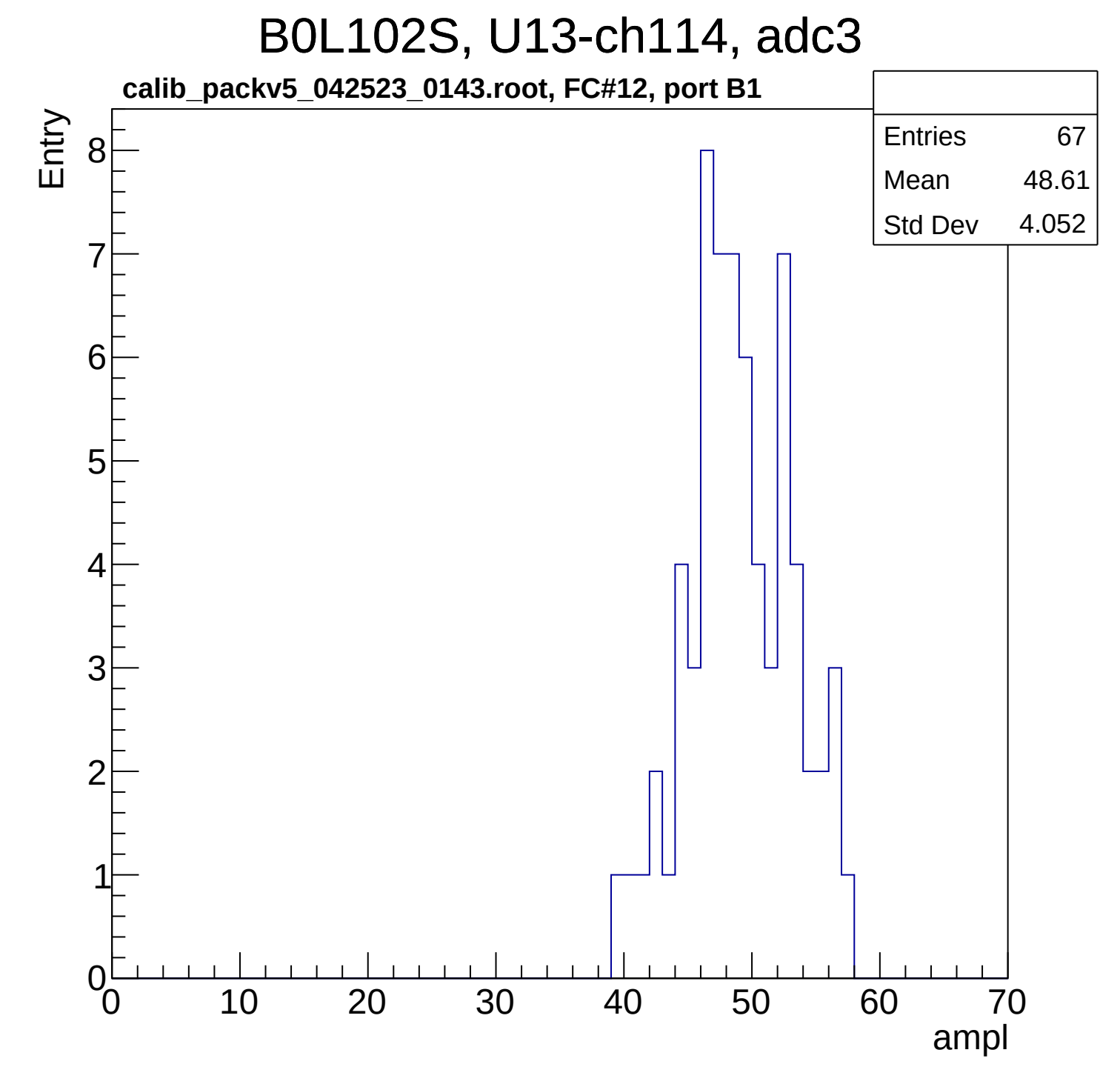
B0L102S, U13-ch114, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 48.61
Std Dev 4.052
Entry
ampl
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2
3
4
5
6
7
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0
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50
60
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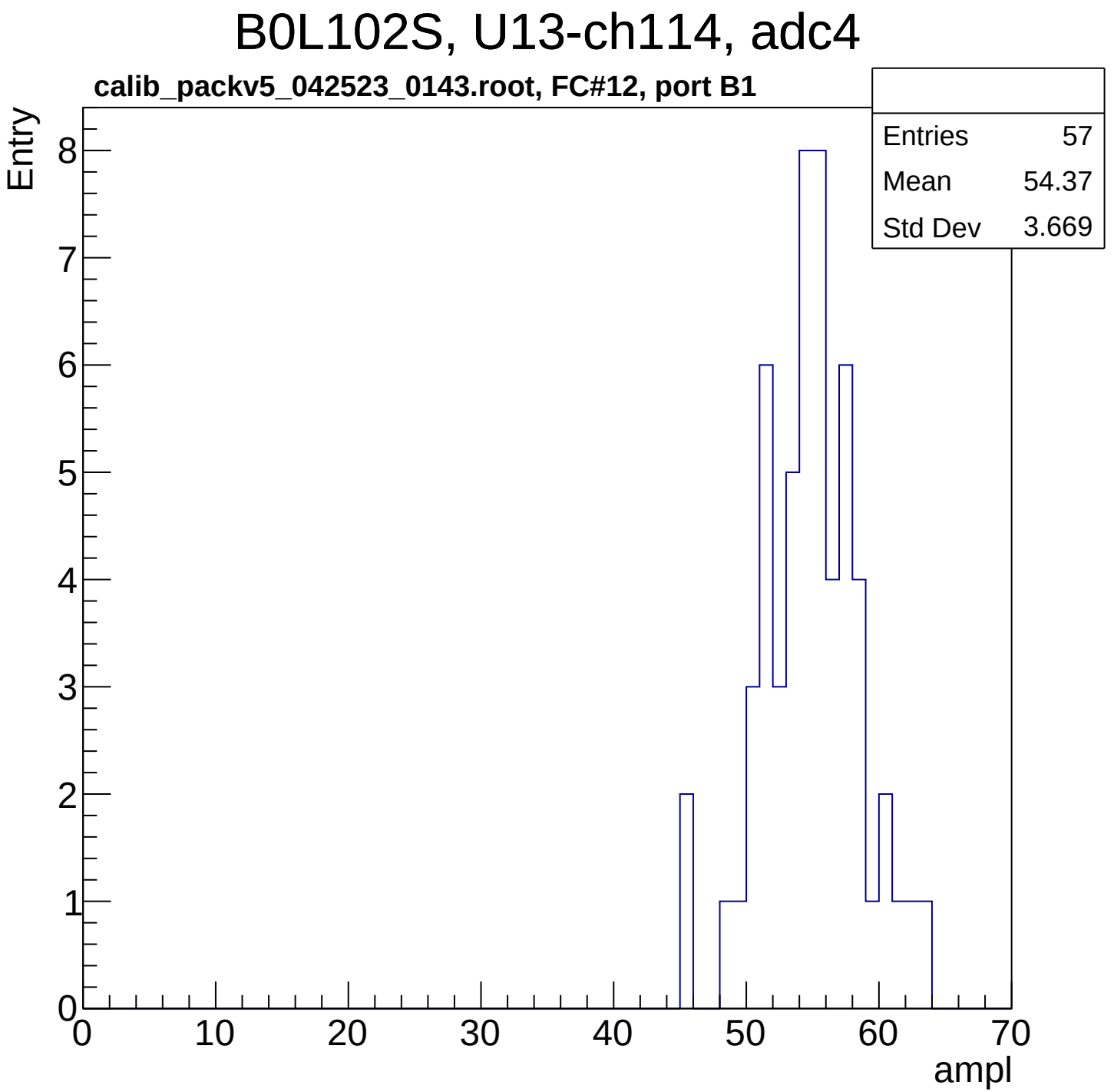
B0L102S, U13-ch114, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.37
Std Dev 3.669
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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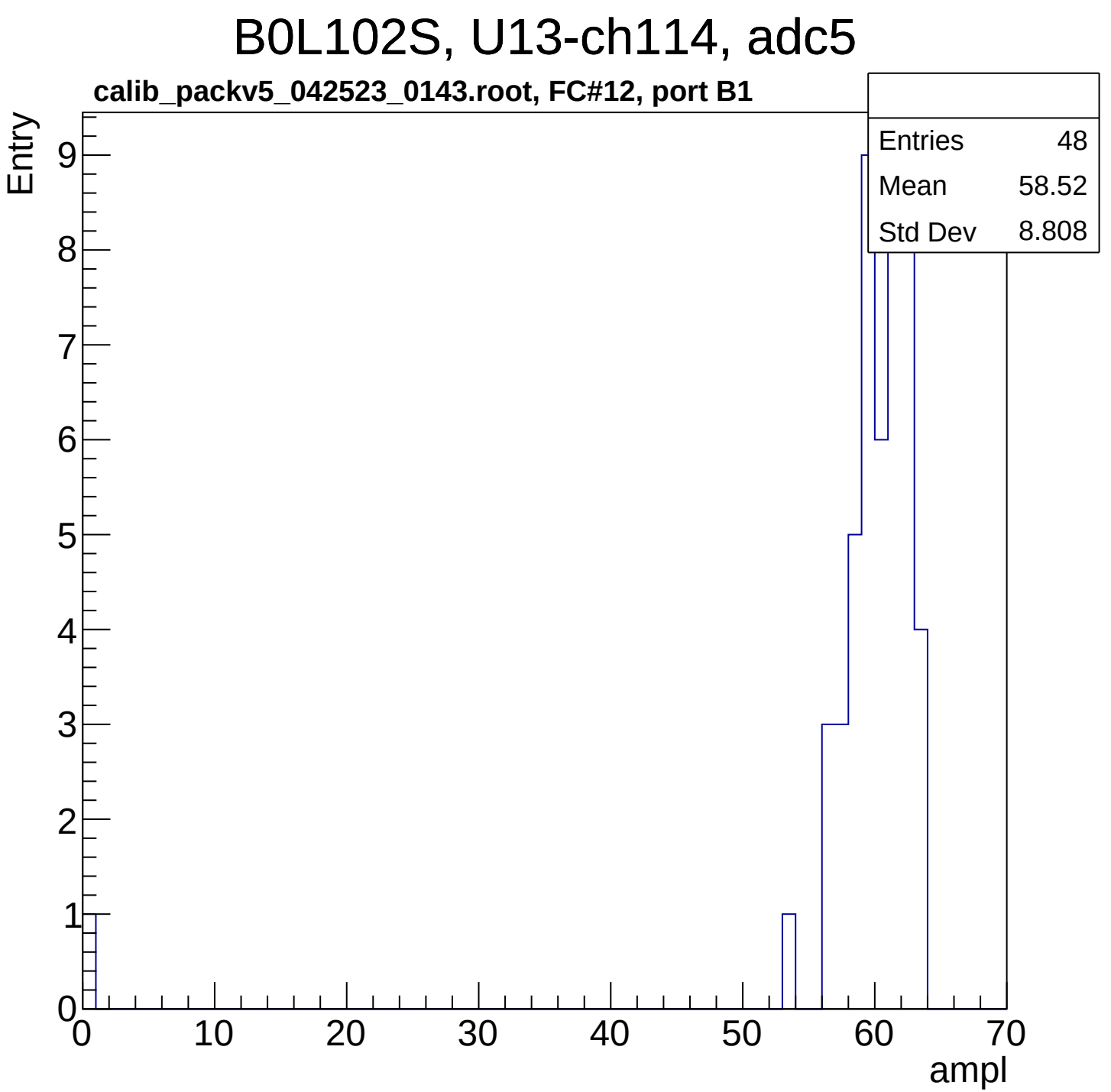
B0L102S, U13-ch114, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.52
Std Dev 8.808
Entry
ampl
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4
5
6
7
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0
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60
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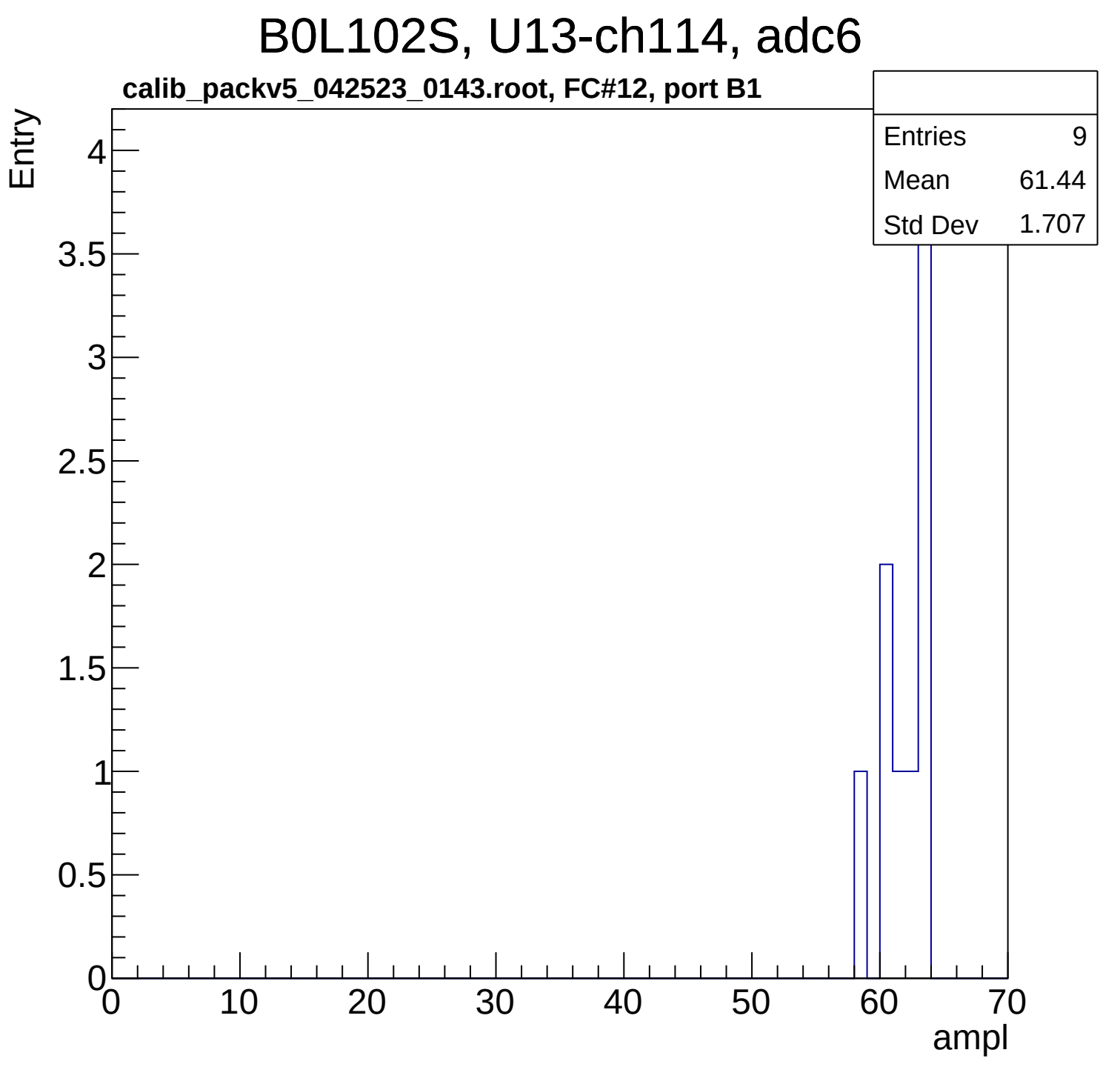
B0L102S, U13-ch114, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.44
Std Dev 1.707
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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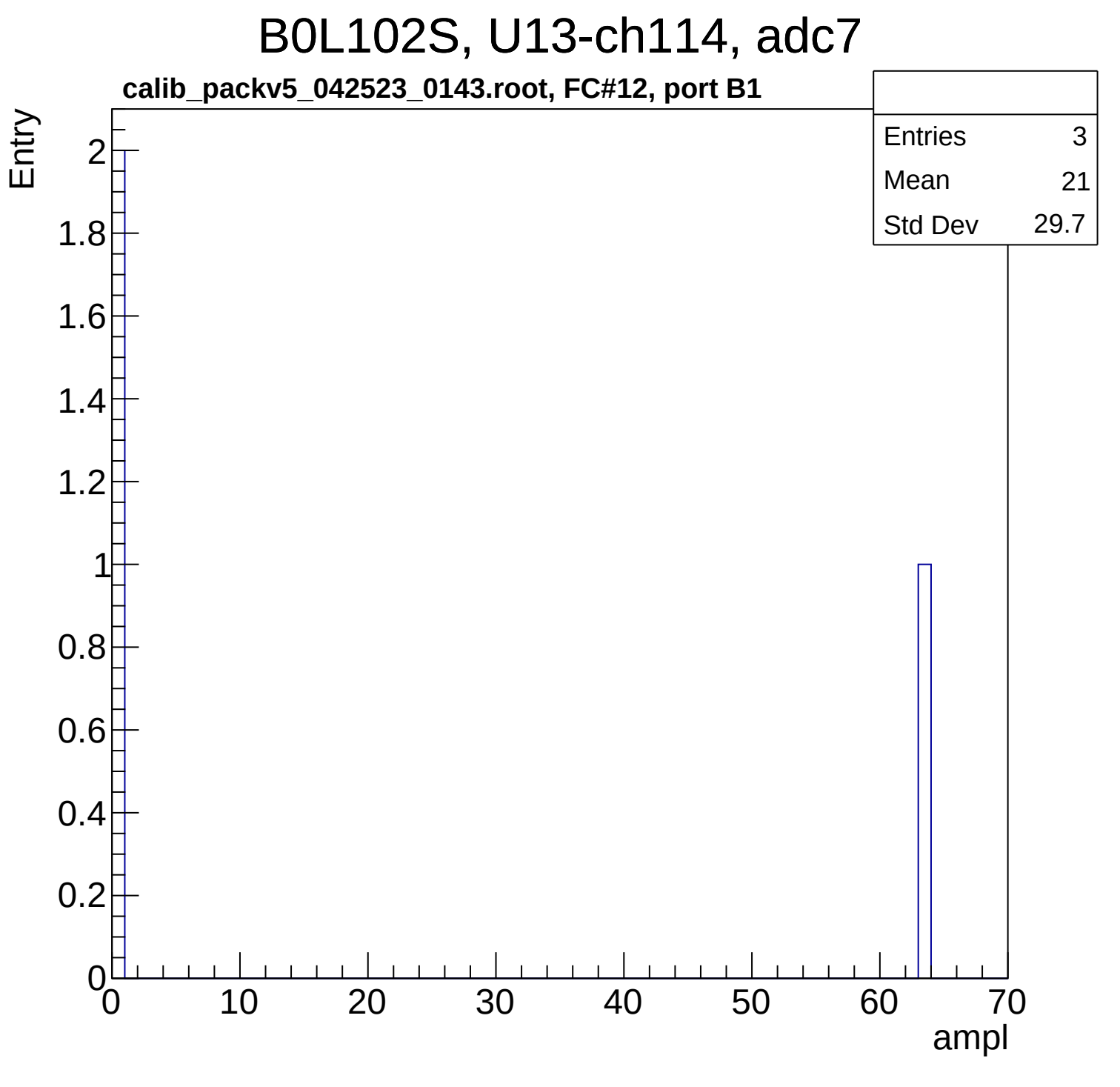
60
70


B0L102S, U13-ch114, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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20
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60
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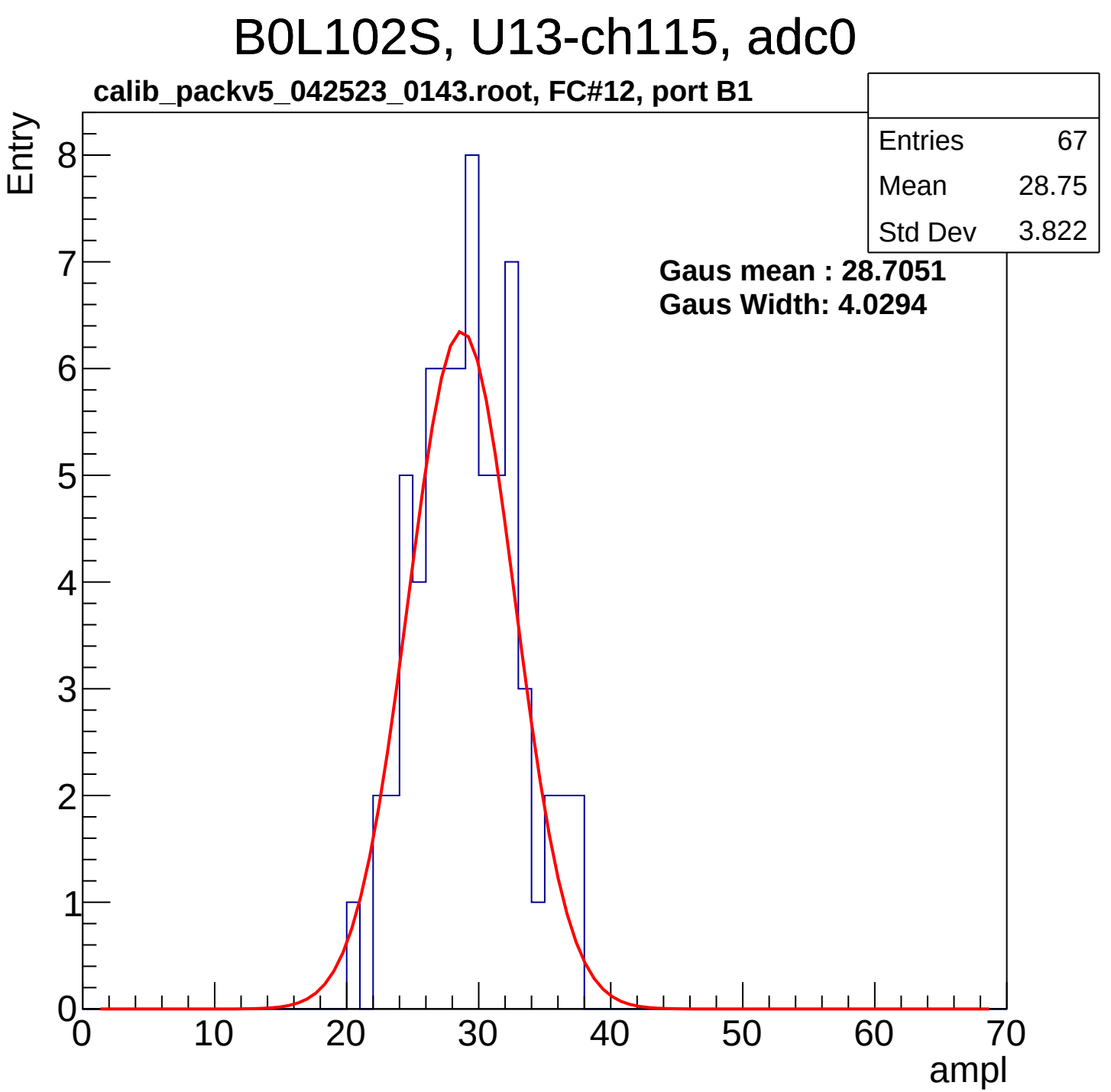
B0L102S, U13-ch115, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.75
Std Dev 3.822
Gaus mean : 28.7051
Gaus Width: 4.0294
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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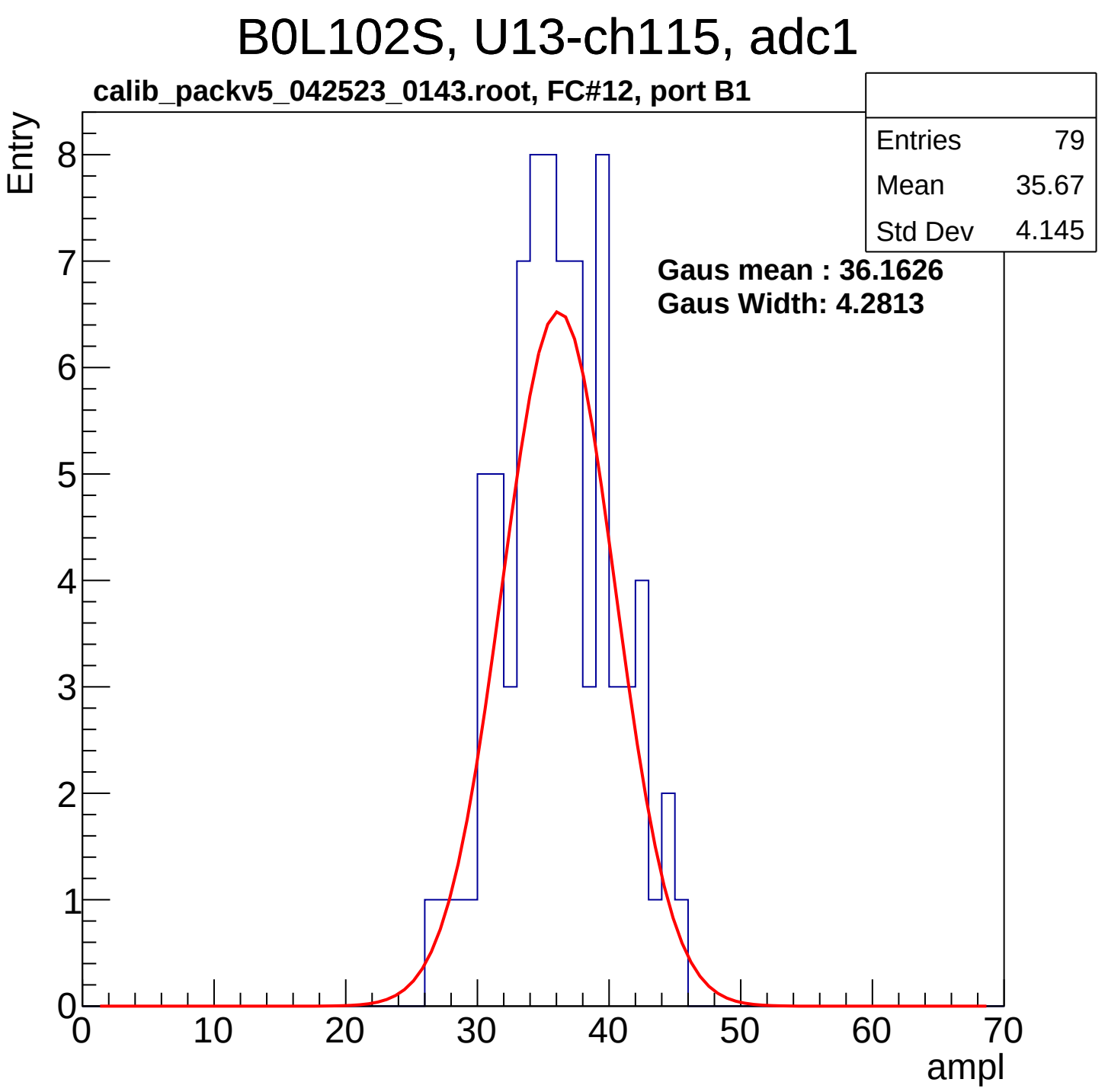
60
70


B0L102S, U13-ch115, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 35.67
Std Dev 4.145
Gaus mean : 36.1626
Gaus Width: 4.2813
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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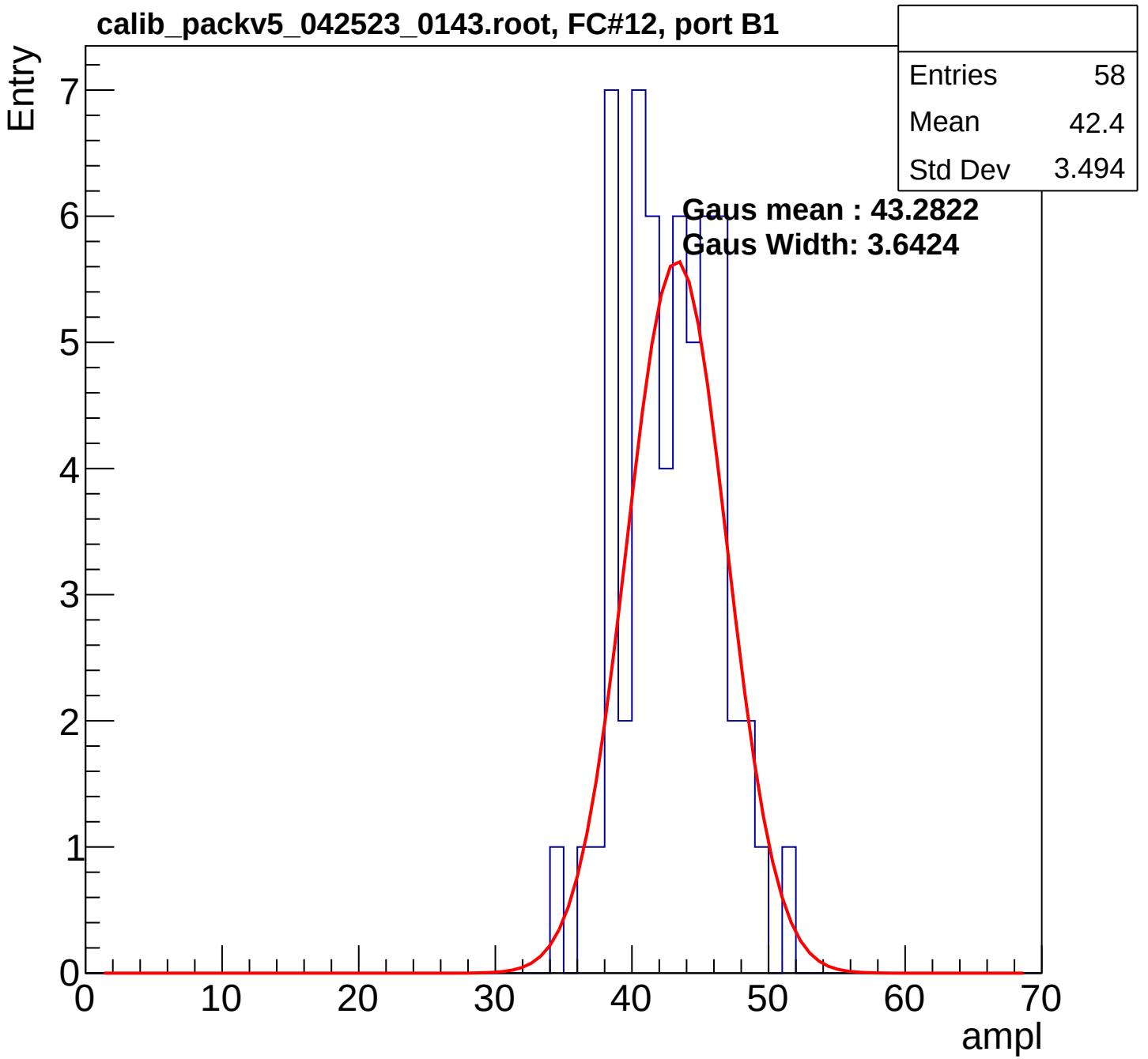
B0L102S, U13-ch115, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.4
Std Dev 3.494
Gaus mean : 43.2822
Gaus Width: 3.6424
Entry
ampl
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1
2
3
4
5
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60
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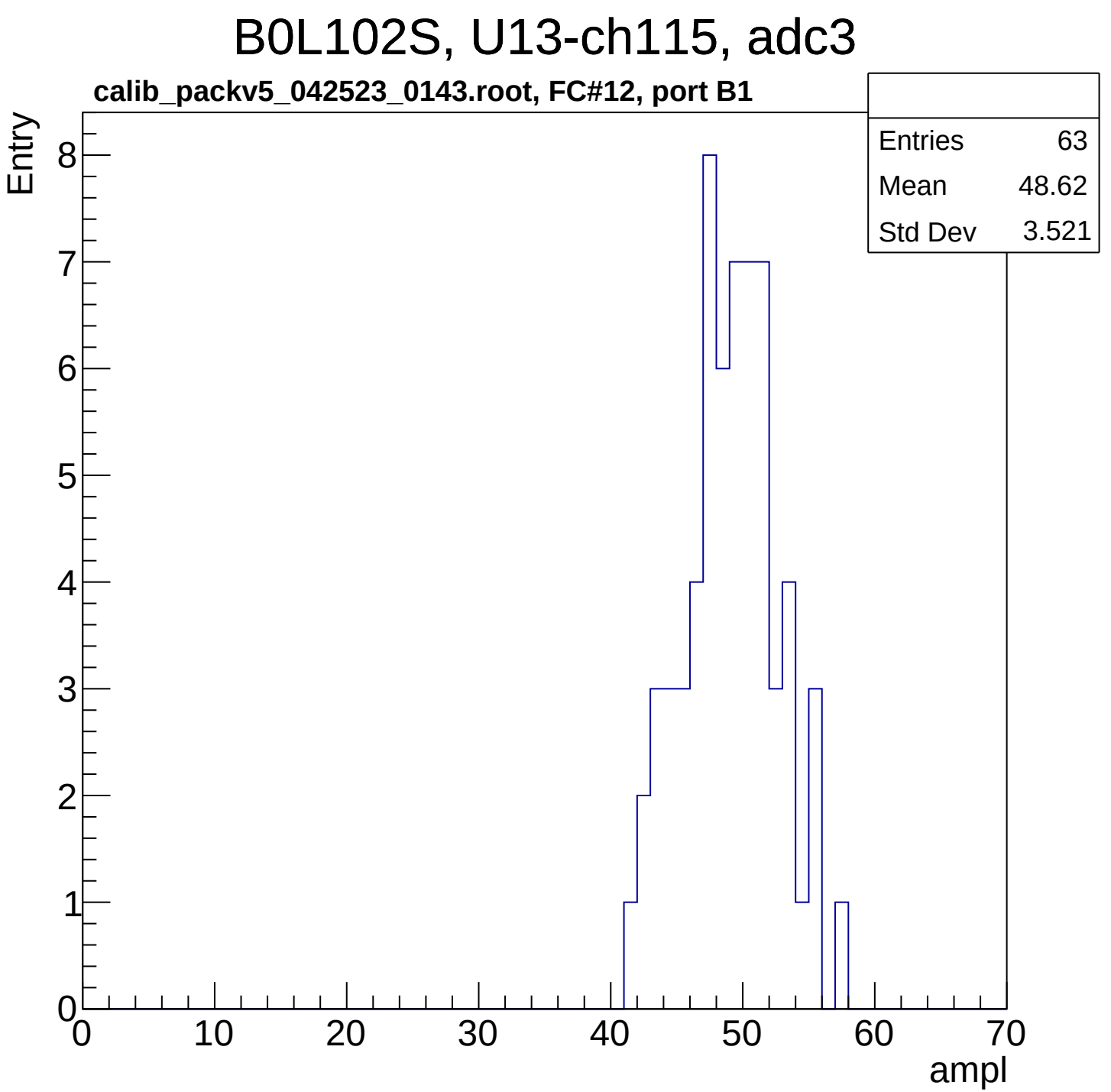
B0L102S, U13-ch115, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 48.62
Std Dev 3.521
Entry
ampl
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2
3
4
5
6
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8
0
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20
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40
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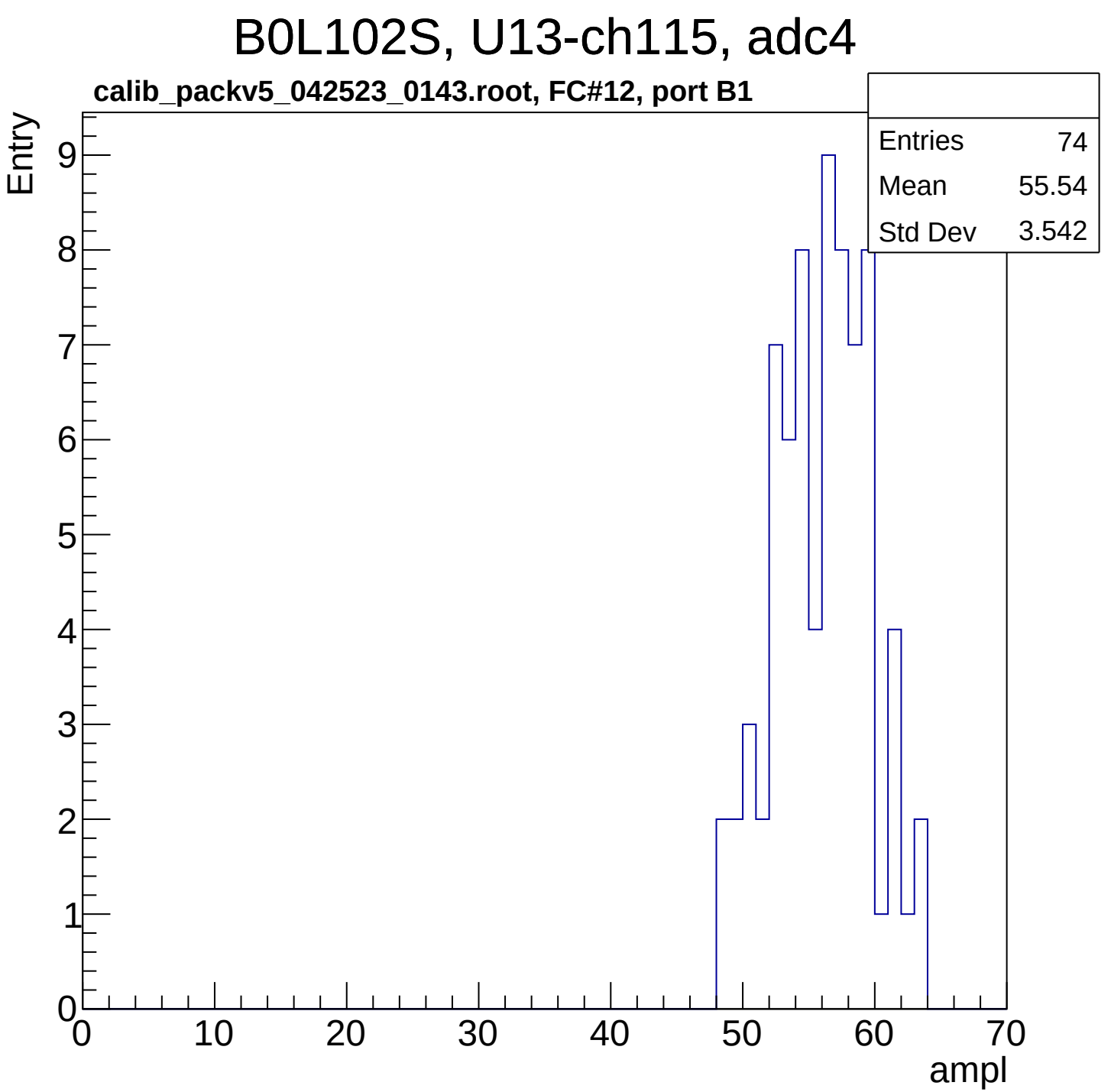
B0L102S, U13-ch115, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 55.54
Std Dev 3.542
Entry
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7
8
9
0
10
20
30
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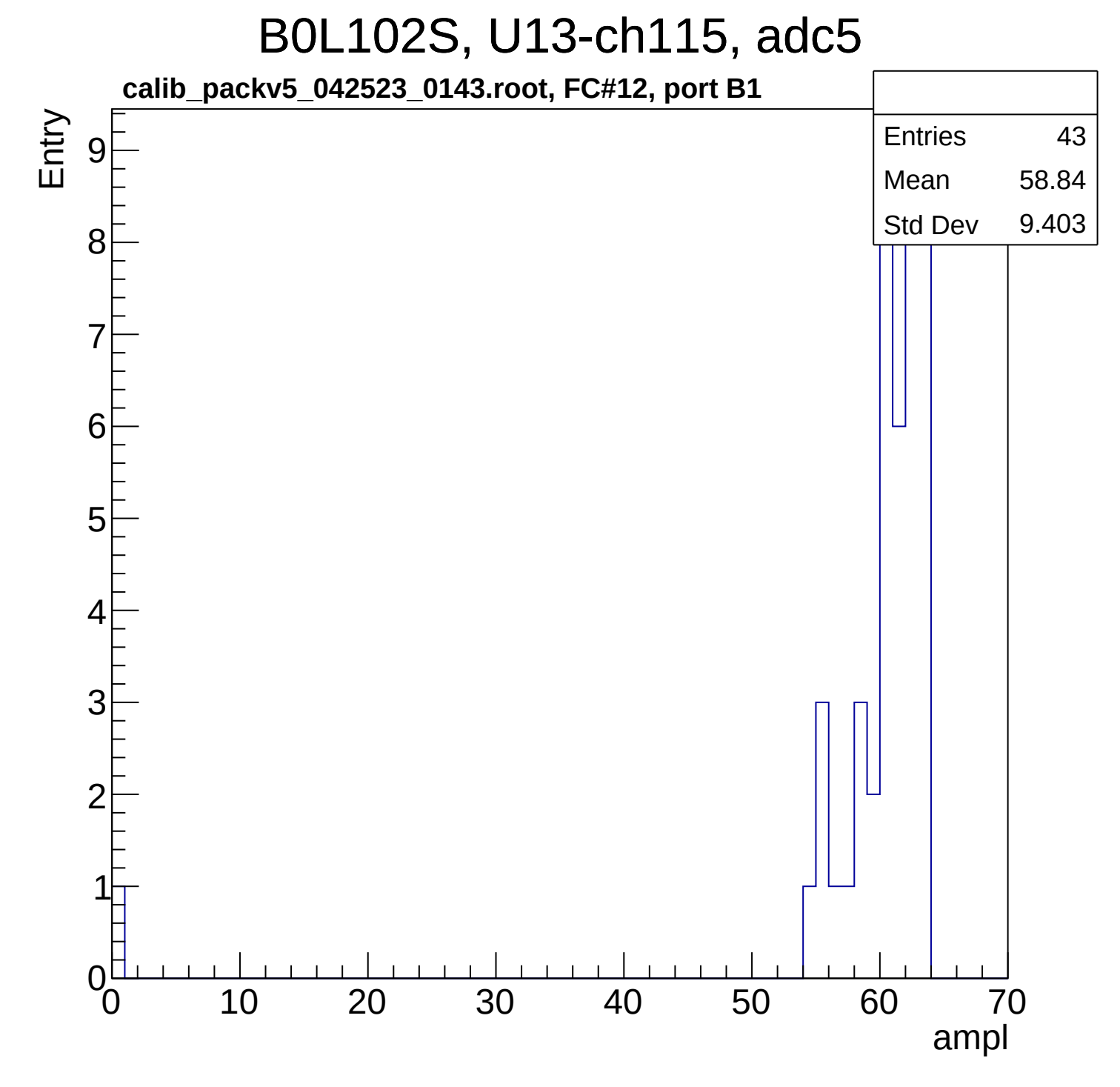
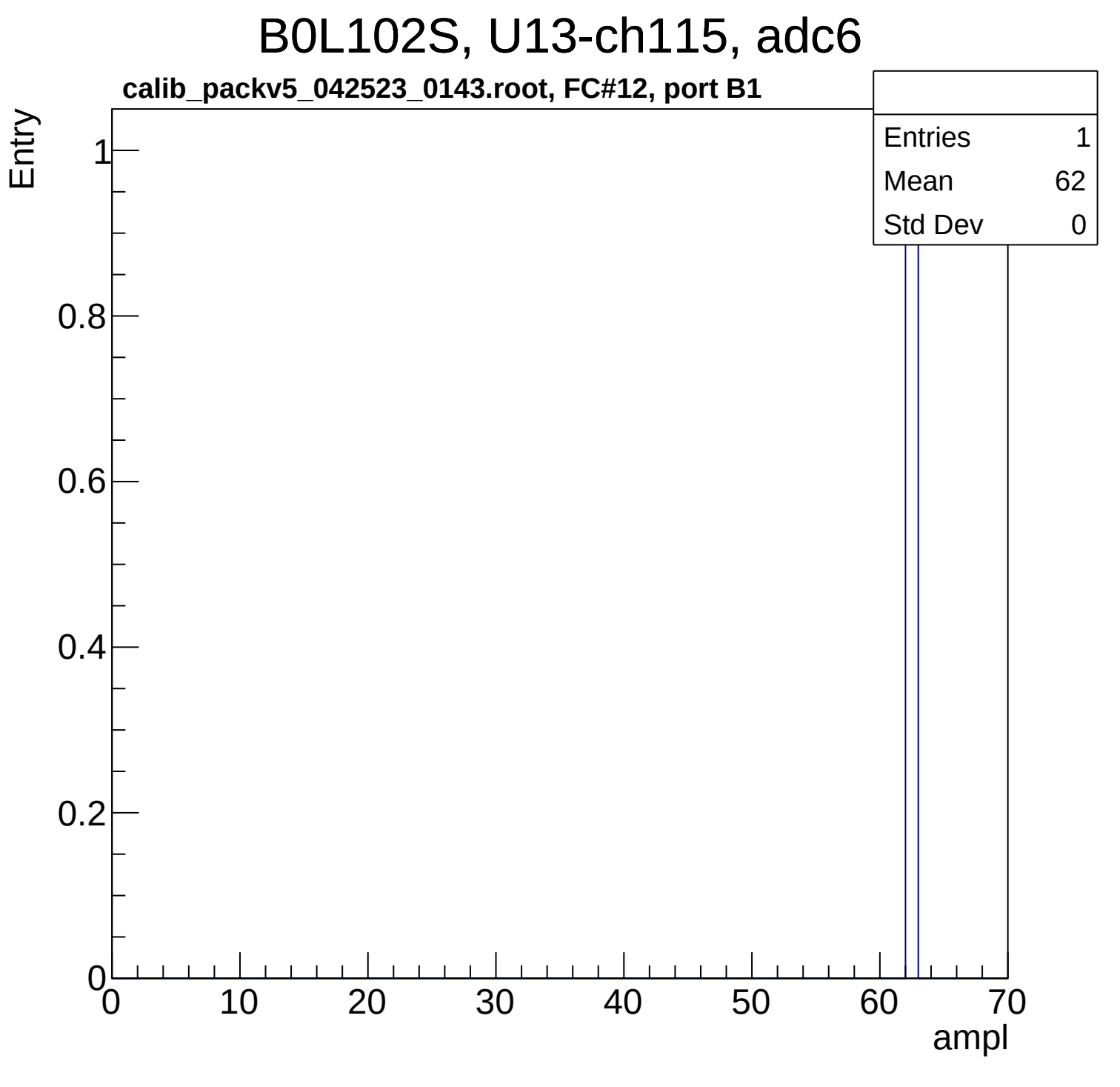
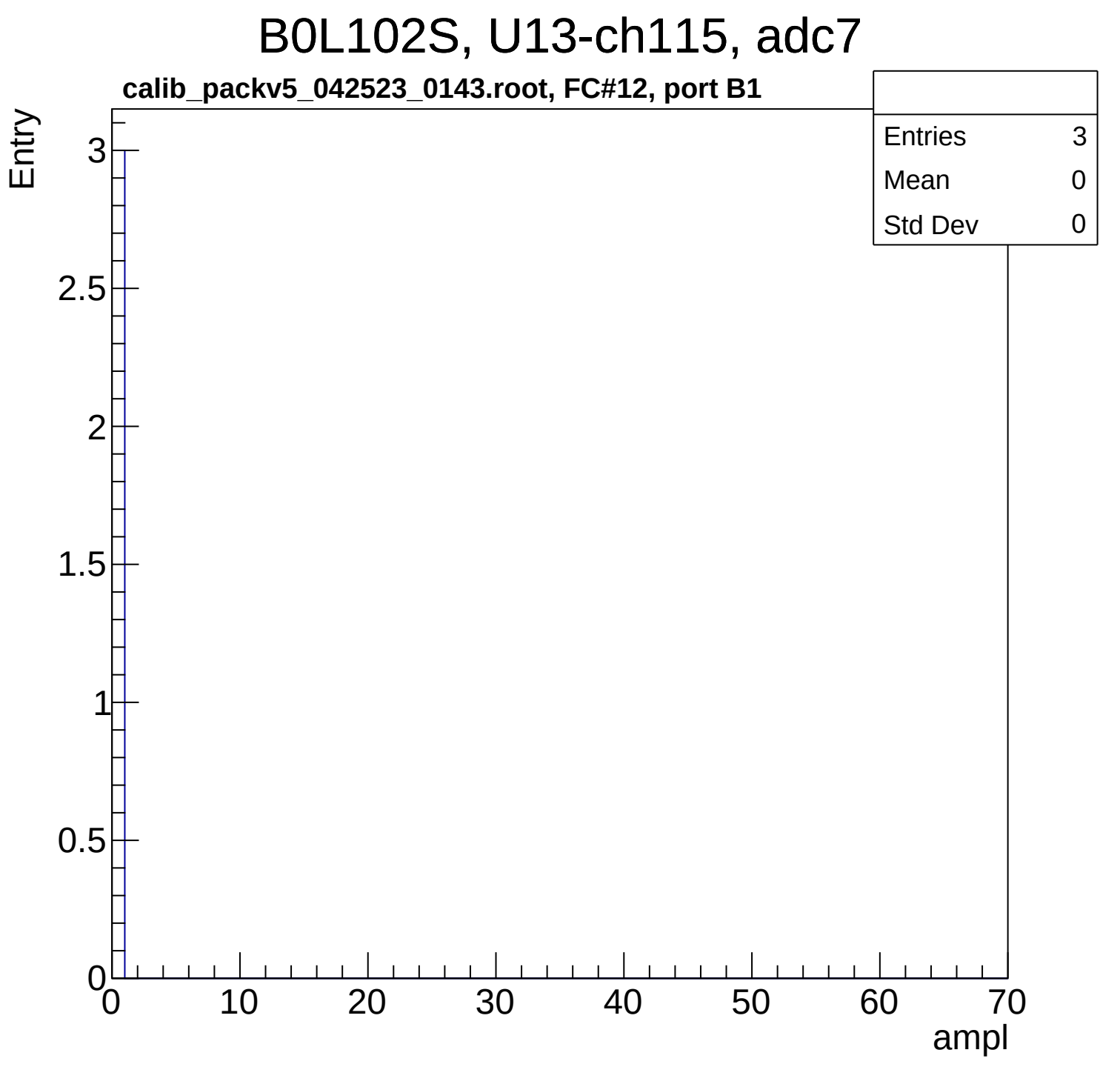
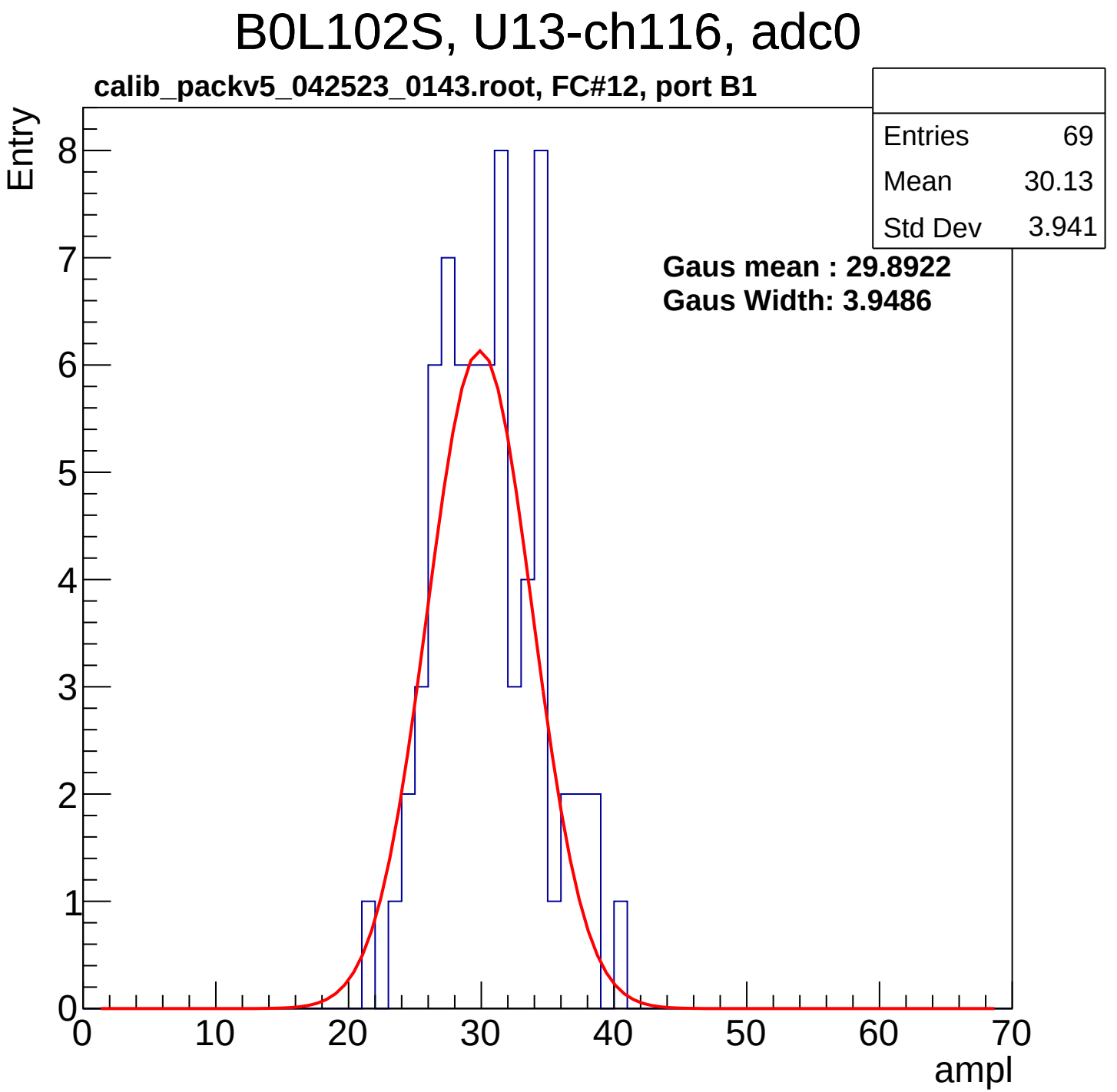
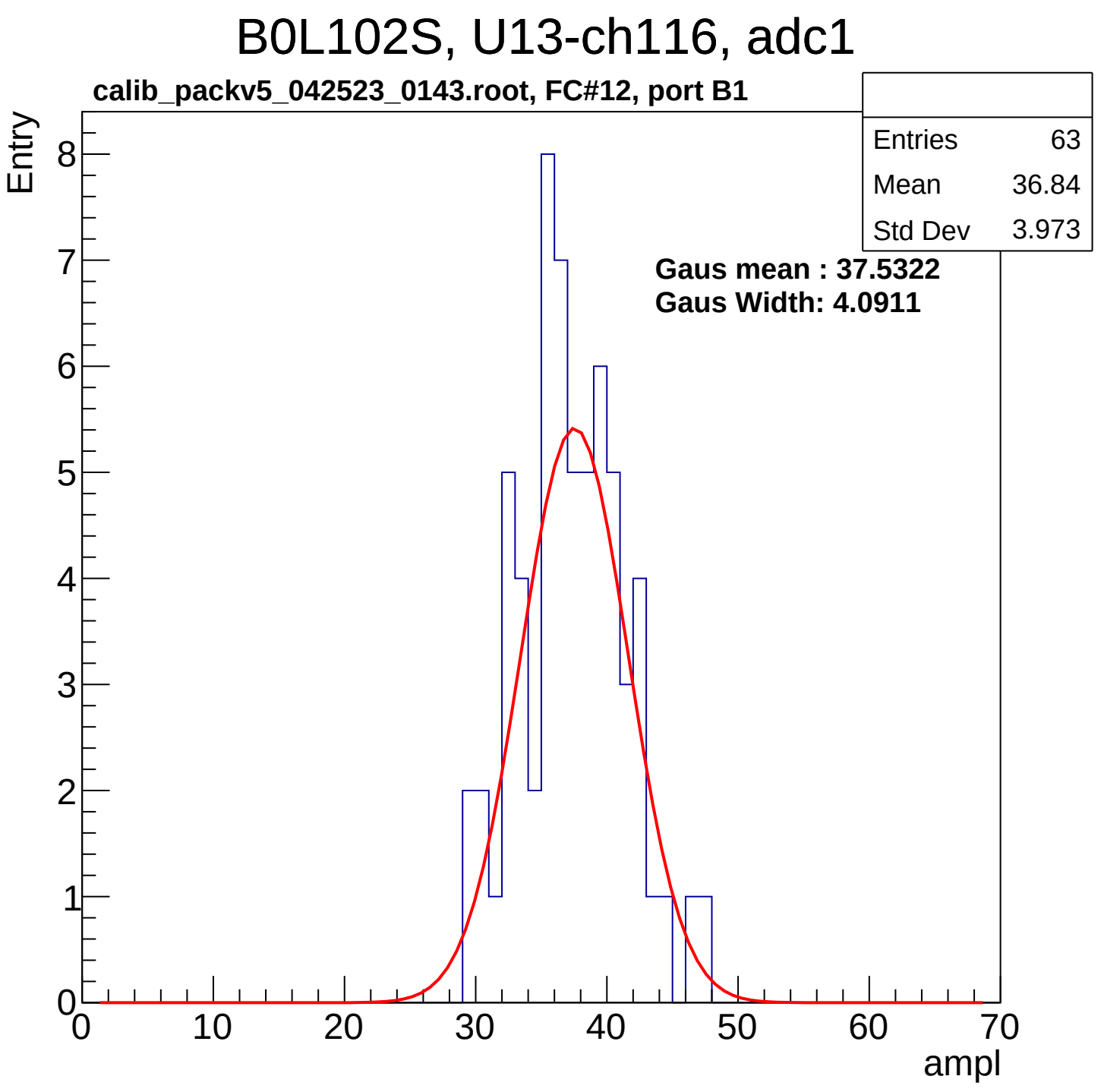
50
60
70
ampl


B0L102S, U13-ch115, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.84
Std Dev 9.403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch115, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch115, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch116, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 30.13
Std Dev 3.941
Gaus mean : 29.8922
Gaus Width: 3.9486
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch116, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 36.84
Std Dev 3.973
Gaus mean : 37.5322
Gaus Width: 4.0911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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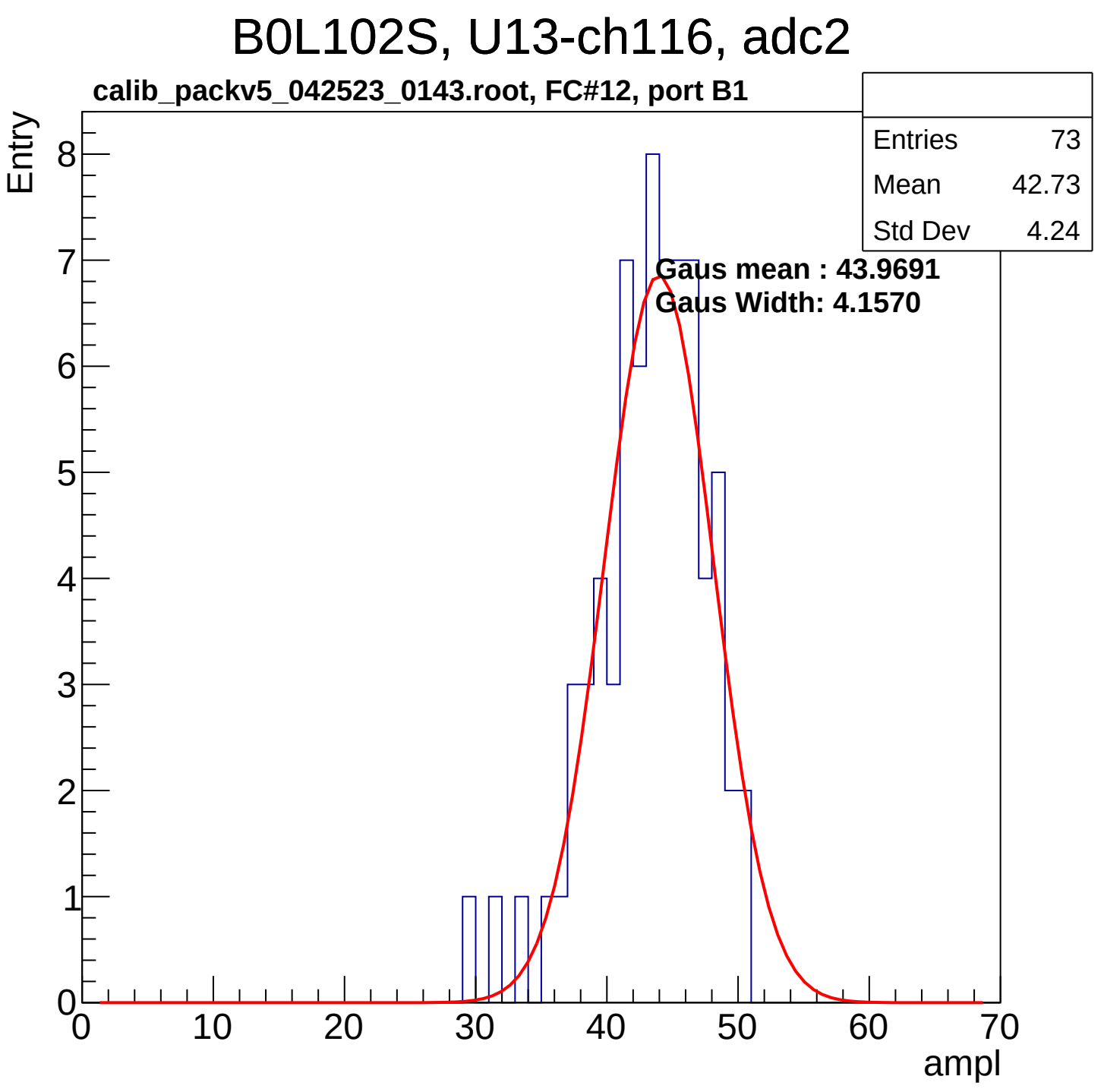
B0L102S, U13-ch116, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 42.73
Std Dev 4.24
Gaus mean : 43.9691
Gaus Width: 4.1570
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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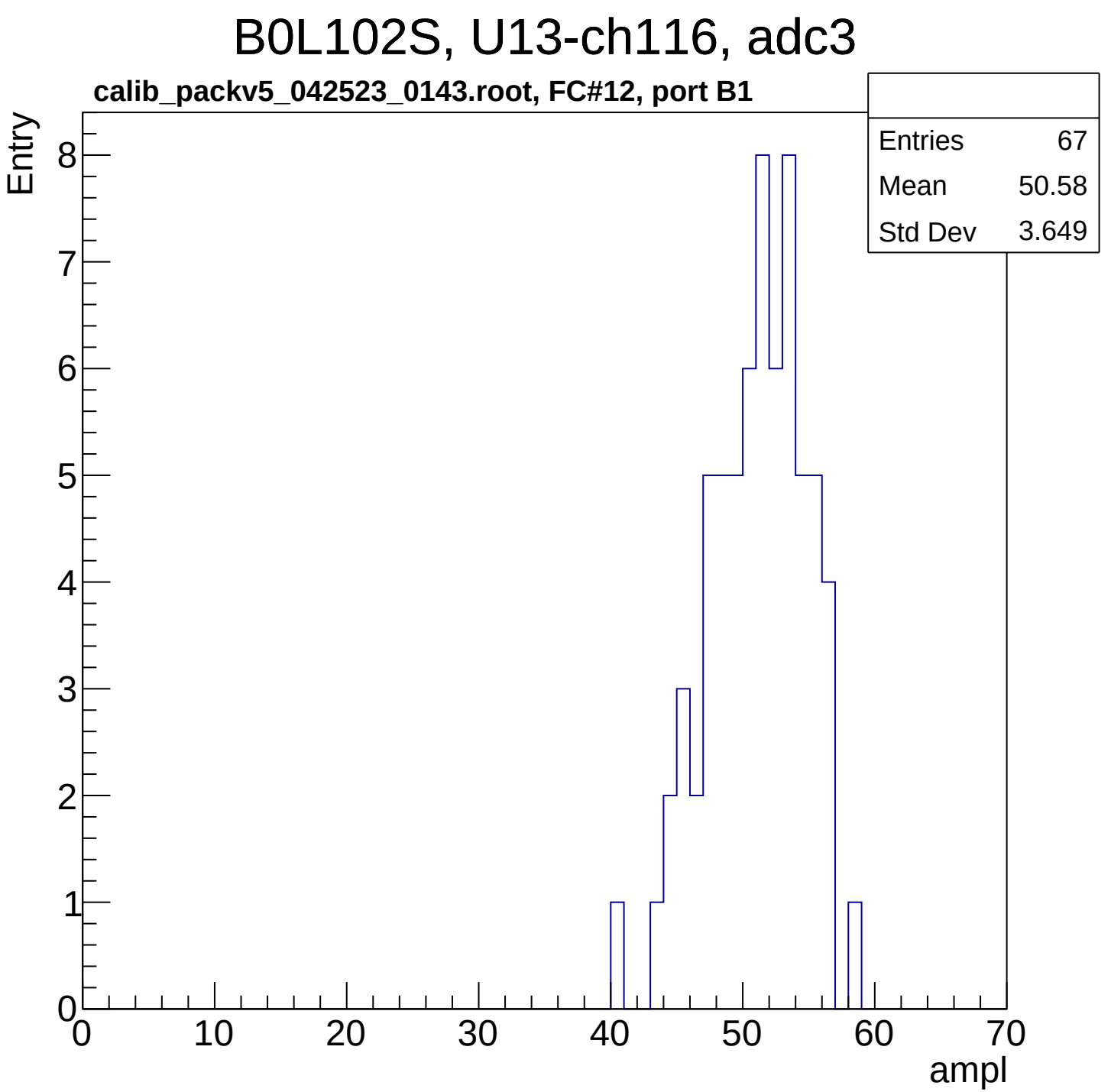
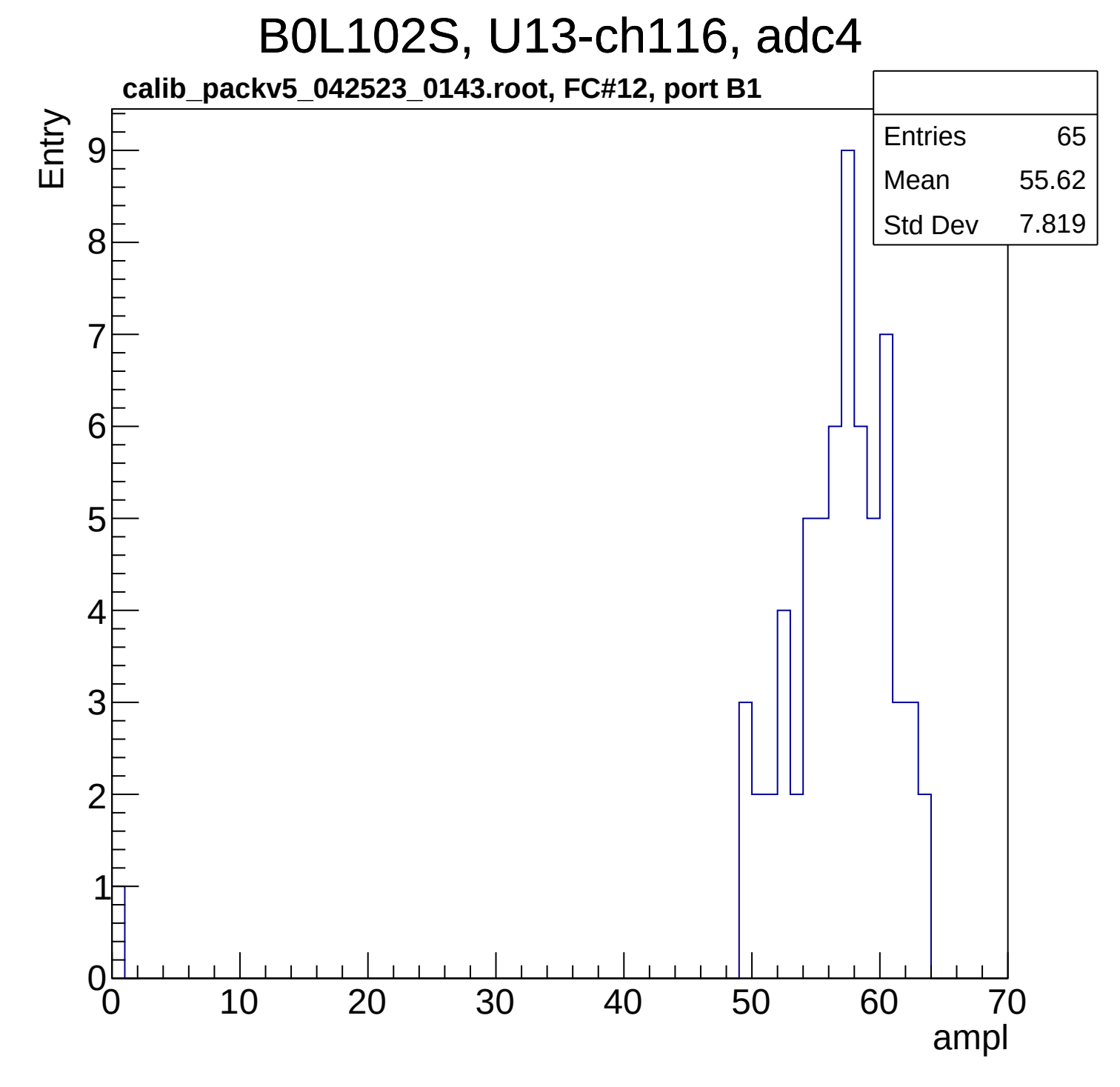

B0L102S, U13-ch116, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.58
Std Dev 3.649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch116, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 55.62
Std Dev 7.819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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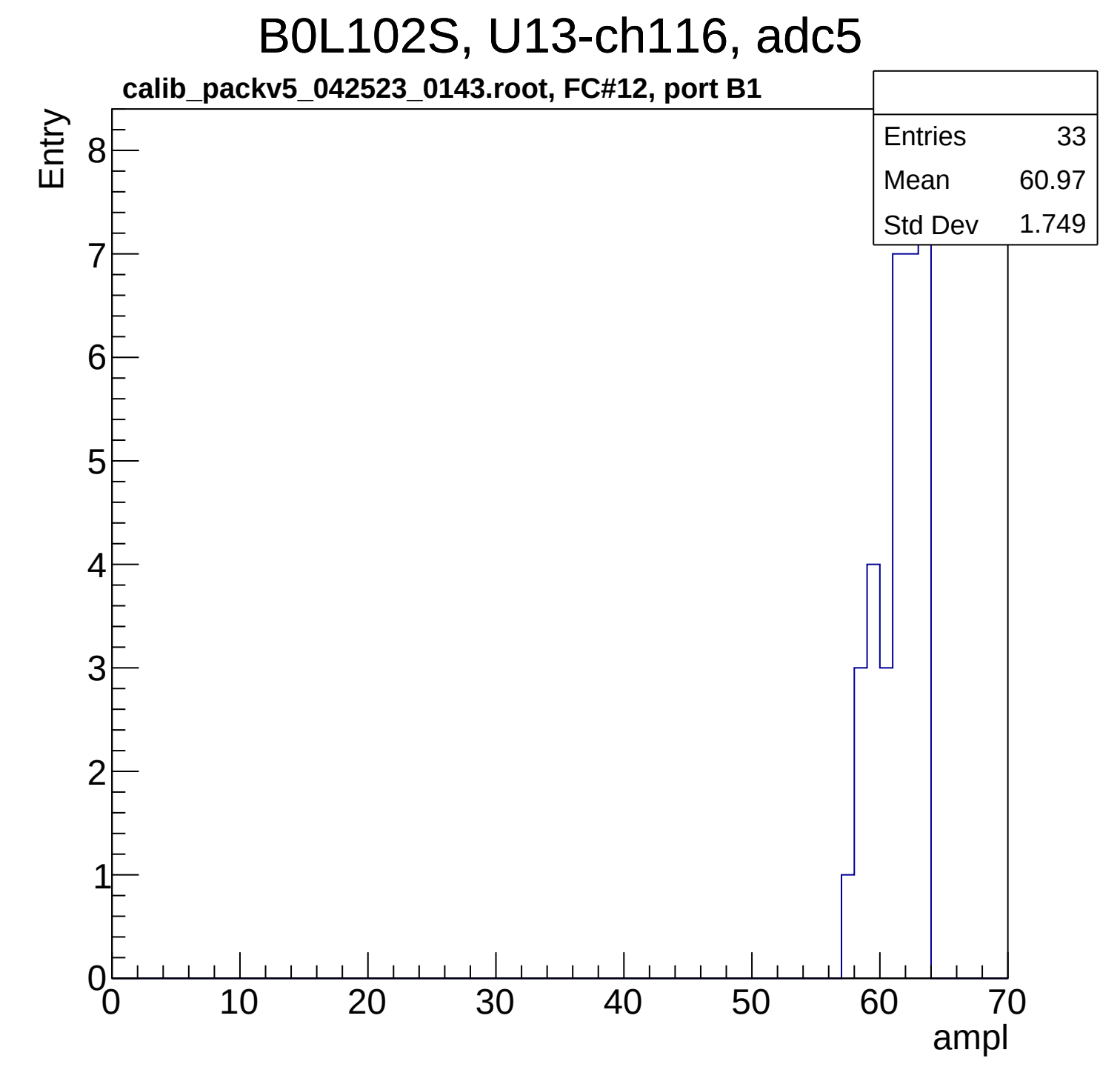
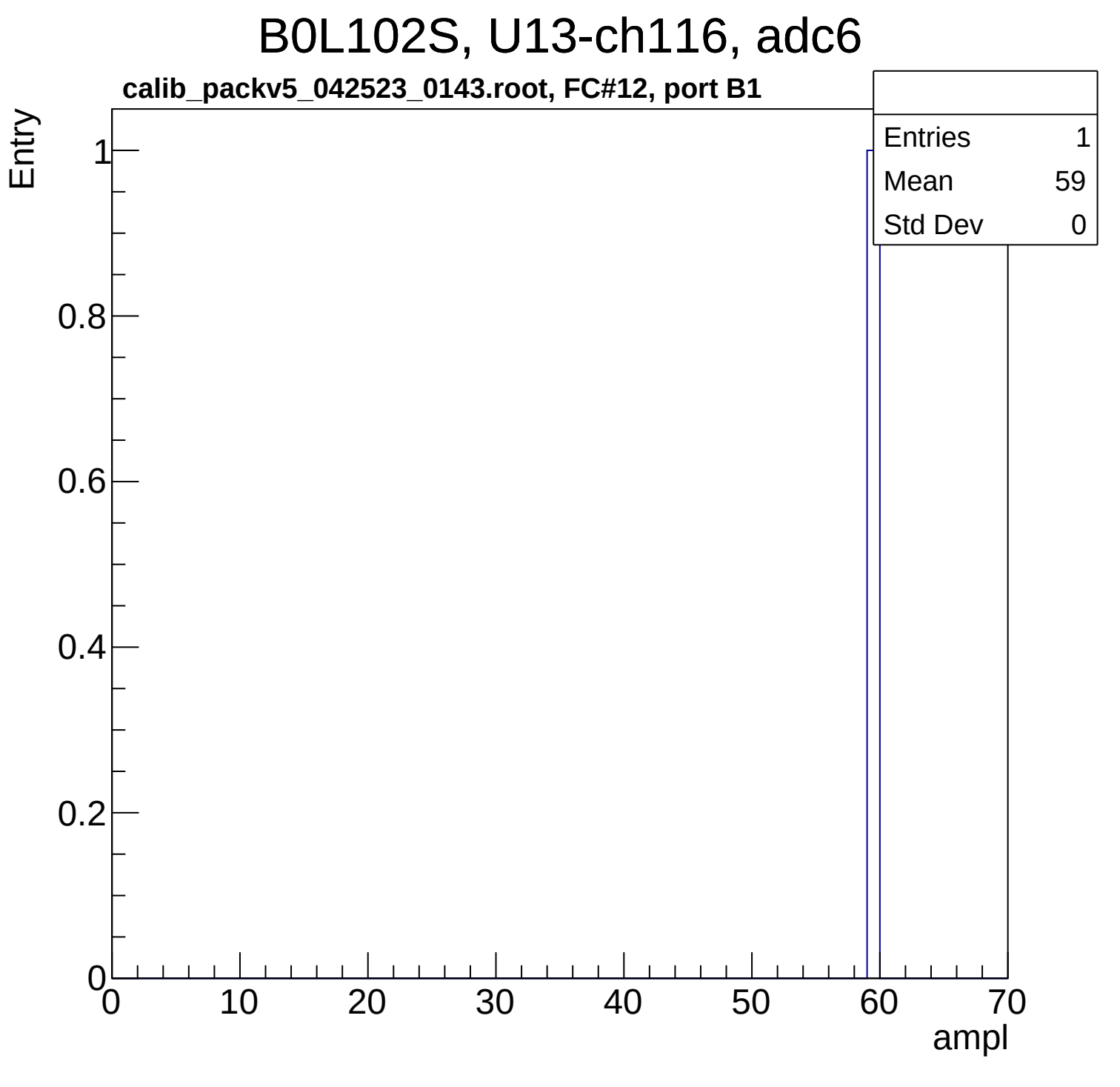
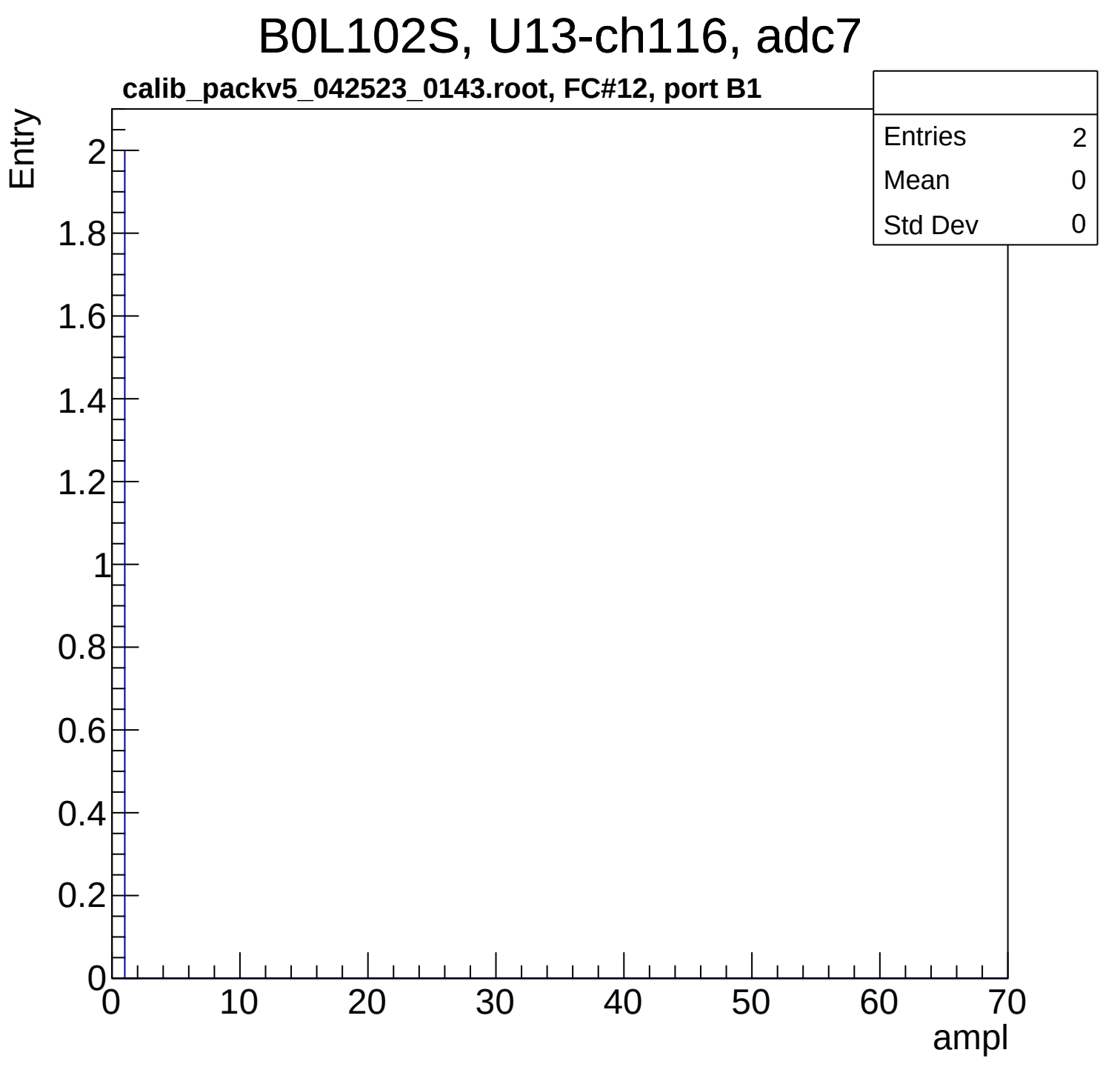
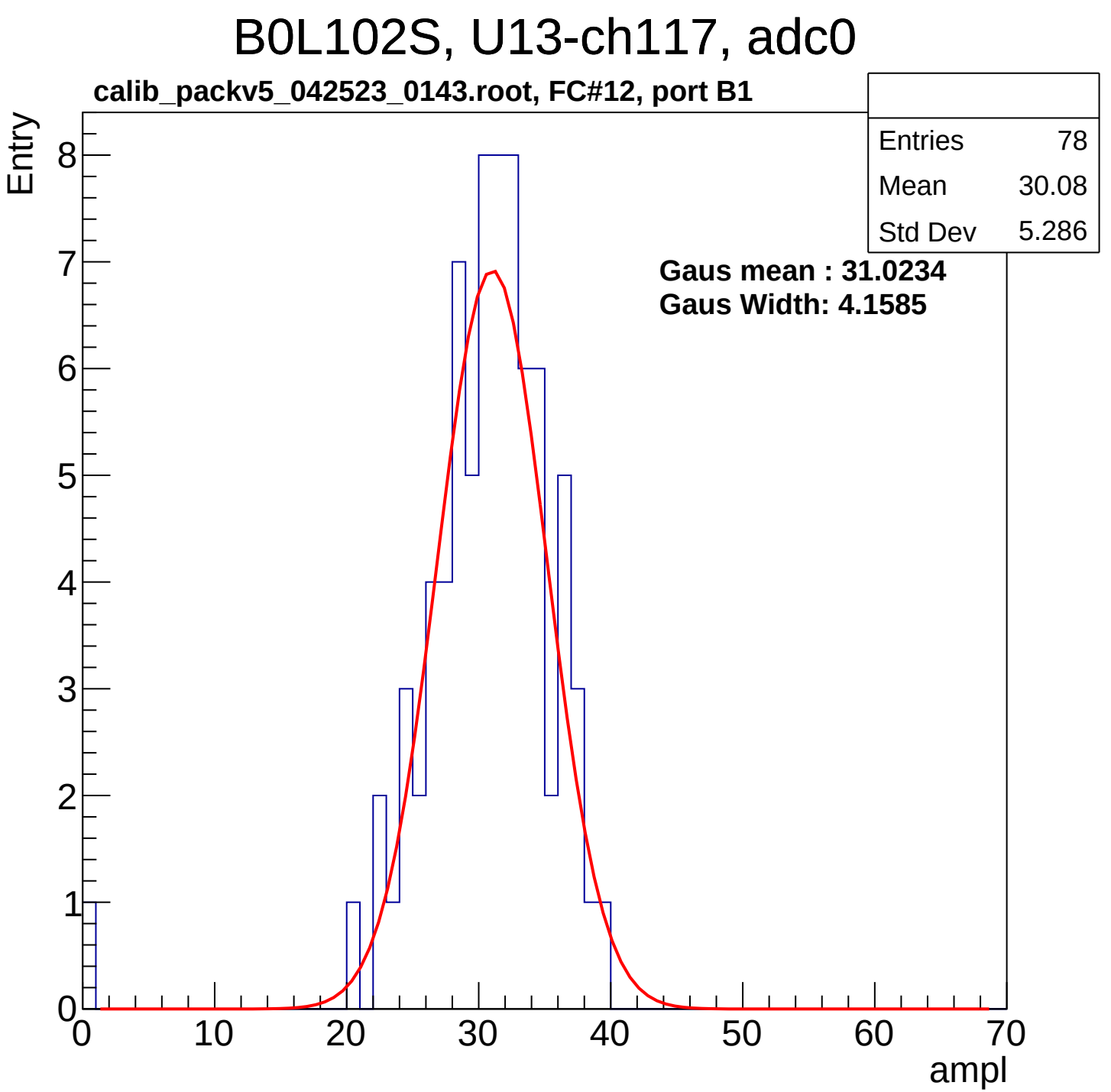
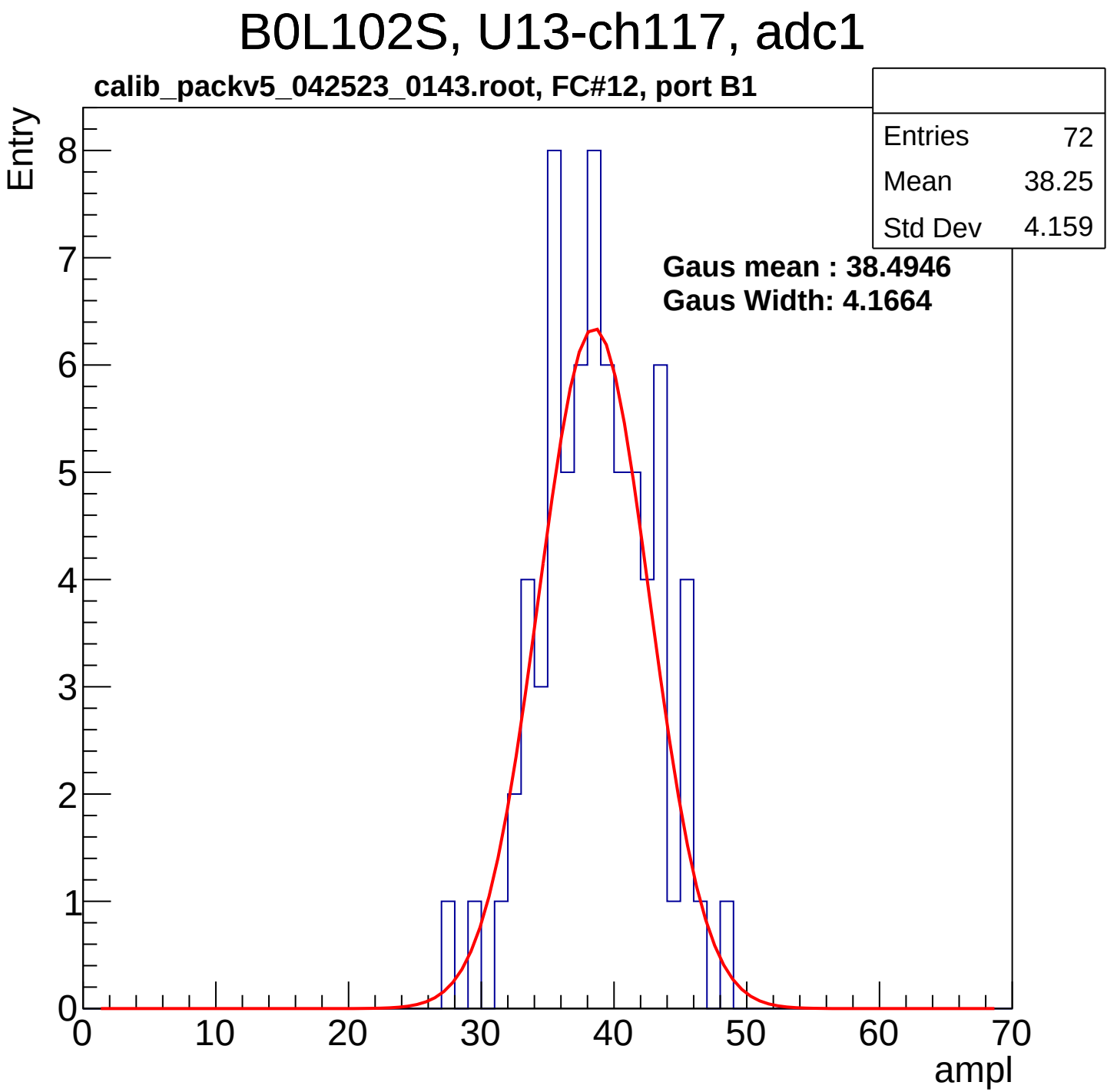
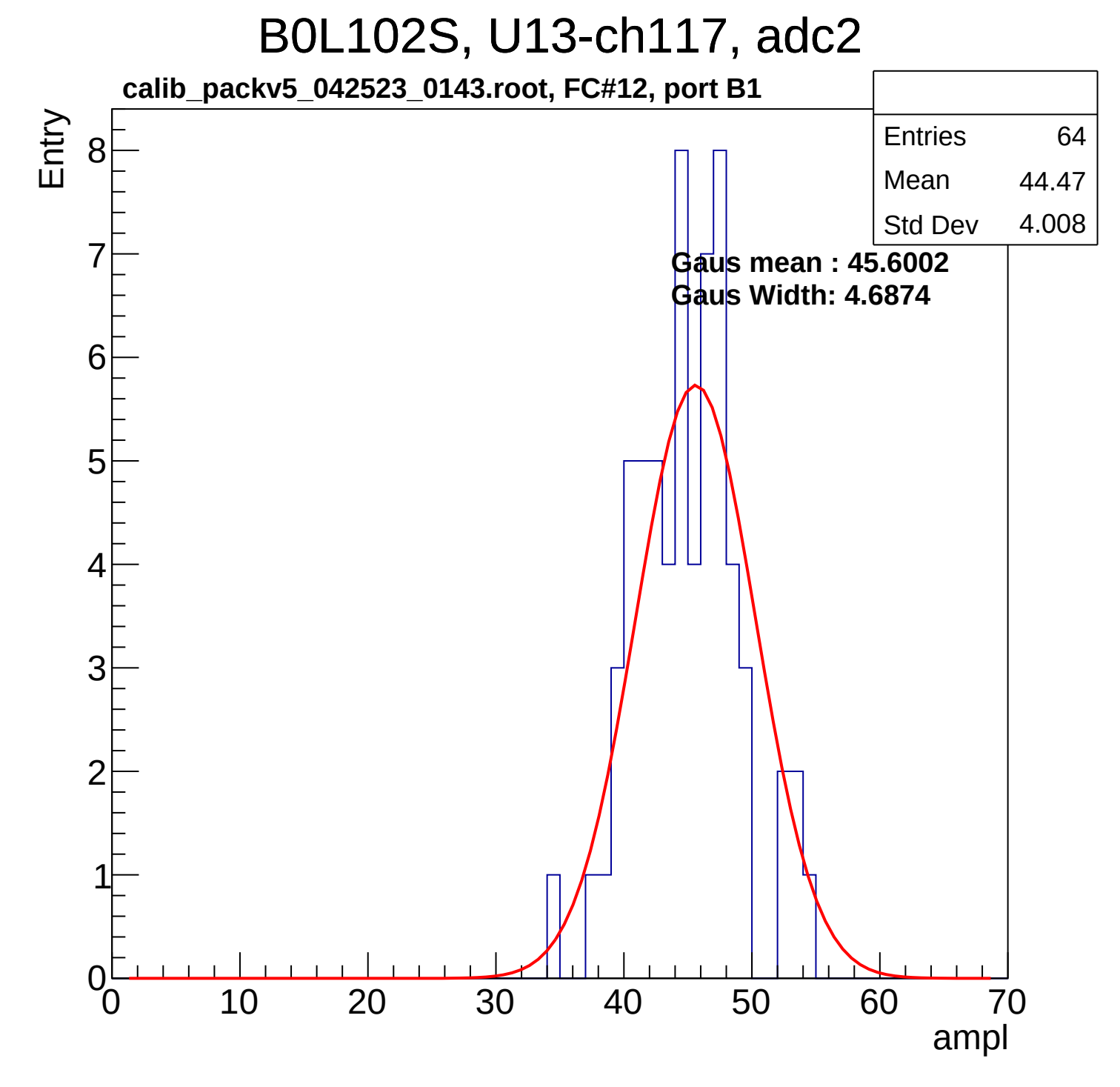

B0L102S, U13-ch116, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.97
Std Dev 1.749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

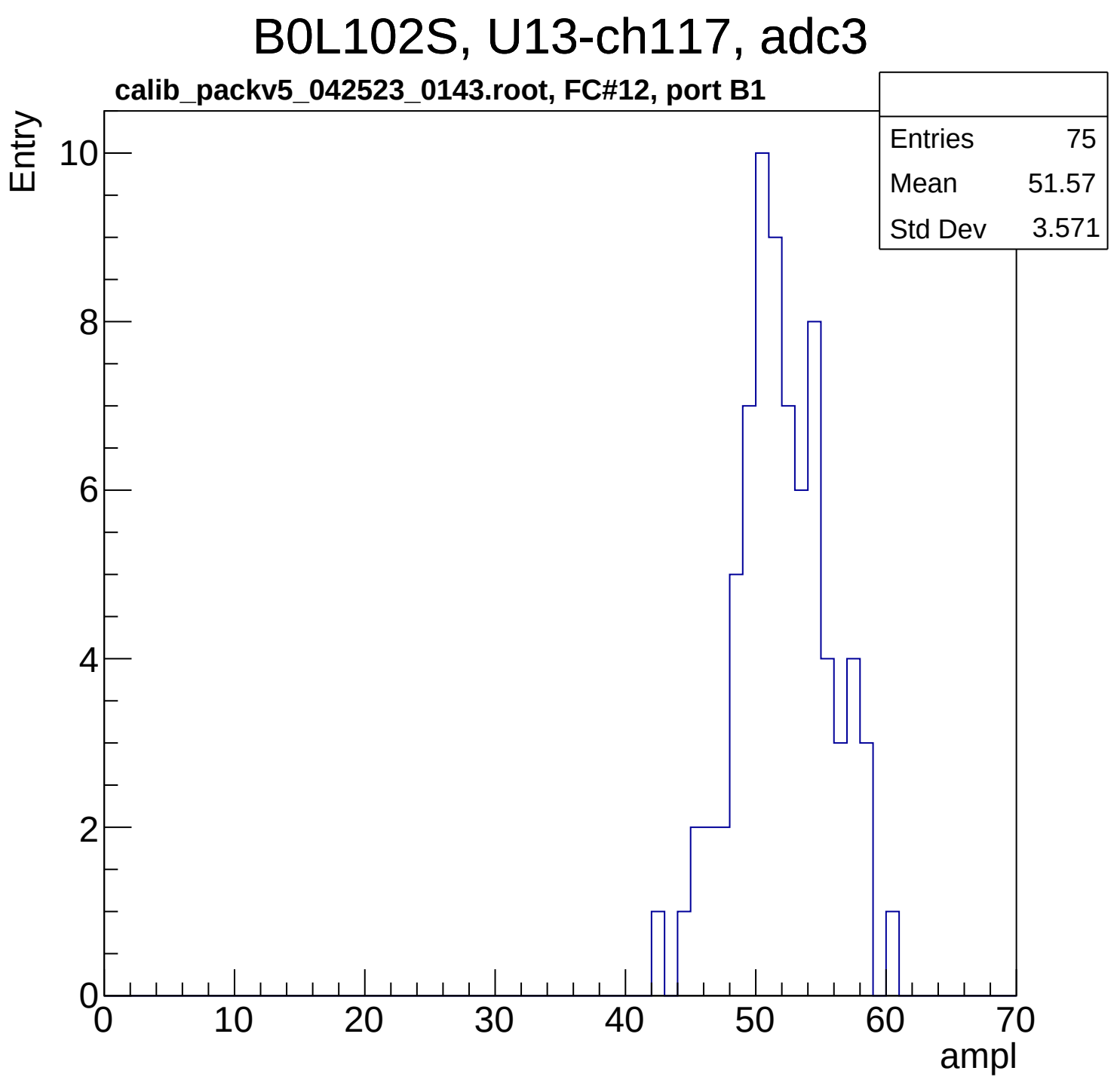

B0L102S, U13-ch116, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

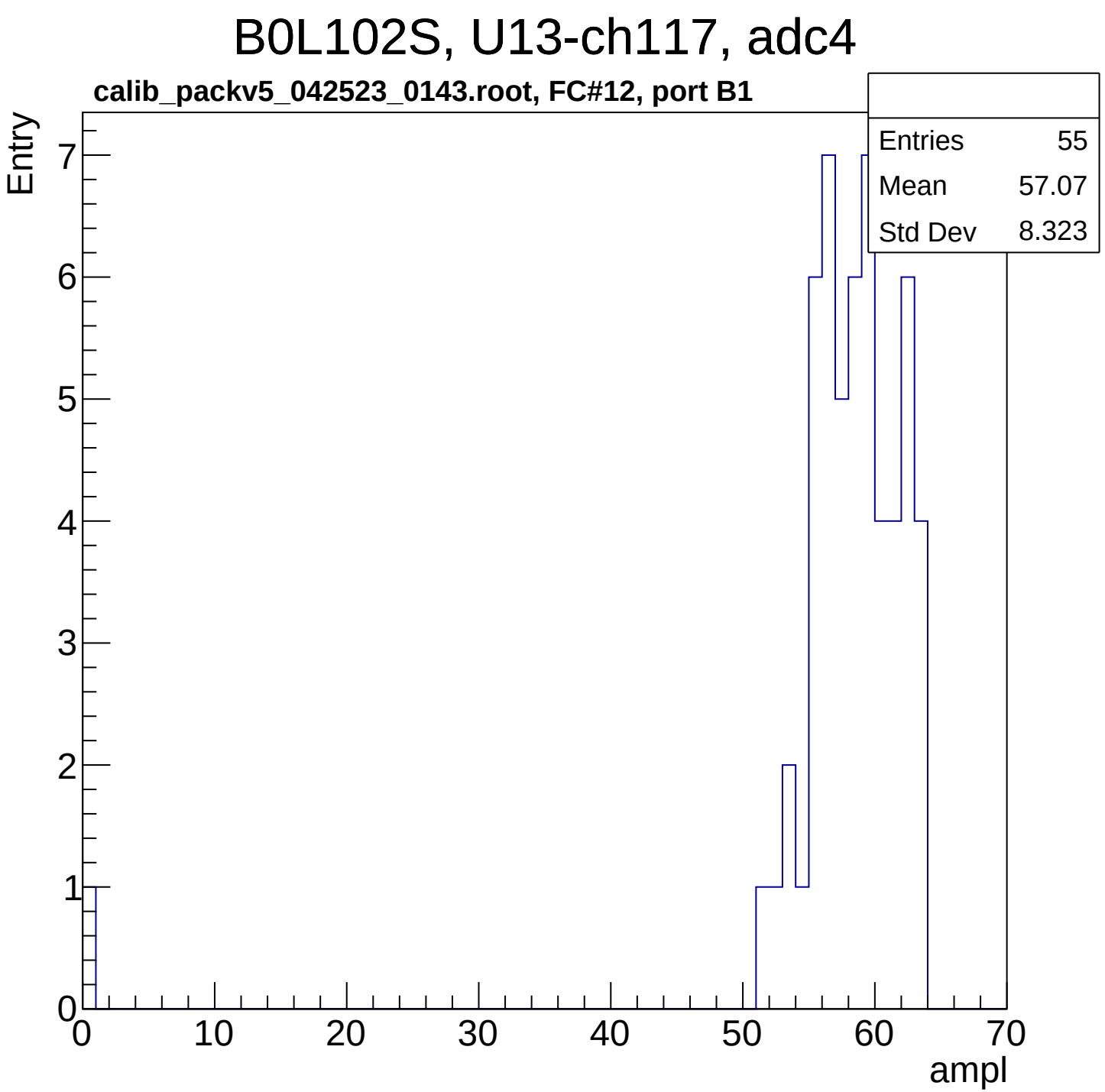

B0L102S, U13-ch116, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

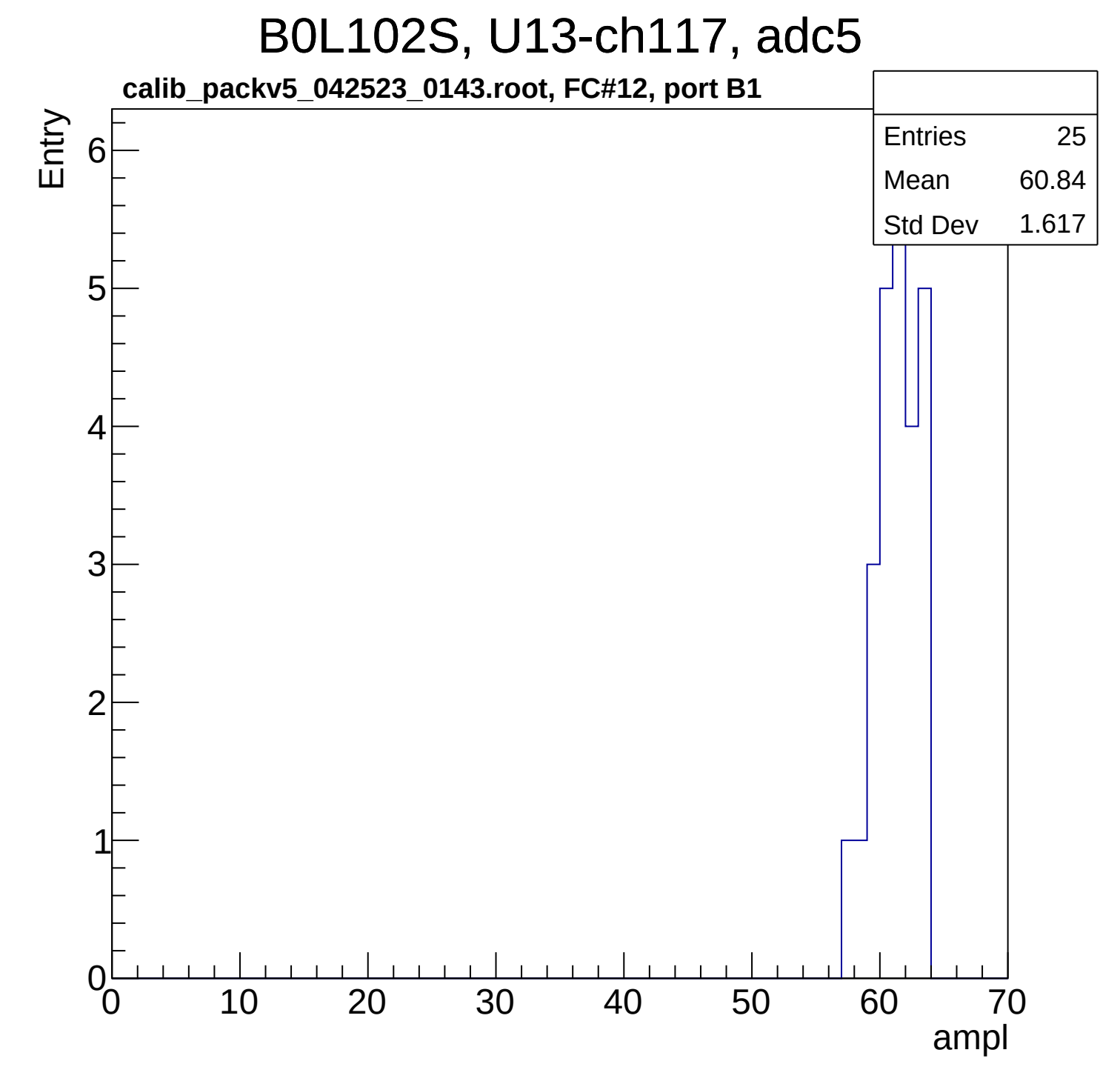

B0L102S, U13-ch117, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 30.08
Std Dev 5.286
Gaus mean : 31.0234
Gaus Width: 4.1585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

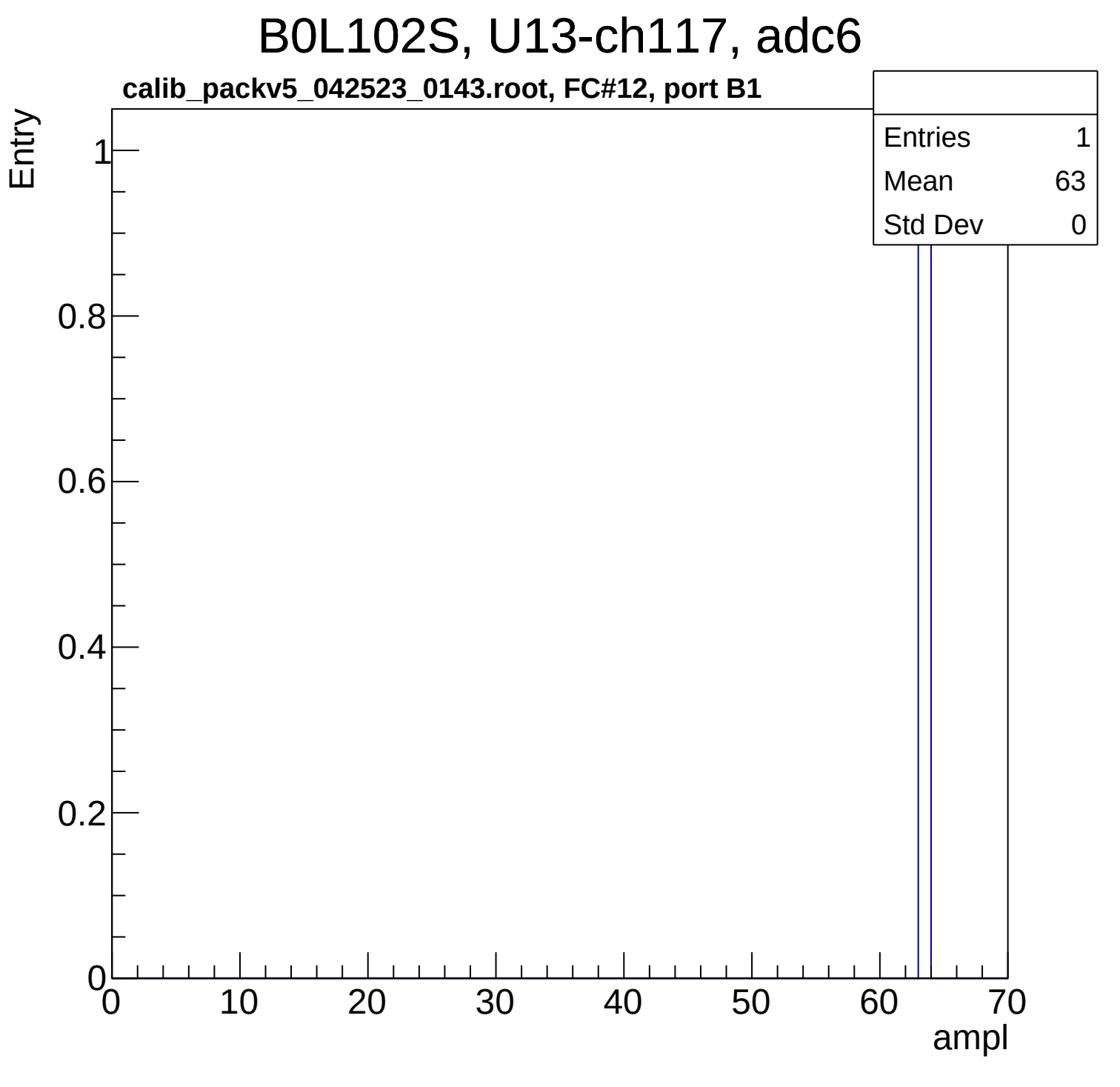

B0L102S, U13-ch117, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 38.25
Std Dev 4.159
Gaus mean : 38.4946
Gaus Width: 4.1664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

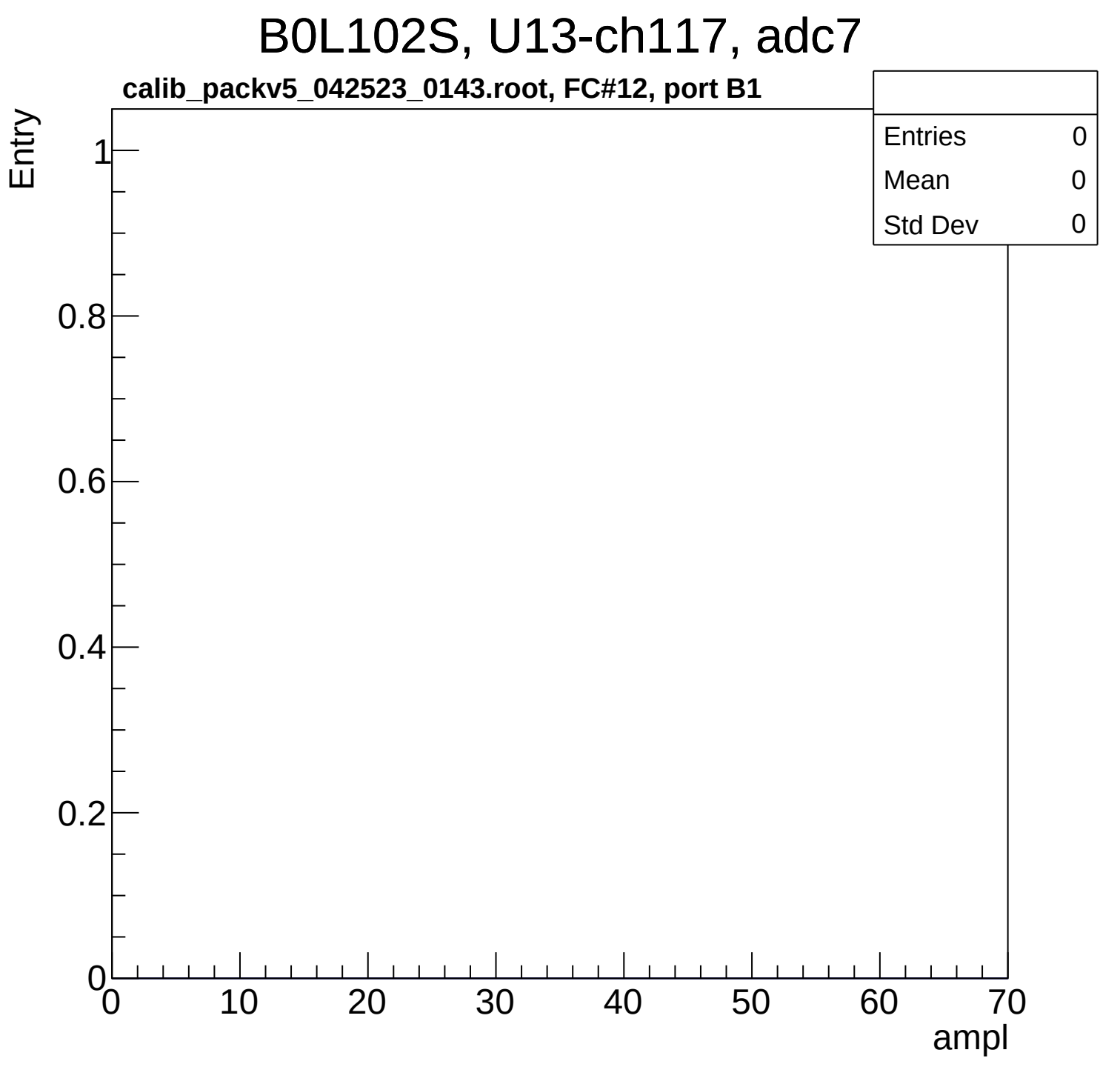

B0L102S, U13-ch117, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 44.47
Std Dev 4.008
Gaus mean : 45.6002
Gaus Width: 4.6874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

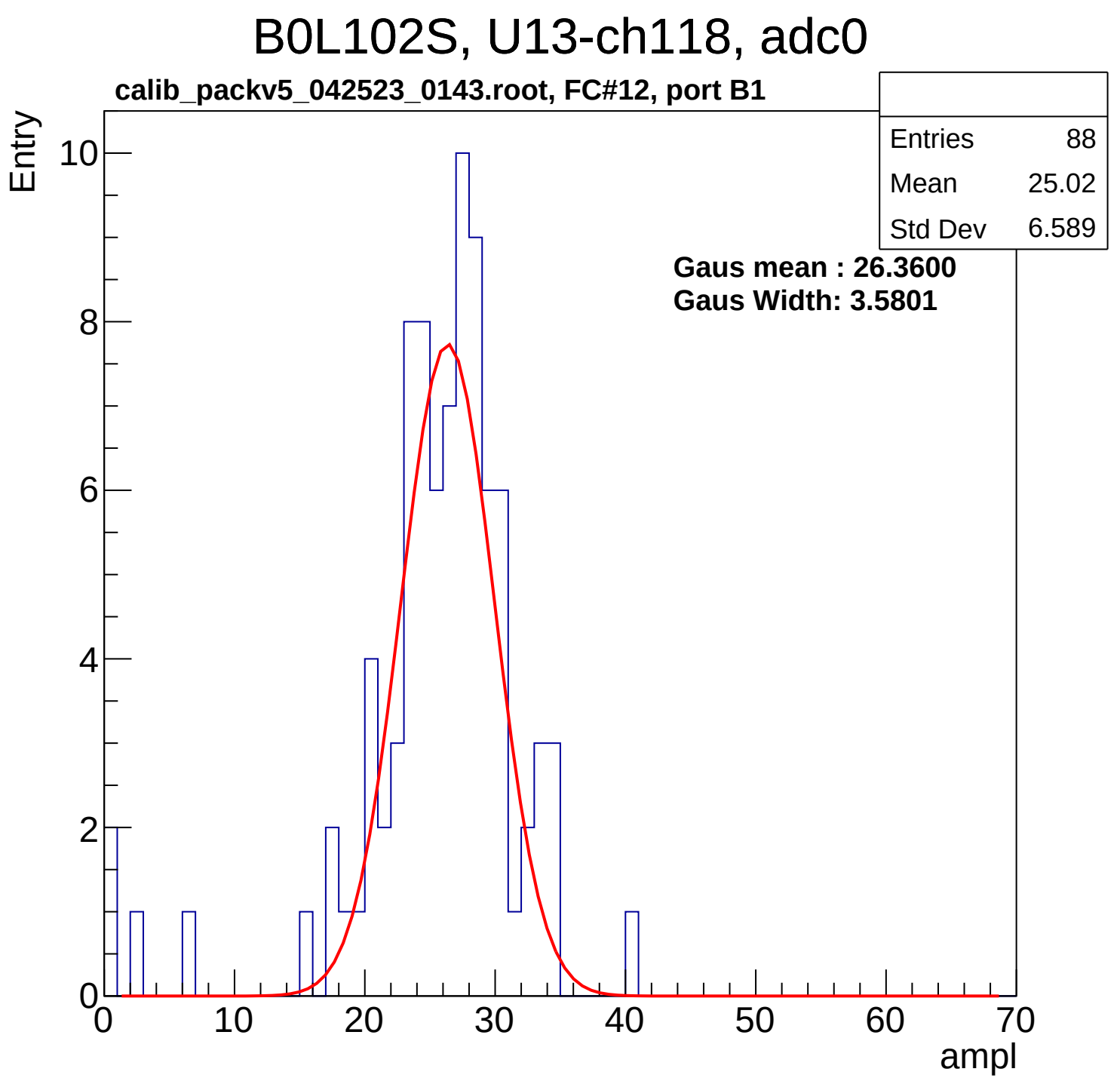

B0L102S, U13-ch117, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 51.57
Std Dev 3.571
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

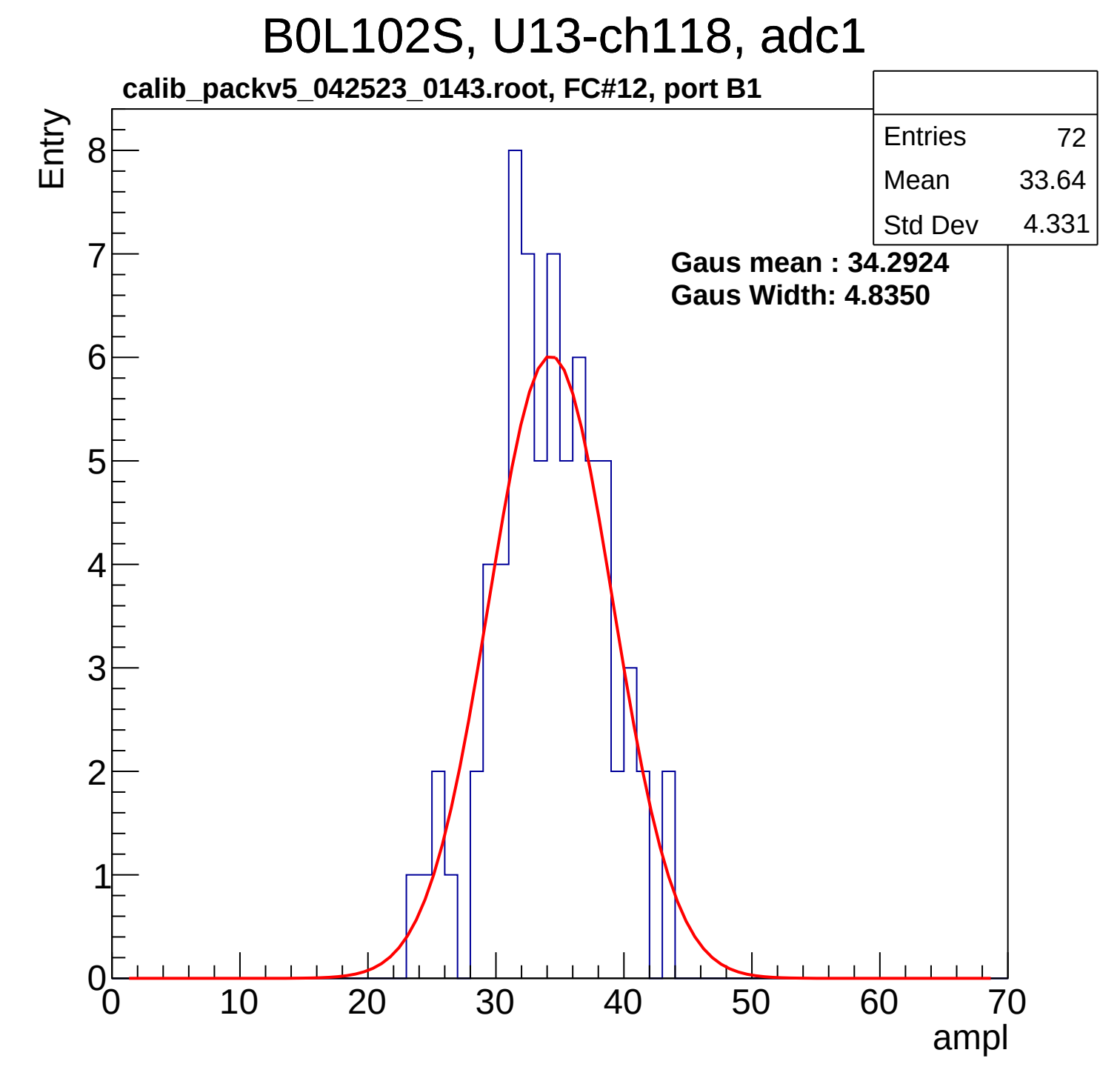

B0L102S, U13-ch117, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 57.07
Std Dev 8.323
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

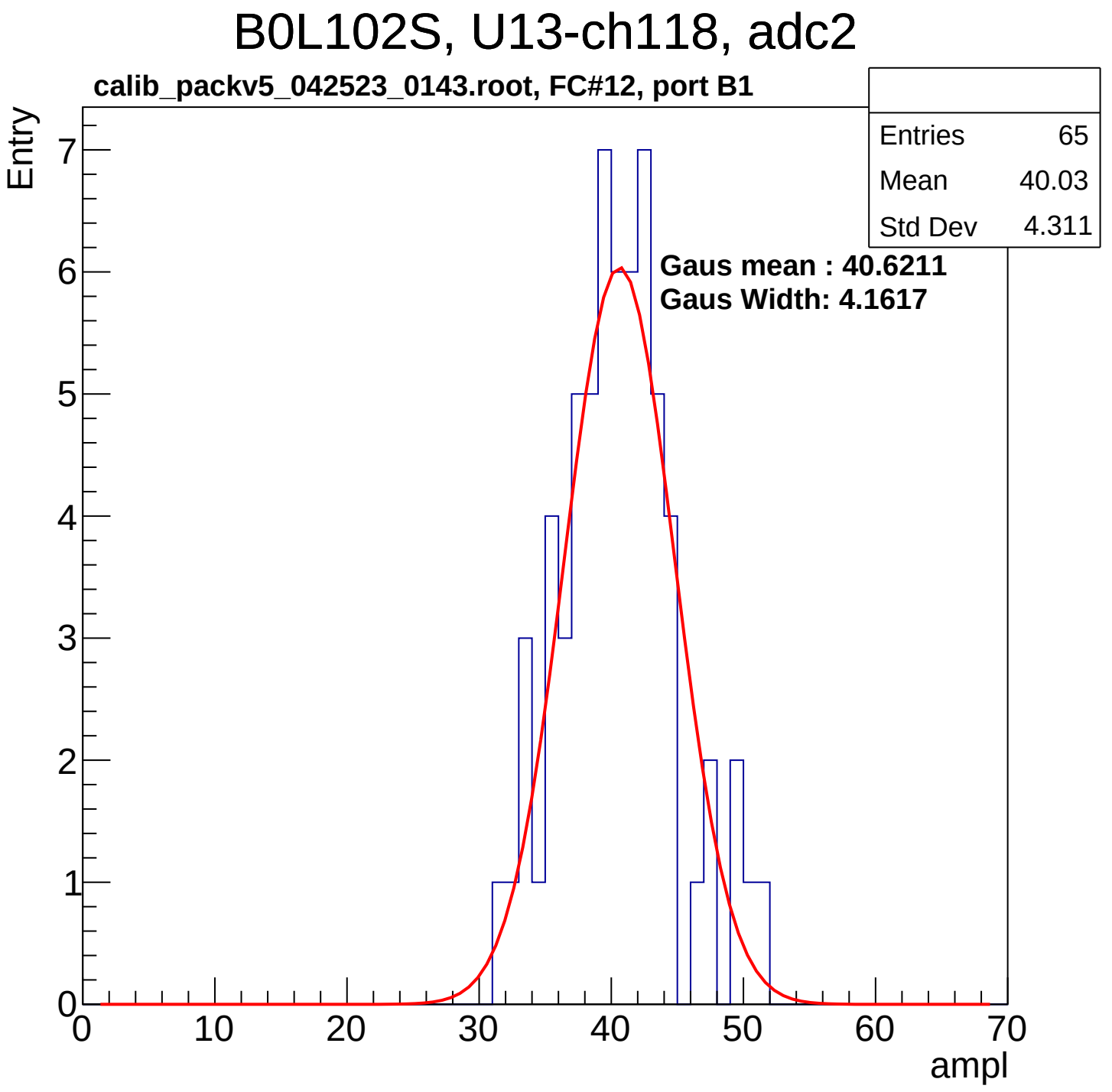

B0L102S, U13-ch117, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 25
Mean 60.84
Std Dev 1.617
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

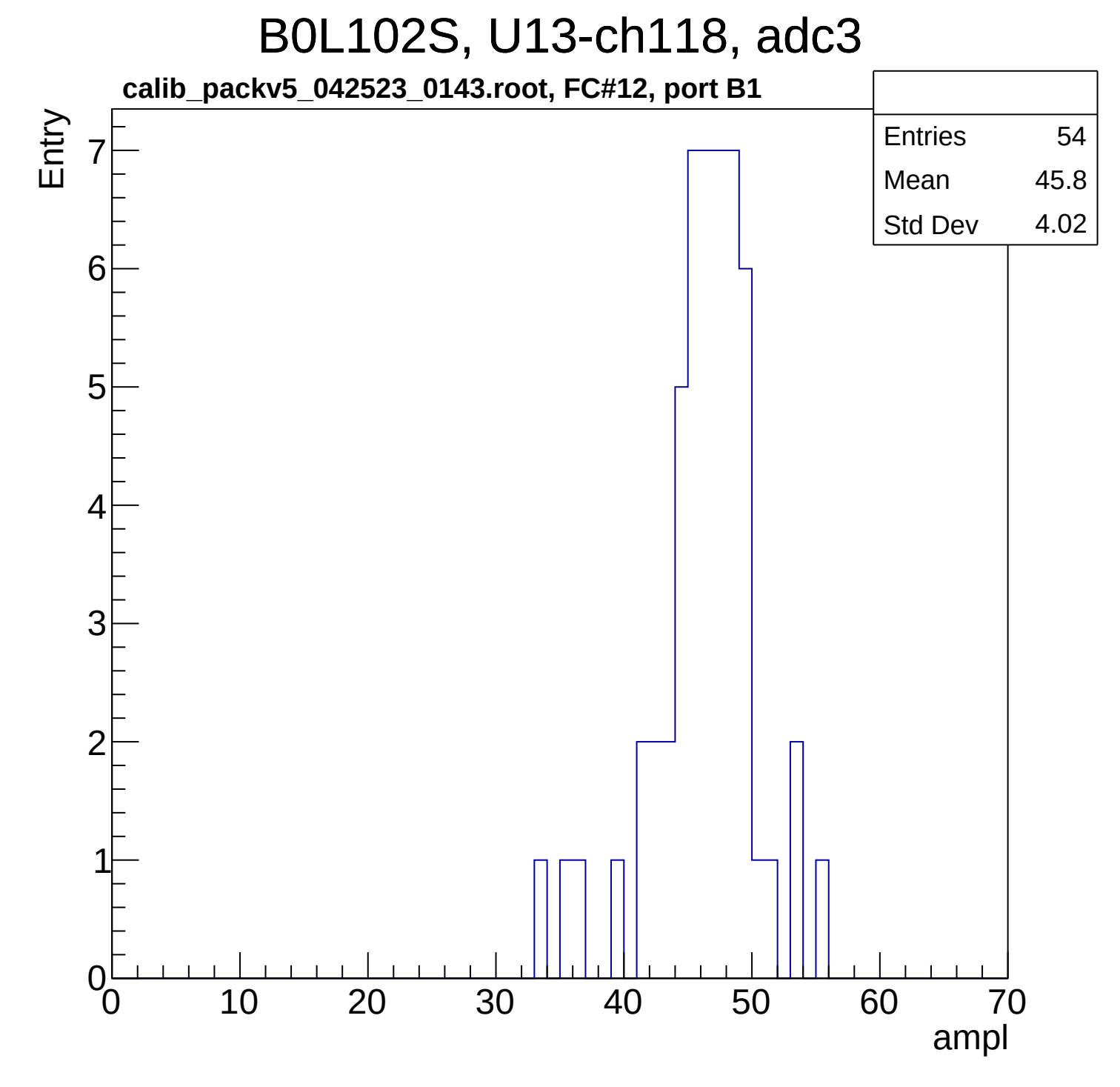

B0L102S, U13-ch117, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

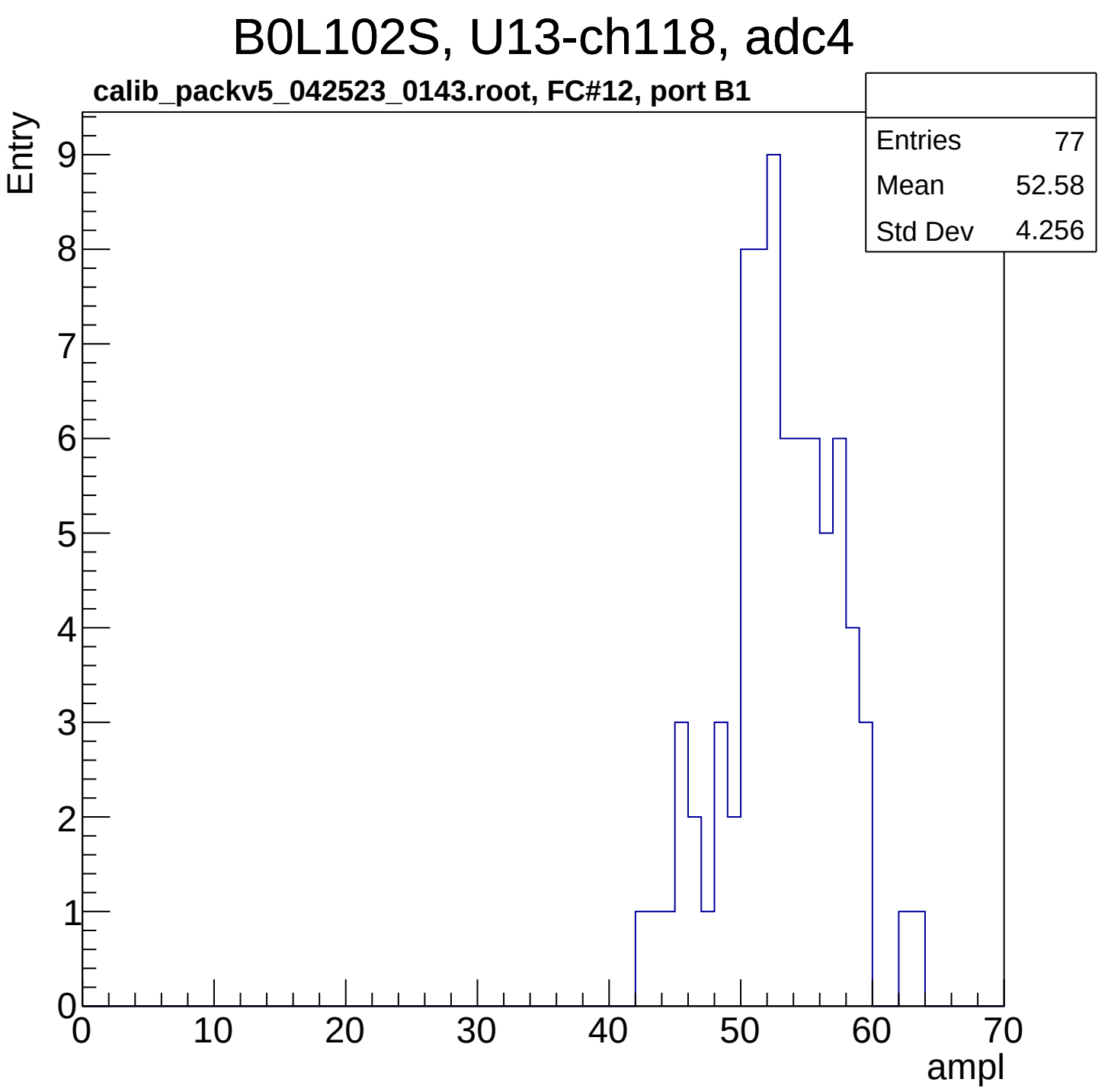

B0L102S, U13-ch117, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch118, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 25.02
Std Dev 6.589
Gaus mean : 26.3600
Gaus Width: 3.5801
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch118, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 33.64
Std Dev 4.331
Gaus mean : 34.2924
Gaus Width: 4.8350
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch118, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 40.03
Std Dev 4.311
Gaus mean : 40.6211
Gaus Width: 4.1617
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch118, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 45.8
Std Dev 4.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch118, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 52.58
Std Dev 4.256
Entry
ampl
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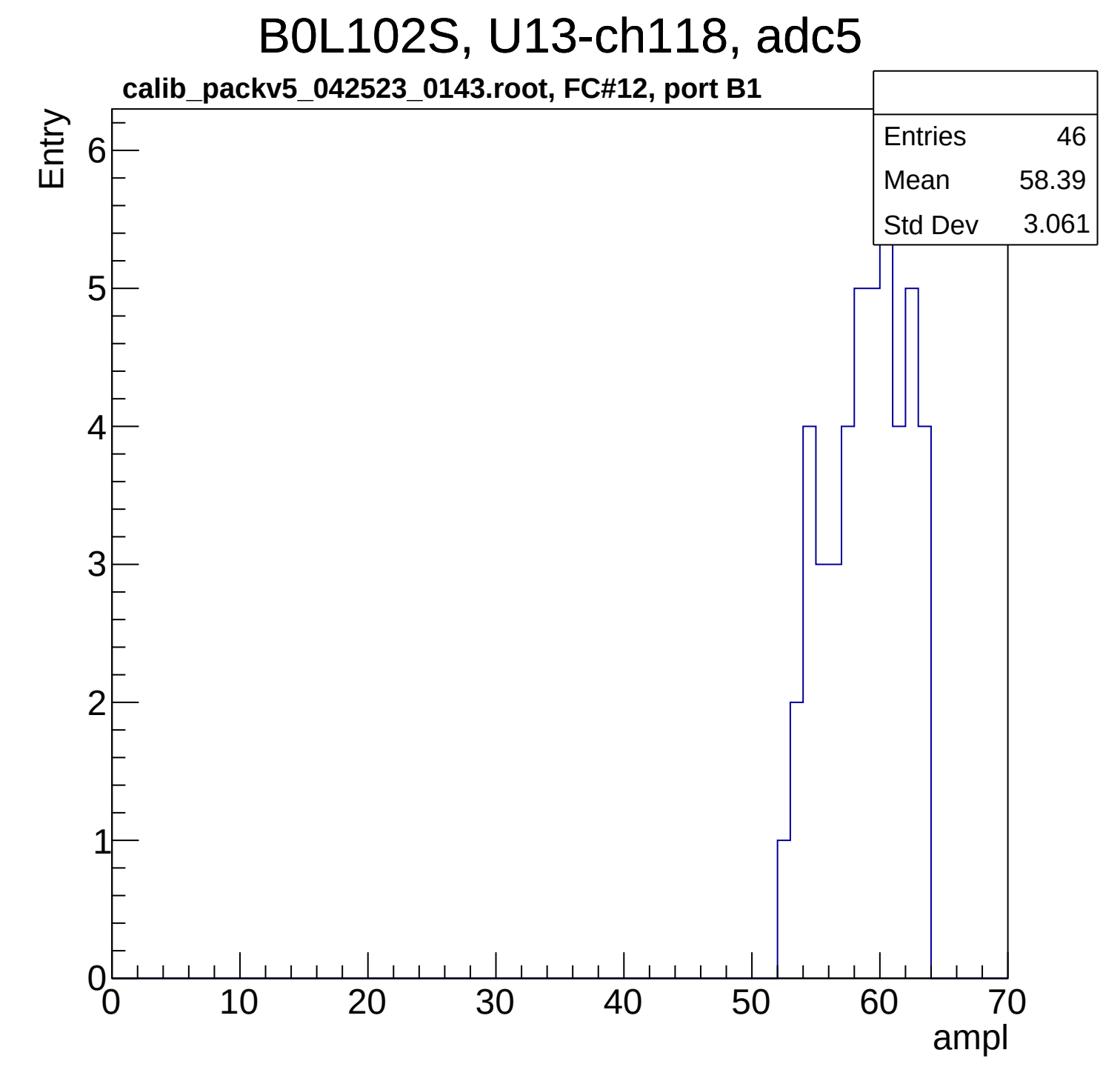
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch118, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.39
Std Dev 3.061
Entry
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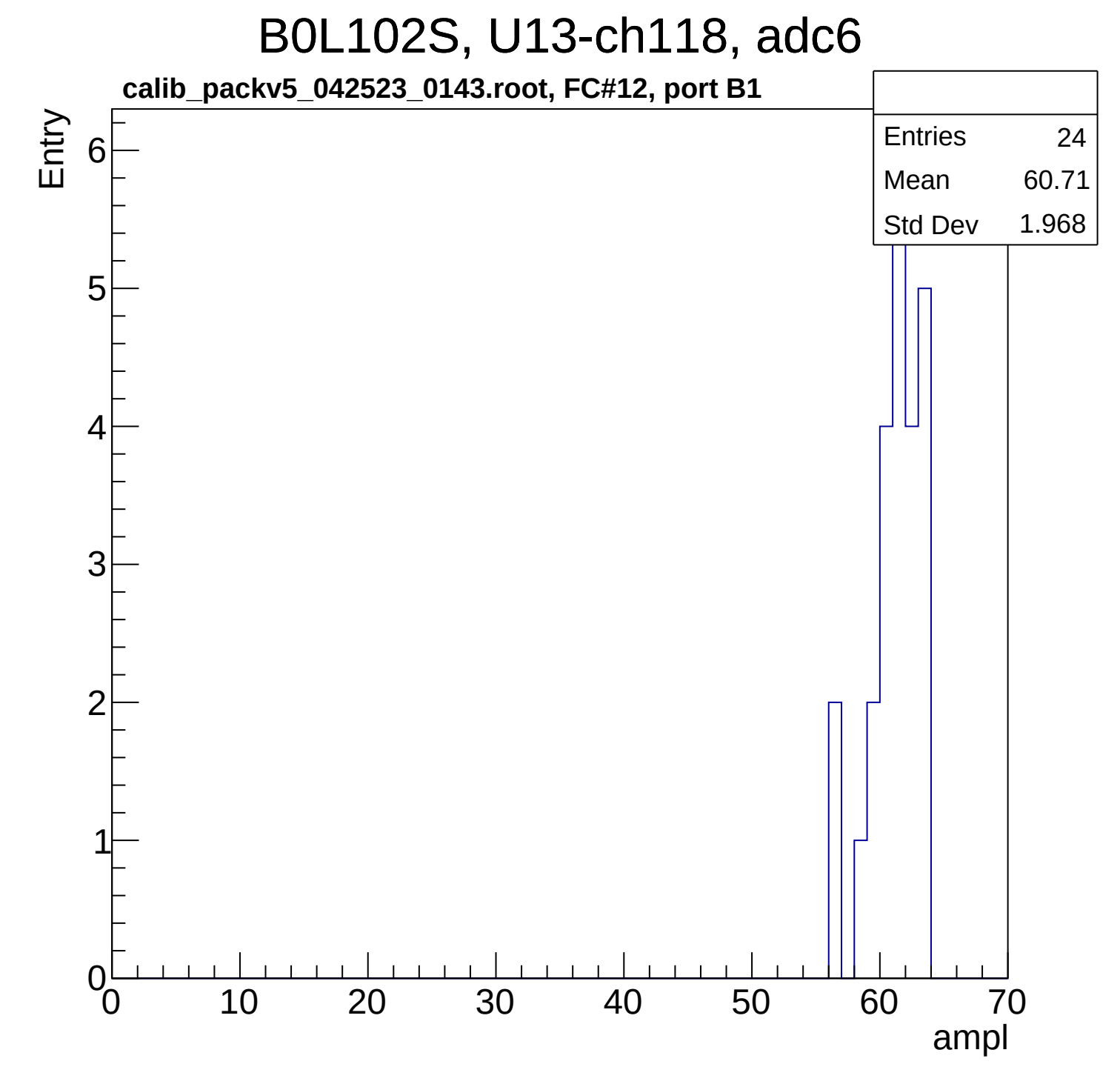
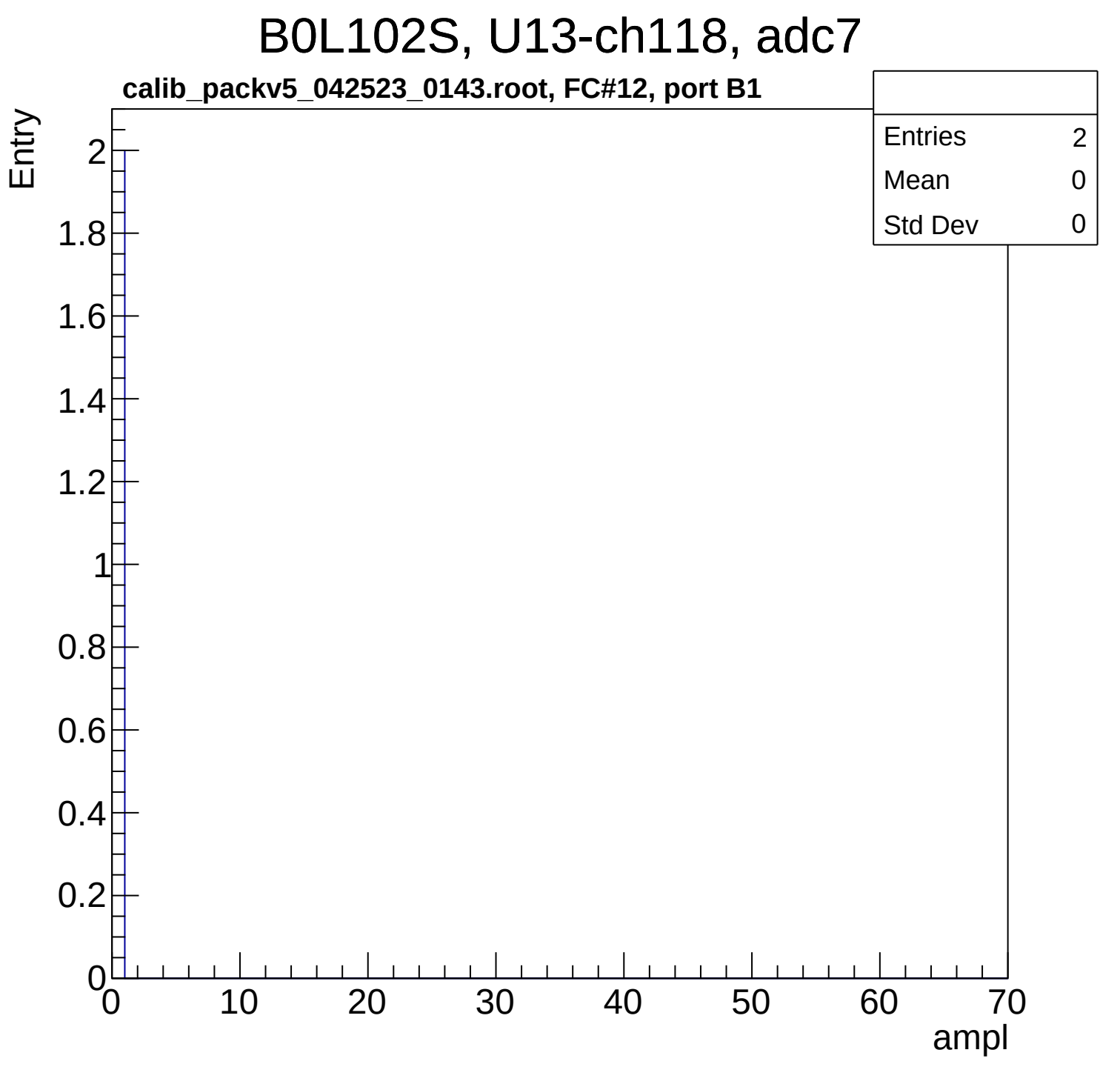
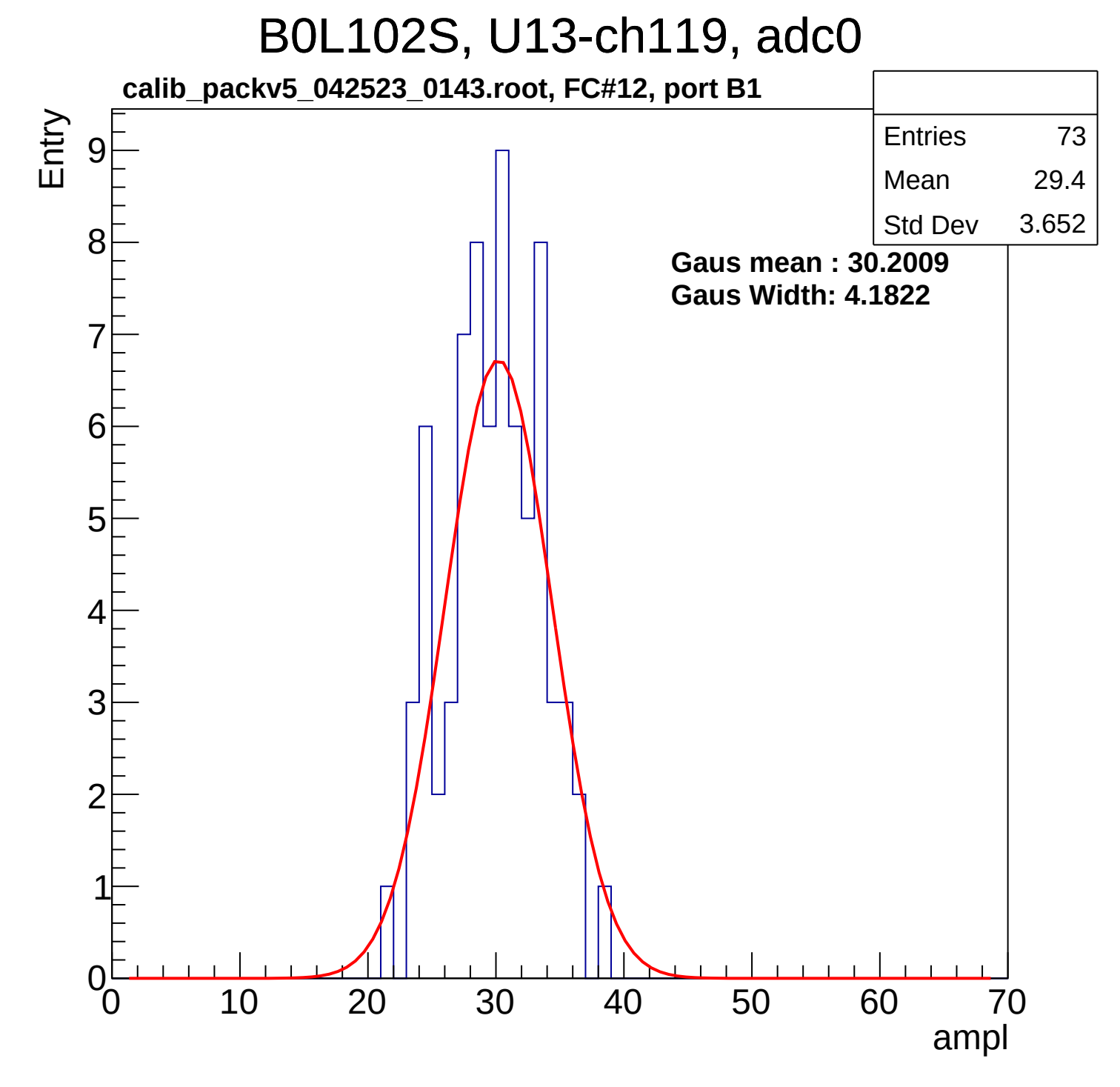
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch118, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 24
Mean 60.71
Std Dev 1.968
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch118, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch119, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.4
Std Dev 3.652
Gaus mean : 30.2009
Gaus Width: 4.1822
Entry
ampl
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2
3
4
5
6
7
8
9
0
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30
40
50
60
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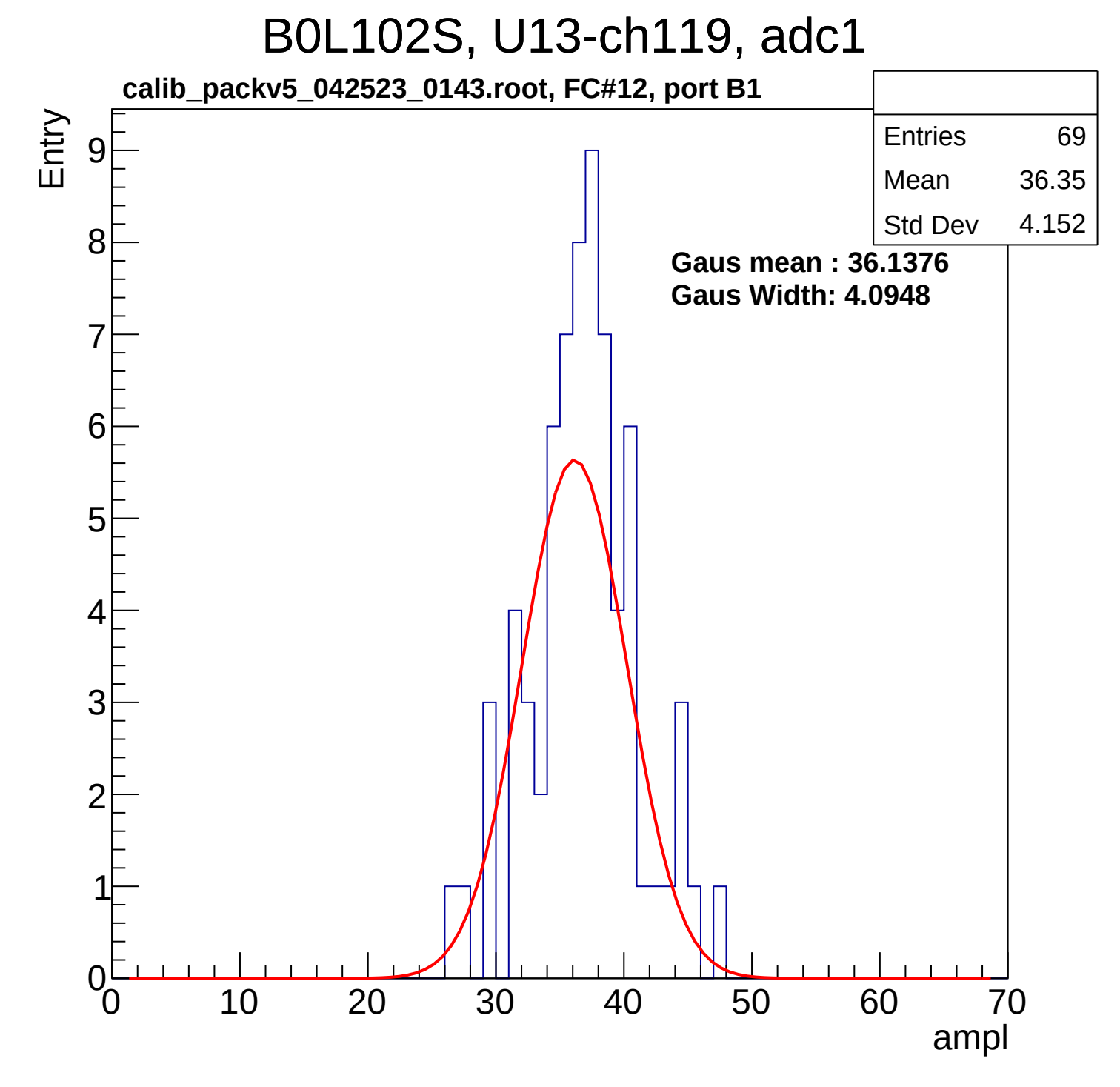
B0L102S, U13-ch119, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 36.35
Std Dev 4.152
Gaus mean : 36.1376
Gaus Width: 4.0948
Entry
ampl
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3
4
5
6
7
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0
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60
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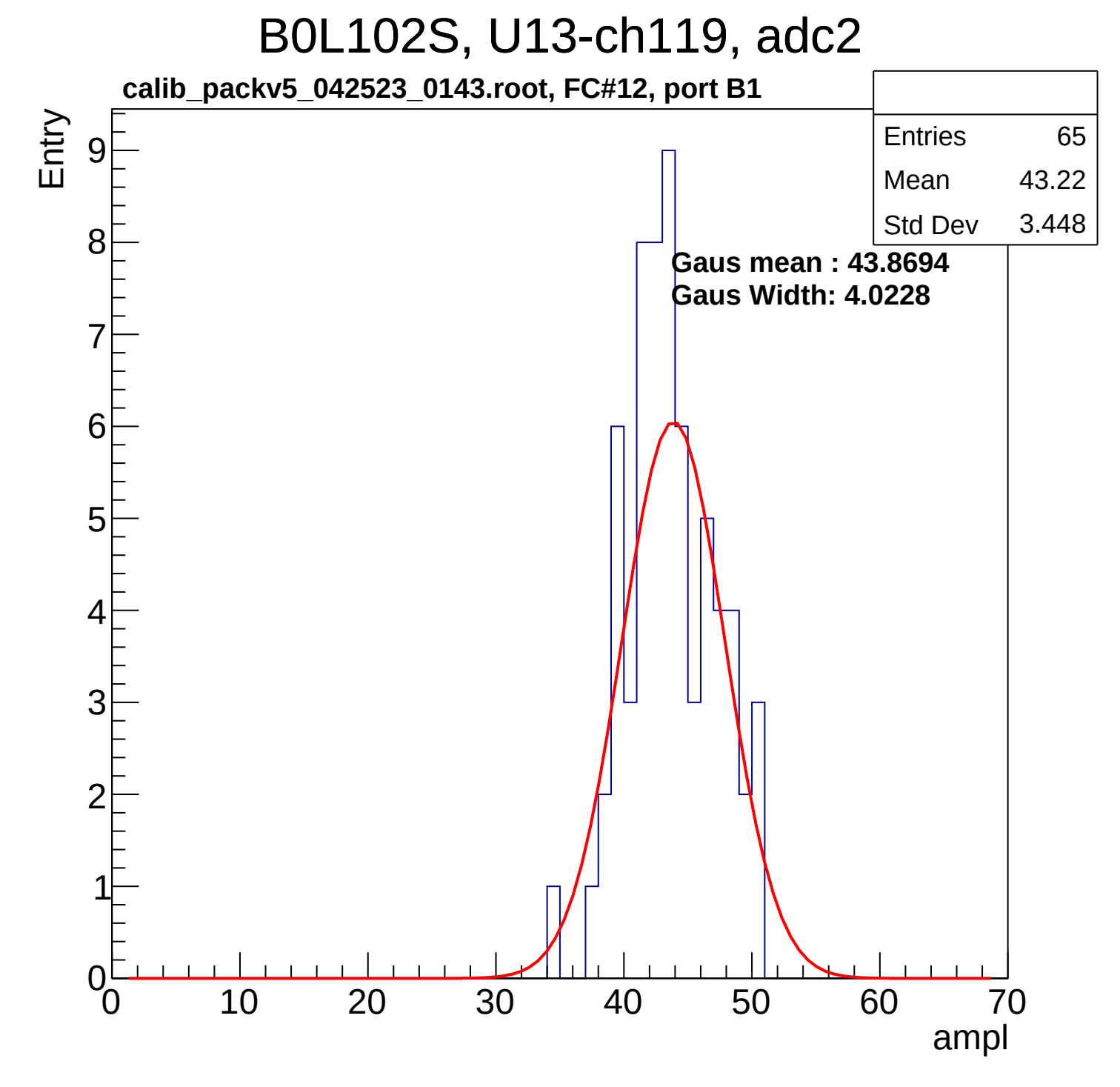
B0L102S, U13-ch119, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.22
Std Dev 3.448
Gaus mean : 43.8694
Gaus Width: 4.0228
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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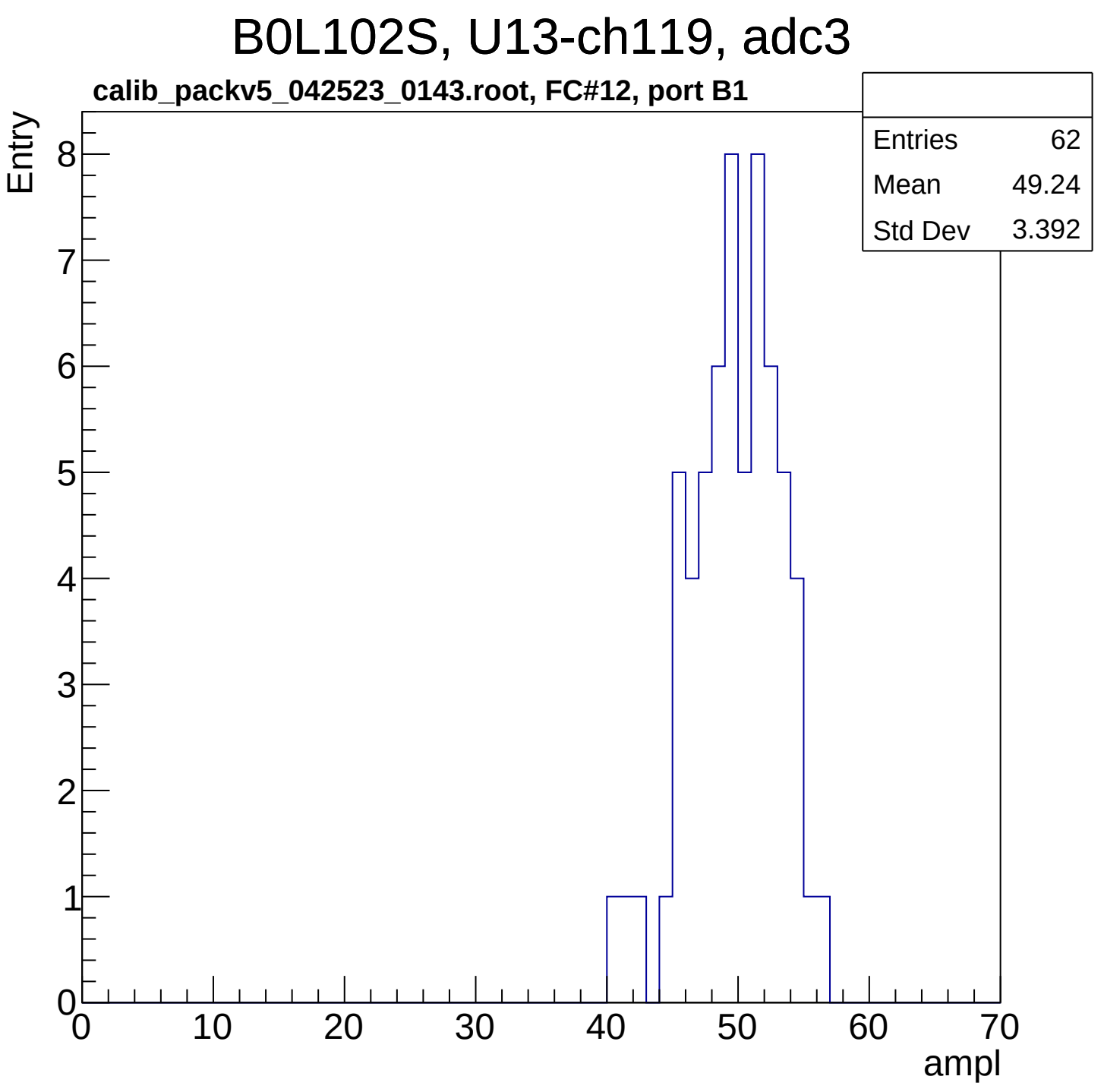
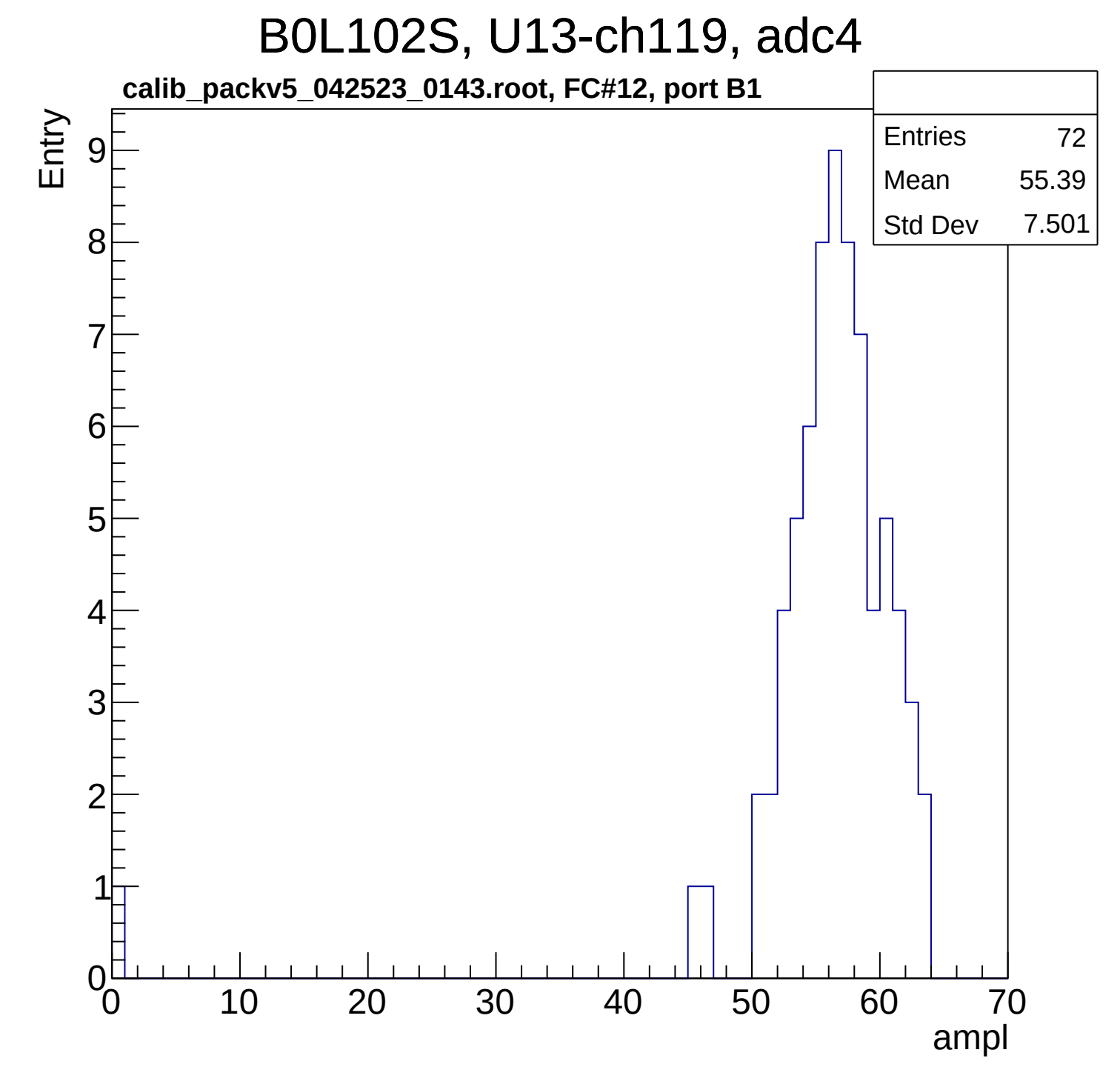
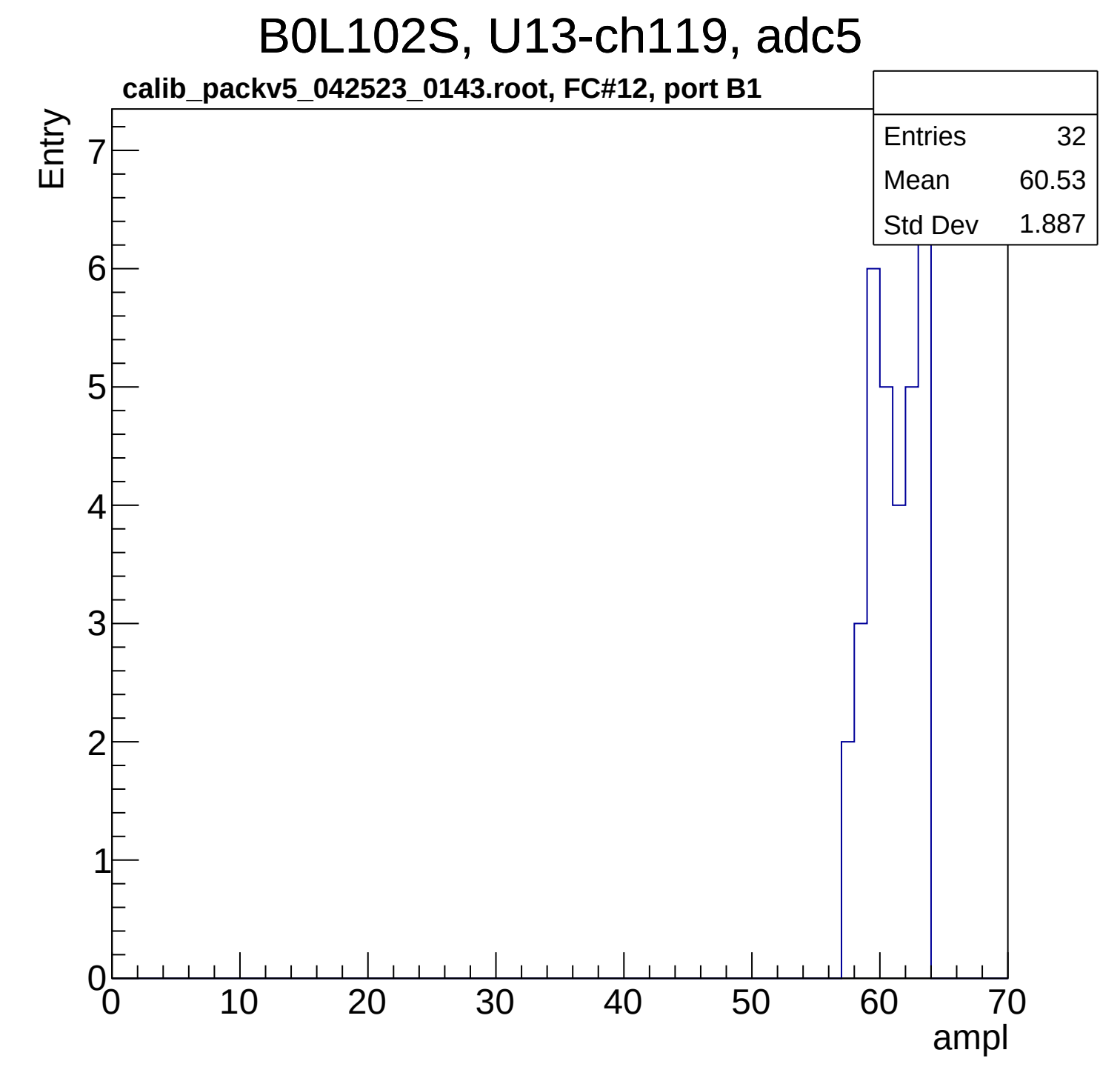

B0L102S, U13-ch119, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 49.24
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch119, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 55.39
Std Dev 7.501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch119, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 60.53
Std Dev 1.887
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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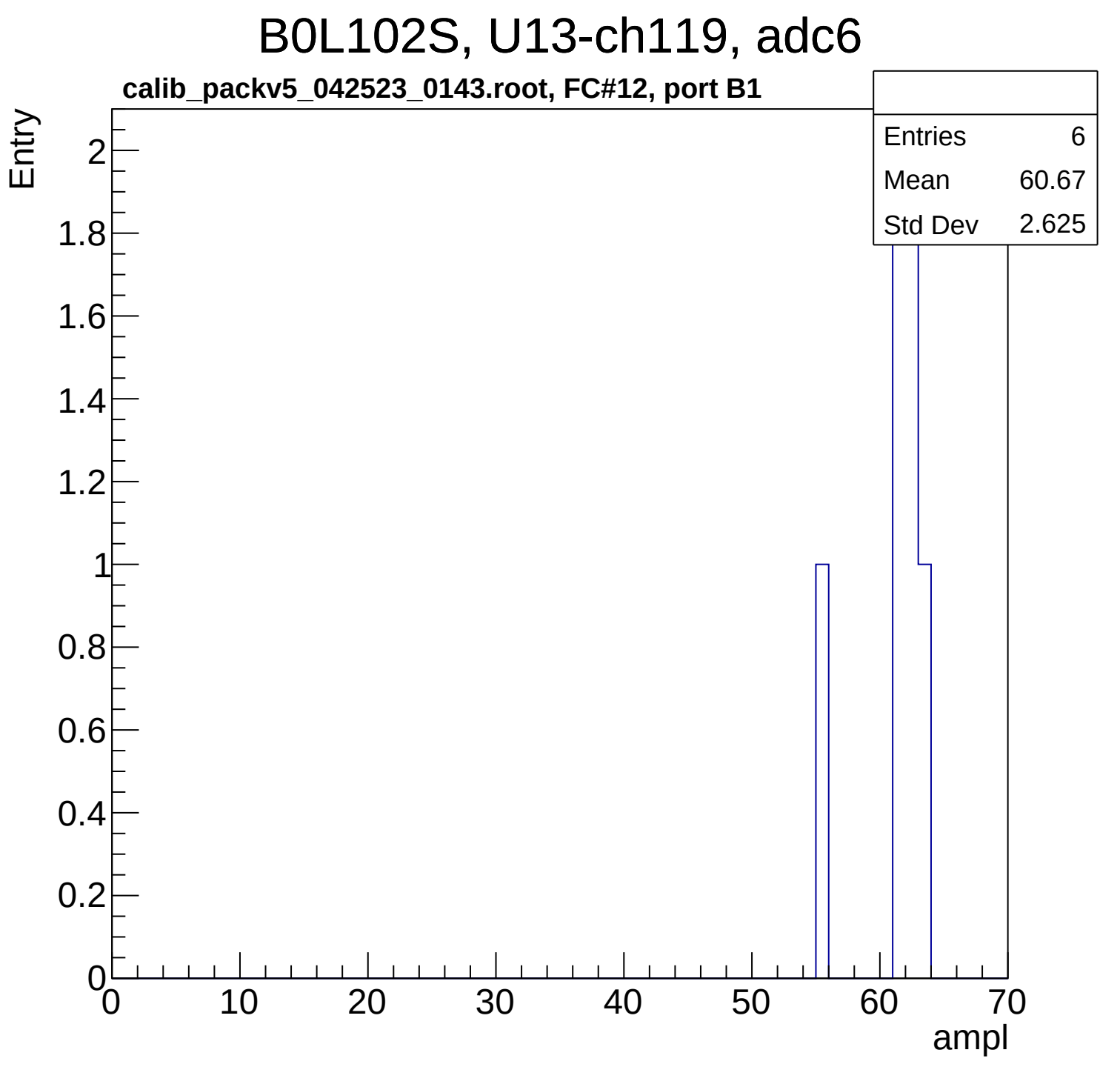
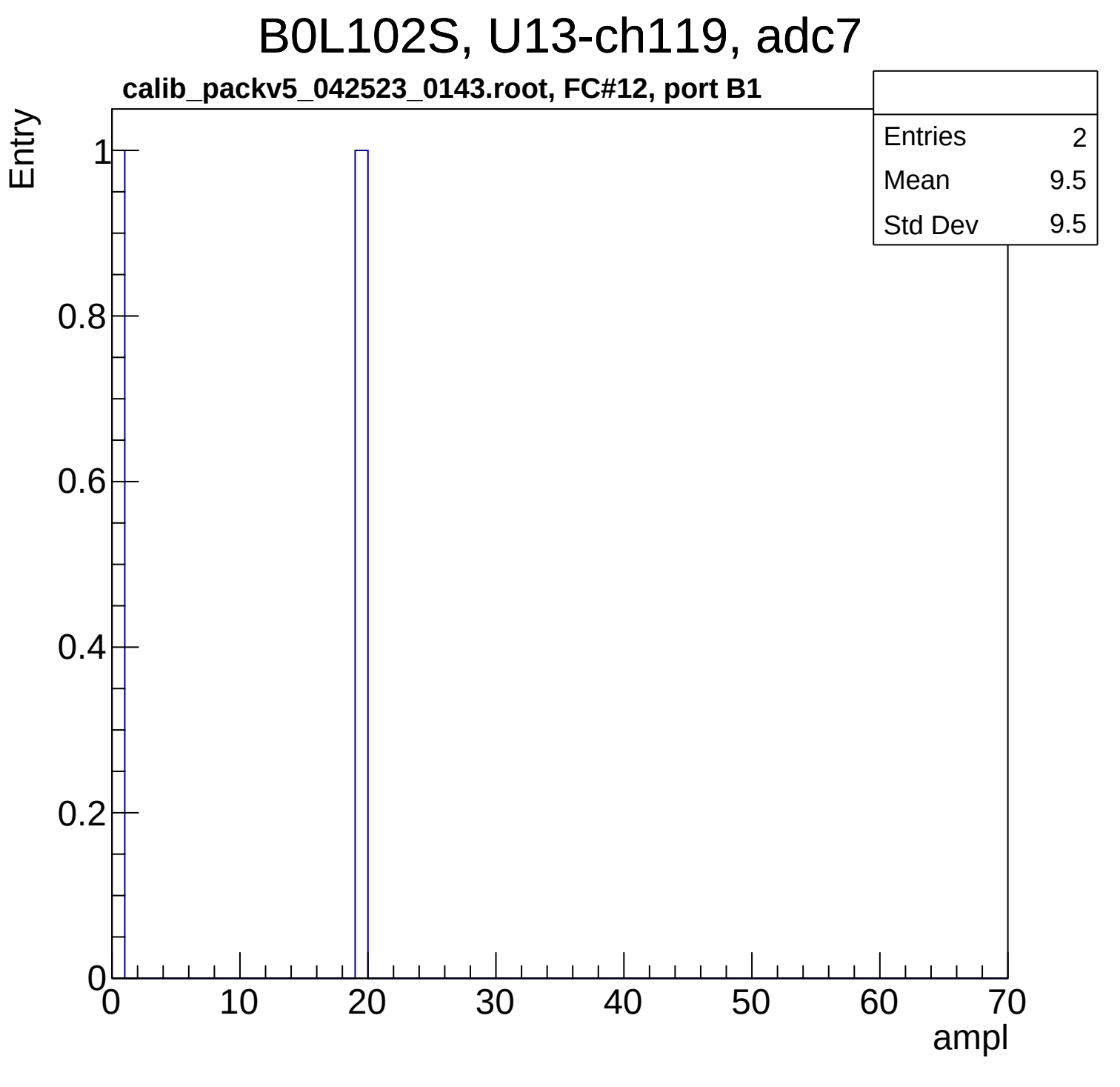
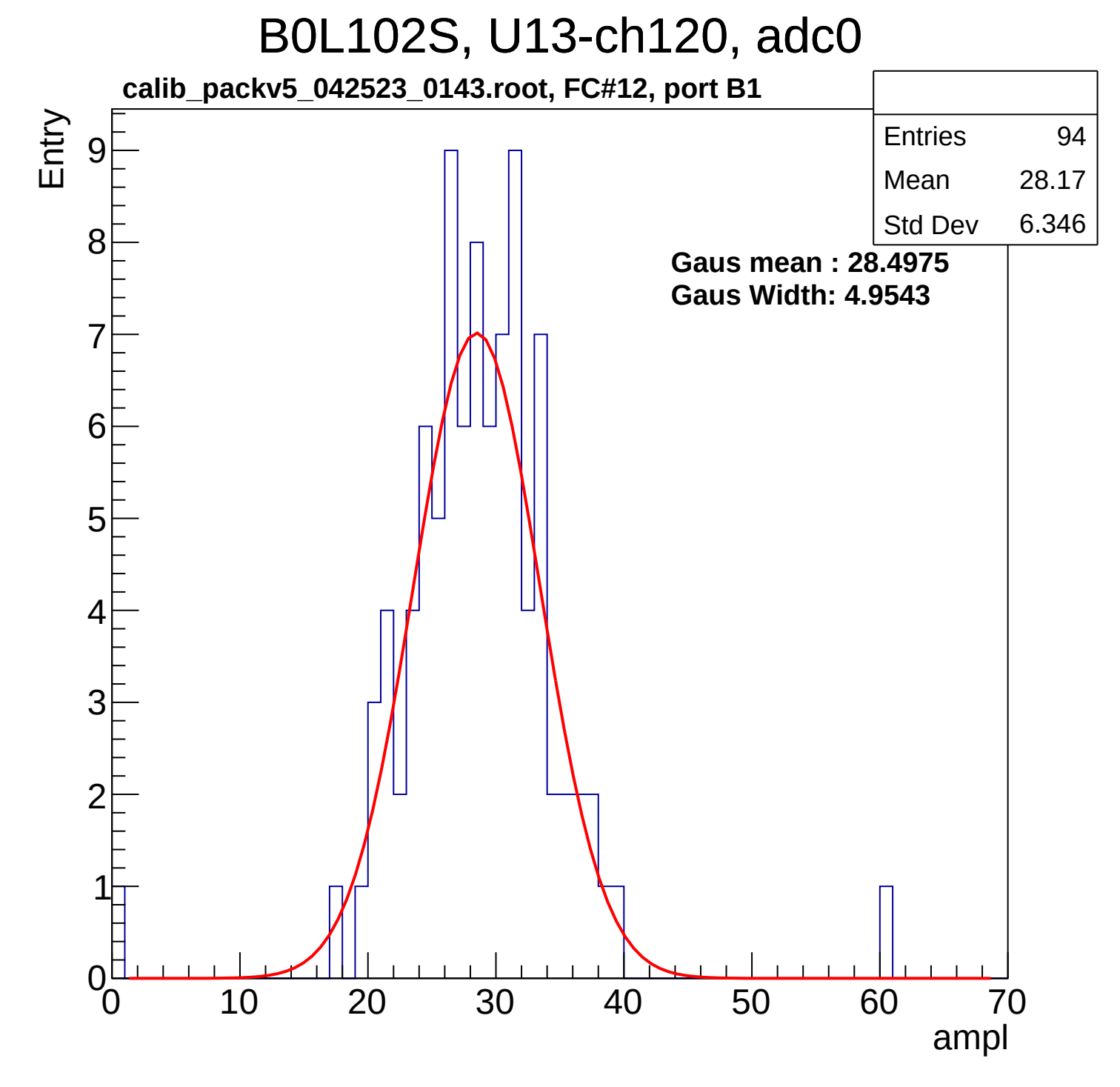
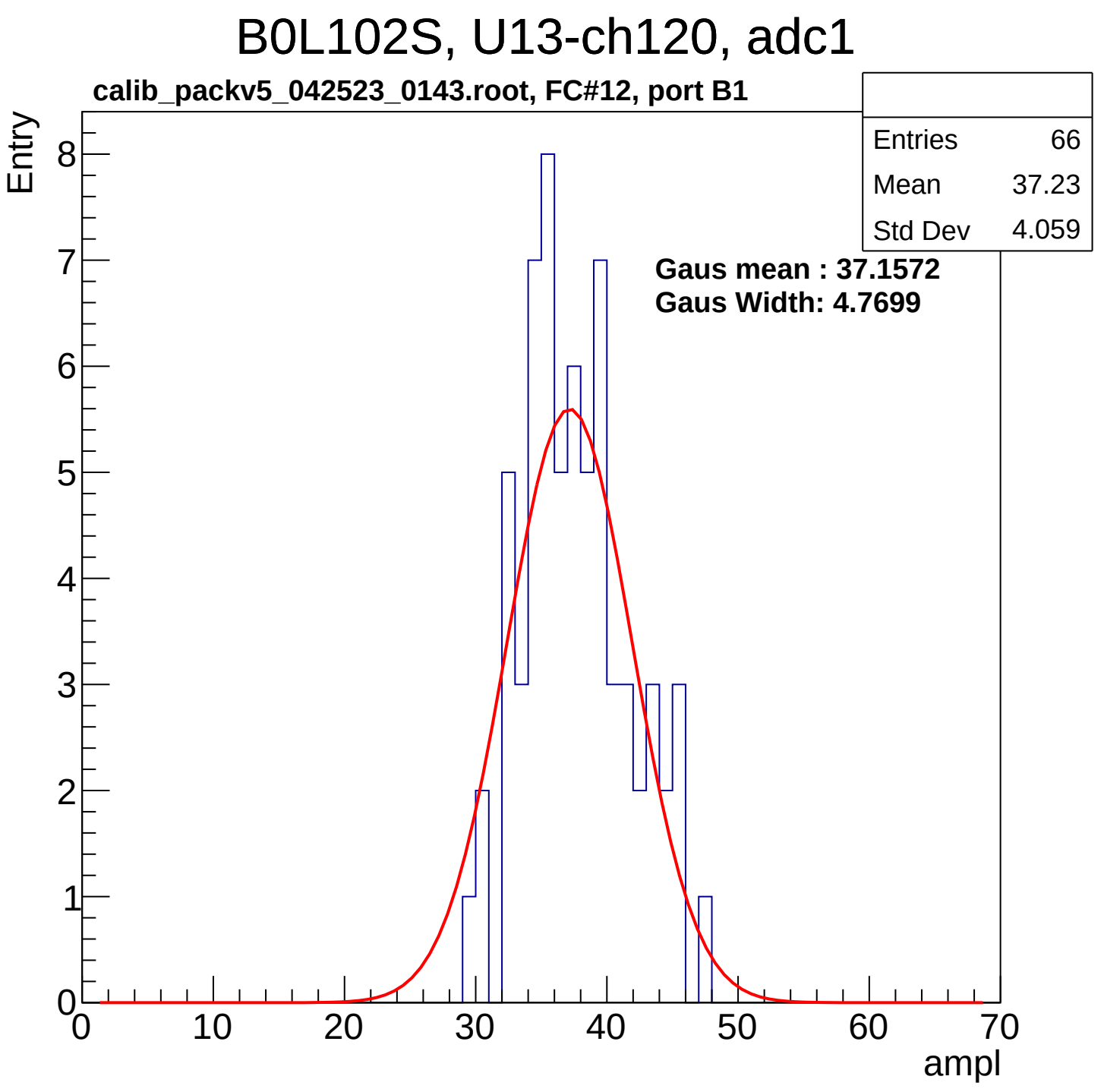
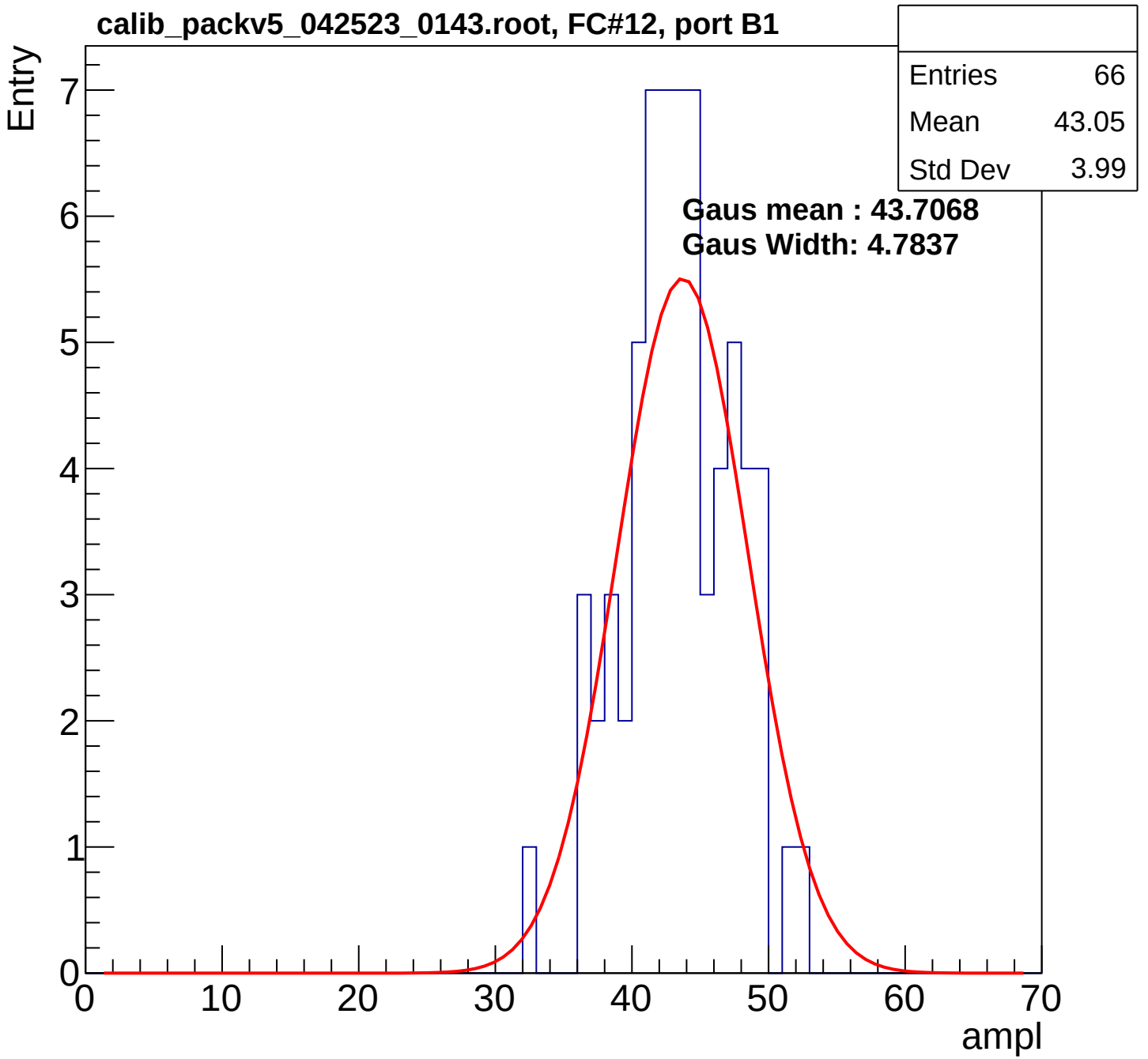

B0L102S, U13-ch119, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 60.67
Std Dev 2.625
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch119, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch120, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 94
Mean 28.17
Std Dev 6.346
Gaus mean : 28.4975
Gaus Width: 4.9543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch120, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.23
Std Dev 4.059
Gaus mean : 37.1572
Gaus Width: 4.7699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch120, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.05
Std Dev 3.99
Gaus mean : 43.7068
Gaus Width: 4.7837
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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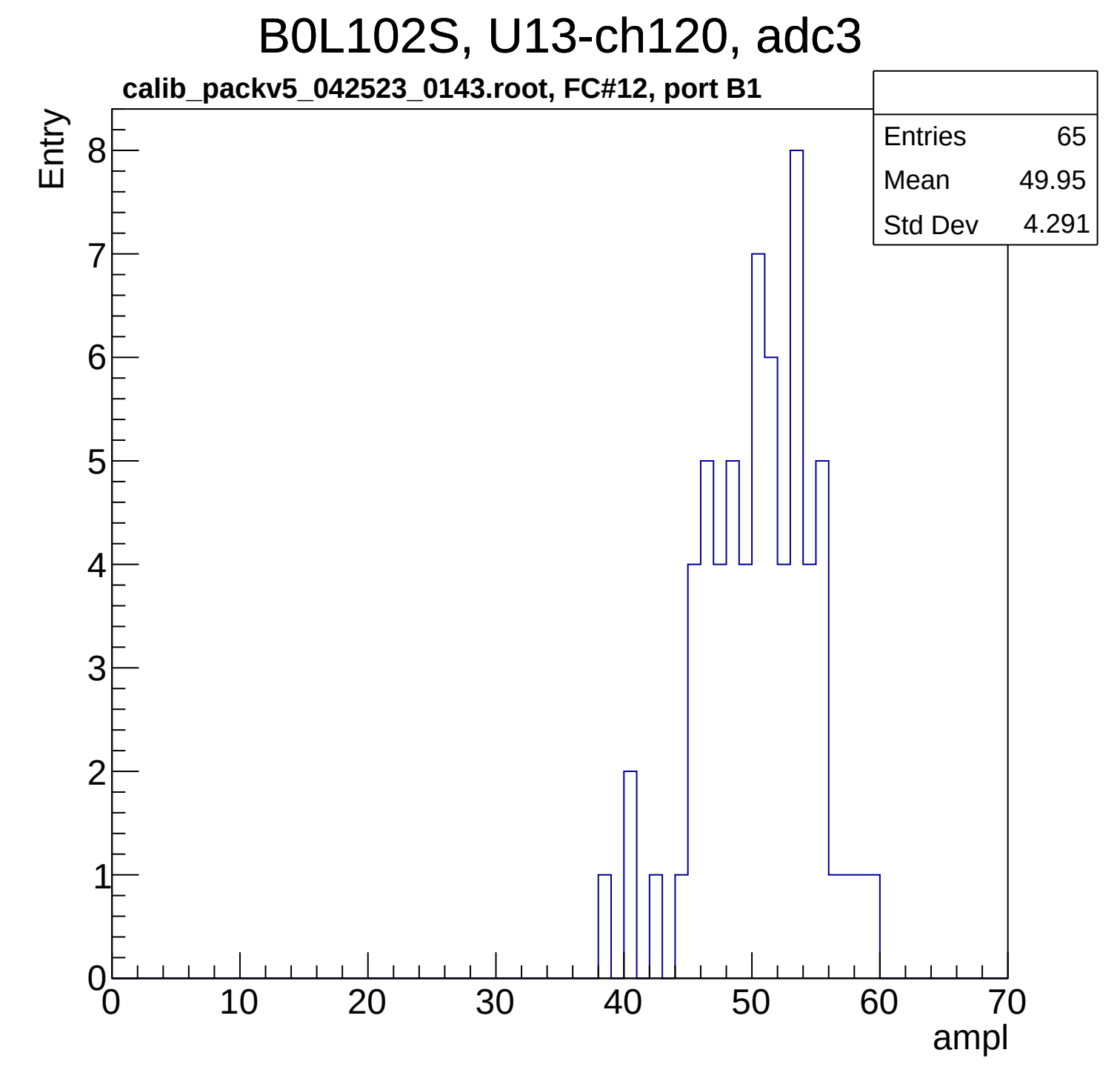
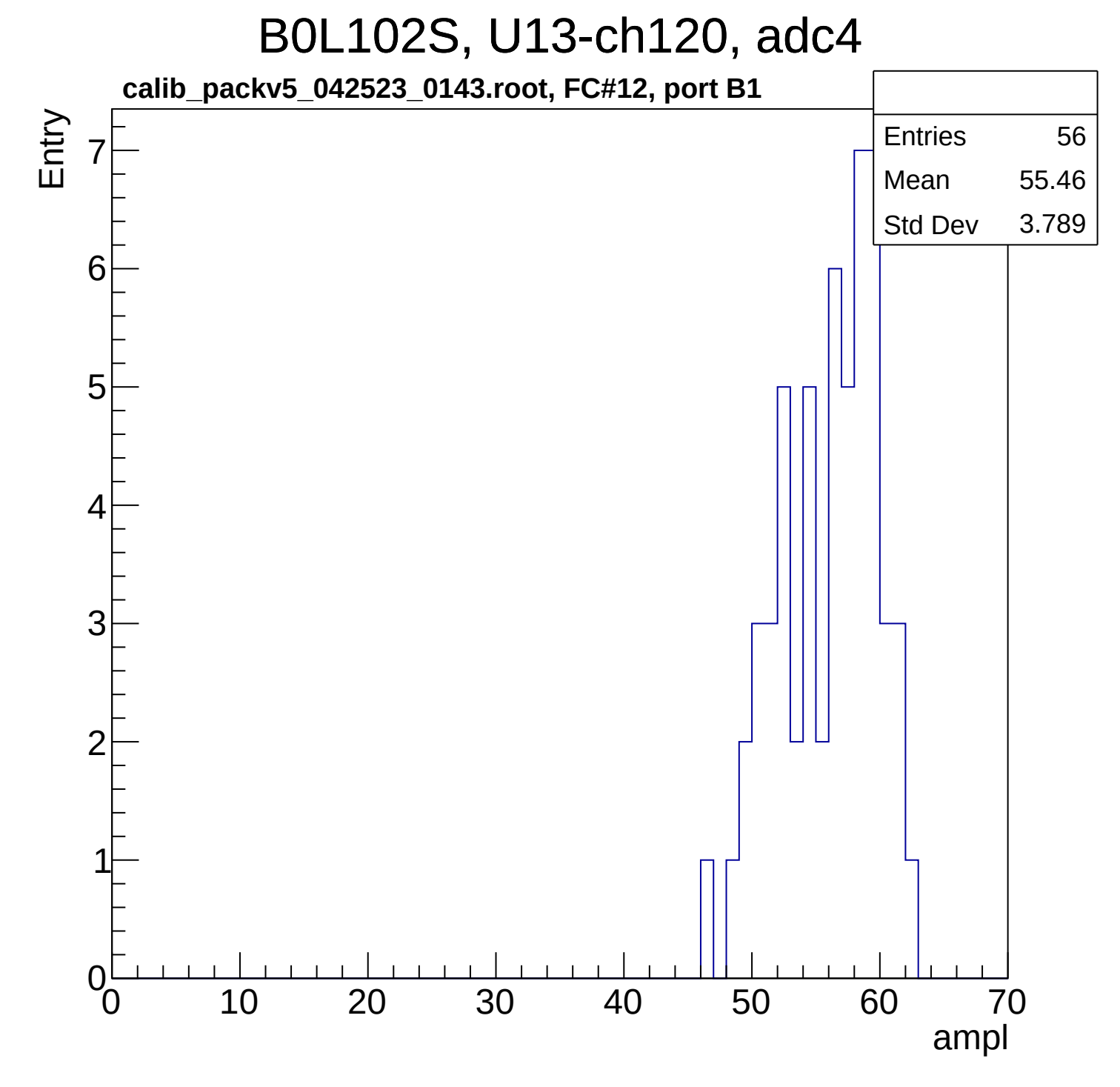
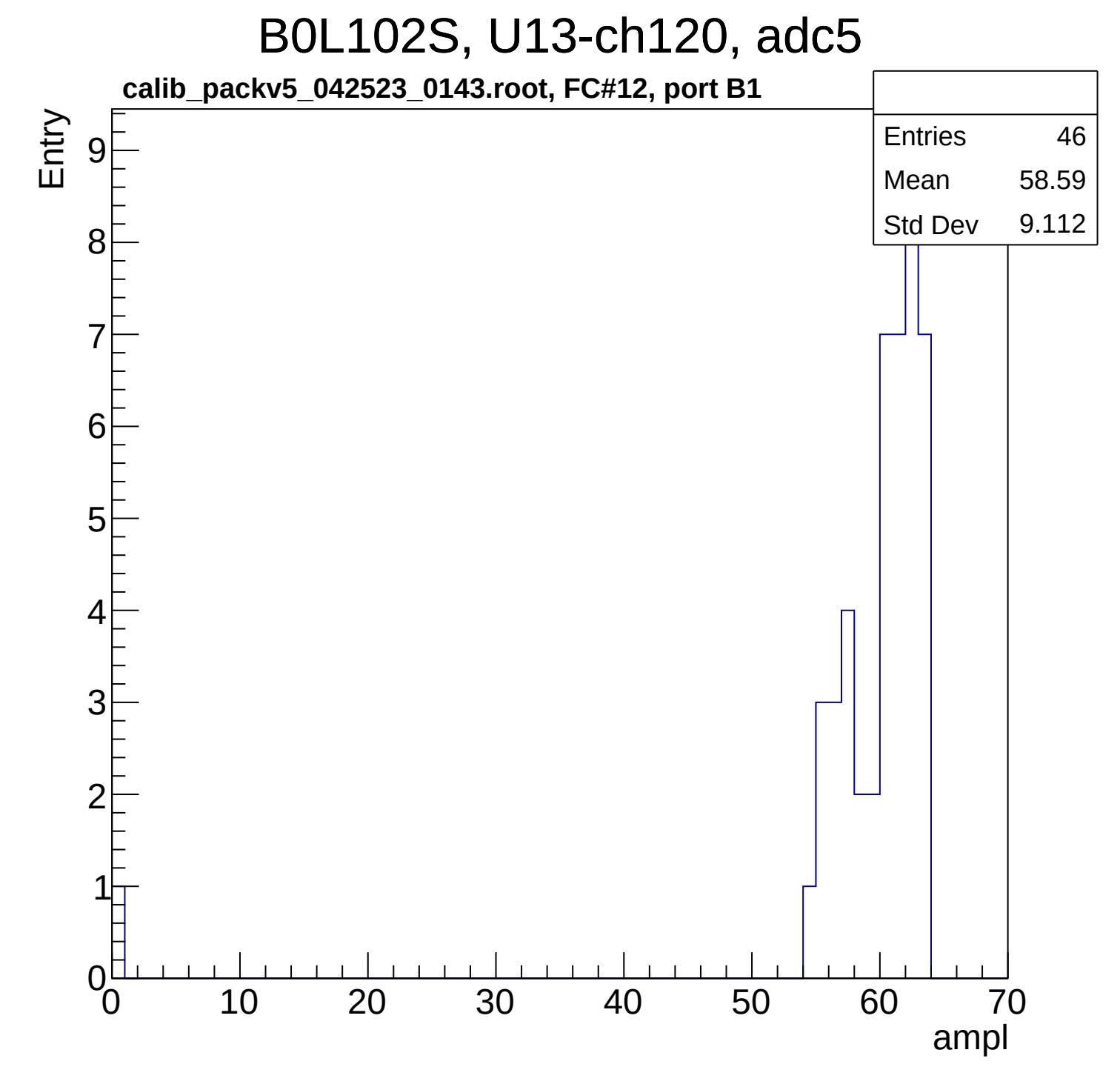
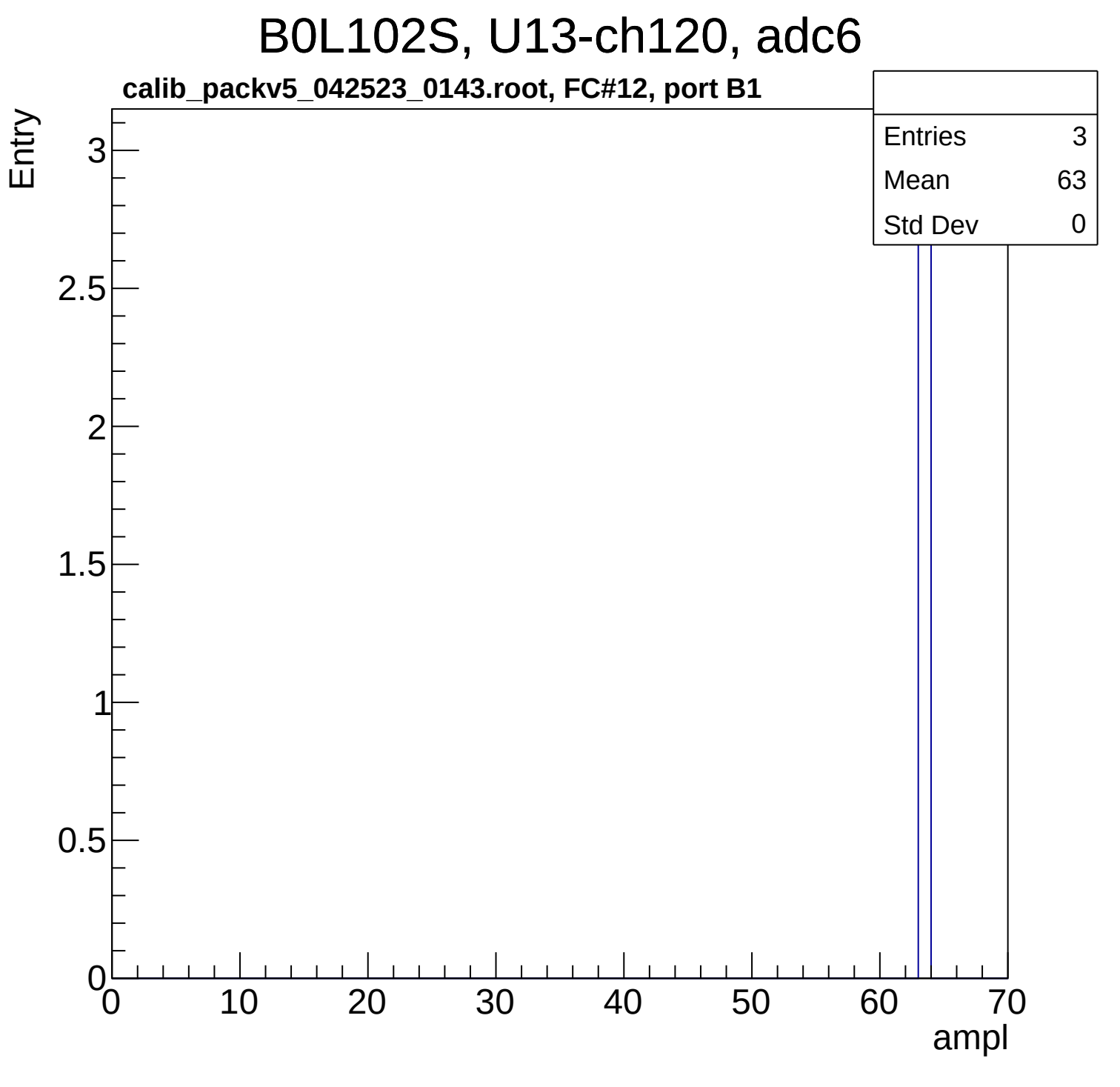
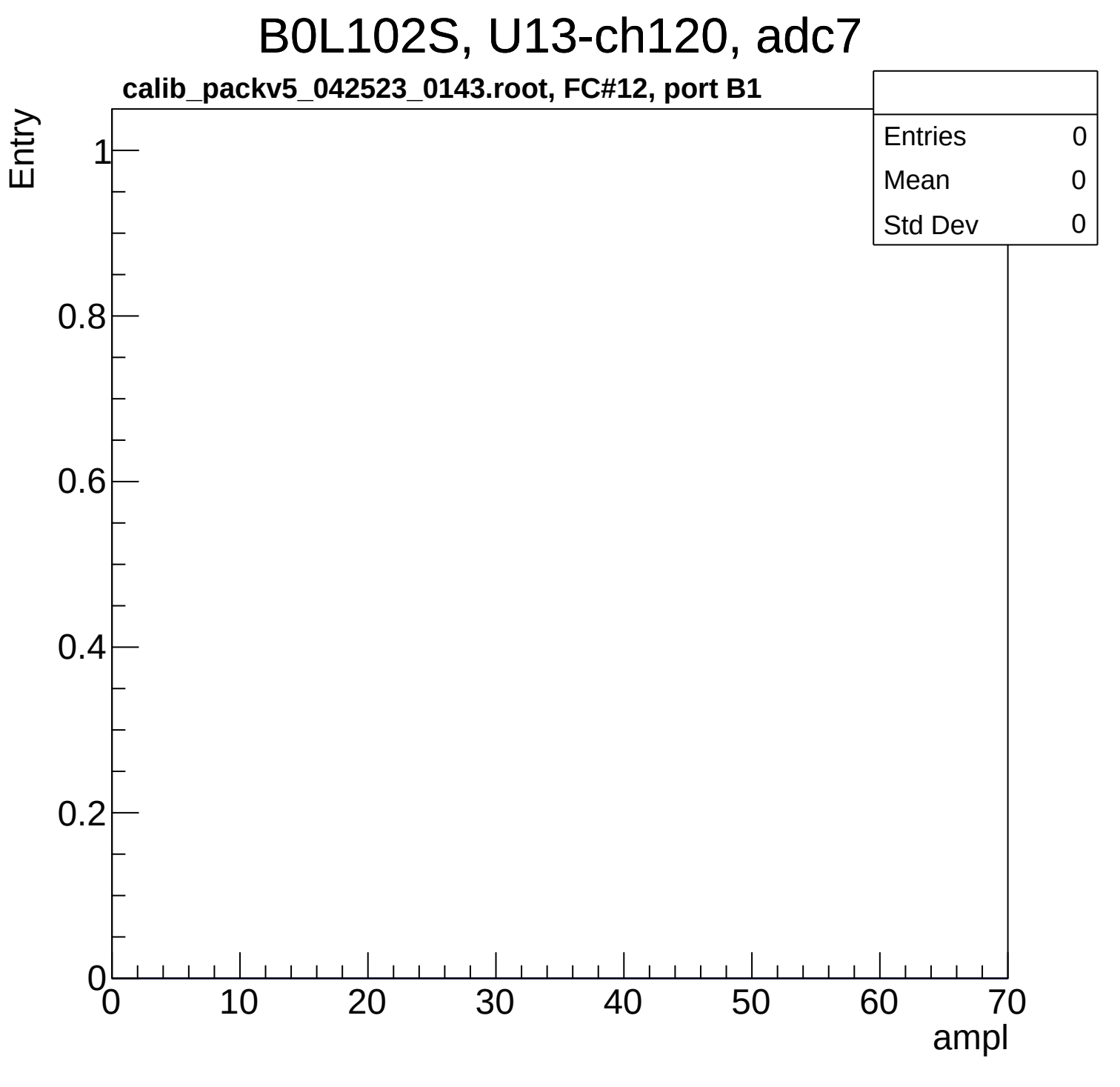
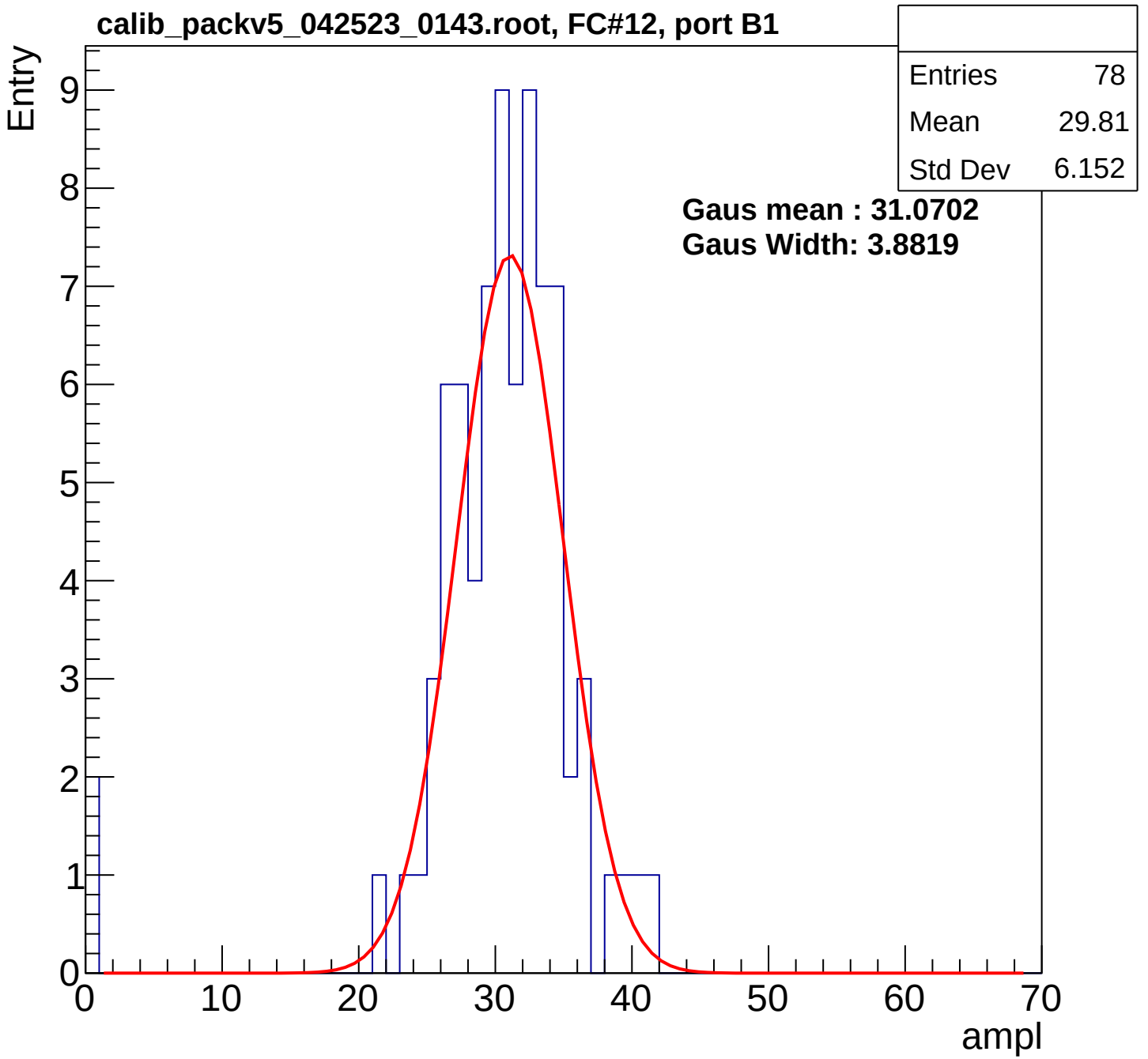

B0L102S, U13-ch120, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.95
Std Dev 4.291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

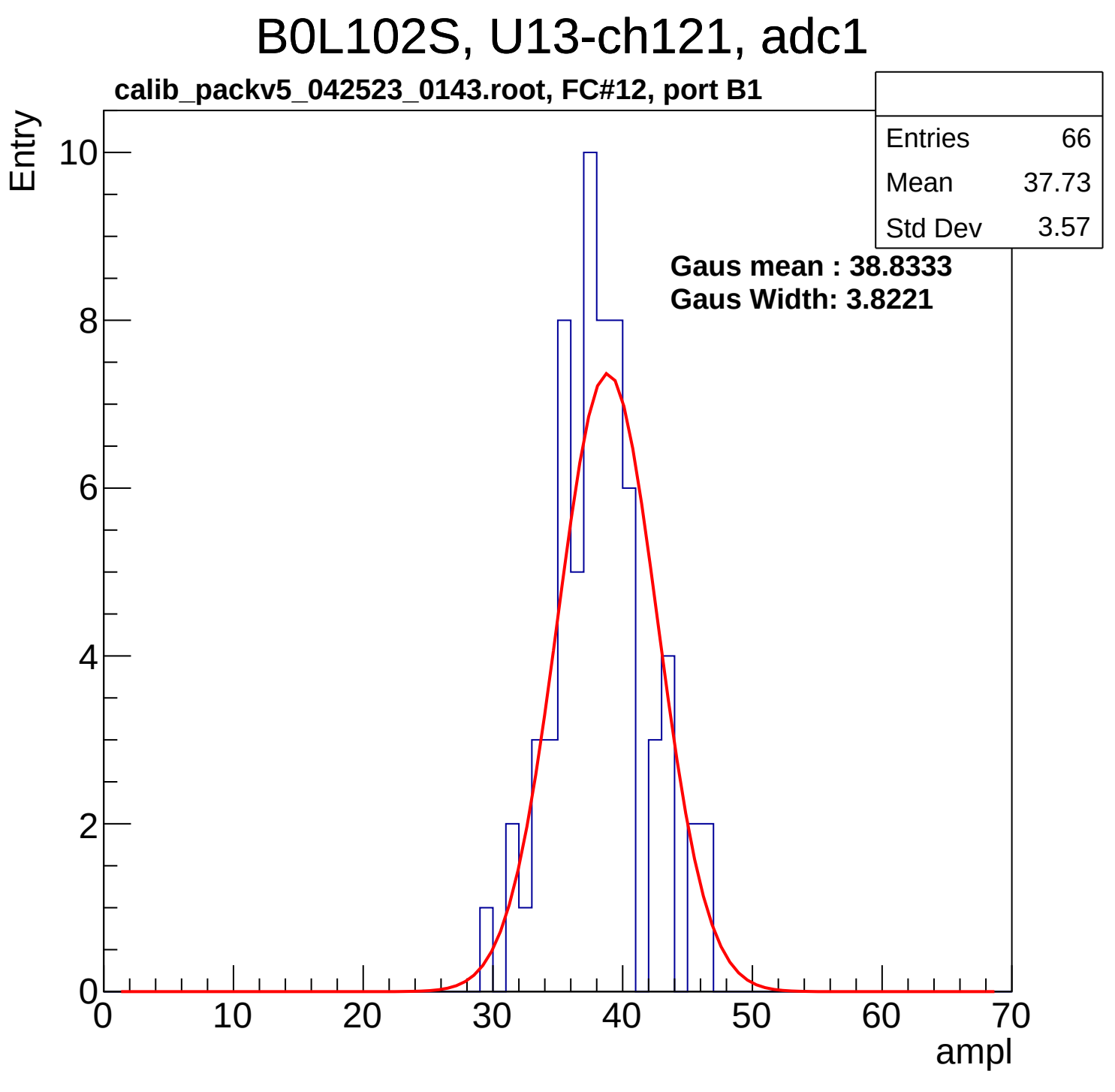

B0L102S, U13-ch120, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.46
Std Dev 3.789
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

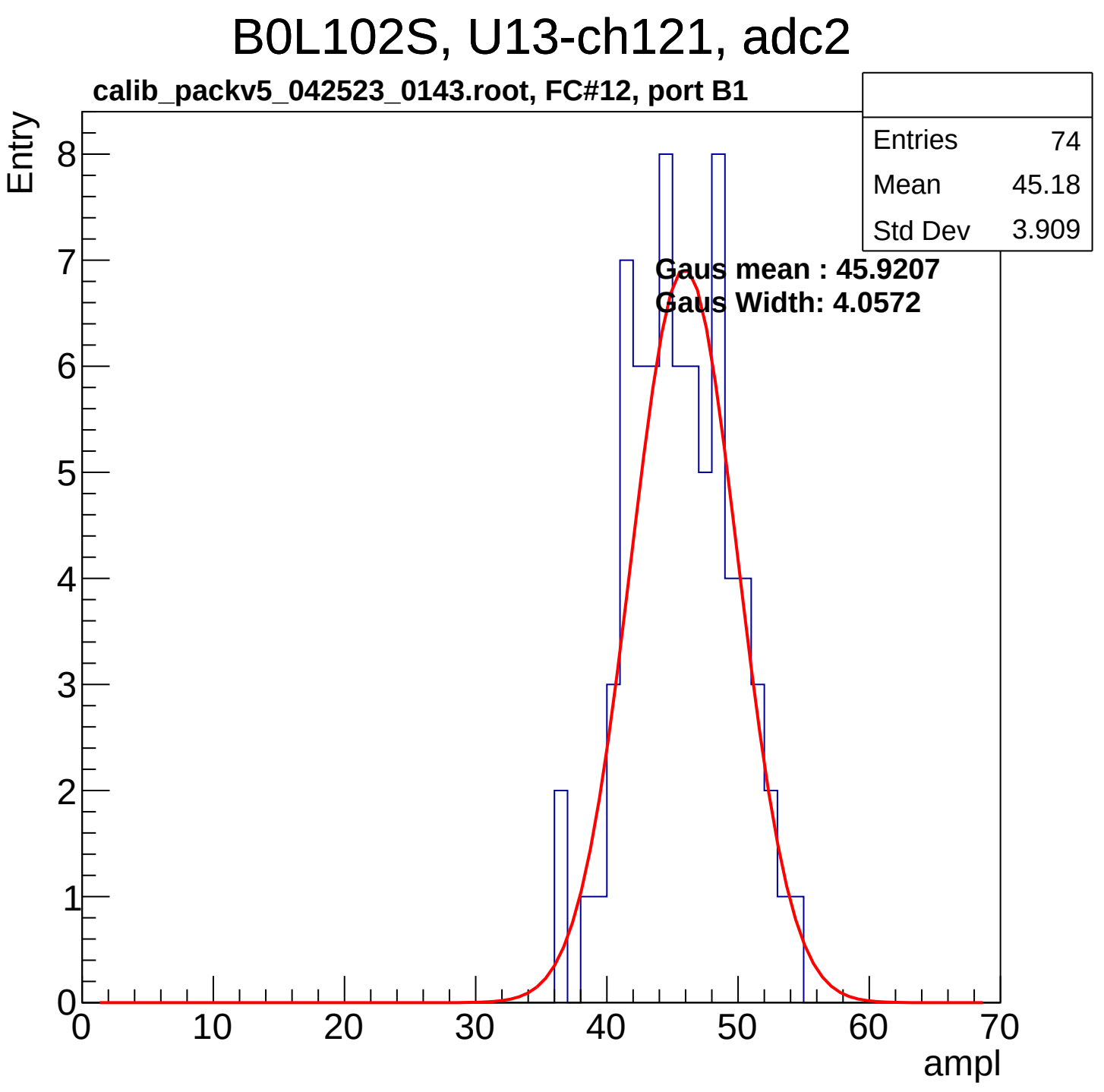

B0L102S, U13-ch120, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.59
Std Dev 9.112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch120, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch120, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch121, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.81
Std Dev 6.152
Gaus mean : 31.0702
Gaus Width: 3.8819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch121, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.73
Std Dev 3.57
Gaus mean : 38.8333
Gaus Width: 3.8221
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch121, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 45.18
Std Dev 3.909
Gaus mean : 45.9207
Gaus Width: 4.0572
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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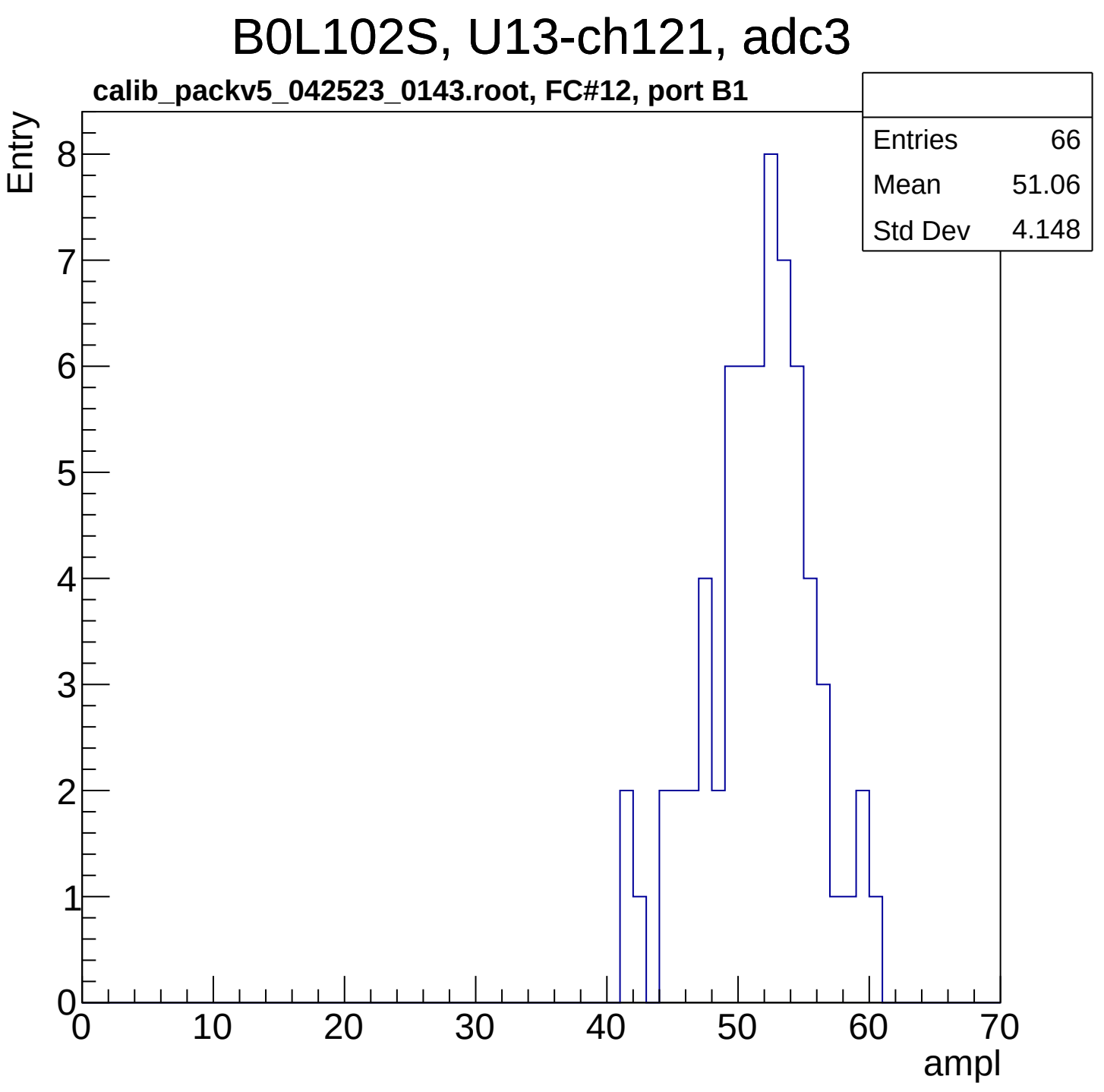
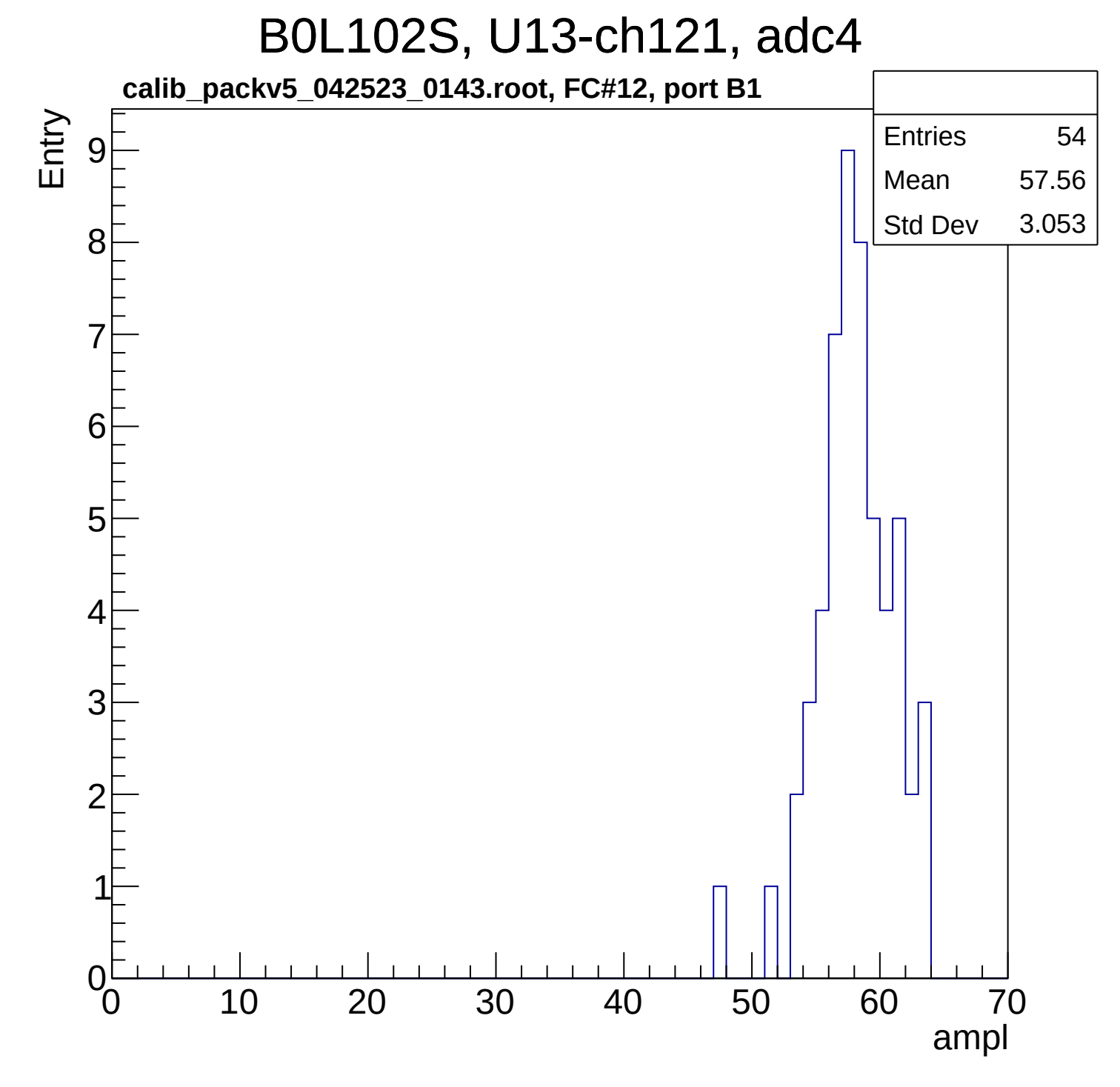
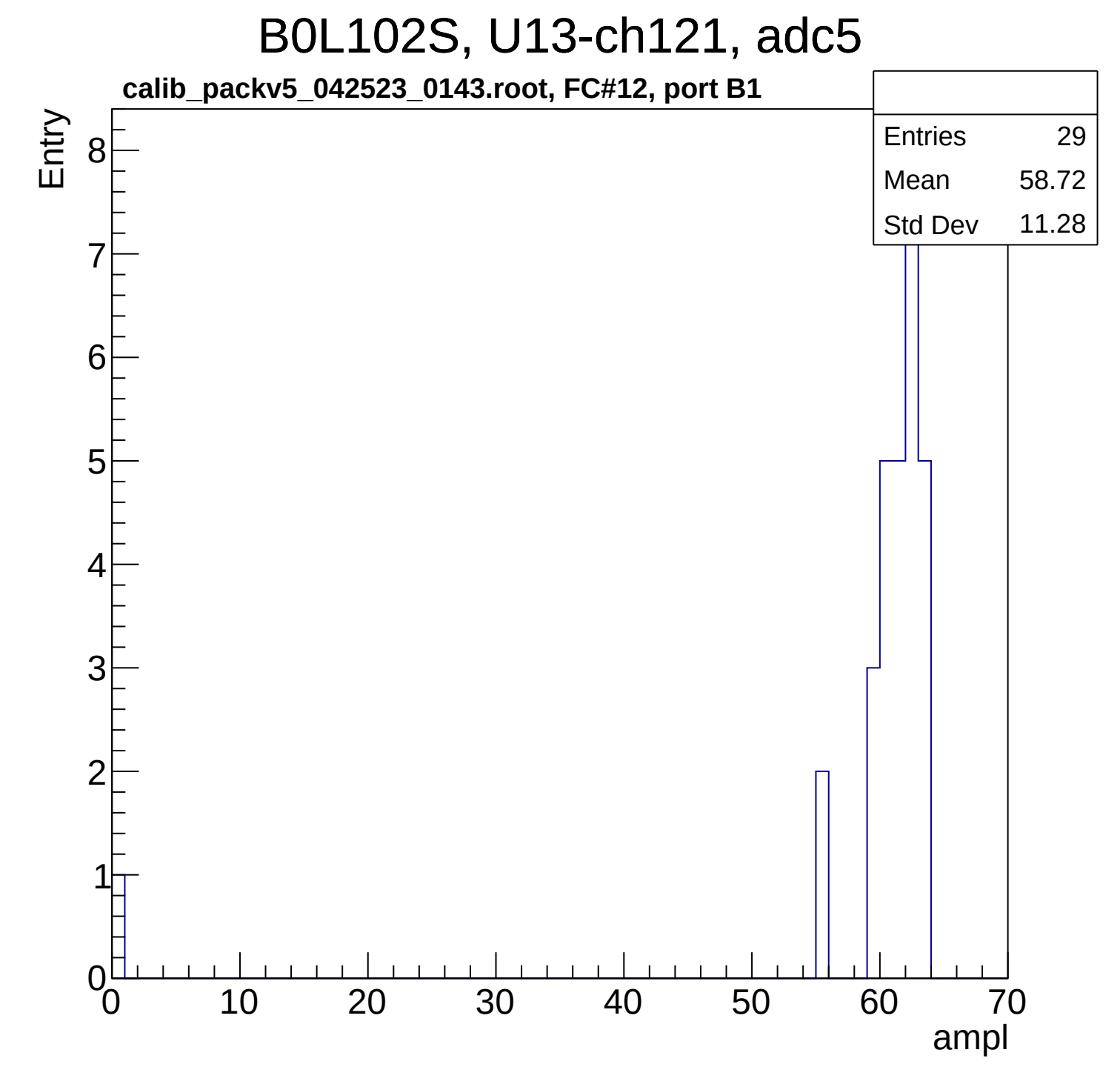

B0L102S, U13-ch121, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 51.06
Std Dev 4.148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch121, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57.56
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch121, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 58.72
Std Dev 11.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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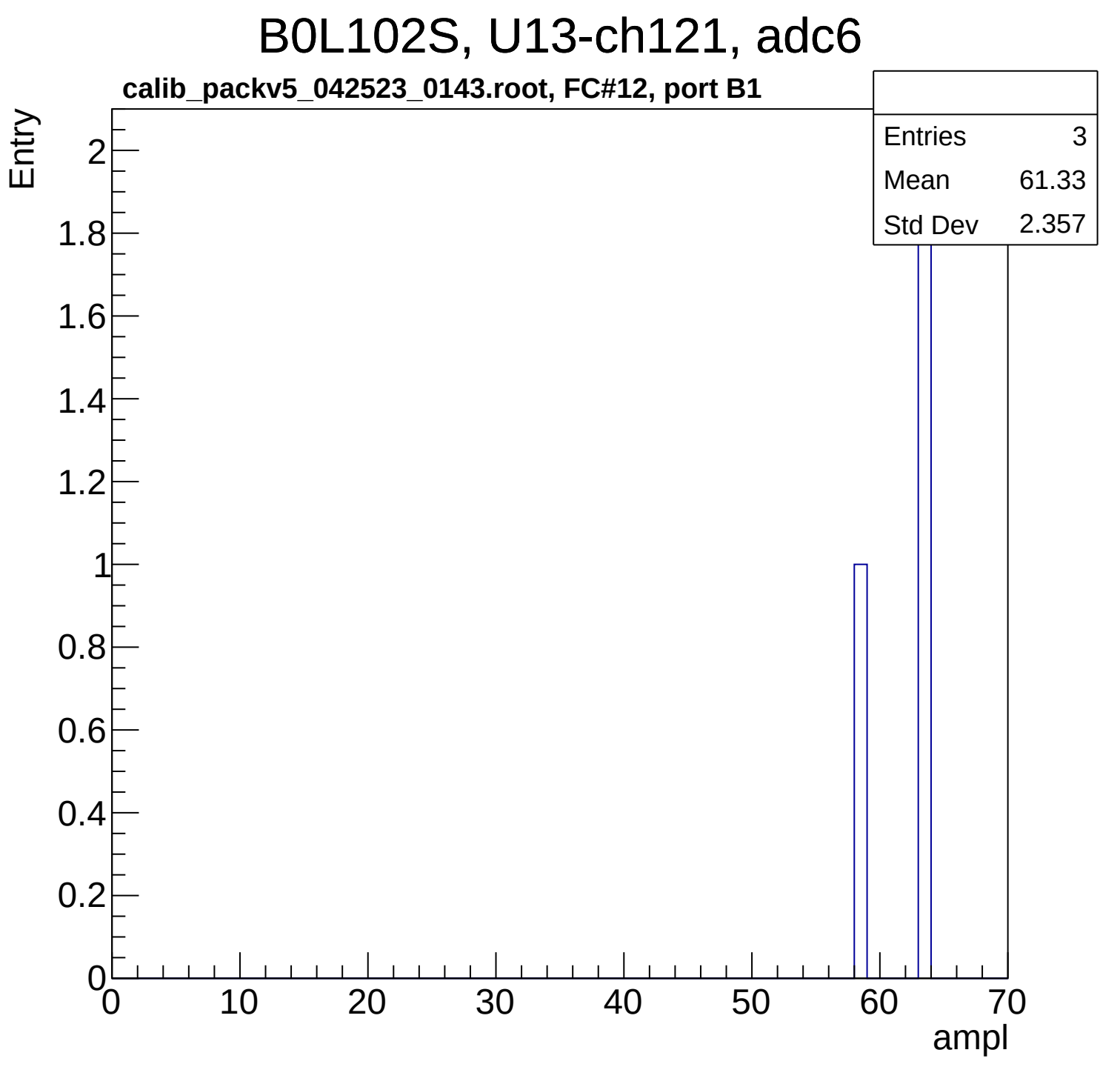
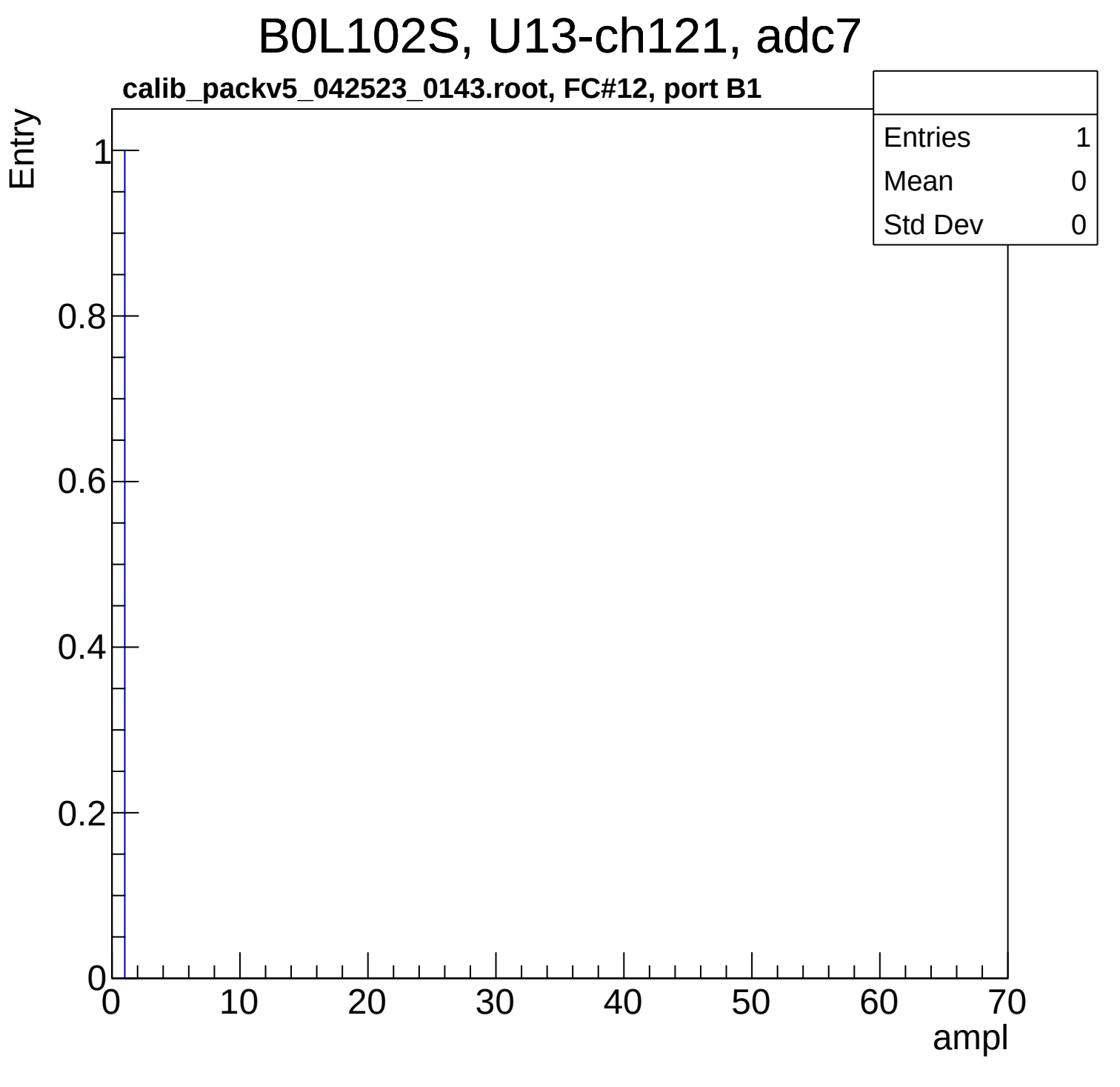
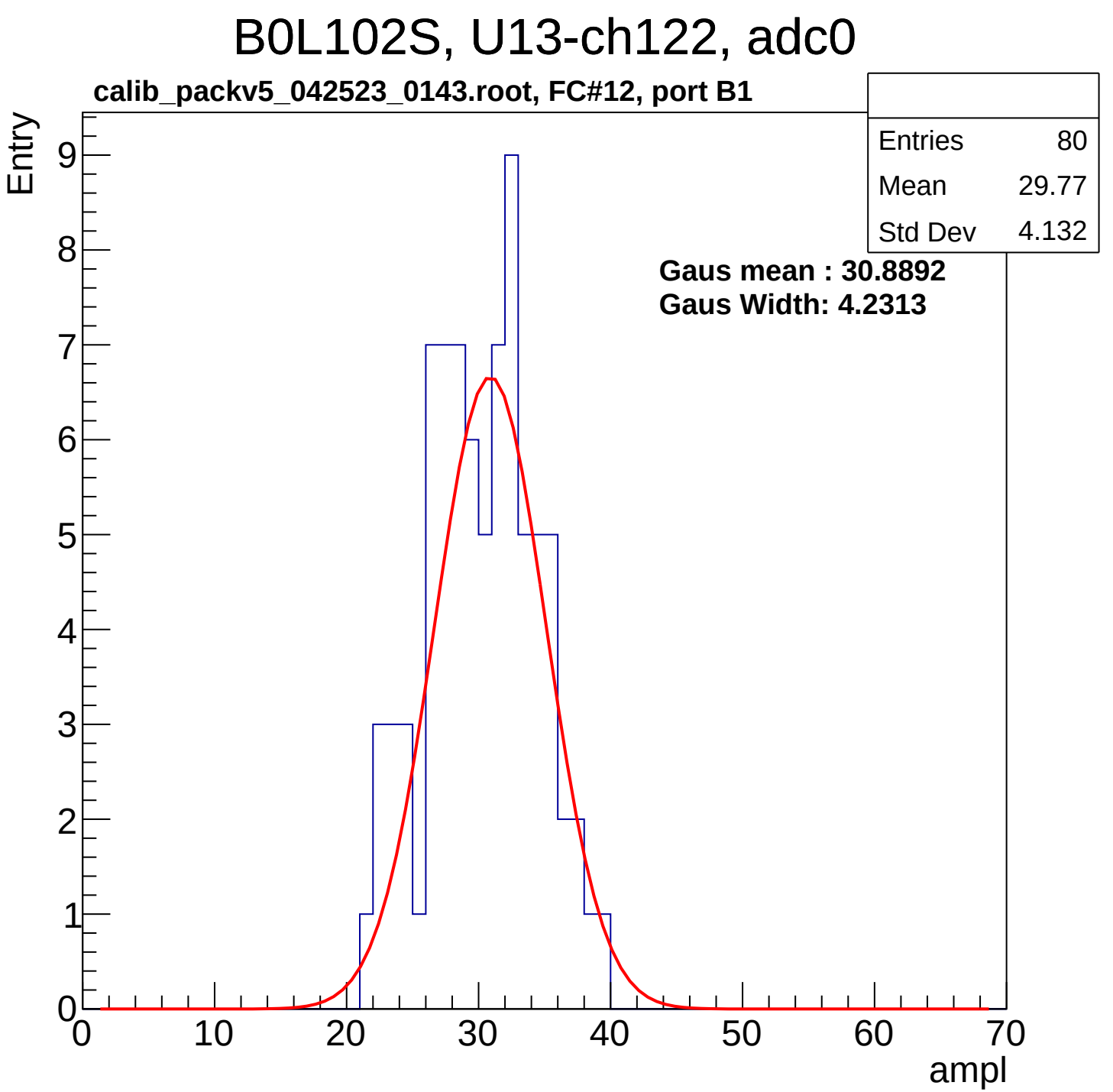
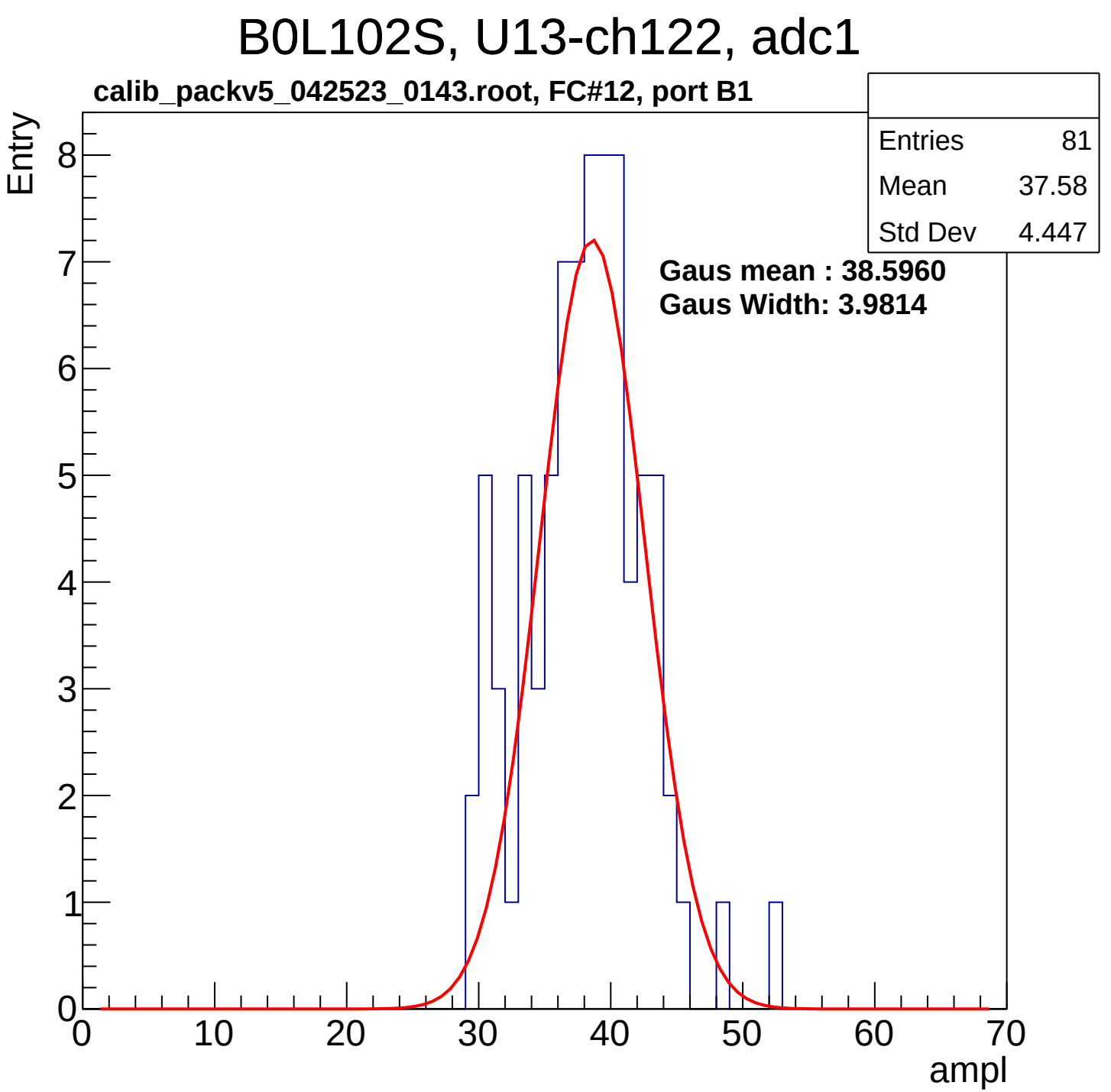
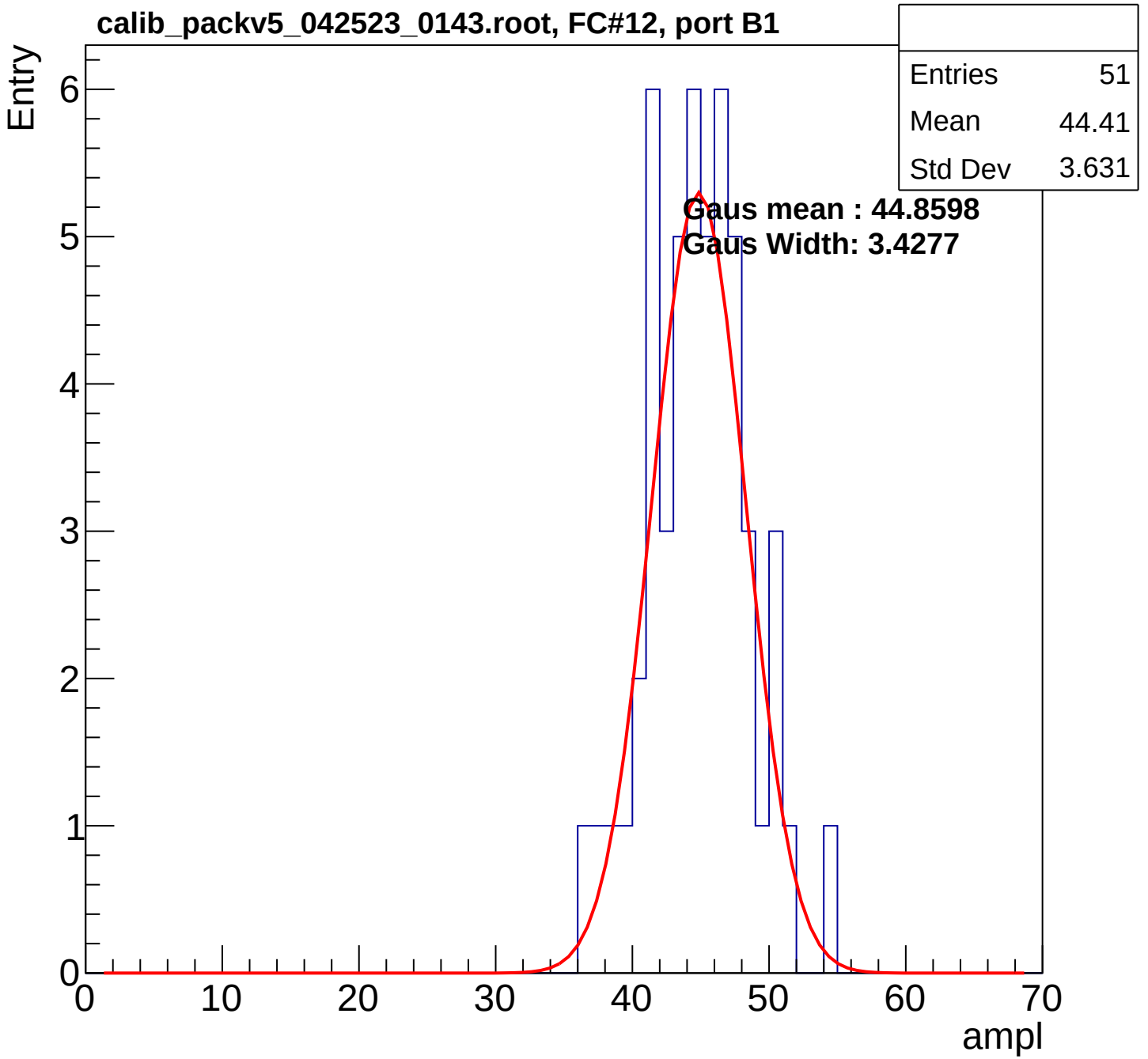
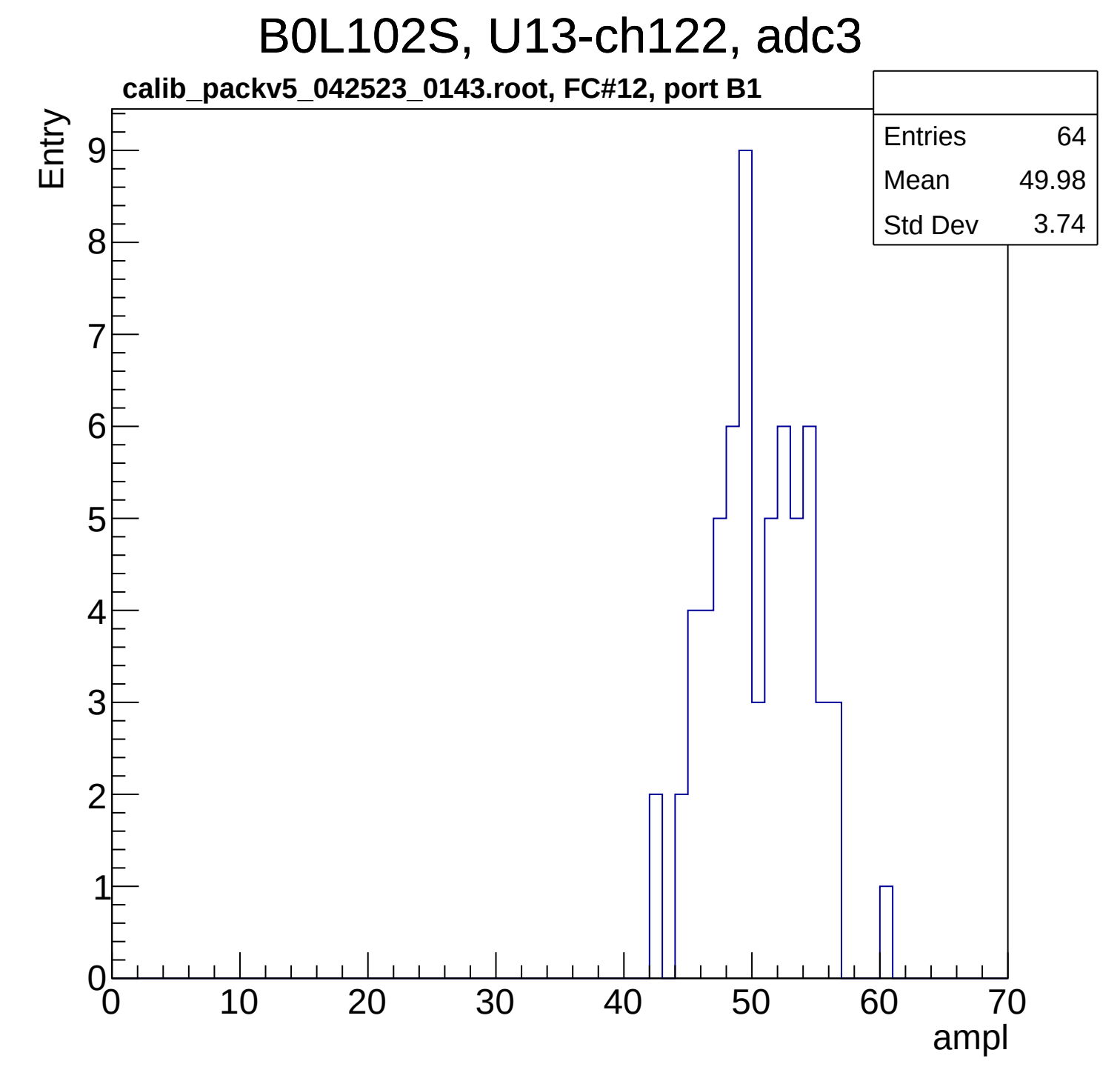

B0L102S, U13-ch121, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 2.357
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch121, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch122, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 29.77
Std Dev 4.132
Gaus mean : 30.8892
Gaus Width: 4.2313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 37.58
Std Dev 4.447
Gaus mean : 38.5960
Gaus Width: 3.9814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 44.41
Std Dev 3.631
Gaus mean : 44.8598
Gaus Width: 3.4277
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.98
Std Dev 3.74
Entry
ampl
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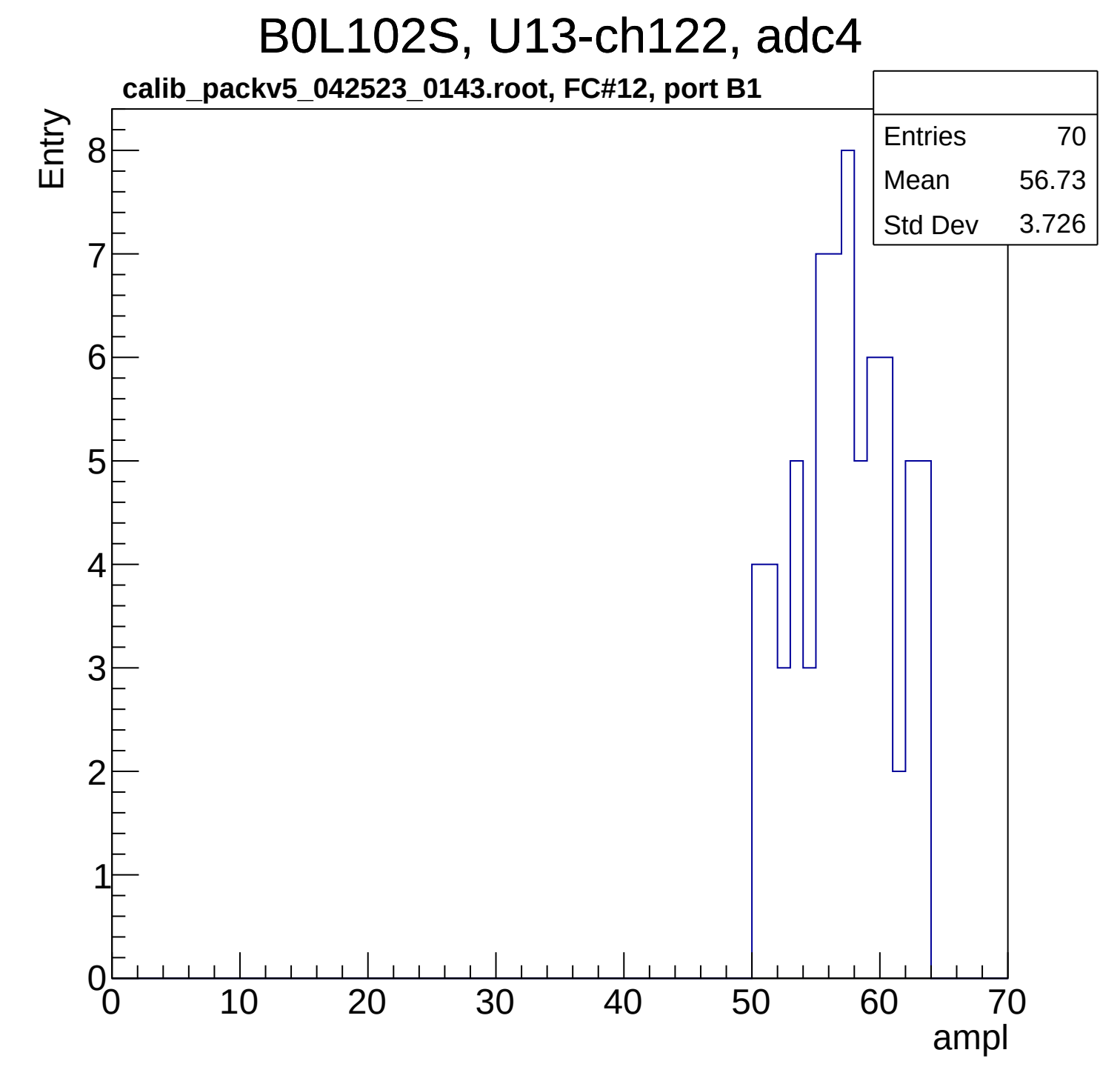
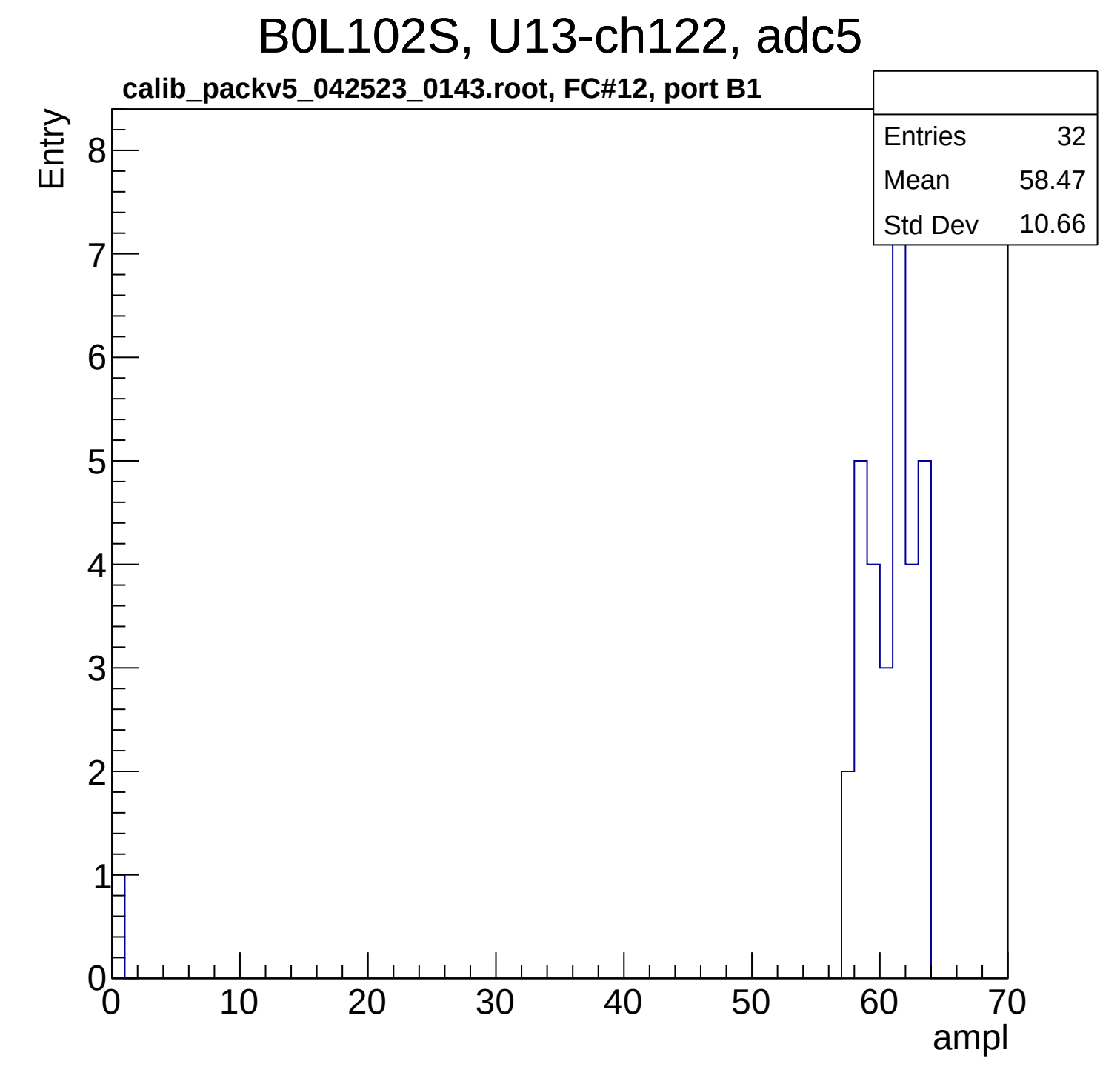
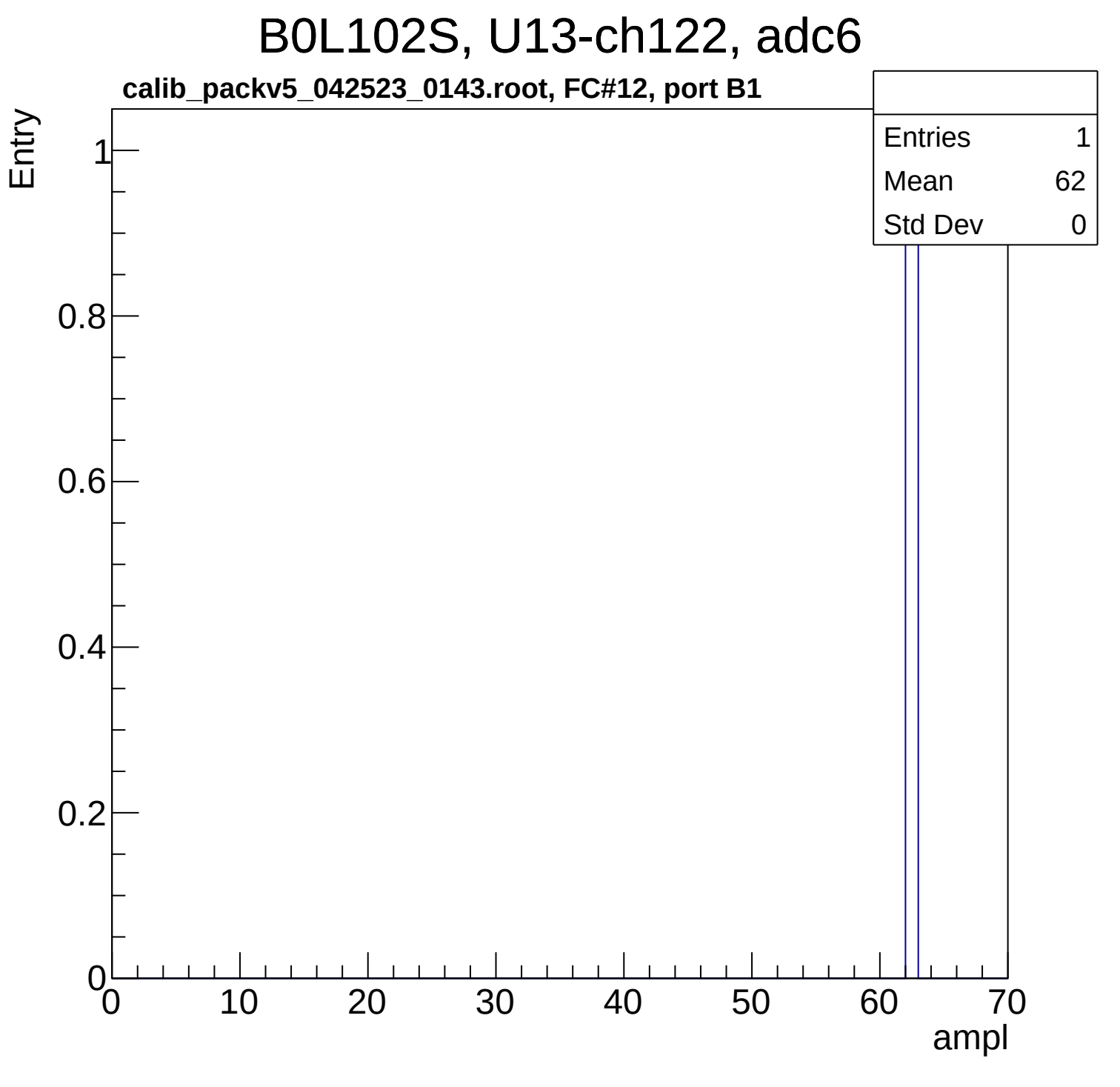
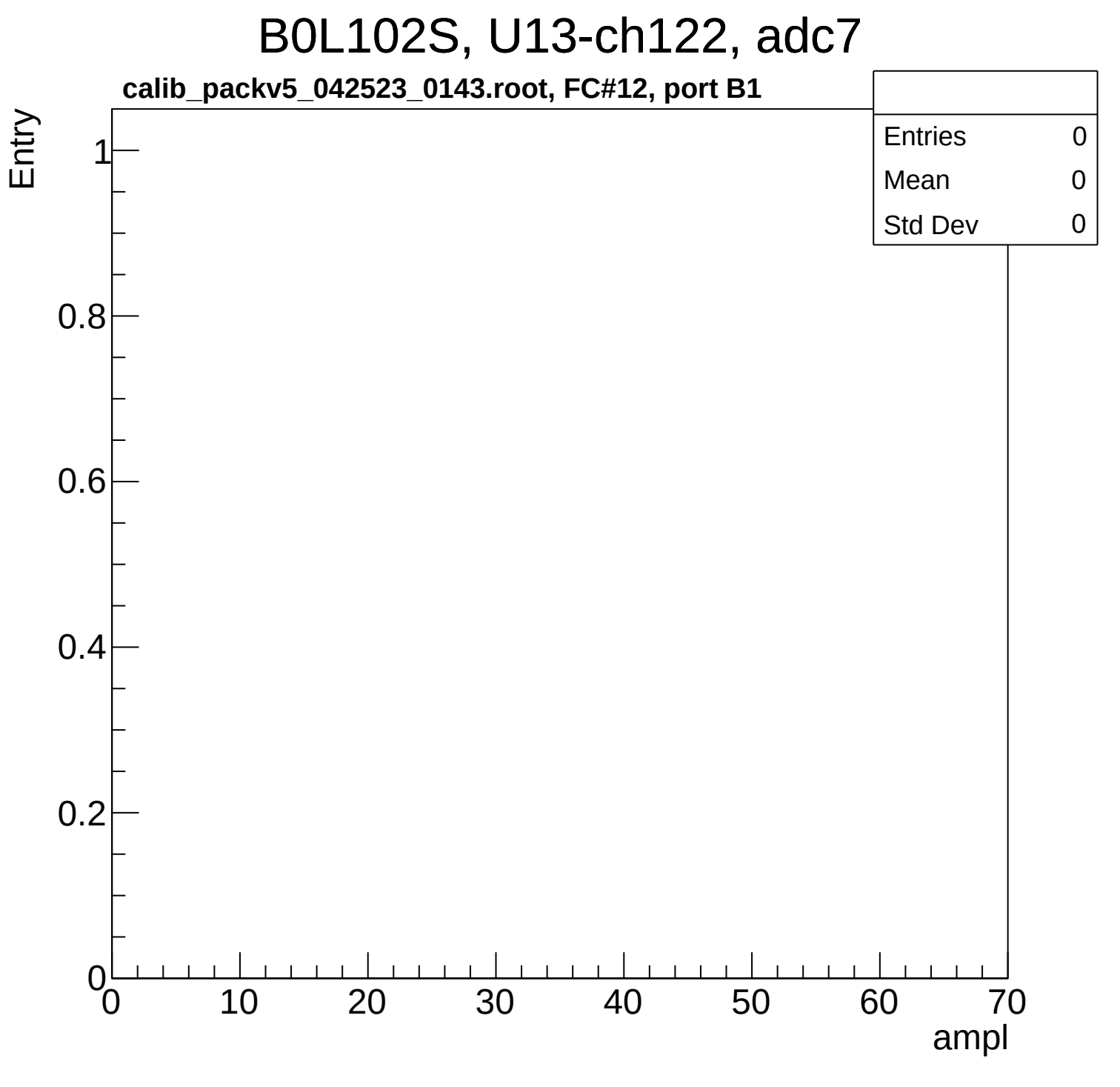
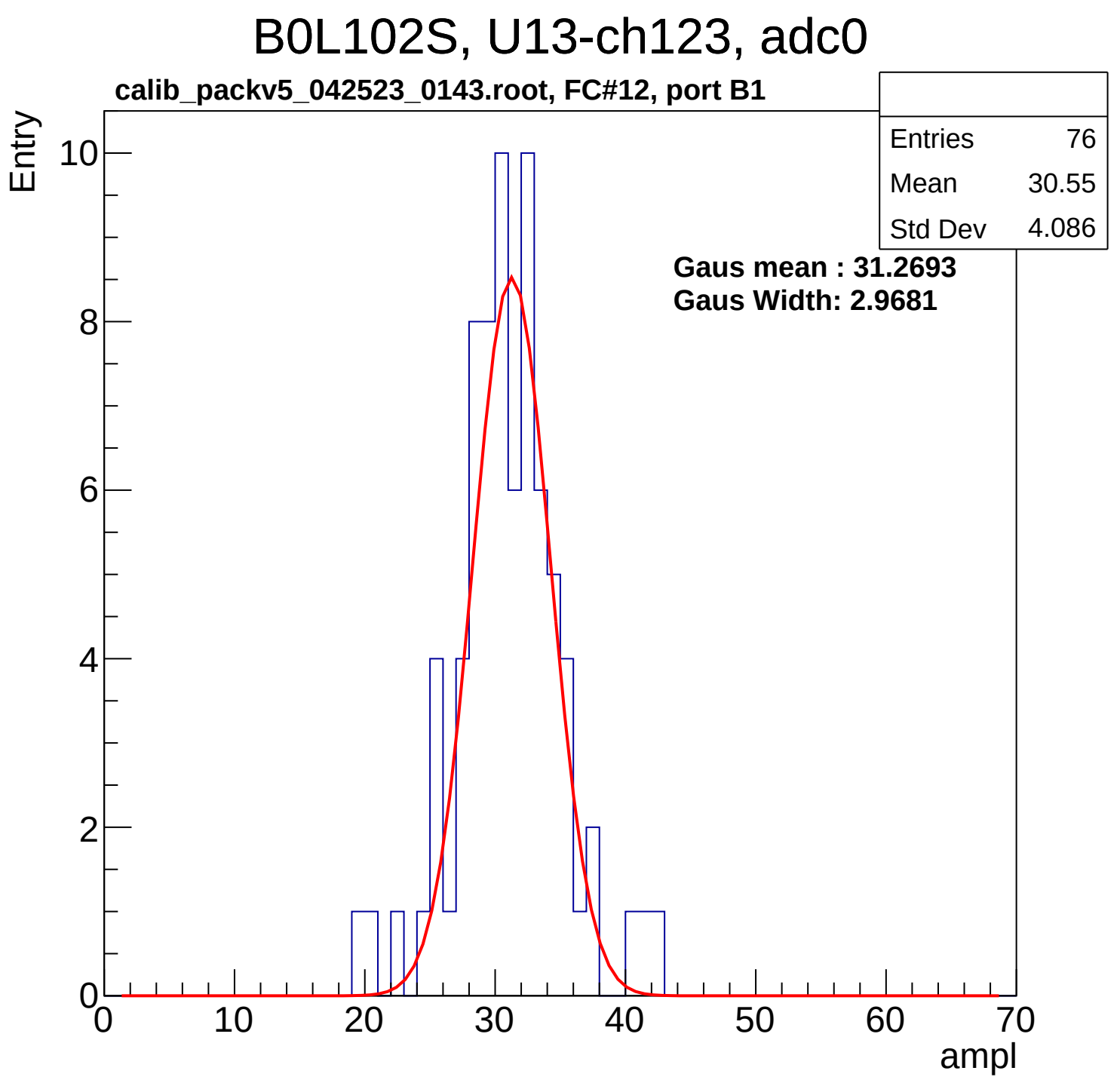
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 56.73
Std Dev 3.726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 58.47
Std Dev 10.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch122, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch122, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch123, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 30.55
Std Dev 4.086
Gaus mean : 31.2693
Gaus Width: 2.9681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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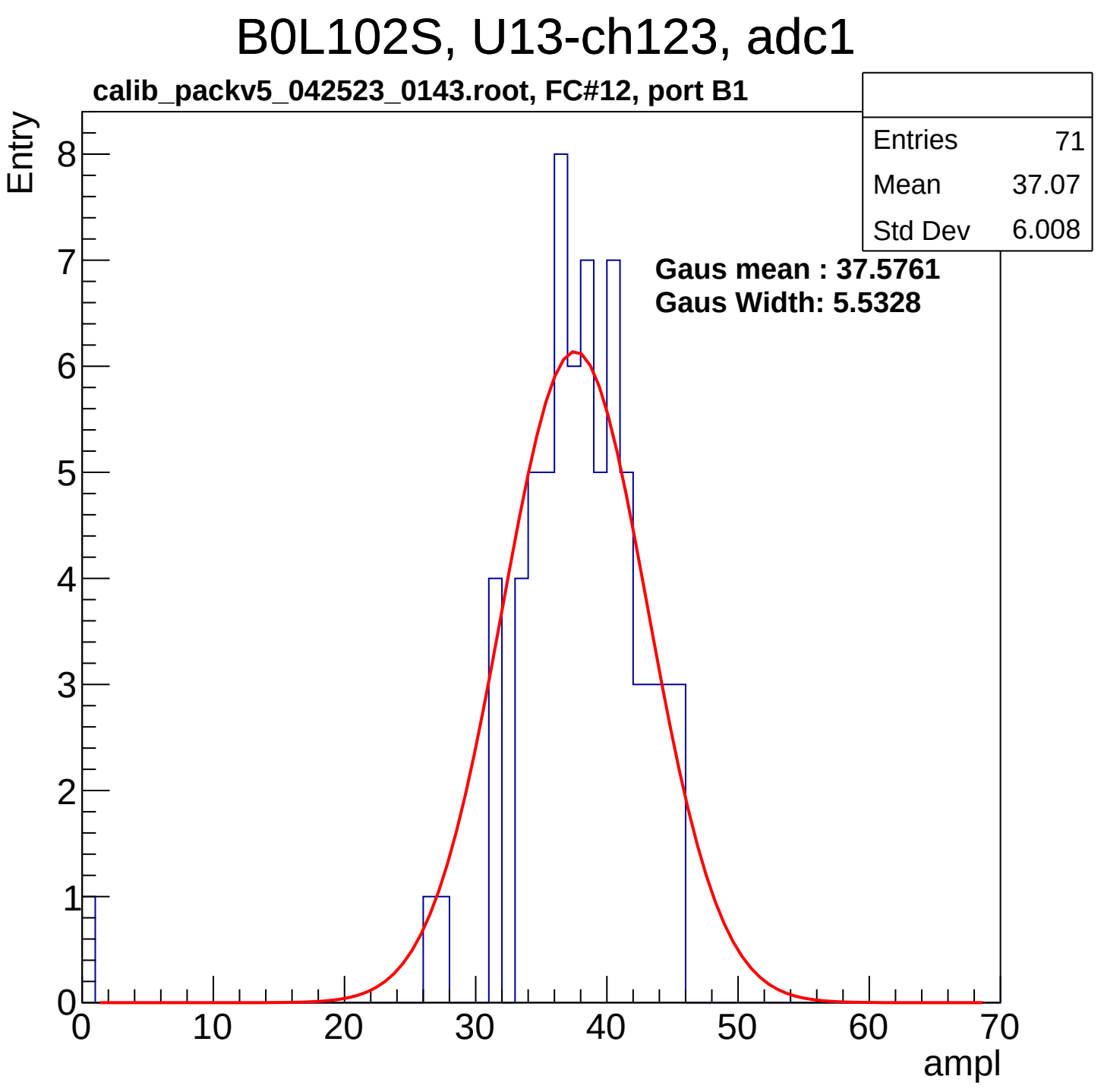
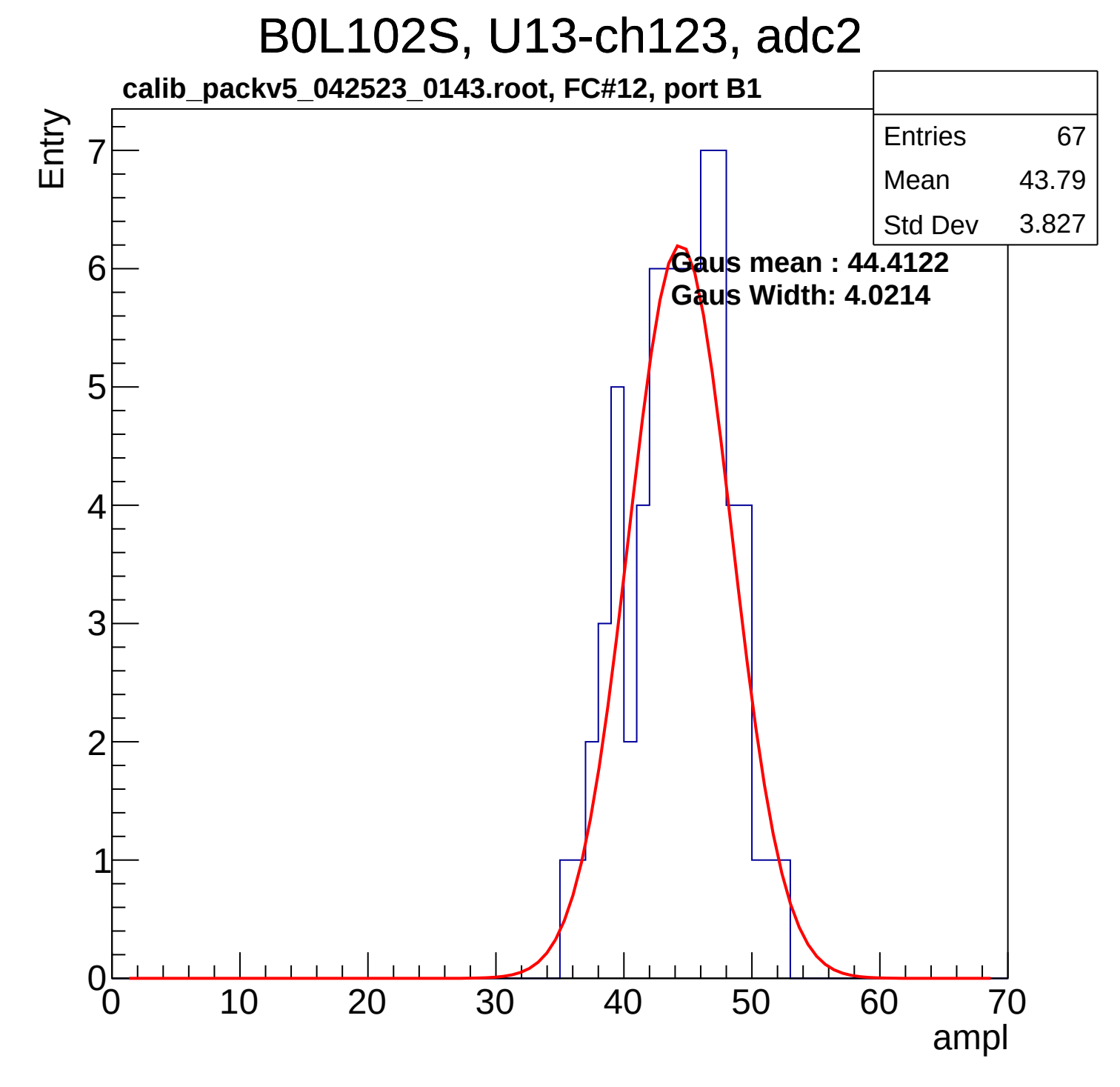

B0L102S, U13-ch123, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 37.07
Std Dev 6.008
Gaus mean : 37.5761
Gaus Width: 5.5328
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch123, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.79
Std Dev 3.827
Gaus mean : 44.4122
Gaus Width: 4.0214
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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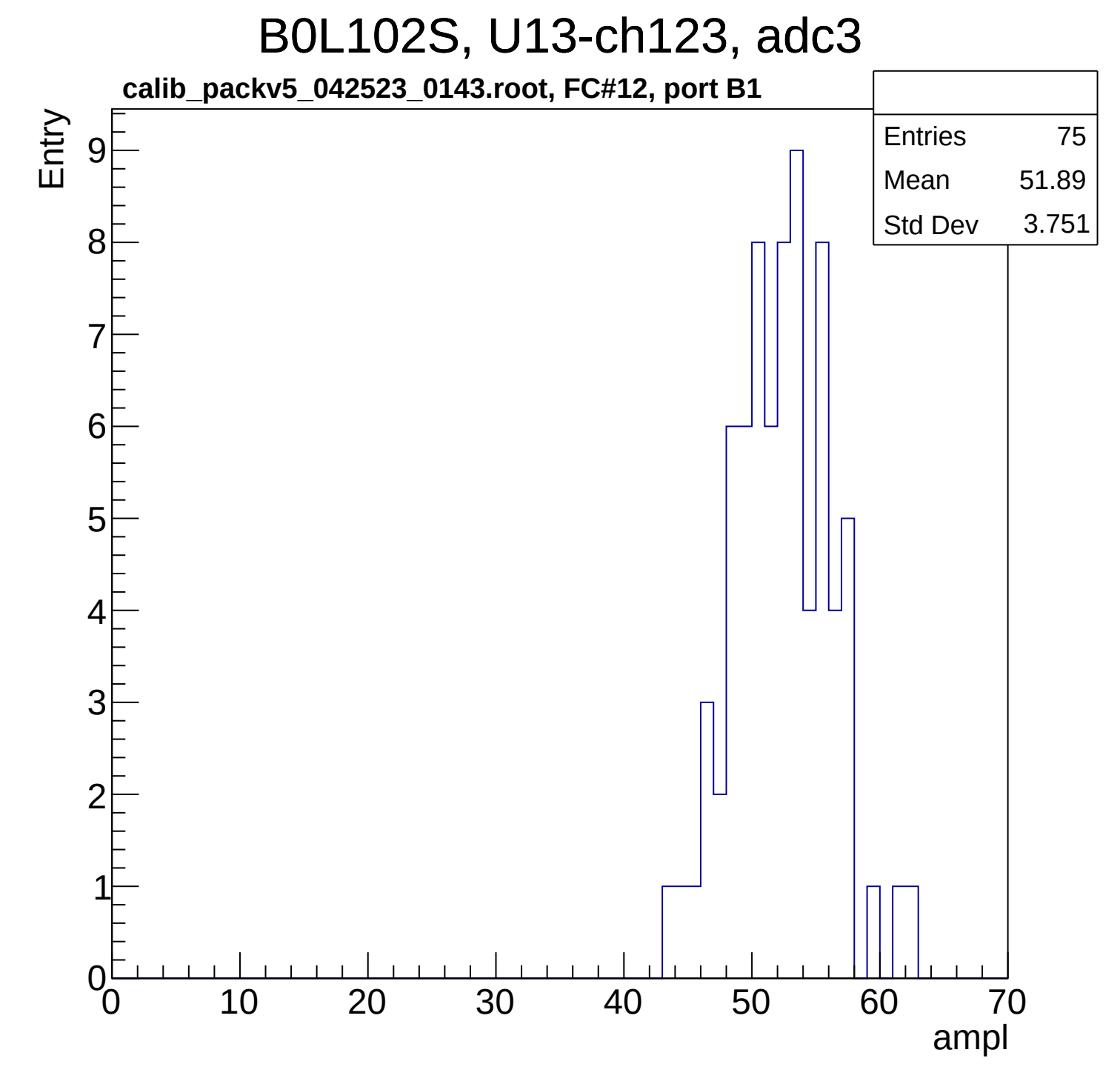
B0L102S, U13-ch123, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 51.89
Std Dev 3.751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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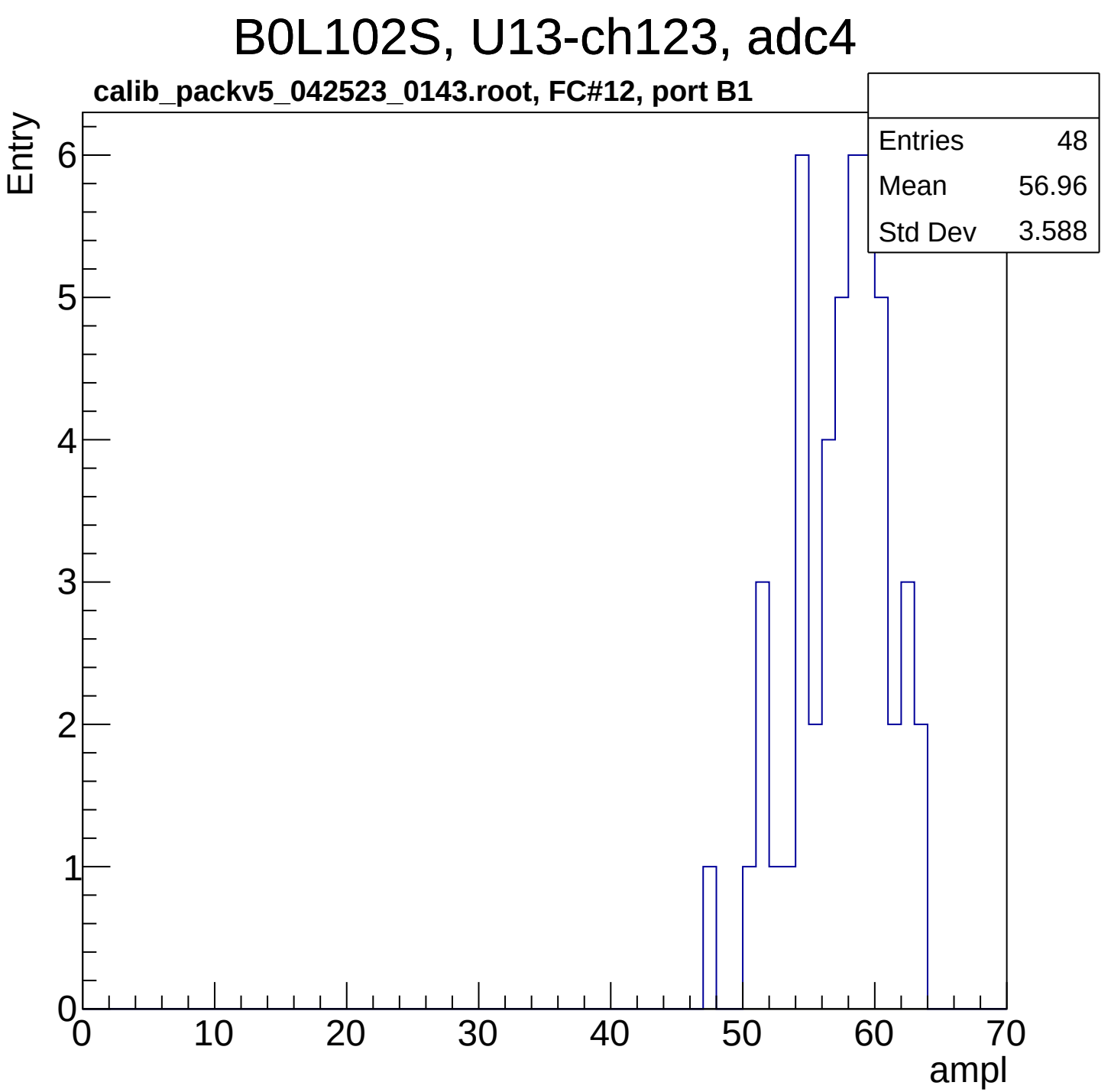
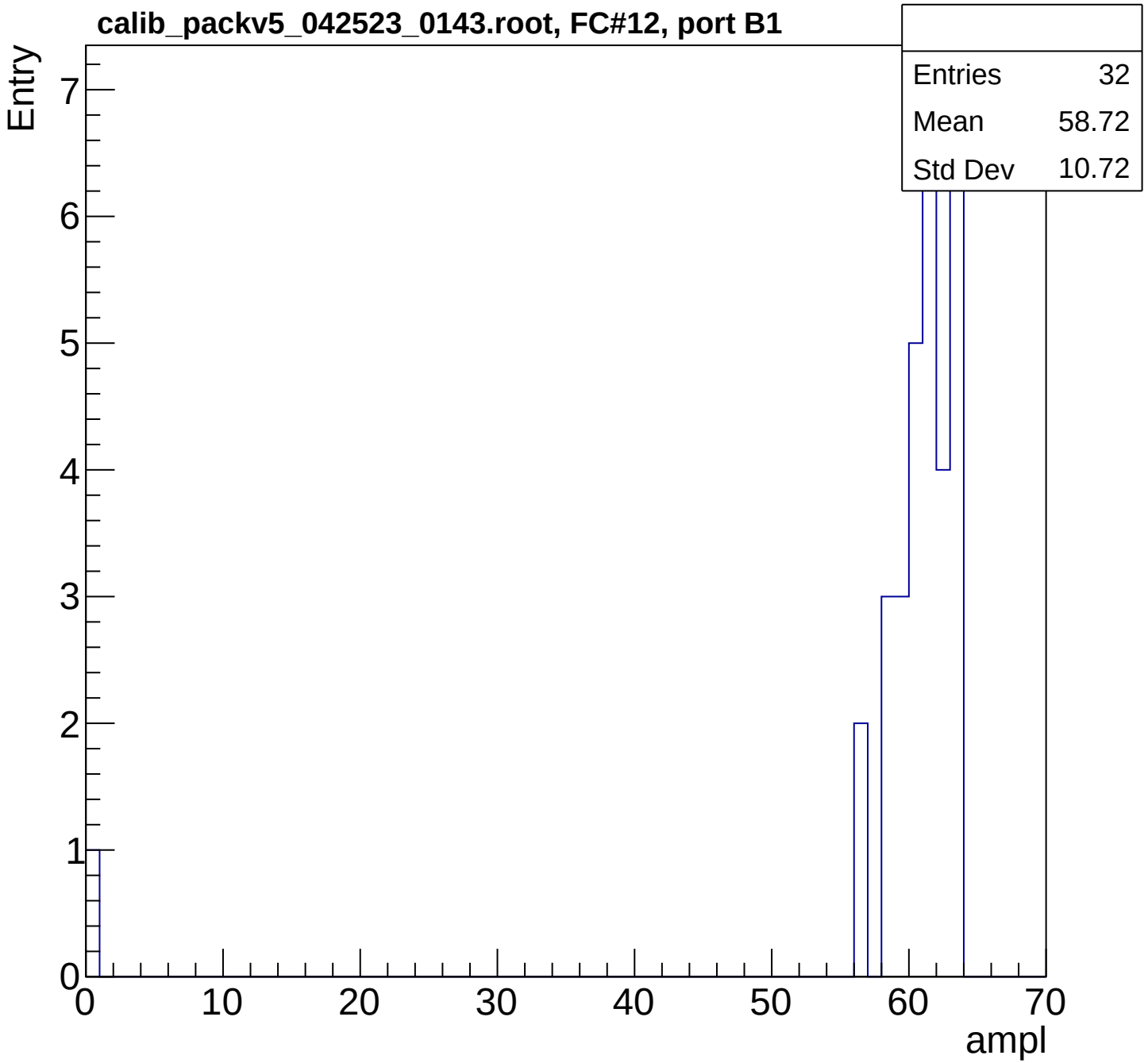

B0L102S, U13-ch123, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 56.96
Std Dev 3.588
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch123, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 58.72
Std Dev 10.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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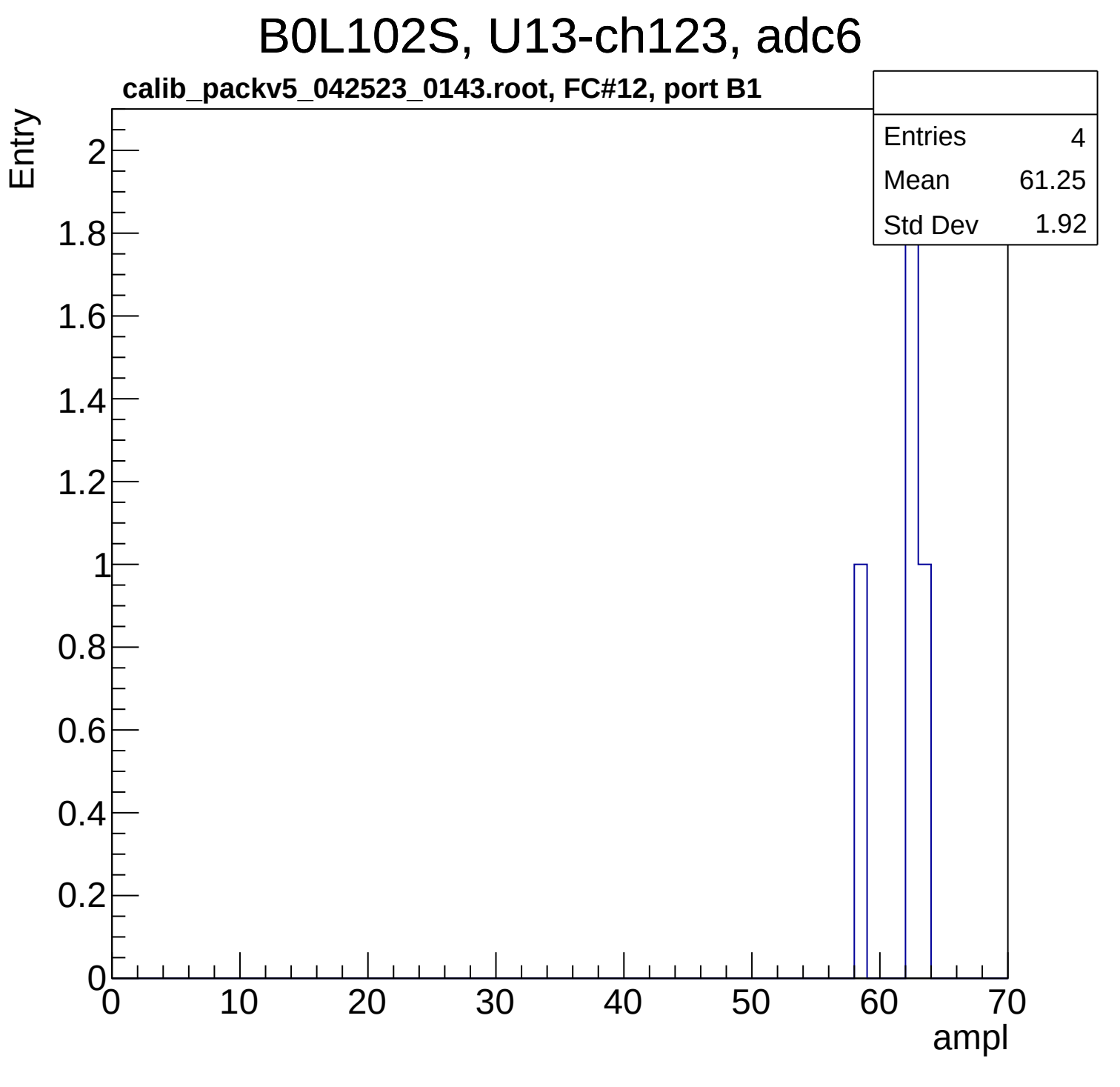
B0L102S, U13-ch123, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.25
Std Dev 1.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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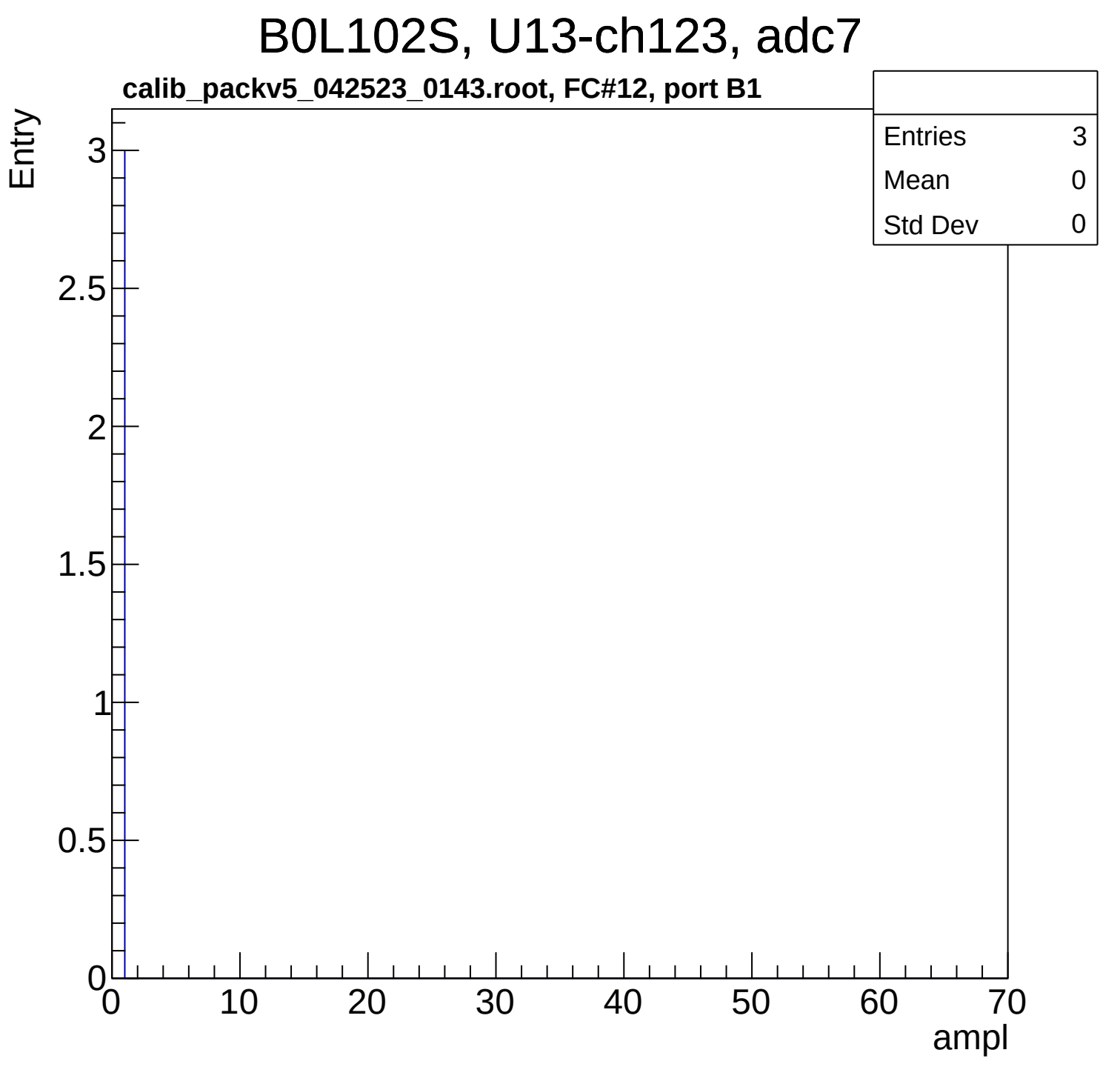
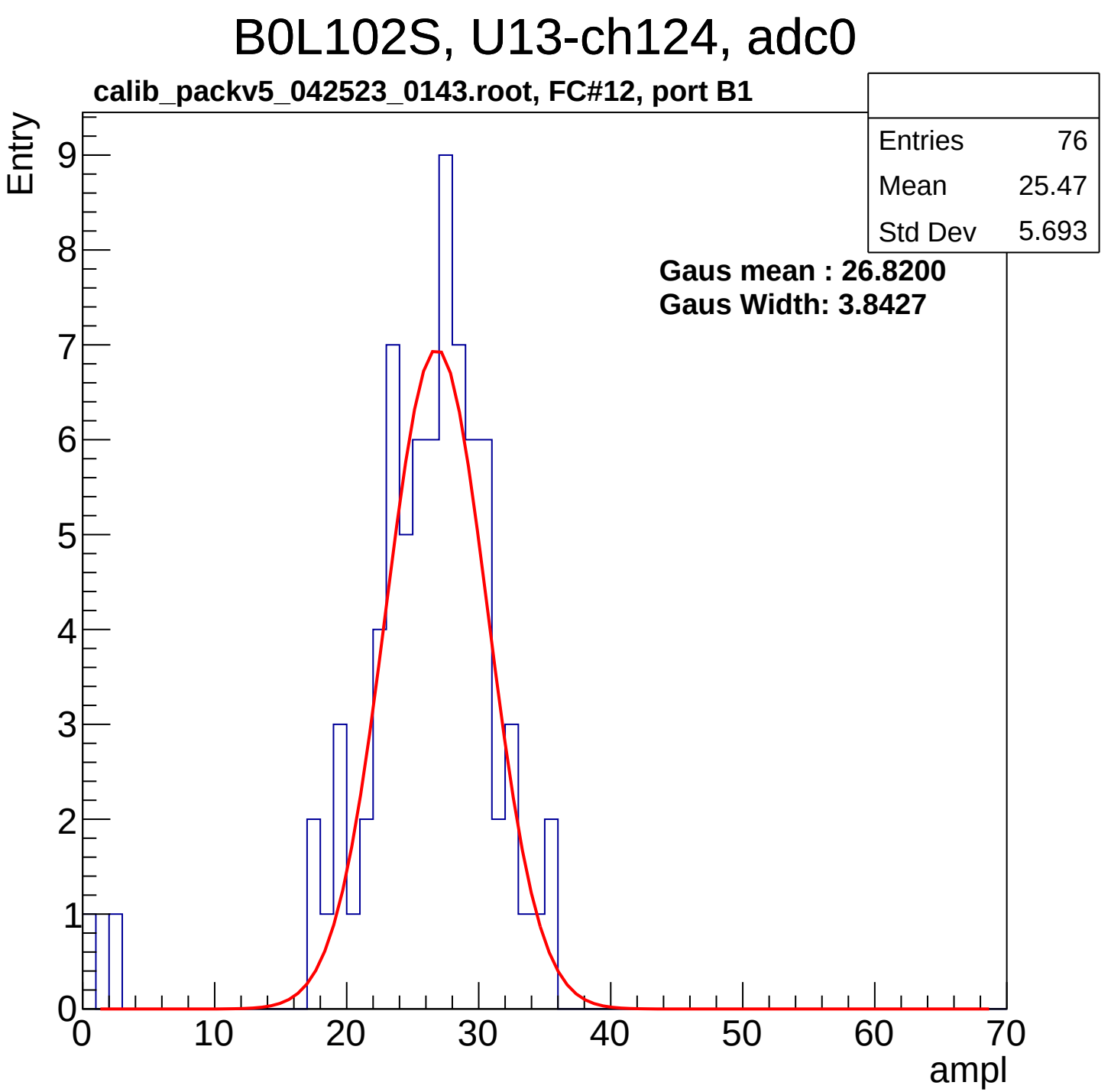
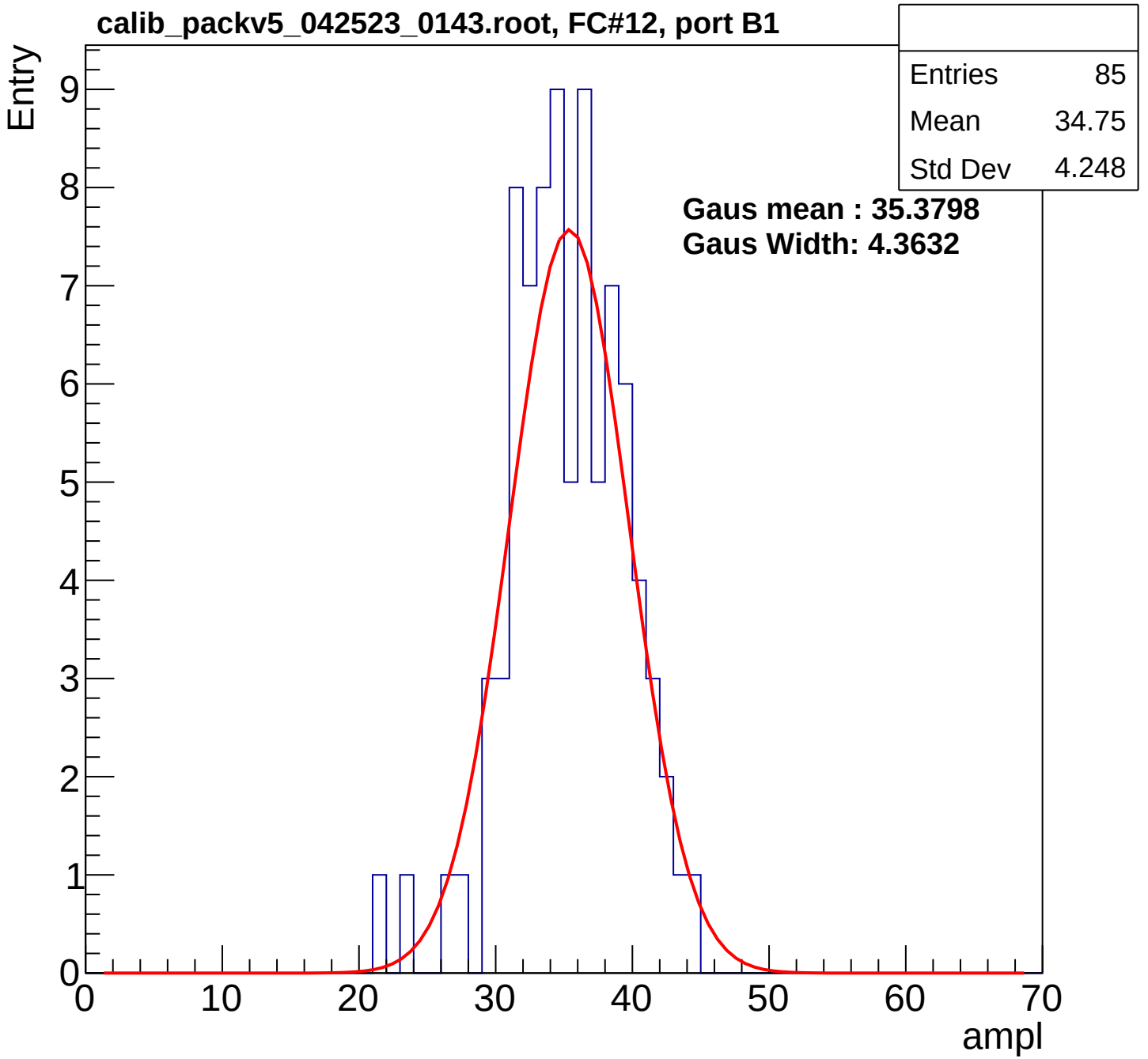
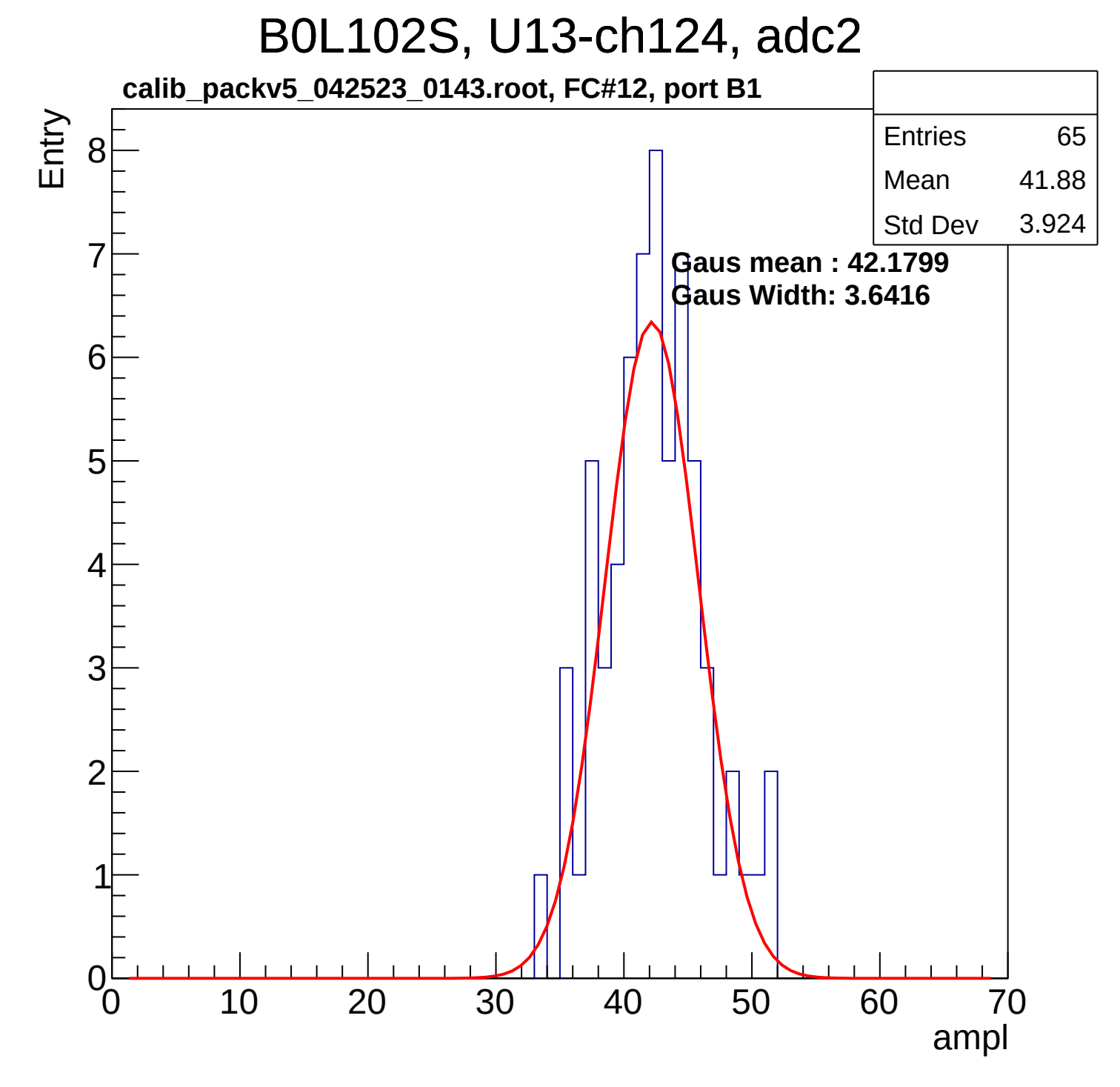
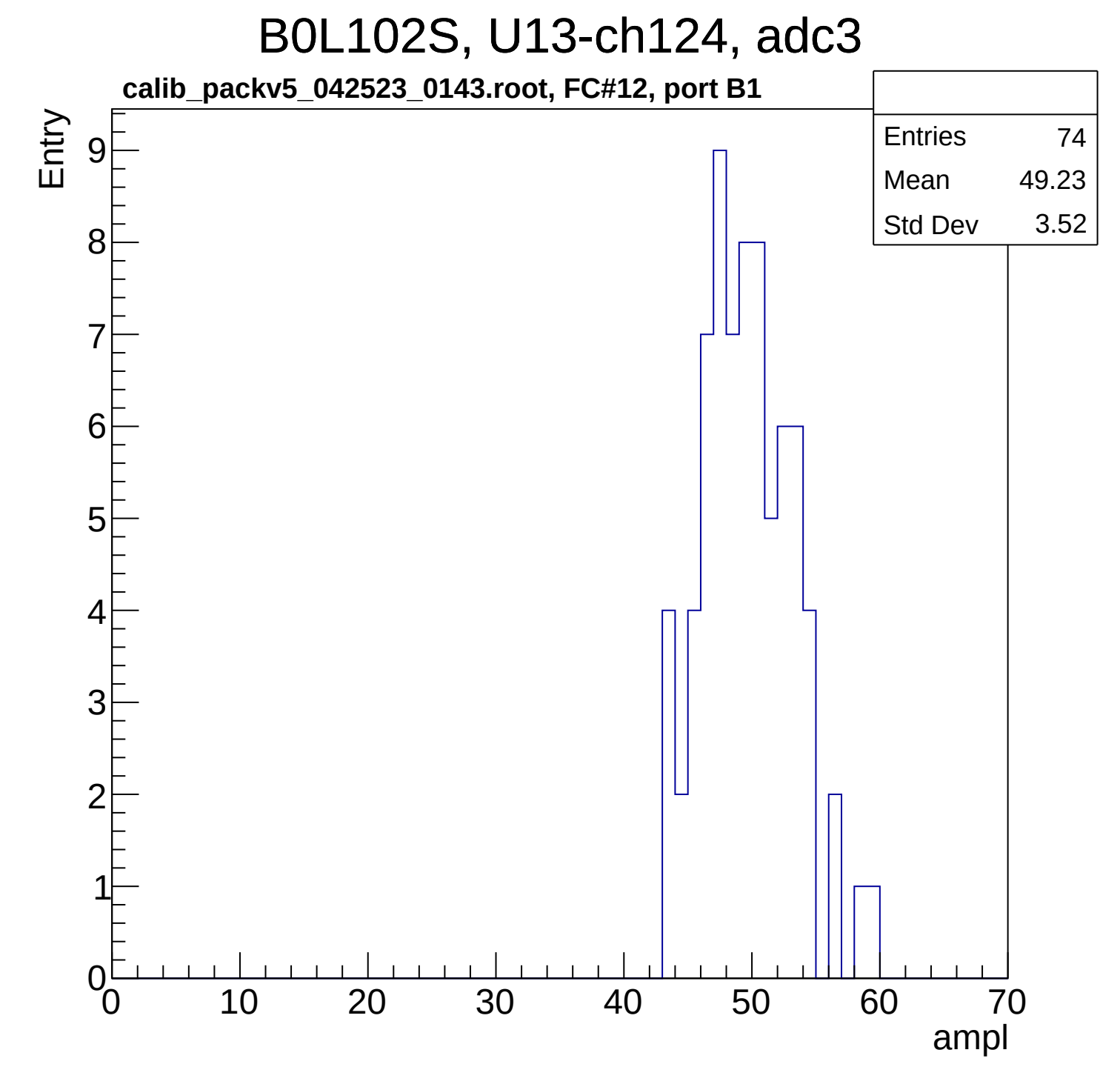
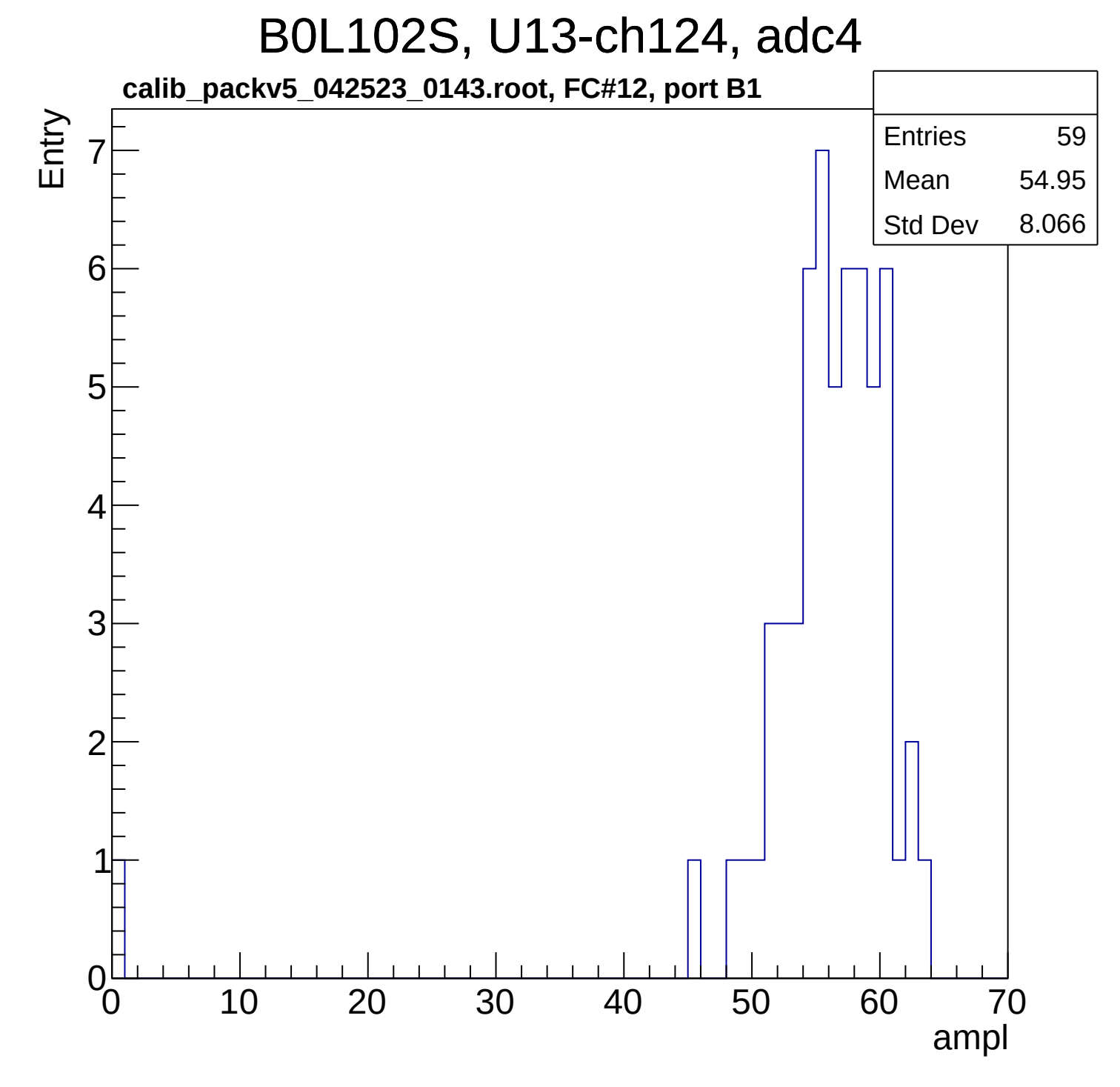

B0L102S, U13-ch123, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

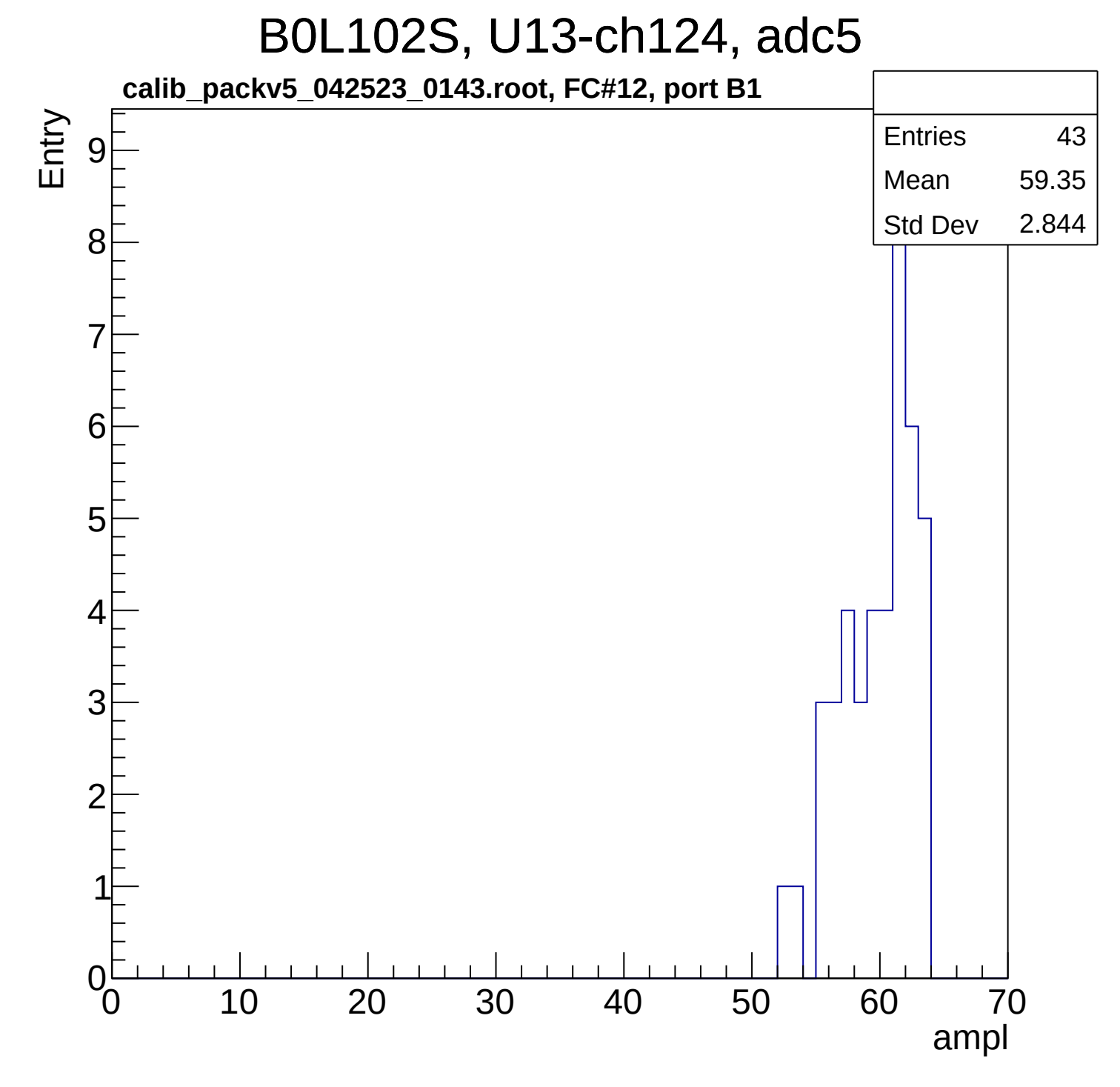

B0L102S, U13-ch124, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 25.47
Std Dev 5.693
Gaus mean : 26.8200
Gaus Width: 3.8427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch124, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 34.75
Std Dev 4.248
Gaus mean : 35.3798
Gaus Width: 4.3632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch124, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 41.88
Std Dev 3.924
Gaus mean : 42.1799
Gaus Width: 3.6416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch124, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 49.23
Std Dev 3.52
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch124, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.95
Std Dev 8.066
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch124, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.35
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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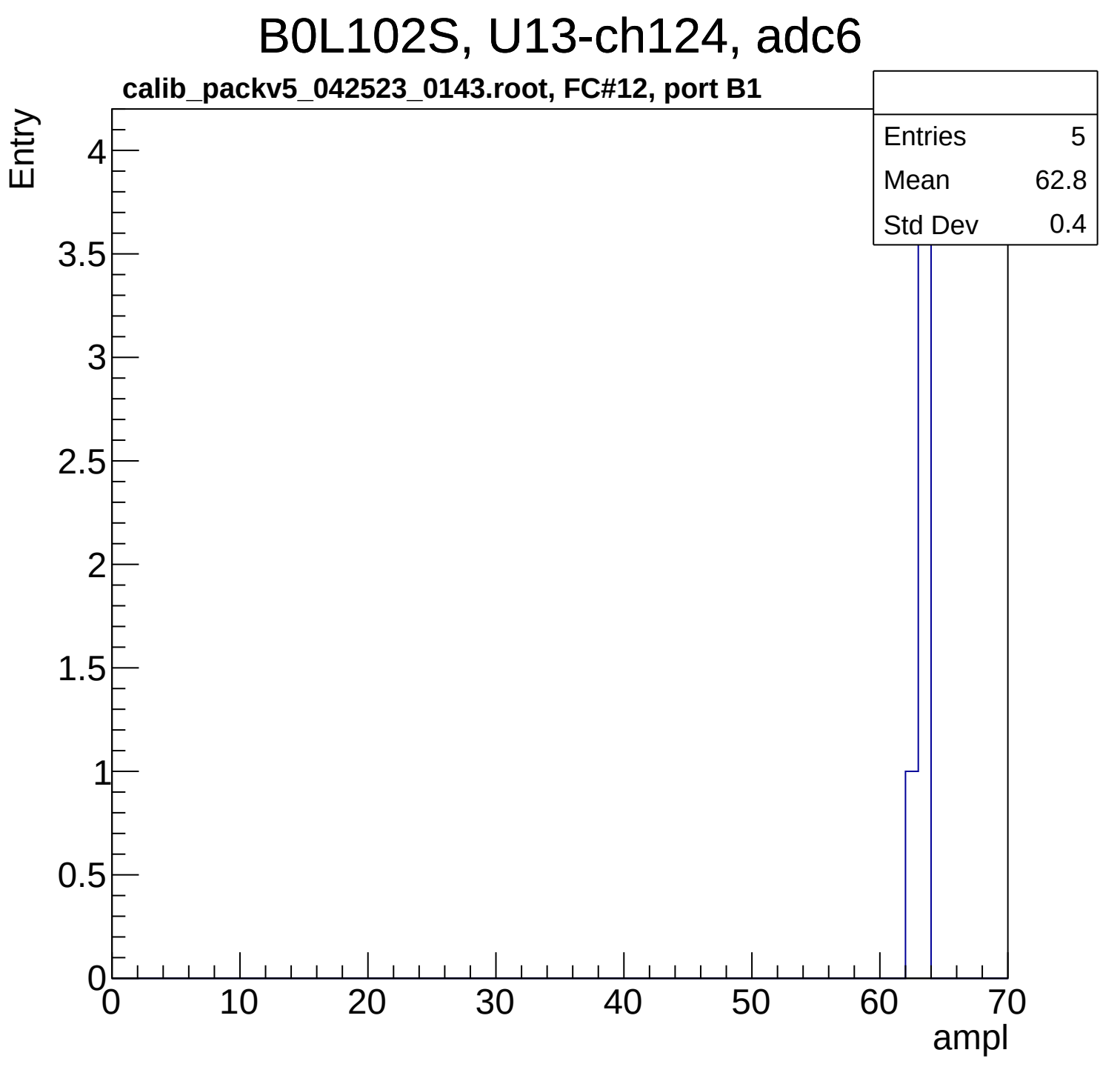
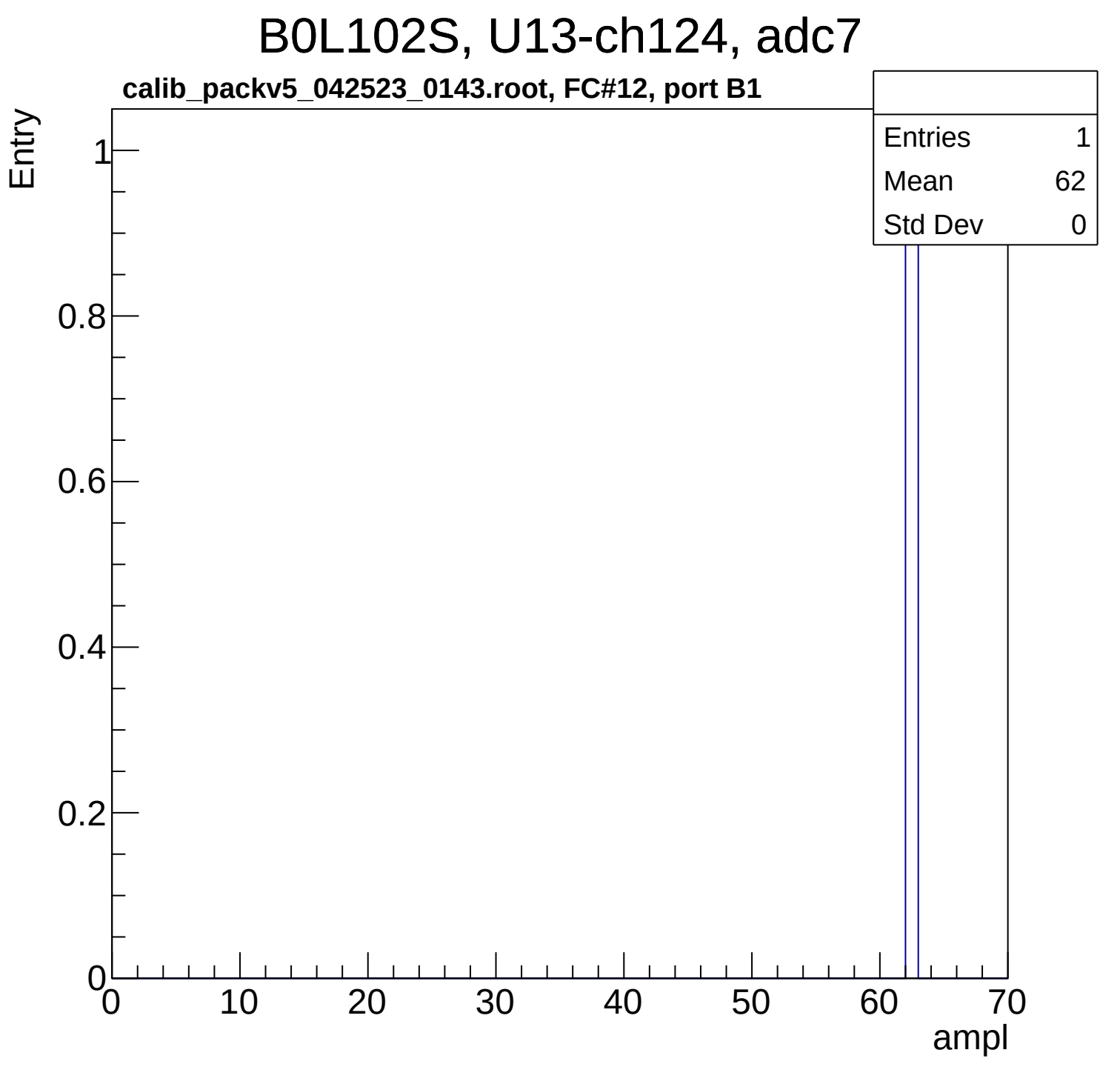
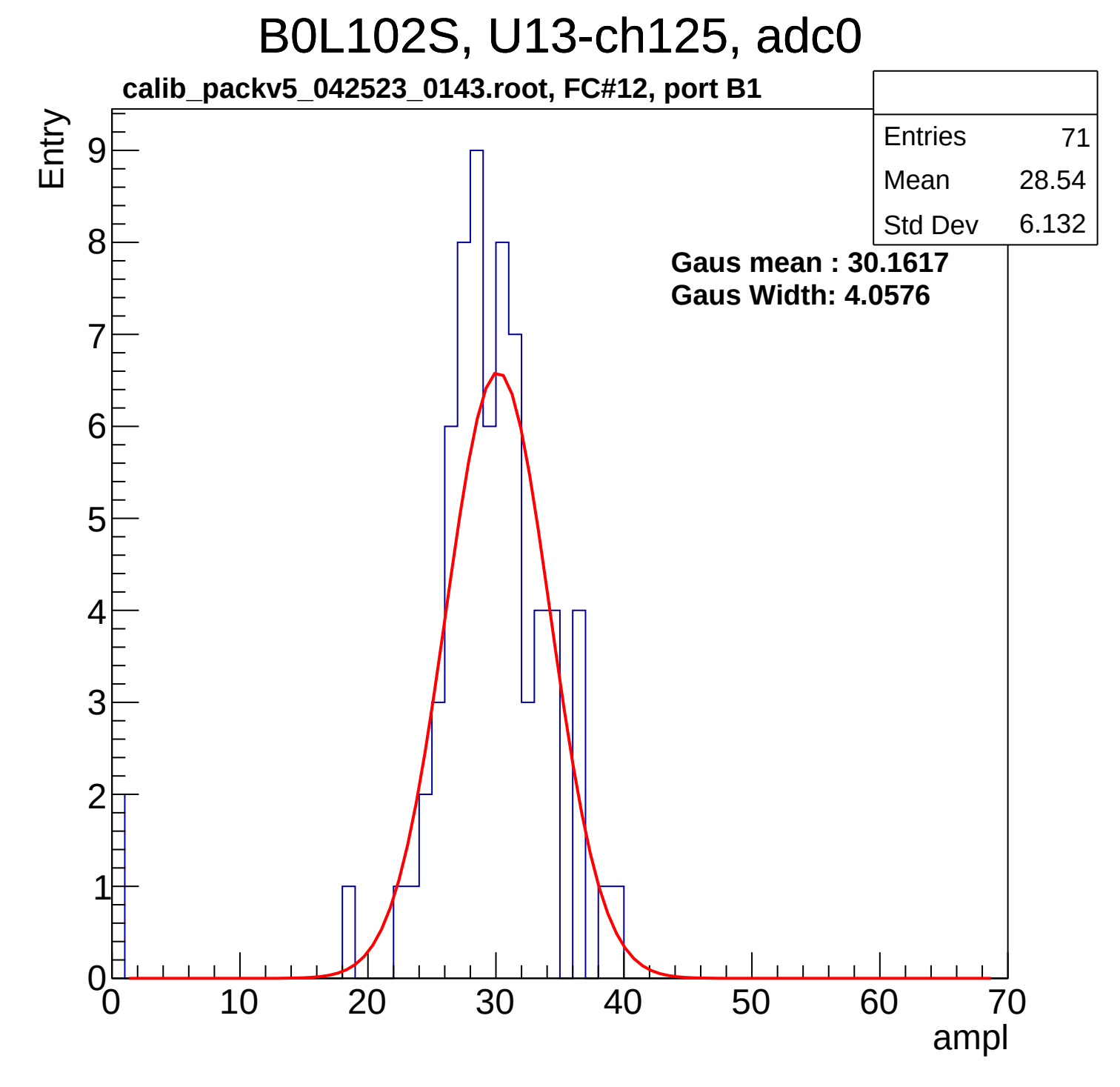

B0L102S, U13-ch124, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch124, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch125, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 28.54
Std Dev 6.132
Gaus mean : 30.1617
Gaus Width: 4.0576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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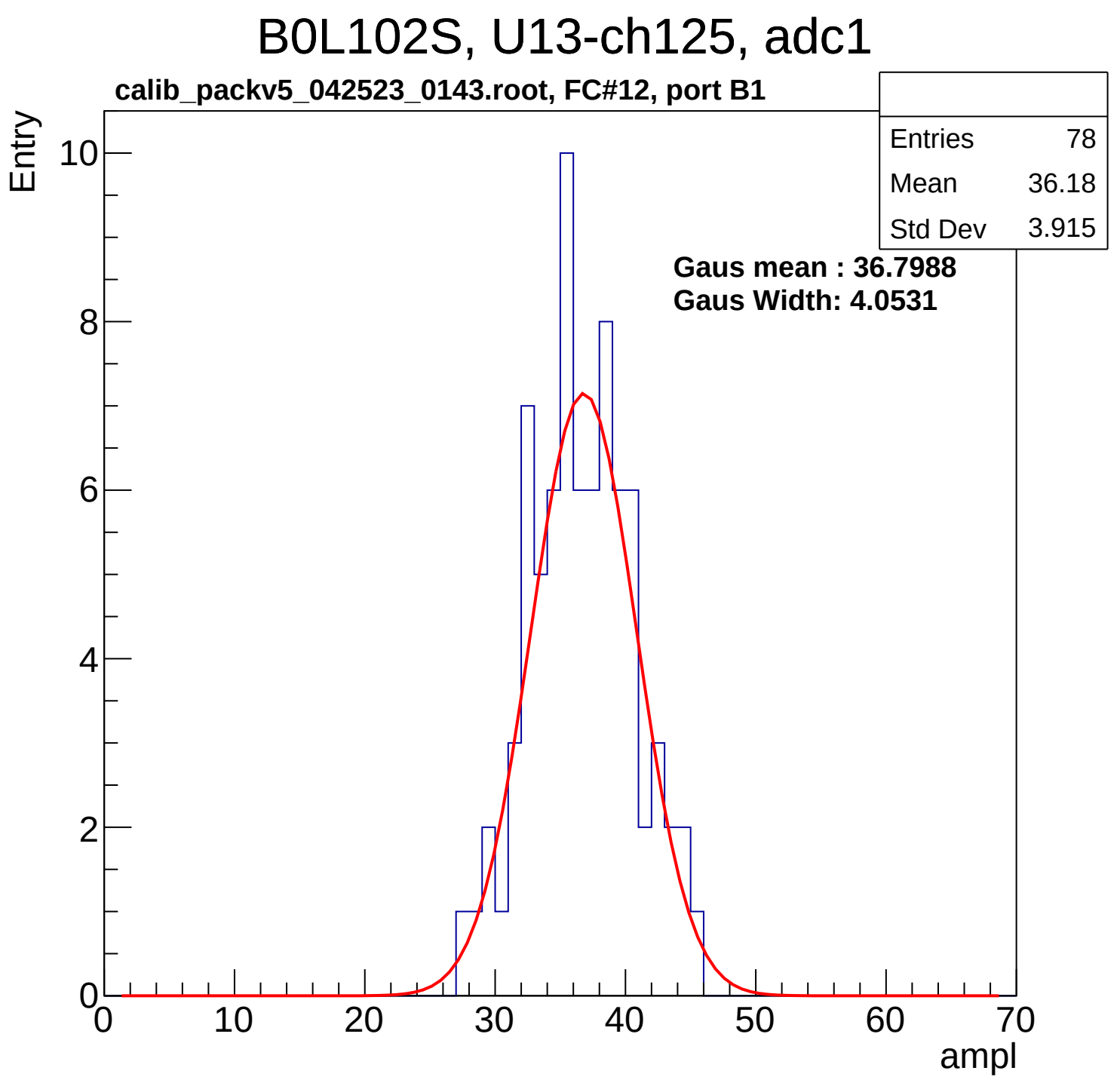
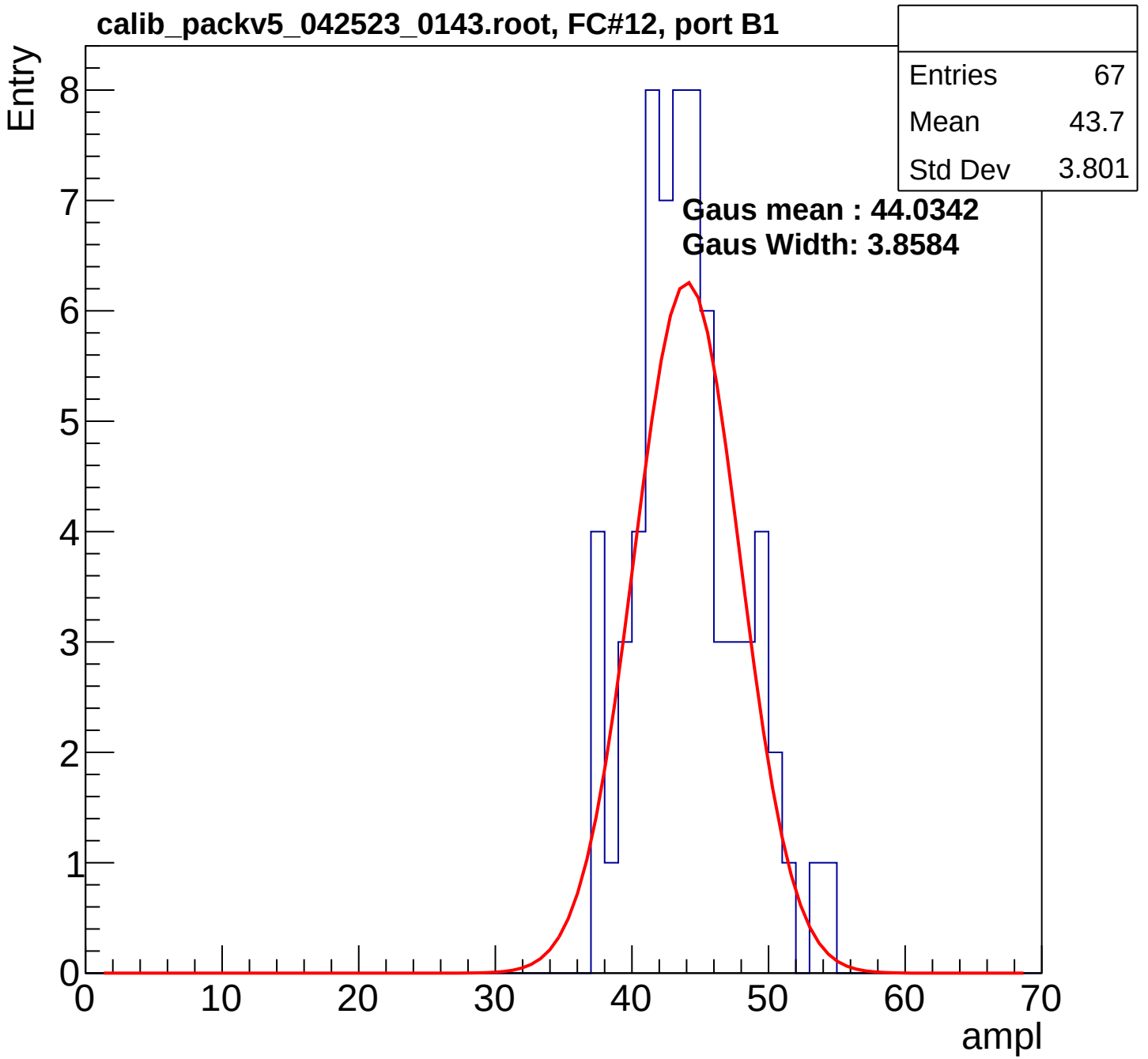

B0L102S, U13-ch125, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 36.18
Std Dev 3.915
Gaus mean : 36.7988
Gaus Width: 4.0531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U13-ch125, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.7
Std Dev 3.801
Gaus mean : 44.0342
Gaus Width: 3.8584
Entry
ampl
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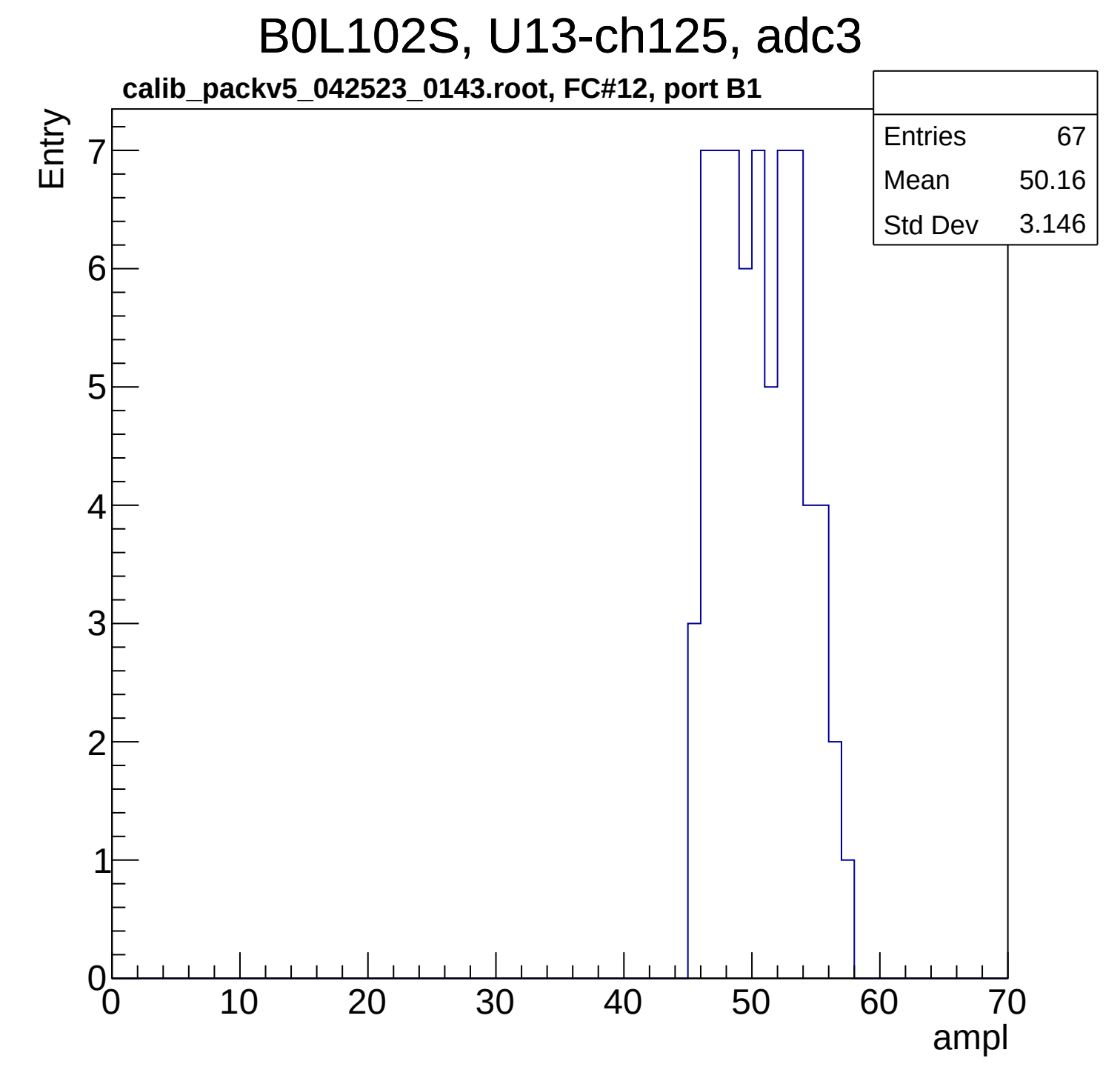
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U13-ch125, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.16
Std Dev 3.146
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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50
60
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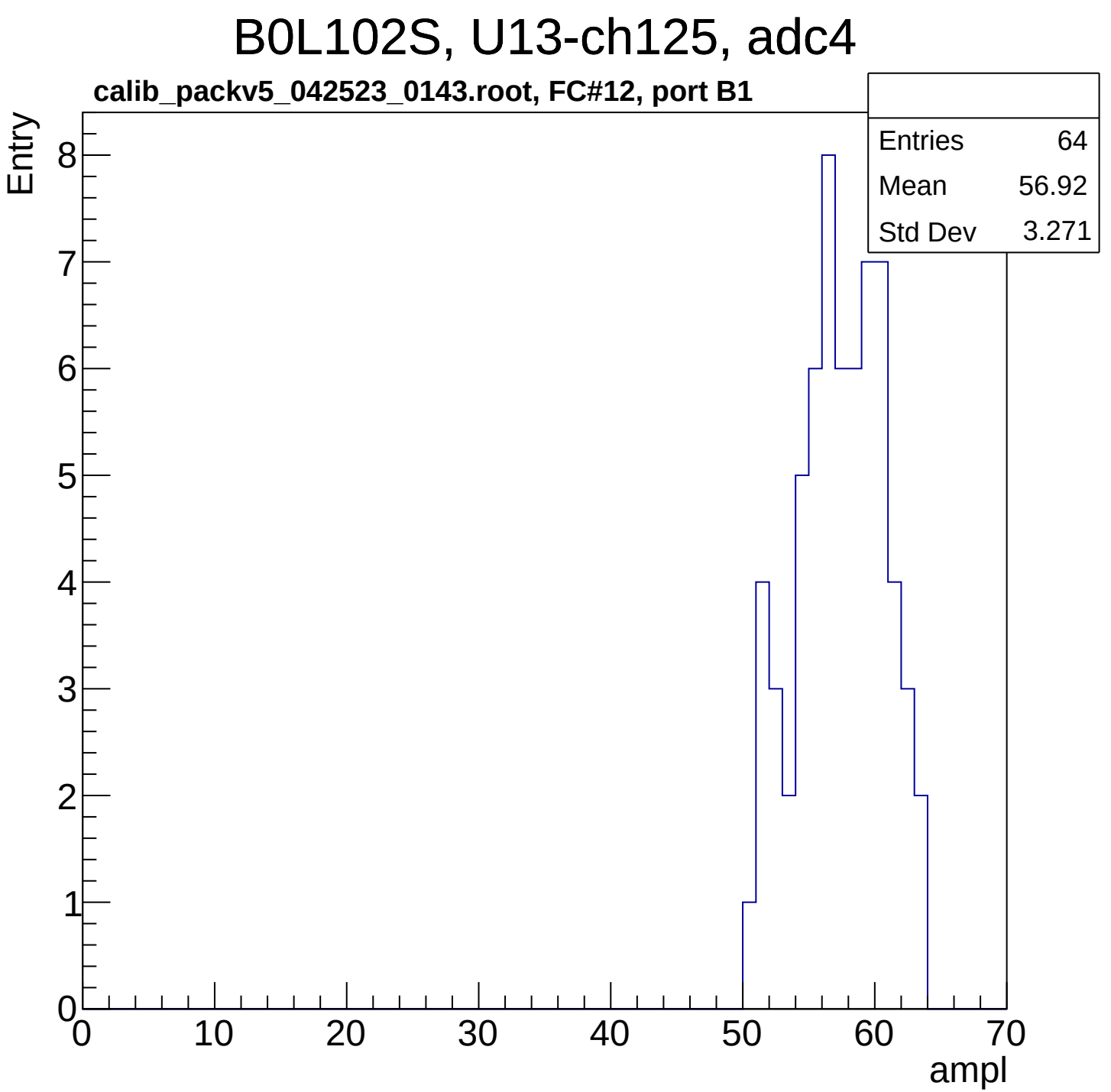
B0L102S, U13-ch125, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.92
Std Dev 3.271
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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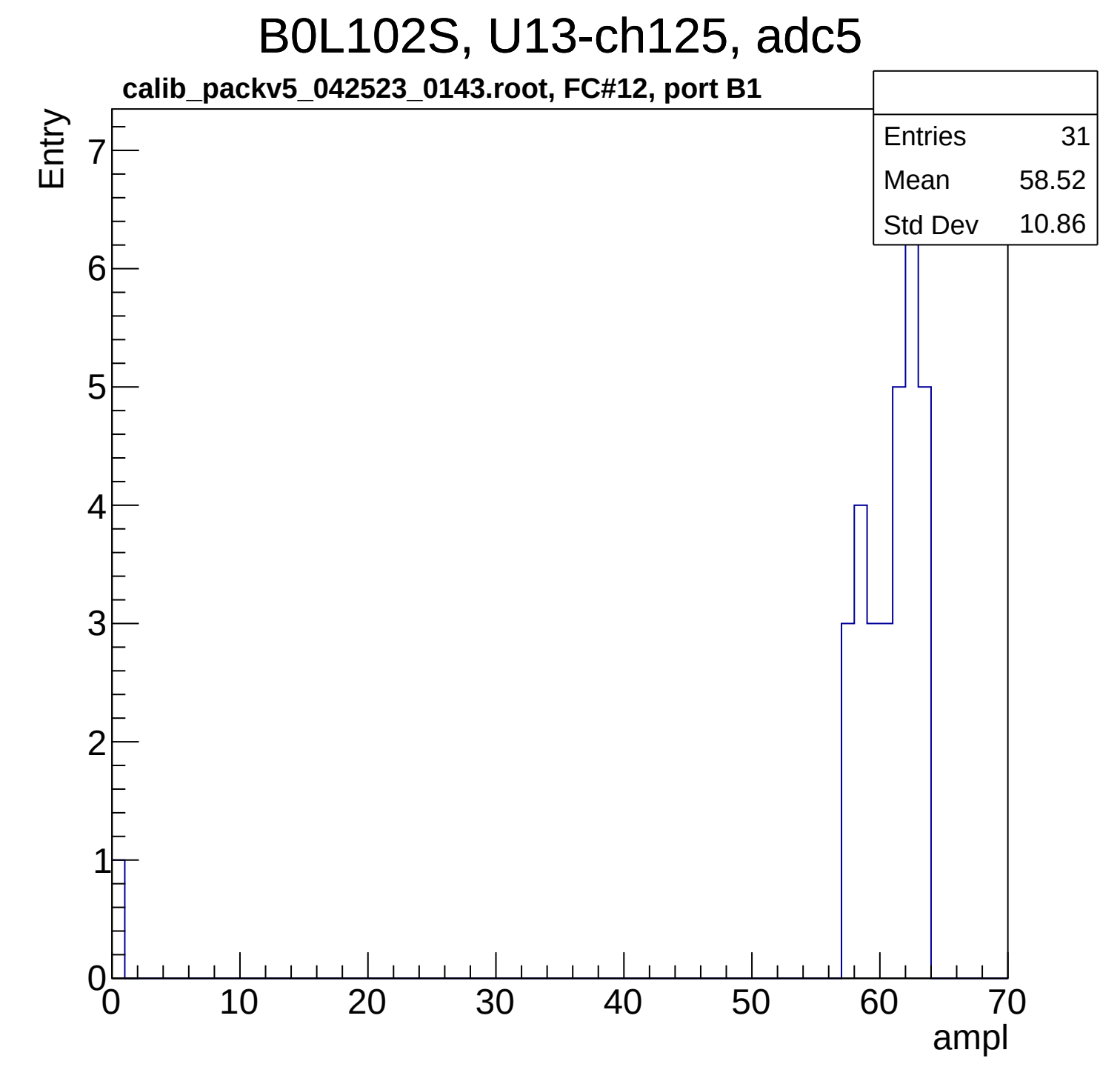
B0L102S, U13-ch125, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 58.52
Std Dev 10.86
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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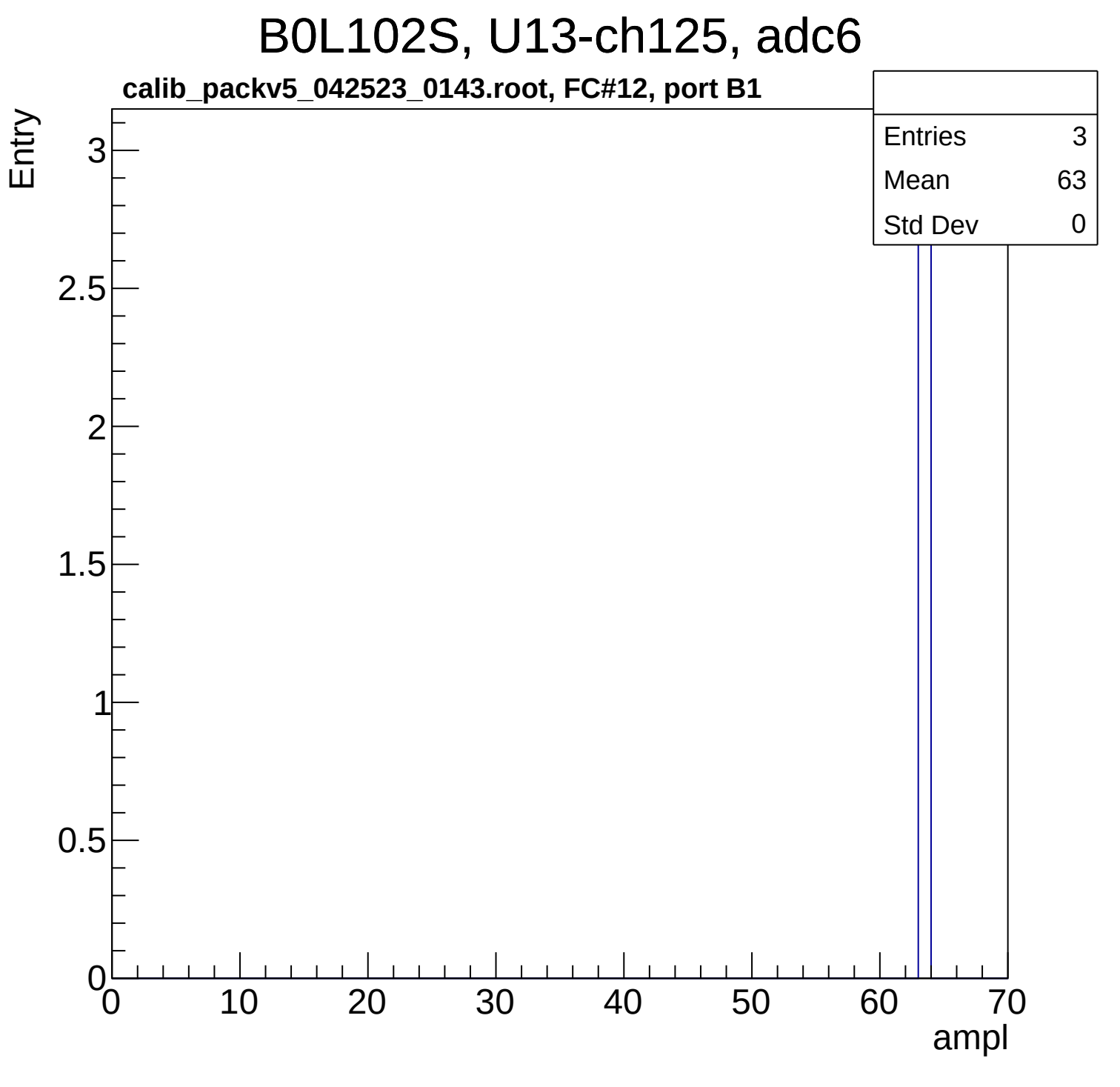
B0L102S, U13-ch125, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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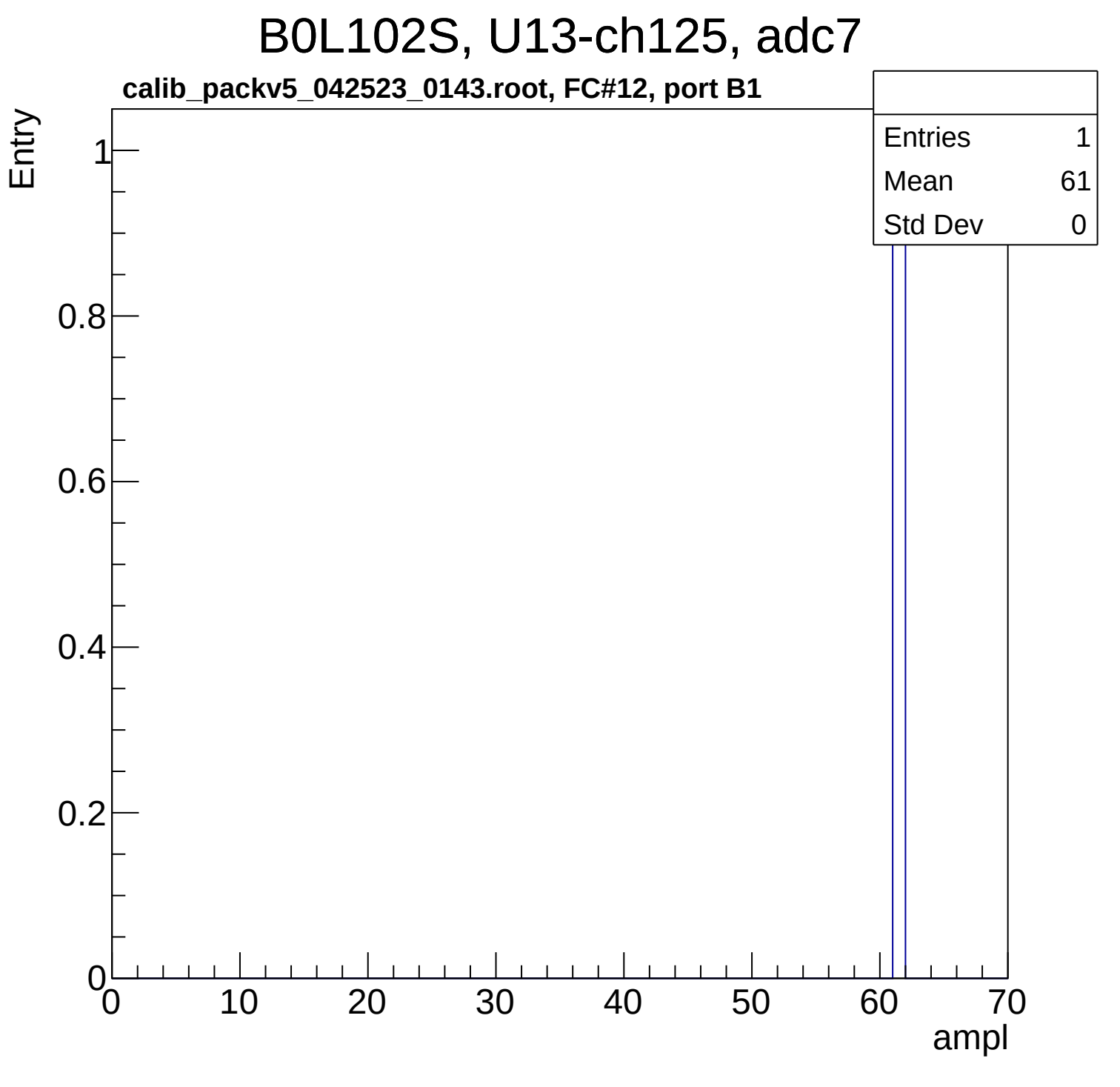
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch125, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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0.8
0.6
0.4
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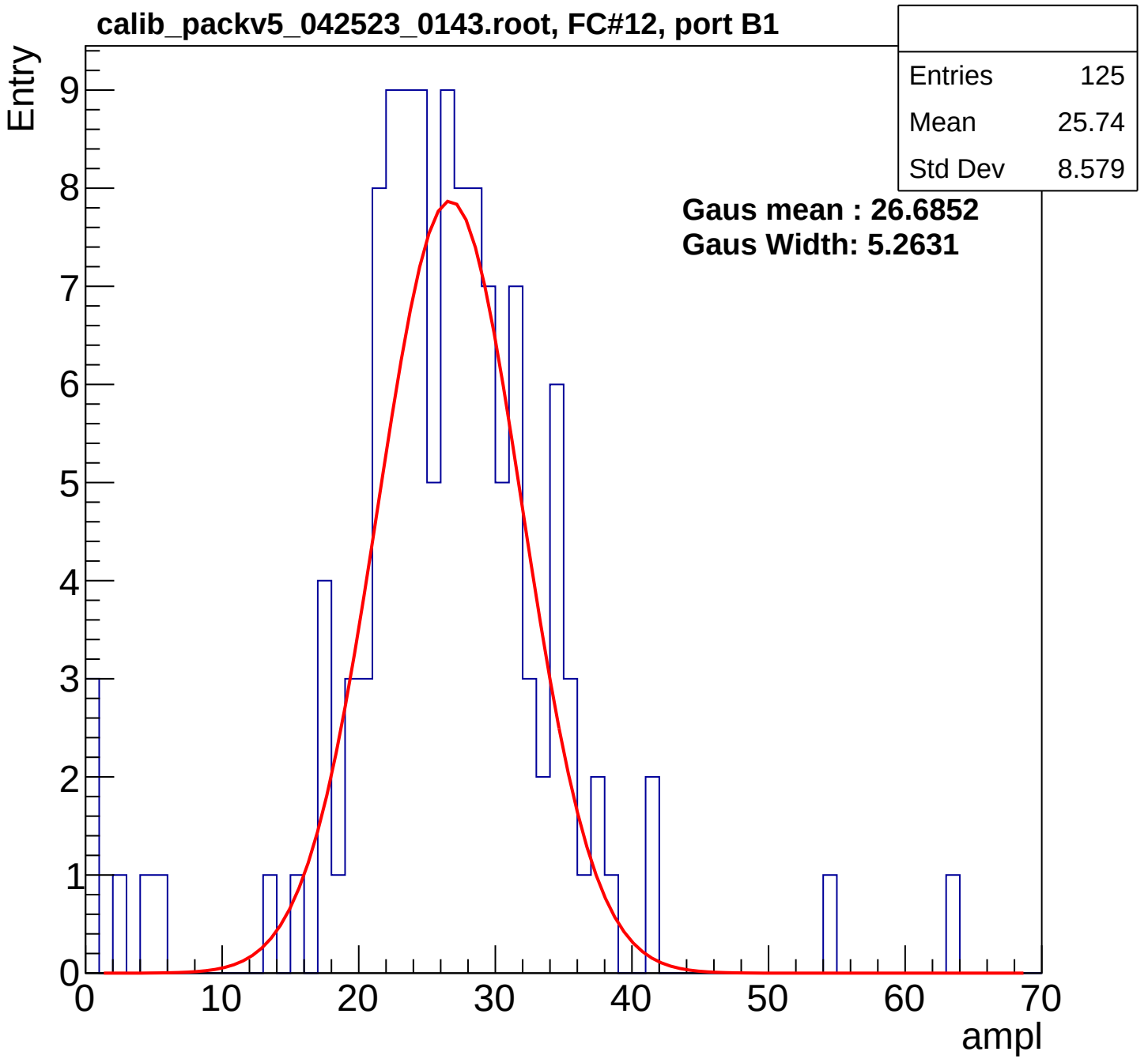
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch126, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 125
Mean 25.74
Std Dev 8.579
Gaus mean : 26.6852
Gaus Width: 5.2631
Entry
ampl
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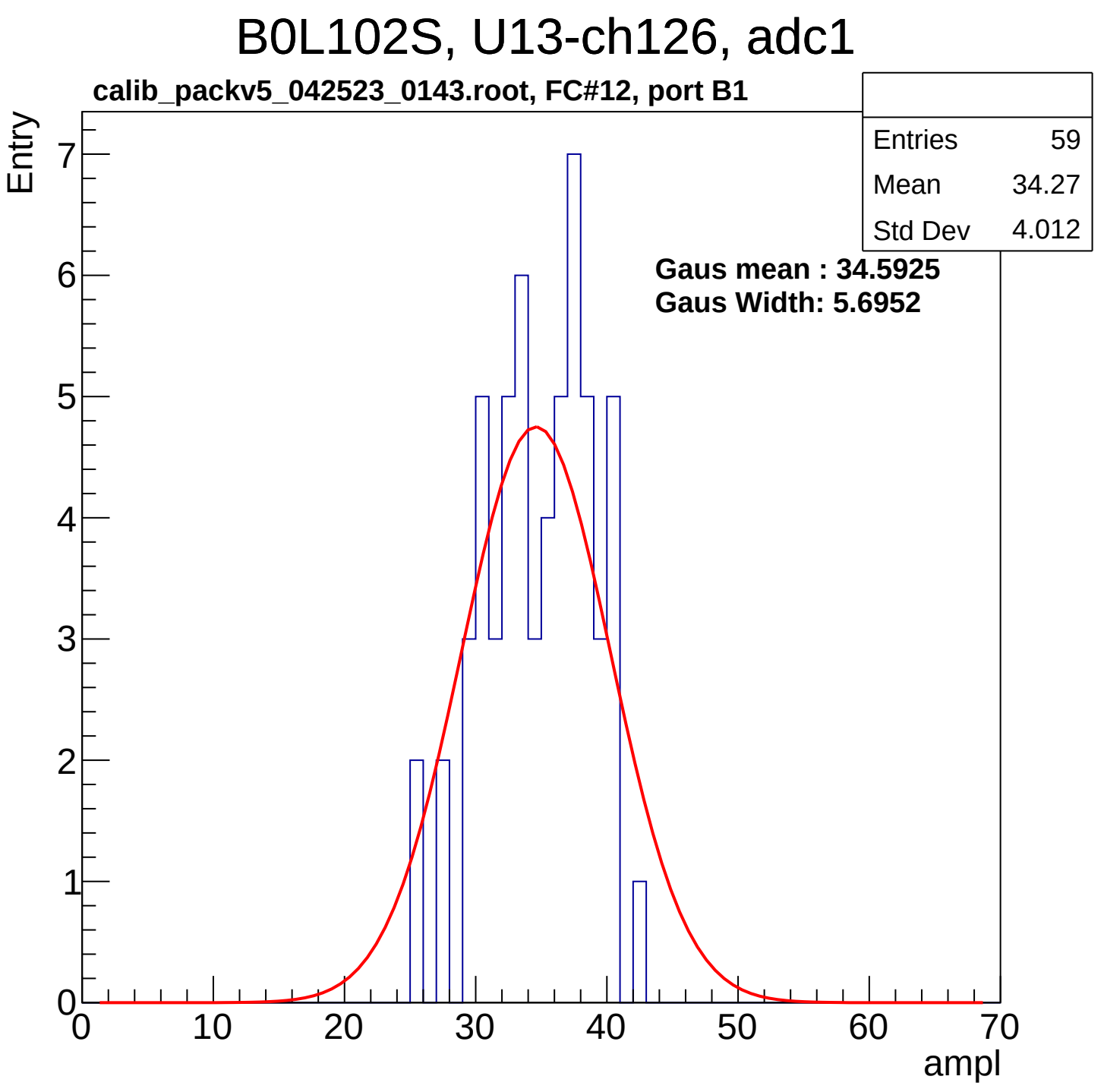
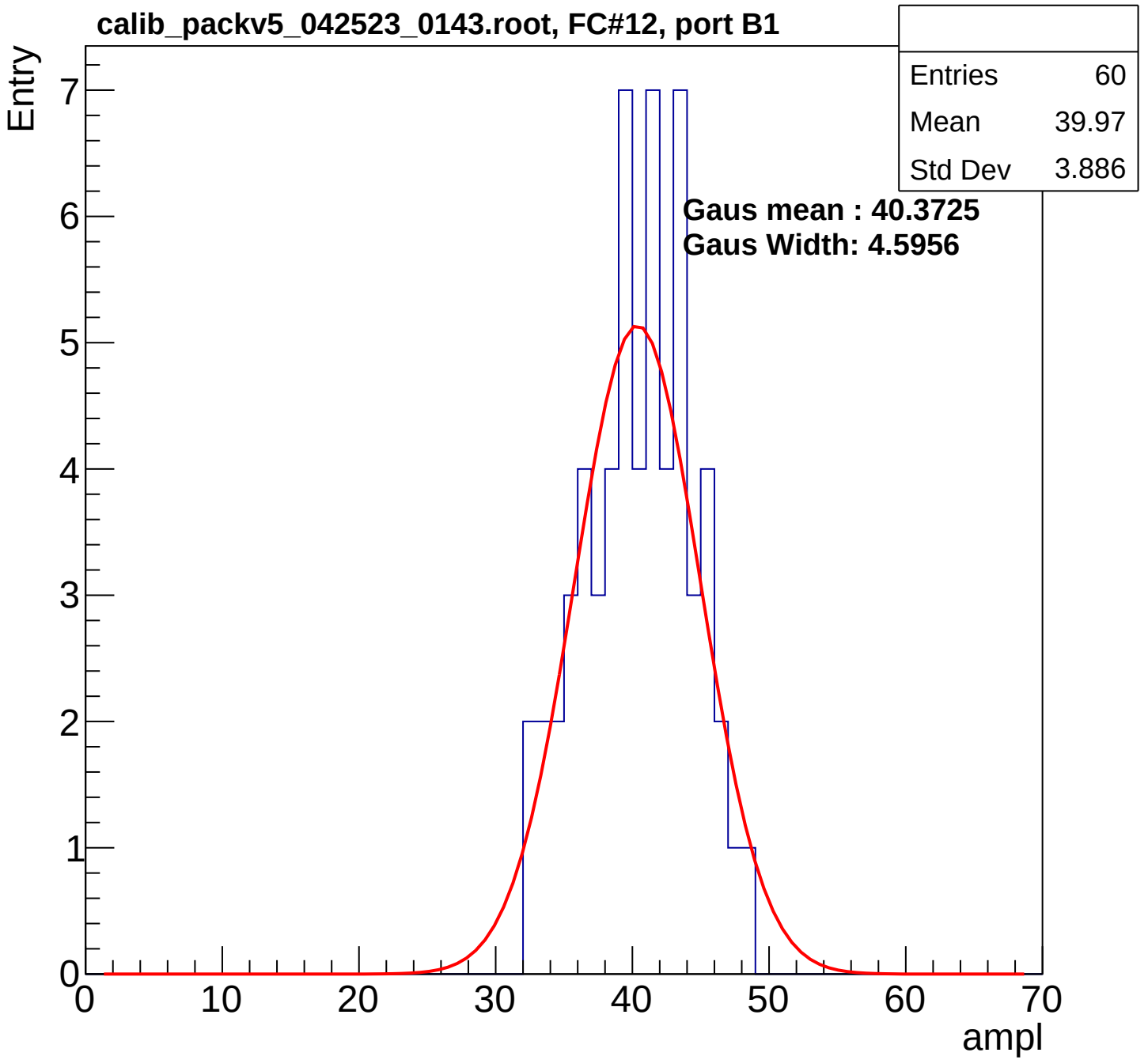
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch126, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 34.27
Std Dev 4.012
Gaus mean : 34.5925
Gaus Width: 5.6952
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch126, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 39.97
Std Dev 3.886
Gaus mean : 40.3725
Gaus Width: 4.5956
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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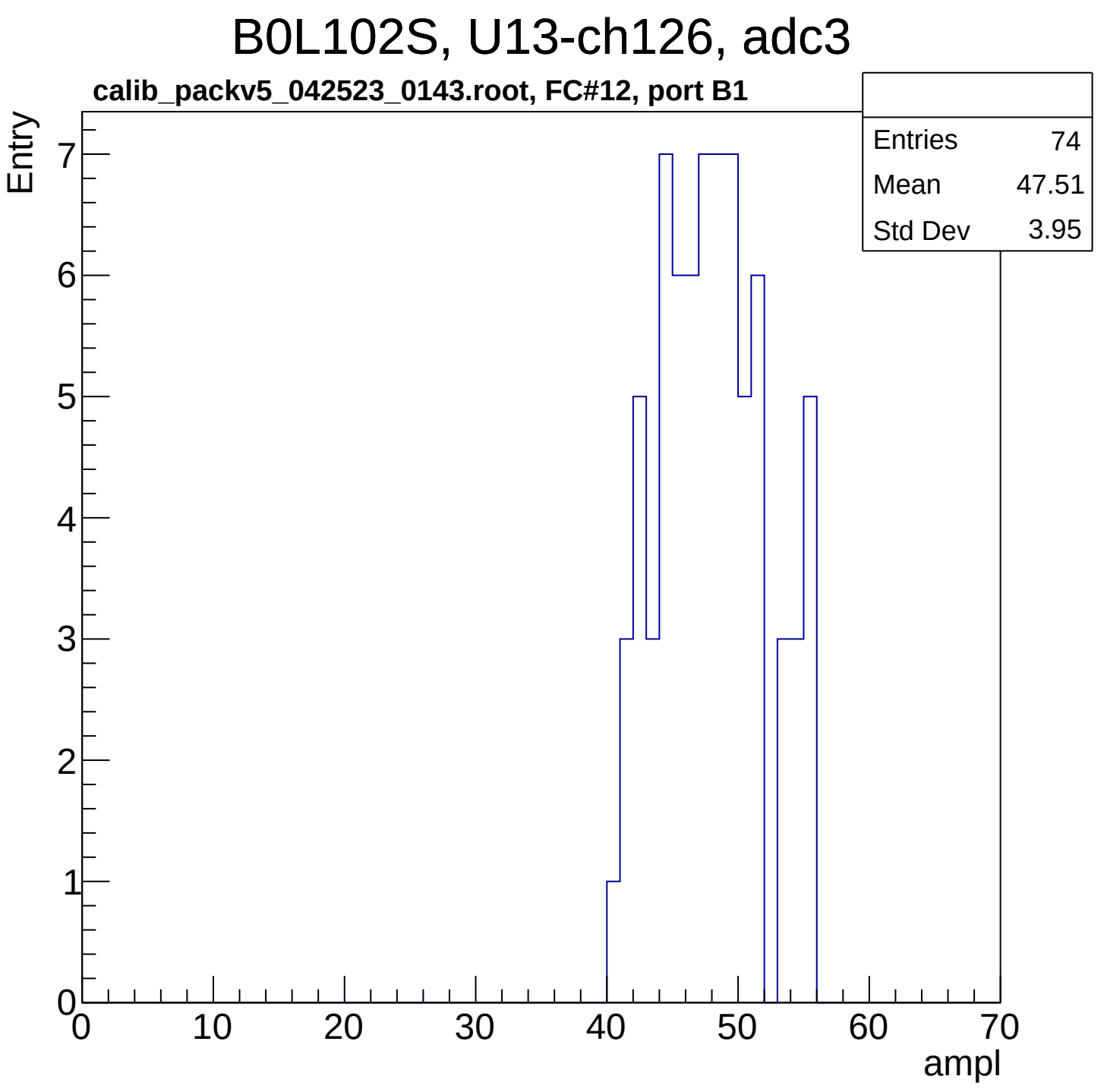
B0L102S, U13-ch126, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 47.51
Std Dev 3.95
Entry
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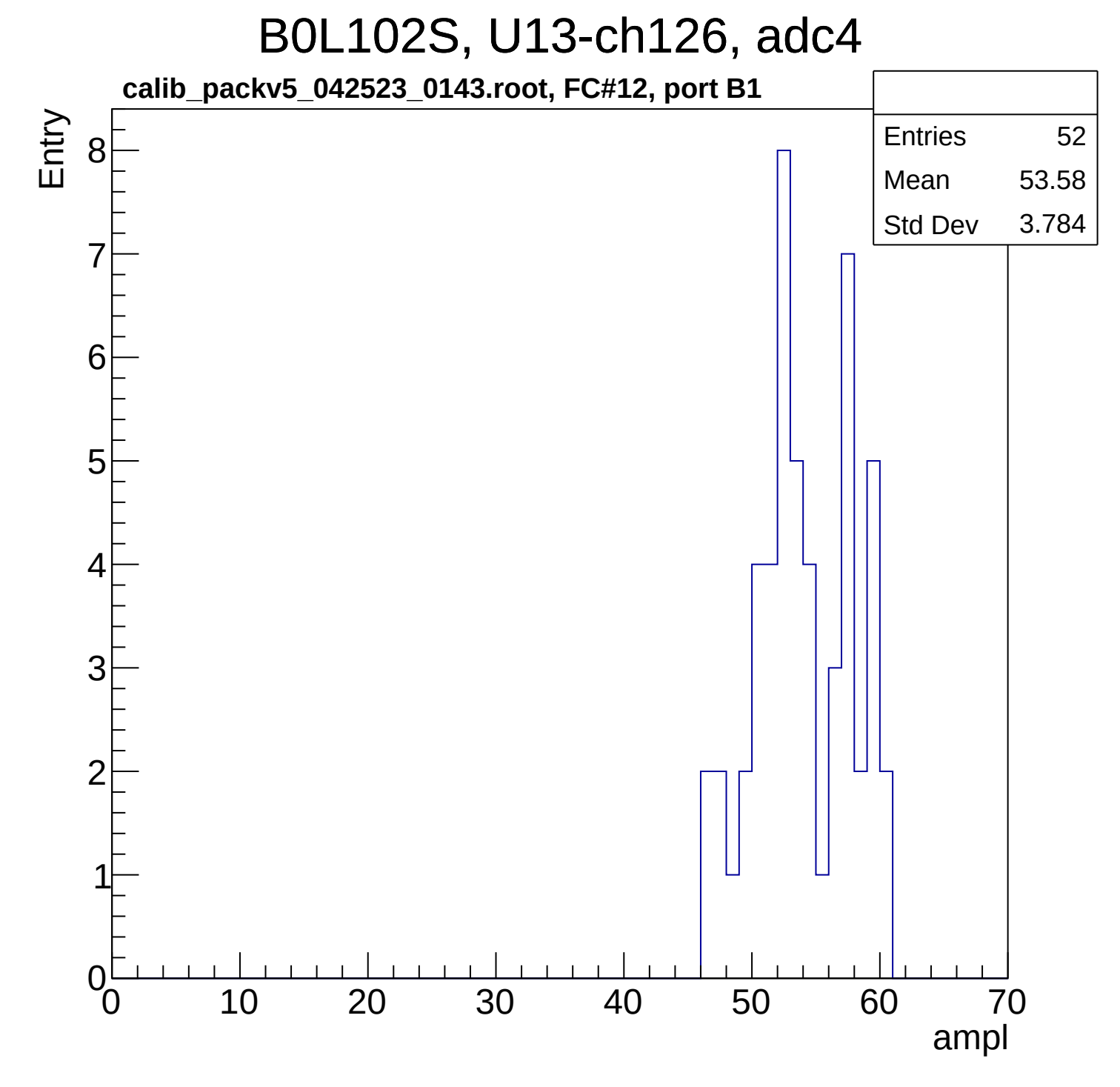
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch126, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 53.58
Std Dev 3.784
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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40
50
60
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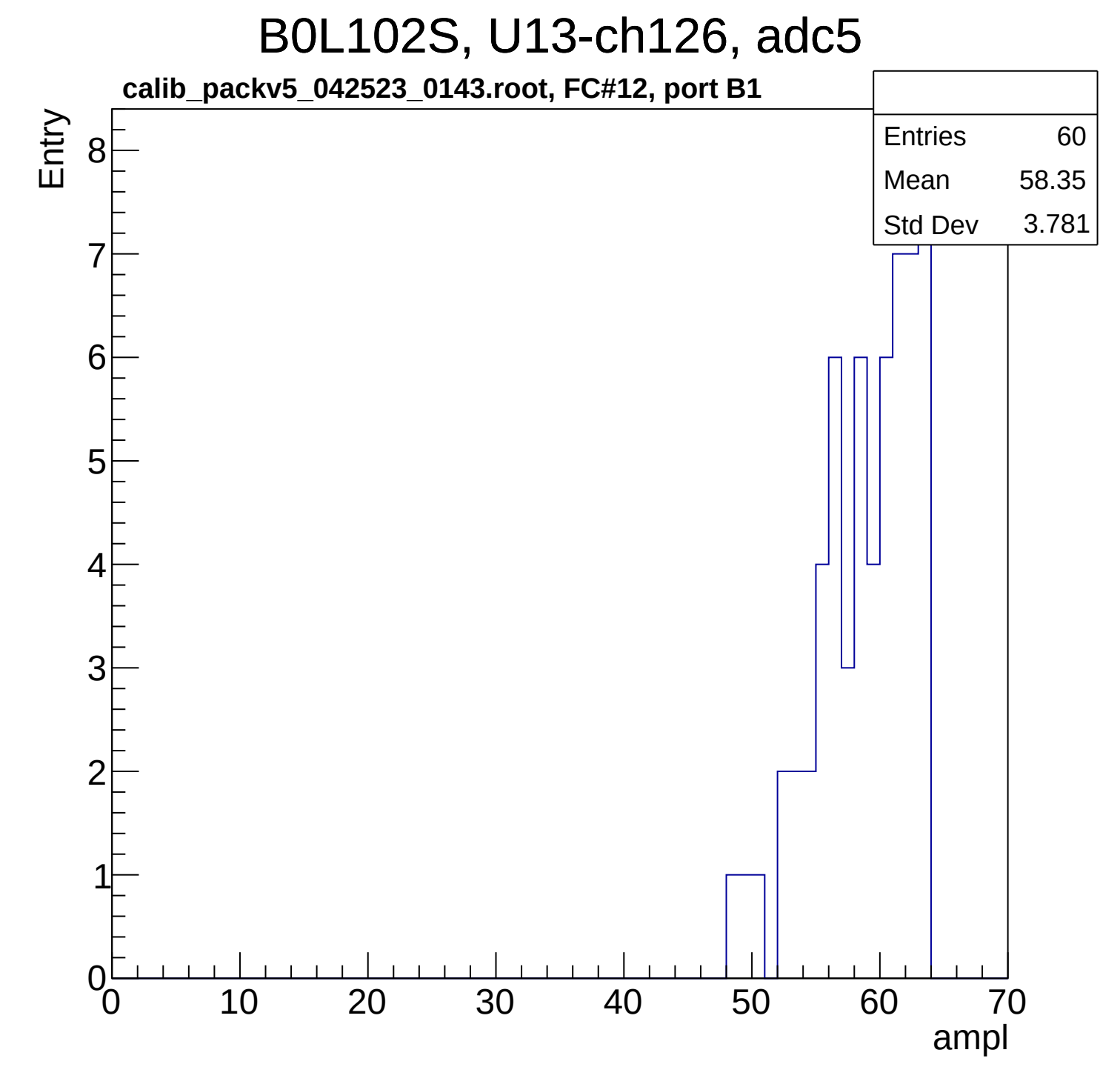
B0L102S, U13-ch126, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.35
Std Dev 3.781
Entry
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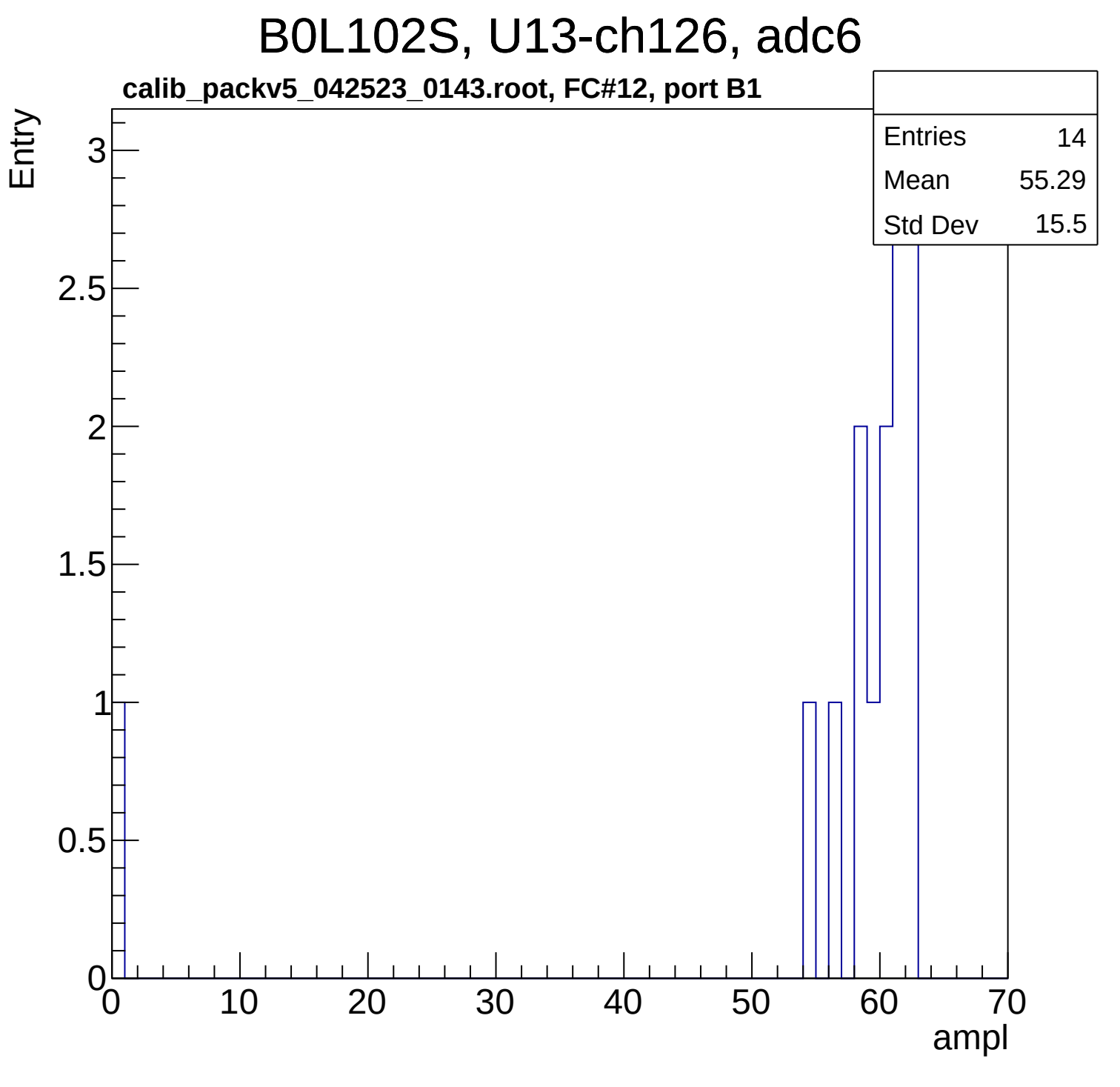
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch126, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 55.29
Std Dev 15.5
Entry
ampl
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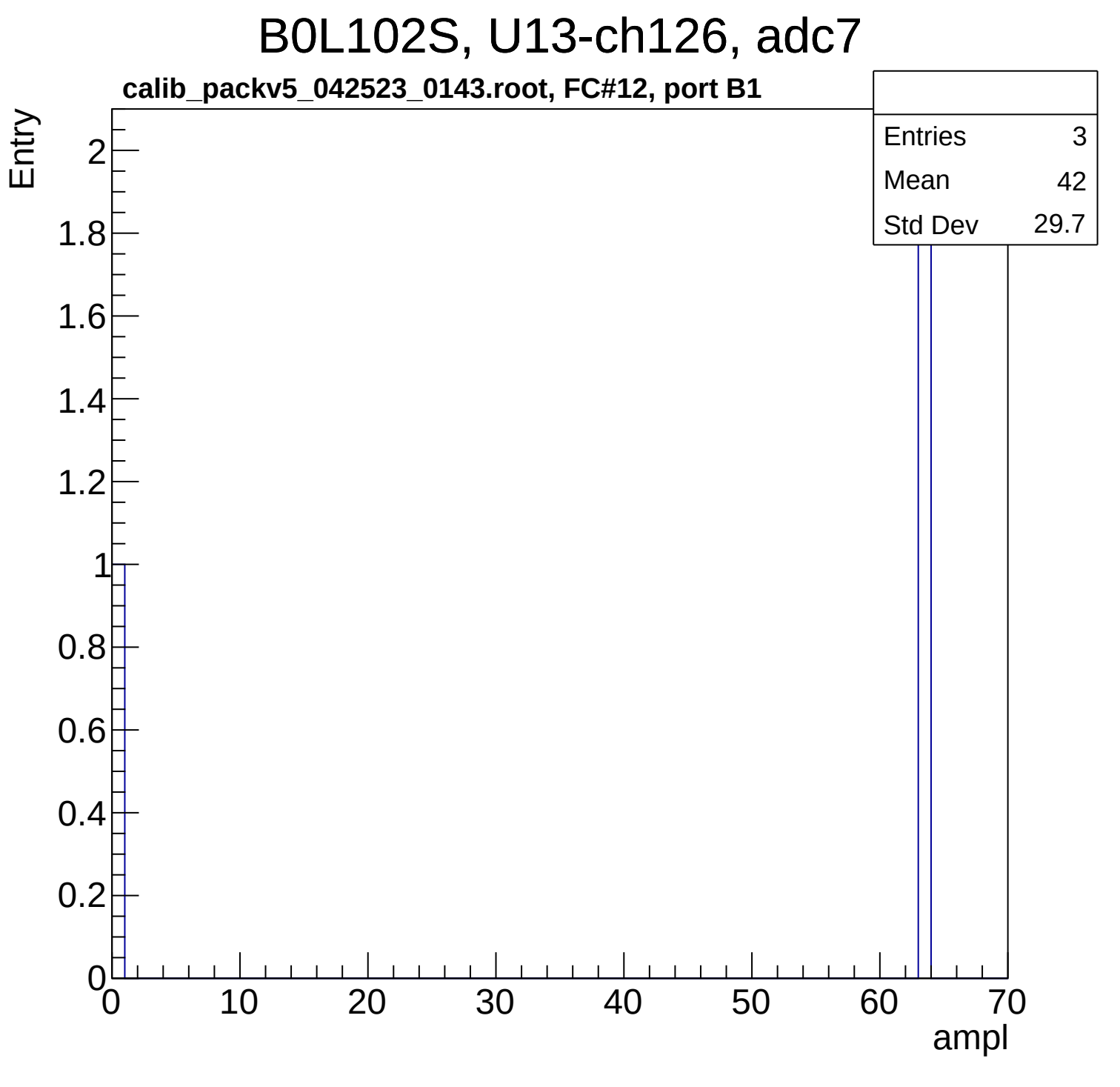
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U13-ch126, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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10
20
30
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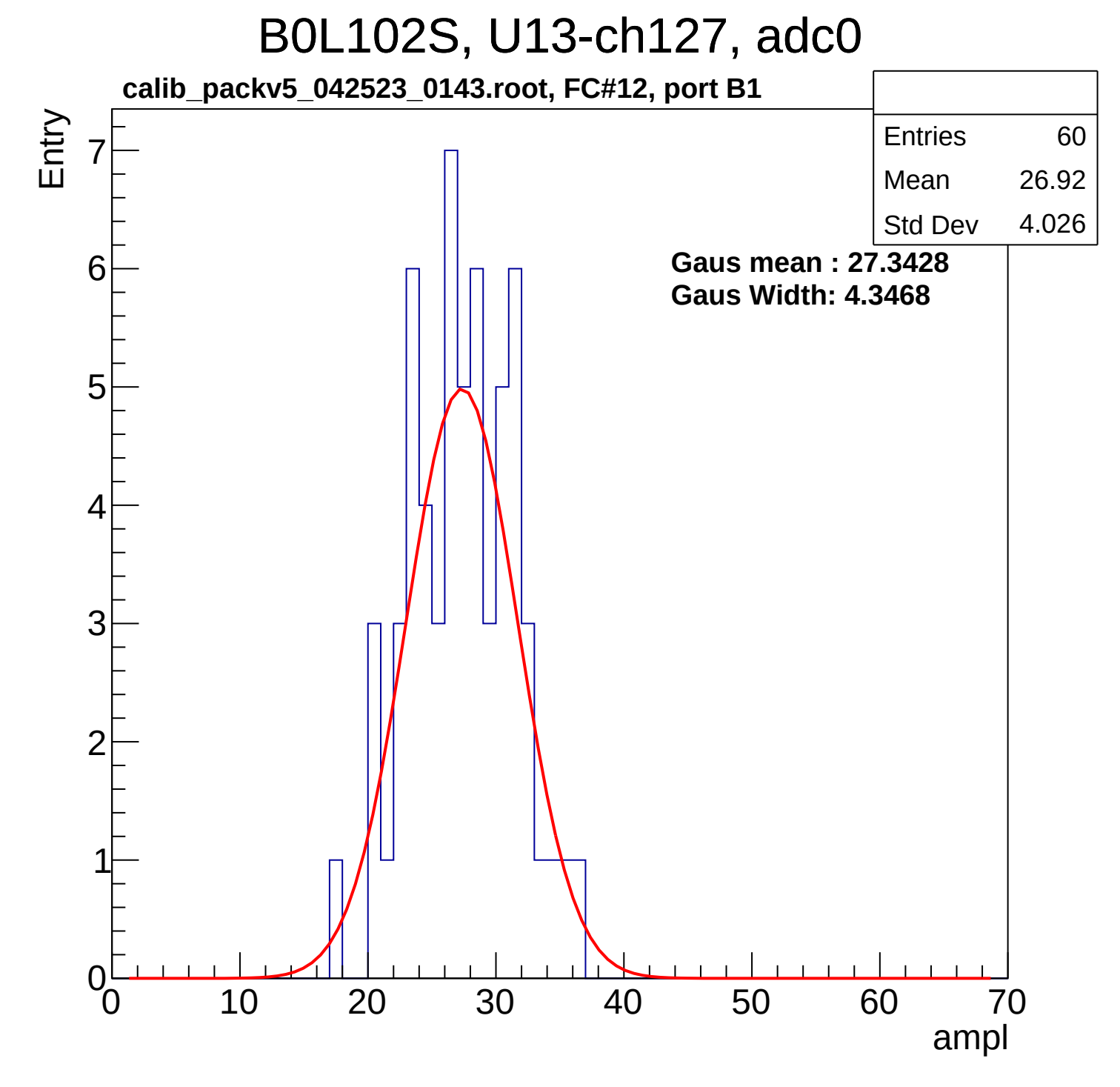
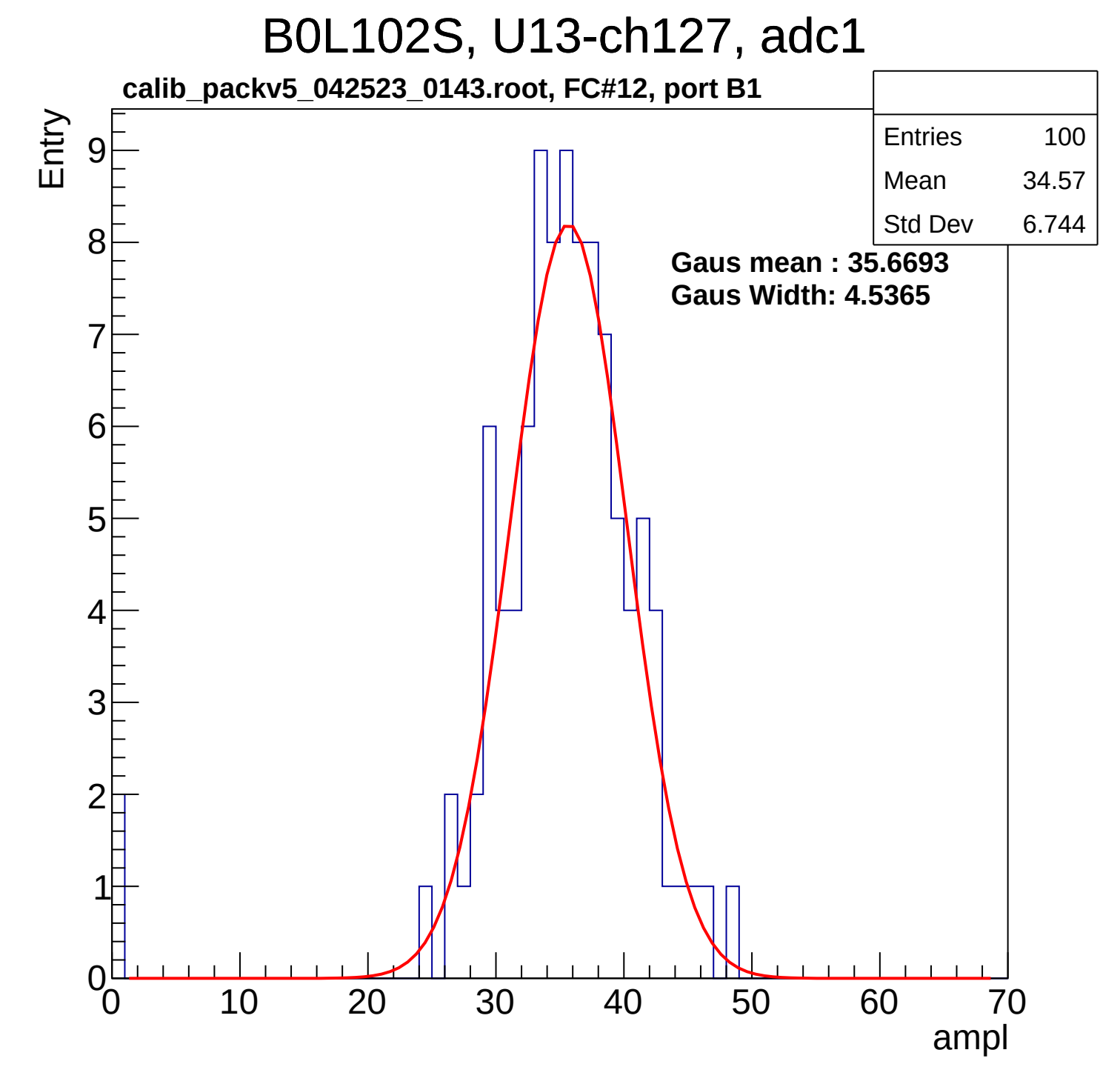
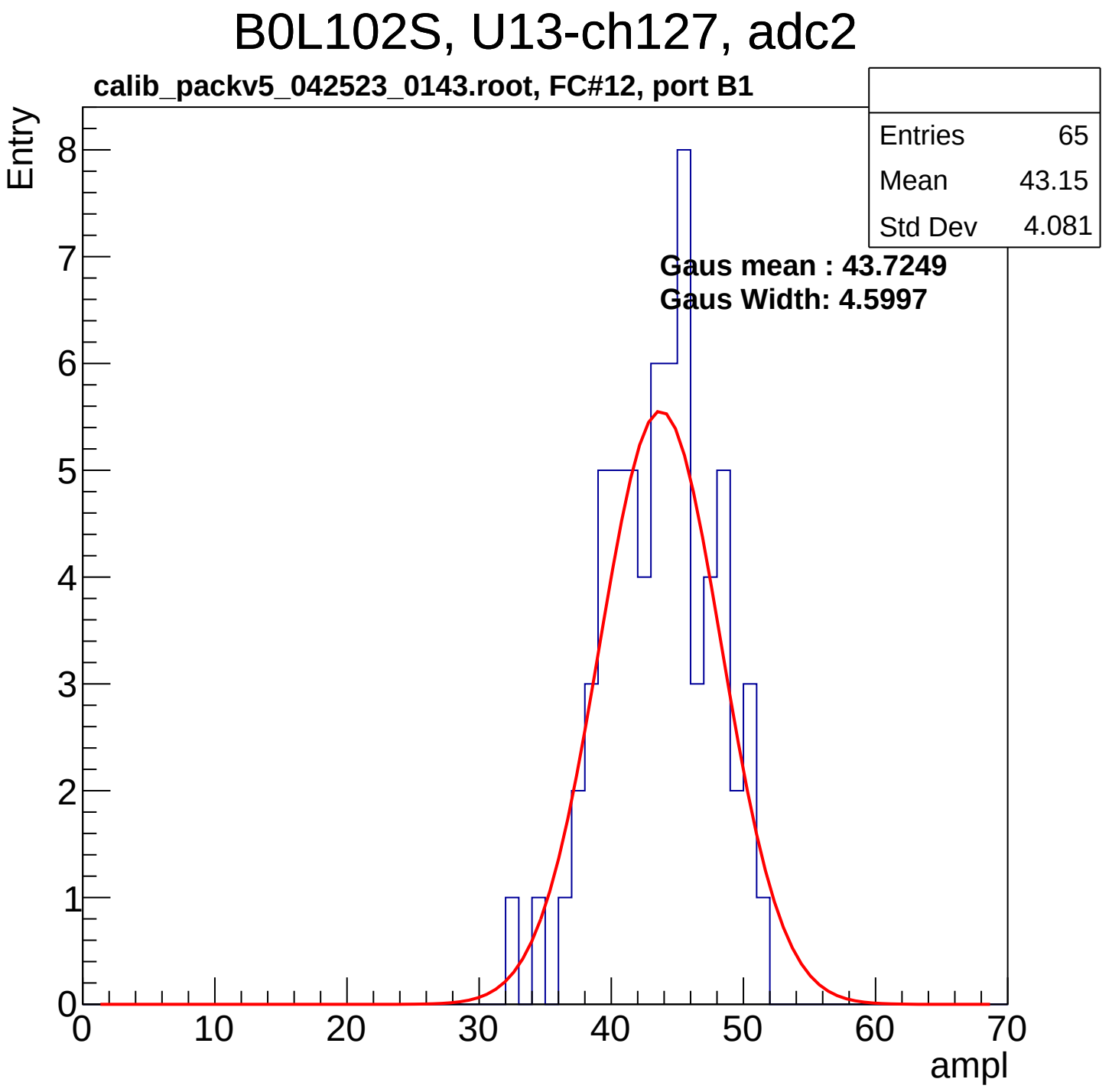
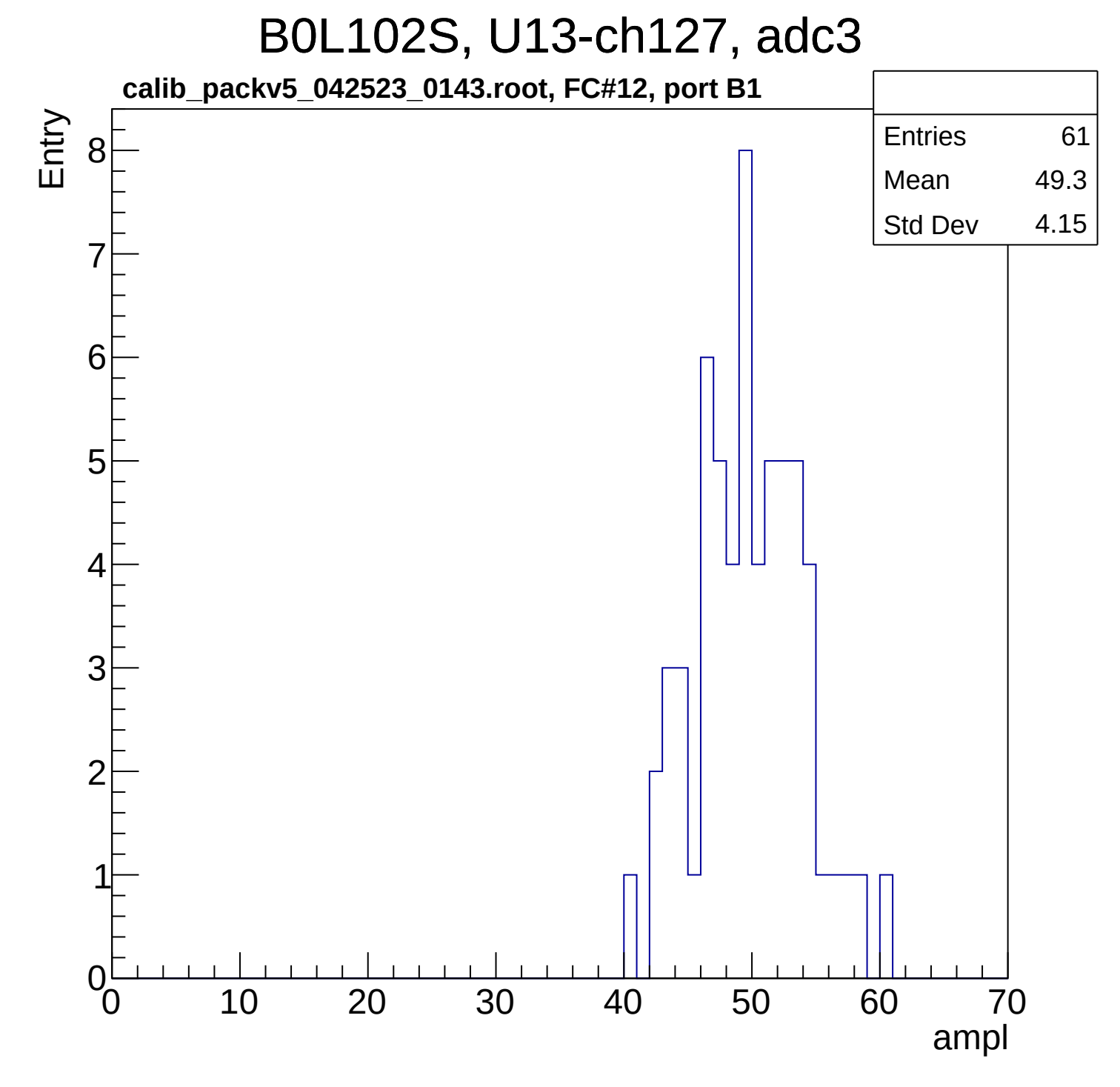
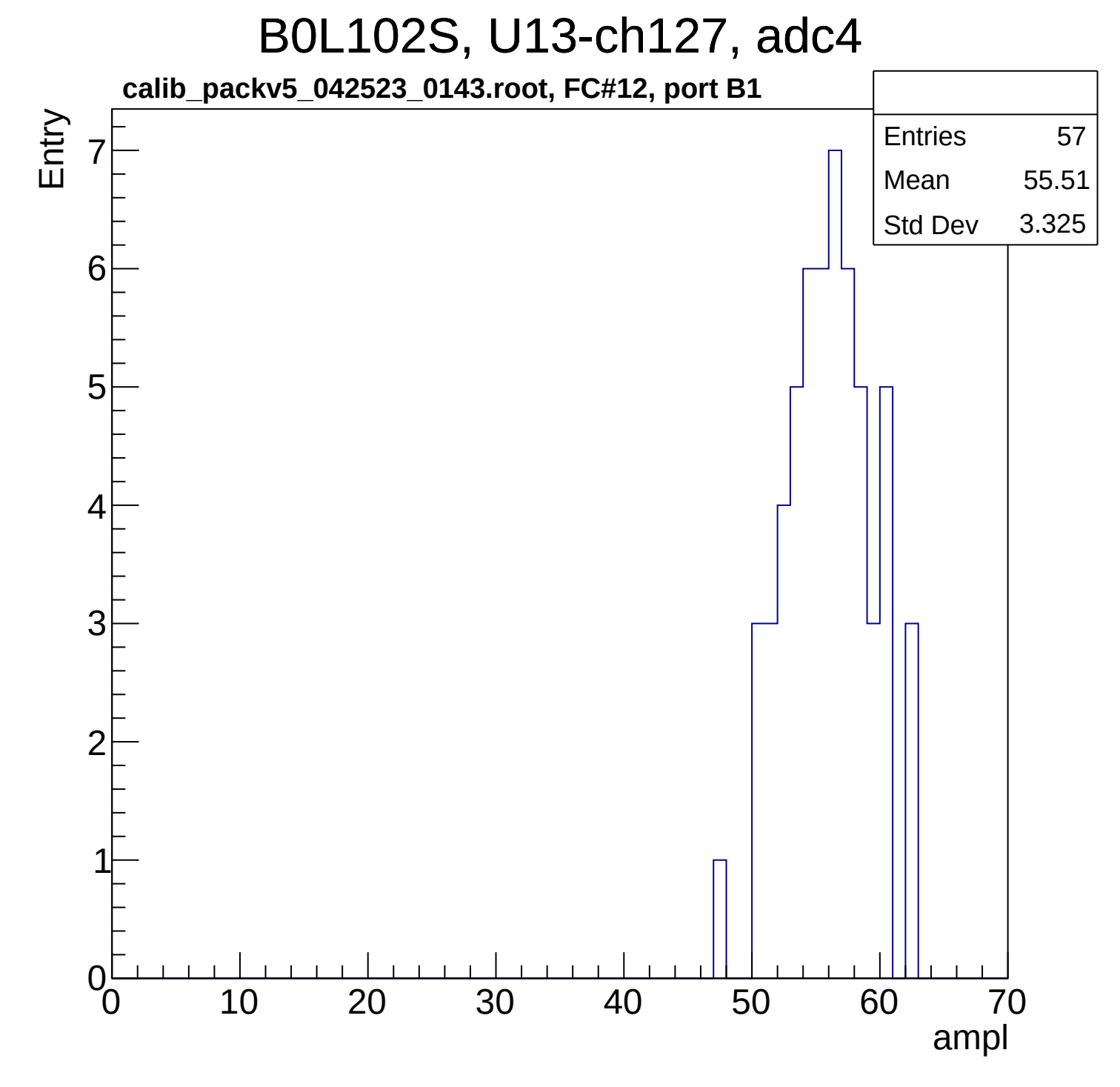
50
60
70
ampl

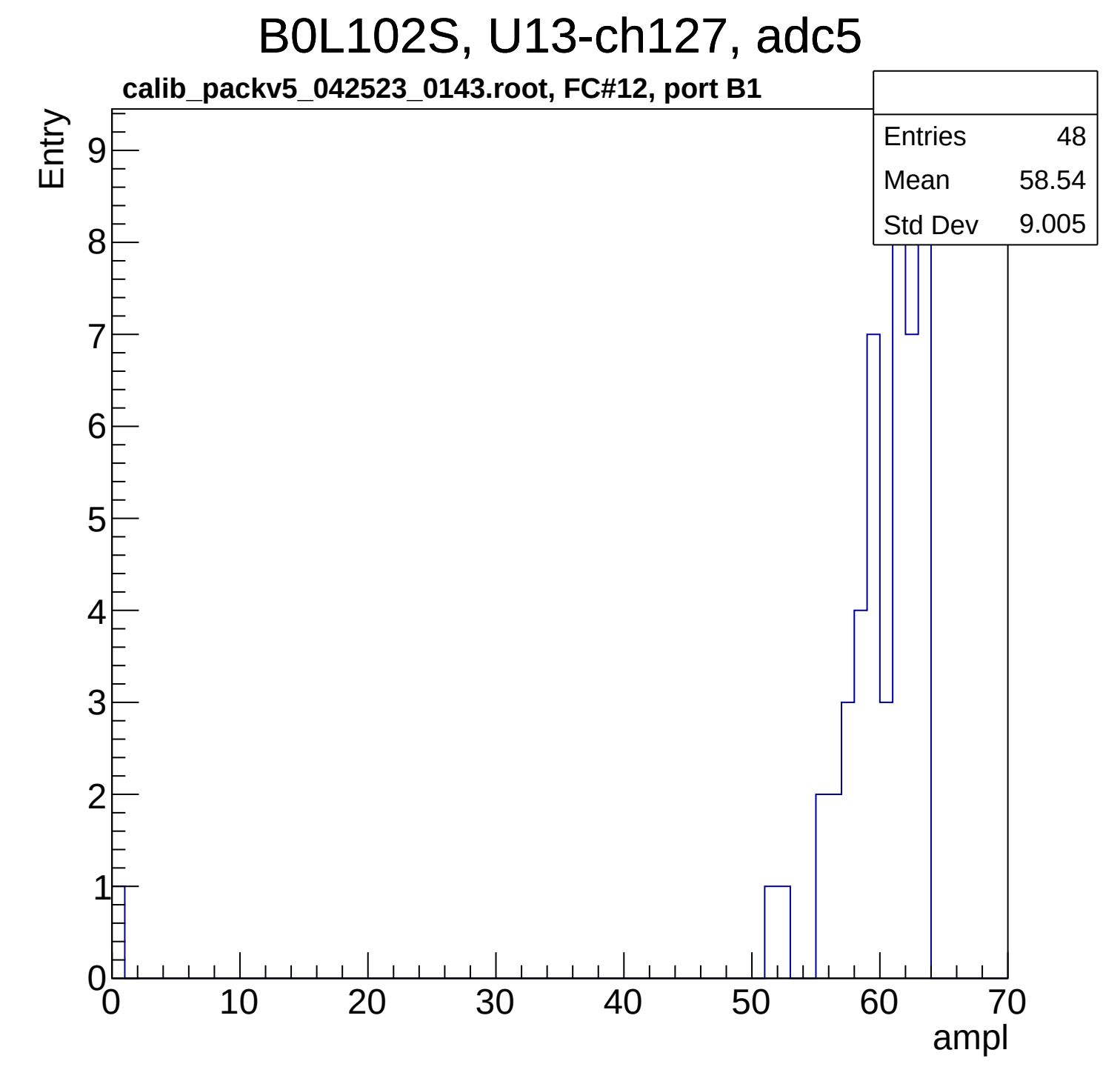

B0L102S, U13-ch127, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 26.92
Std Dev 4.026
Gaus mean : 27.3428
Gaus Width: 4.3468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

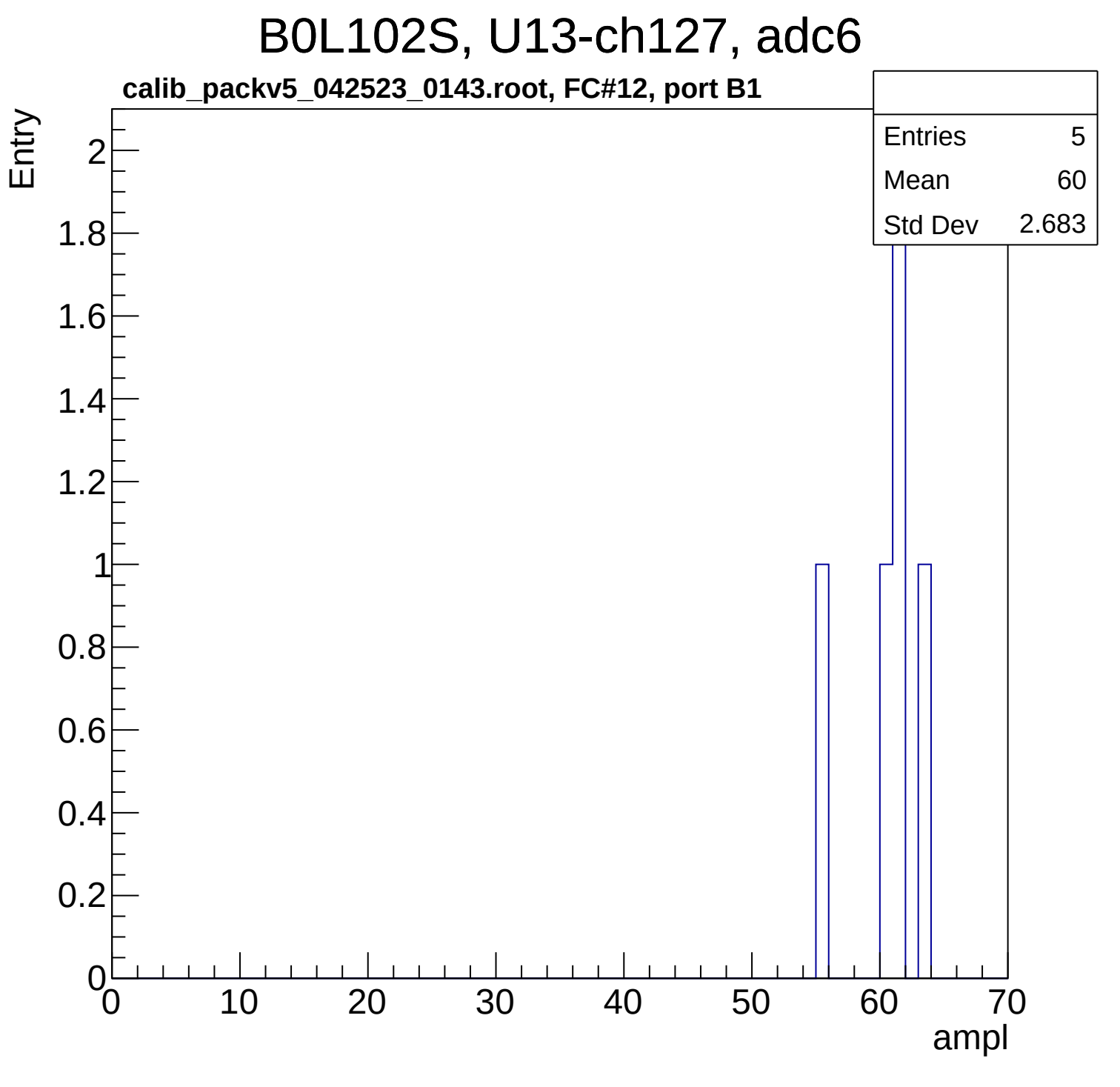

B0L102S, U13-ch127, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 100
Mean 34.57
Std Dev 6.744
Gaus mean : 35.6693
Gaus Width: 4.5365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

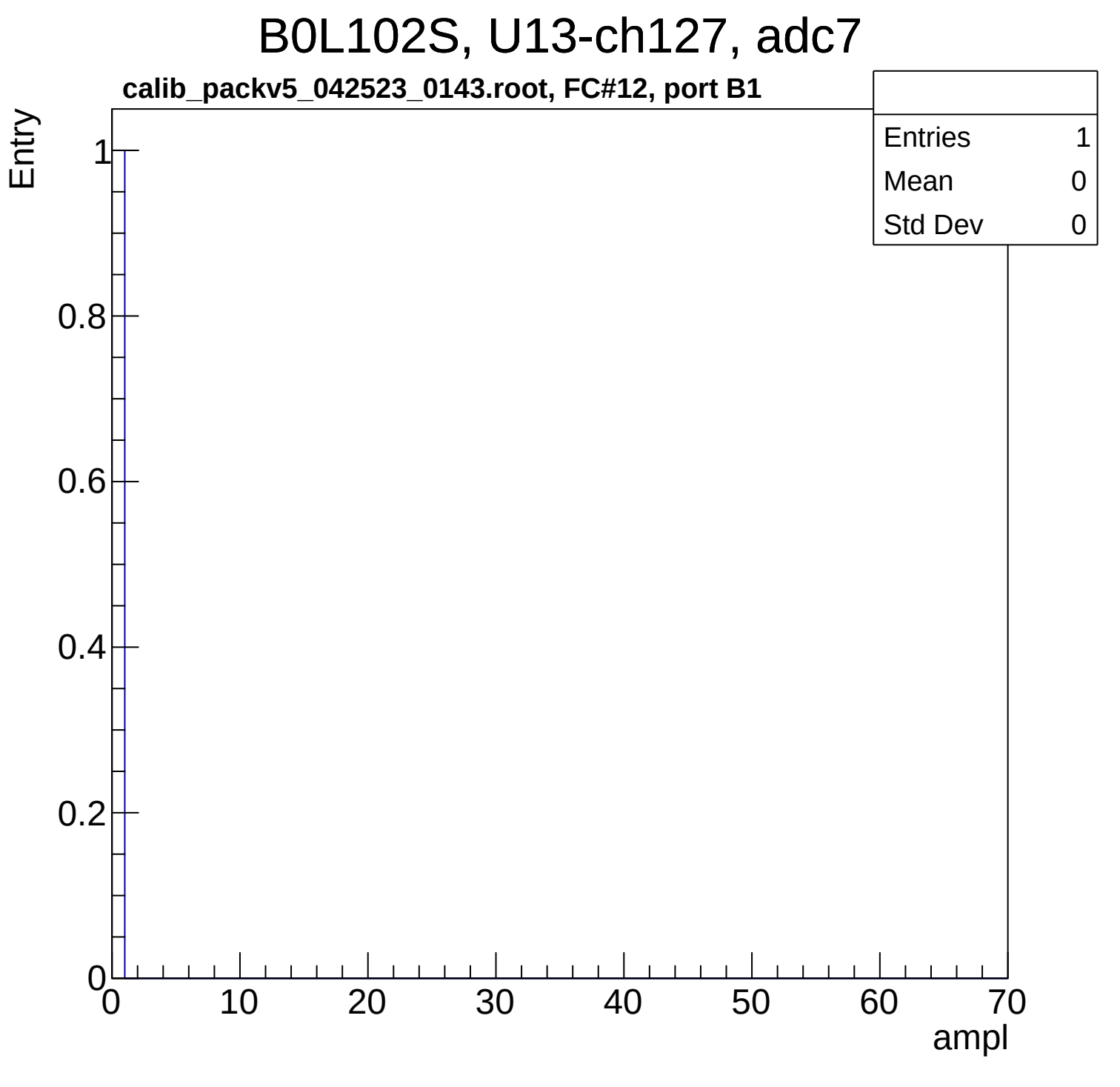

B0L102S, U13-ch127, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.15
Std Dev 4.081
Gaus mean : 43.7249
Gaus Width: 4.5997
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

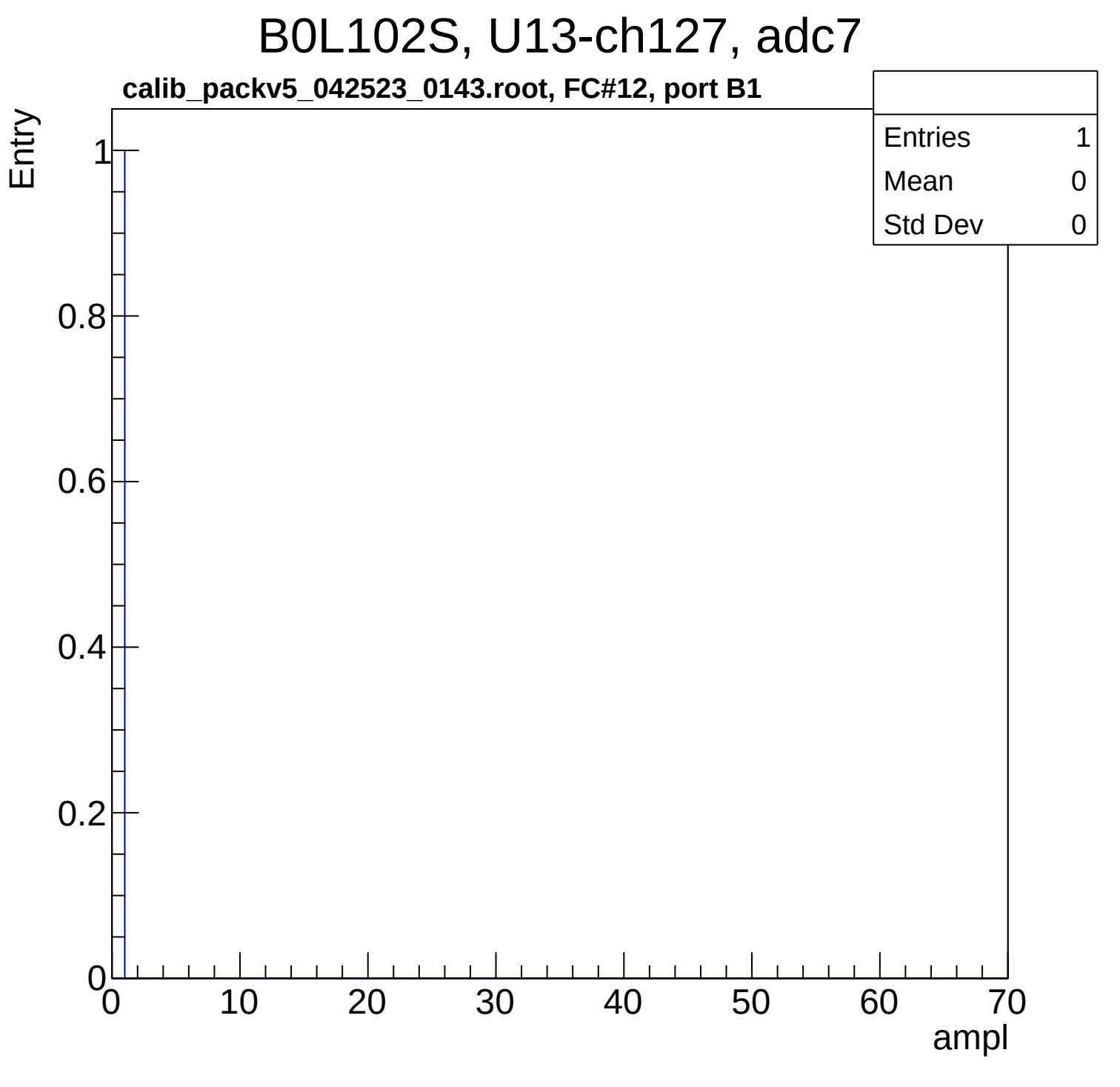

B0L102S, U13-ch127, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 49.3
Std Dev 4.15
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch127, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.51
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U13-ch127, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.54
Std Dev 9.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U13-ch127, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 60
Std Dev 2.683
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl