

B1L102S, U26-ch0, adc0

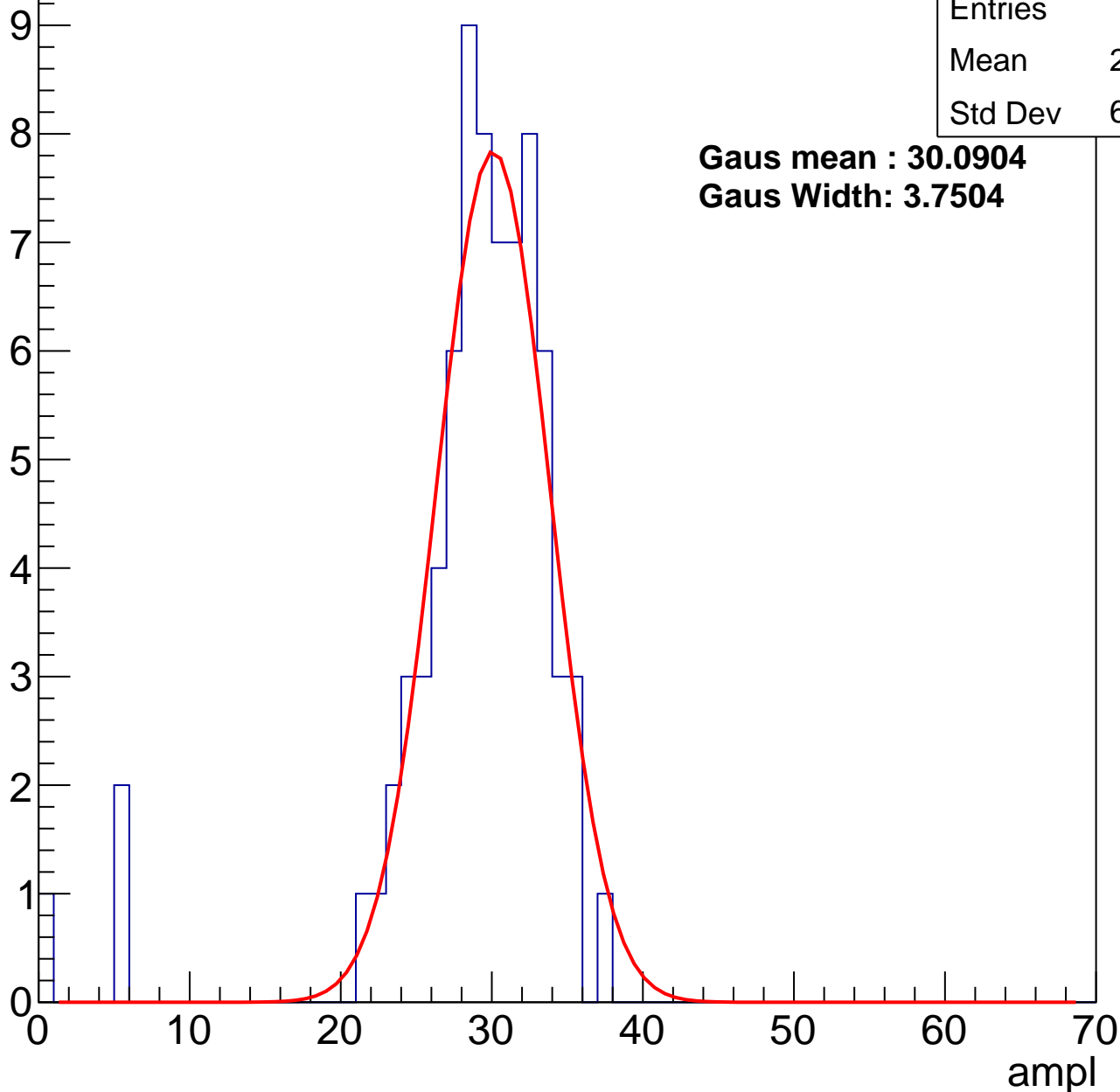
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.28
Std Dev	6.089

Gaus mean : 30.0904

Gaus Width: 3.7504



B1L102S, U26-ch0, adc1

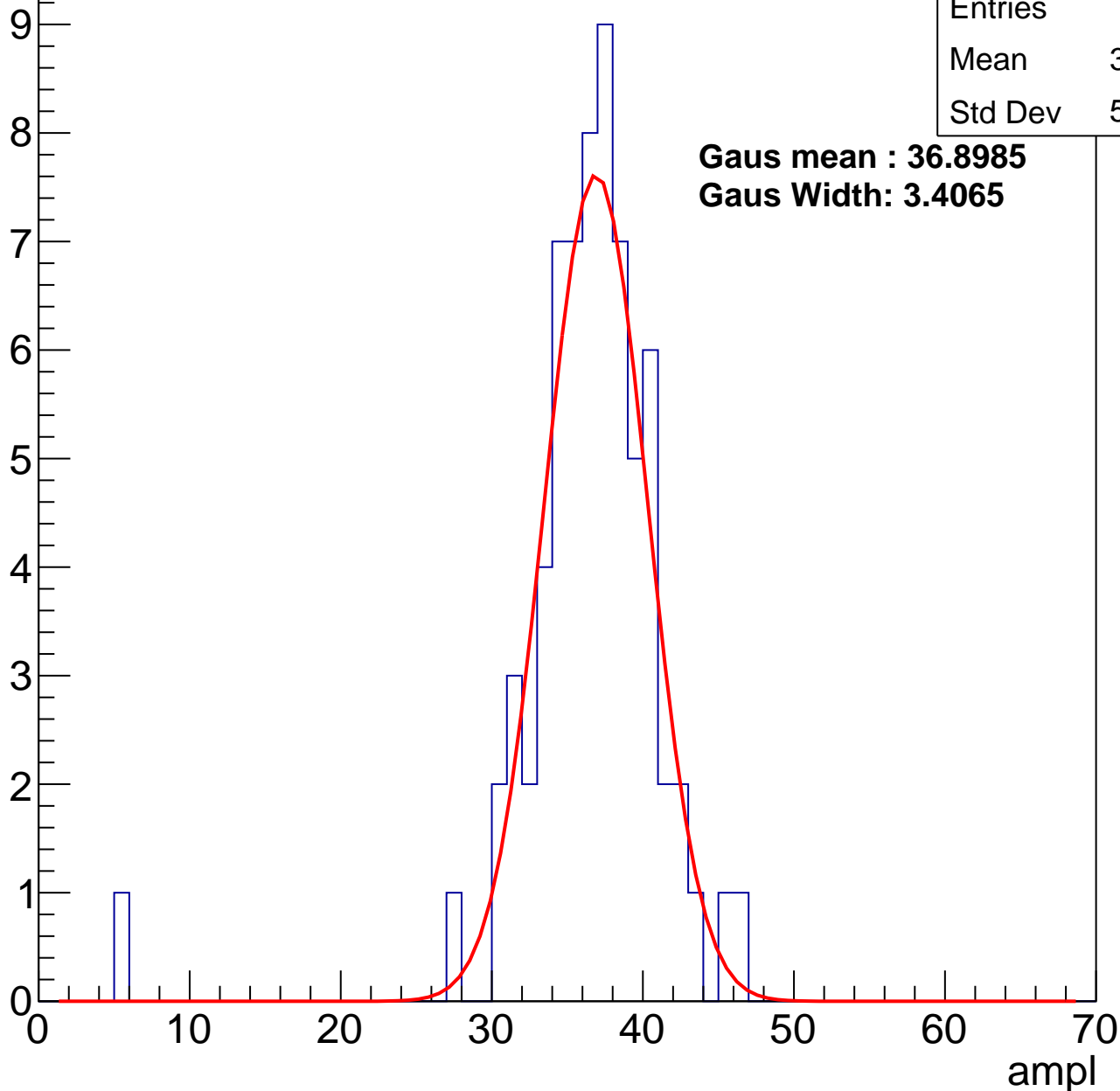
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.03
Std Dev	5.139

Gaus mean : 36.8985

Gaus Width: 3.4065



B1L102S, U26-ch0, adc2

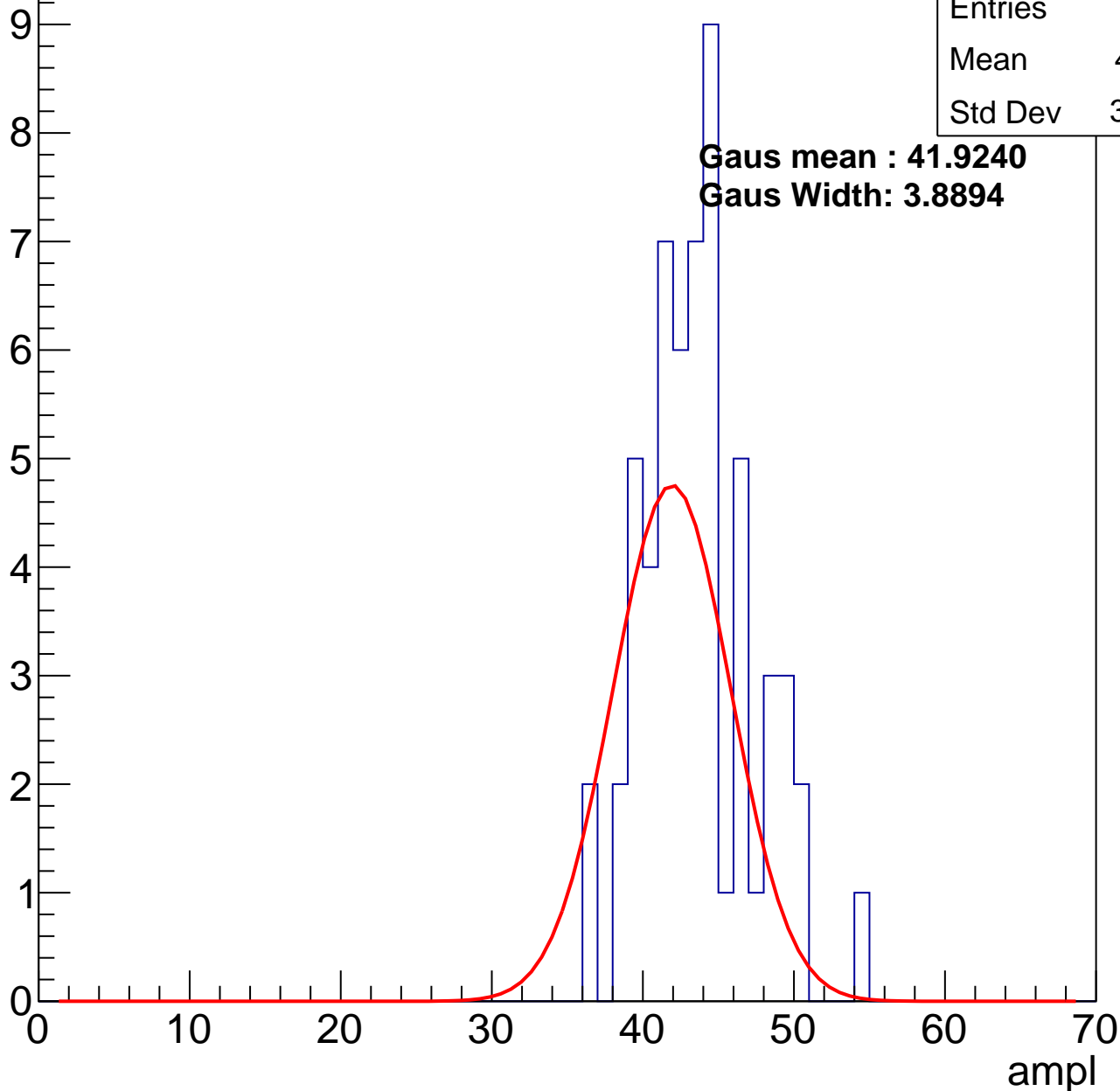
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.21
Std Dev	3.647

Gaus mean : 41.9240

Gaus Width: 3.8894

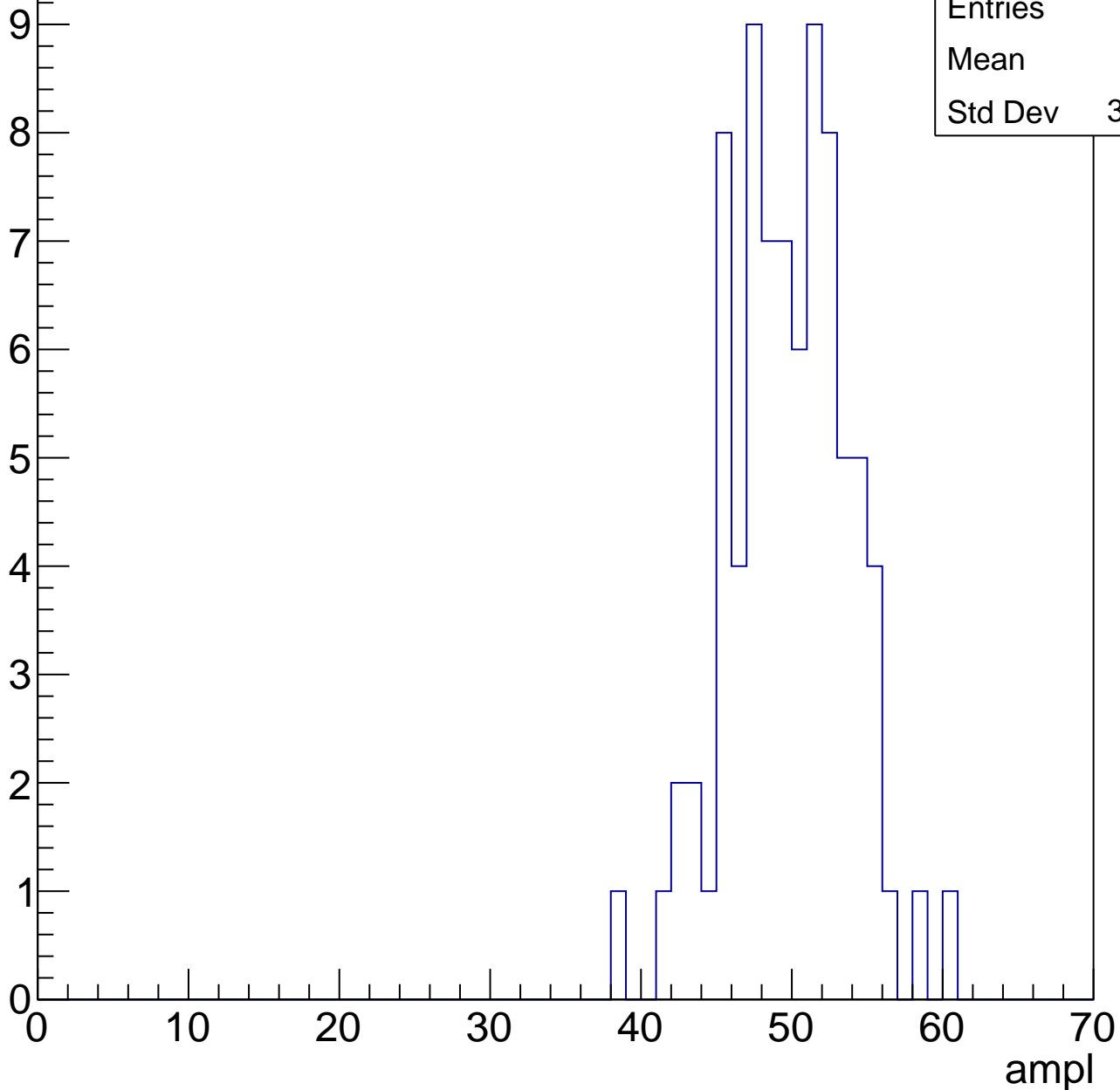


B1L102S, U26-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	49.3
Std Dev	3.972

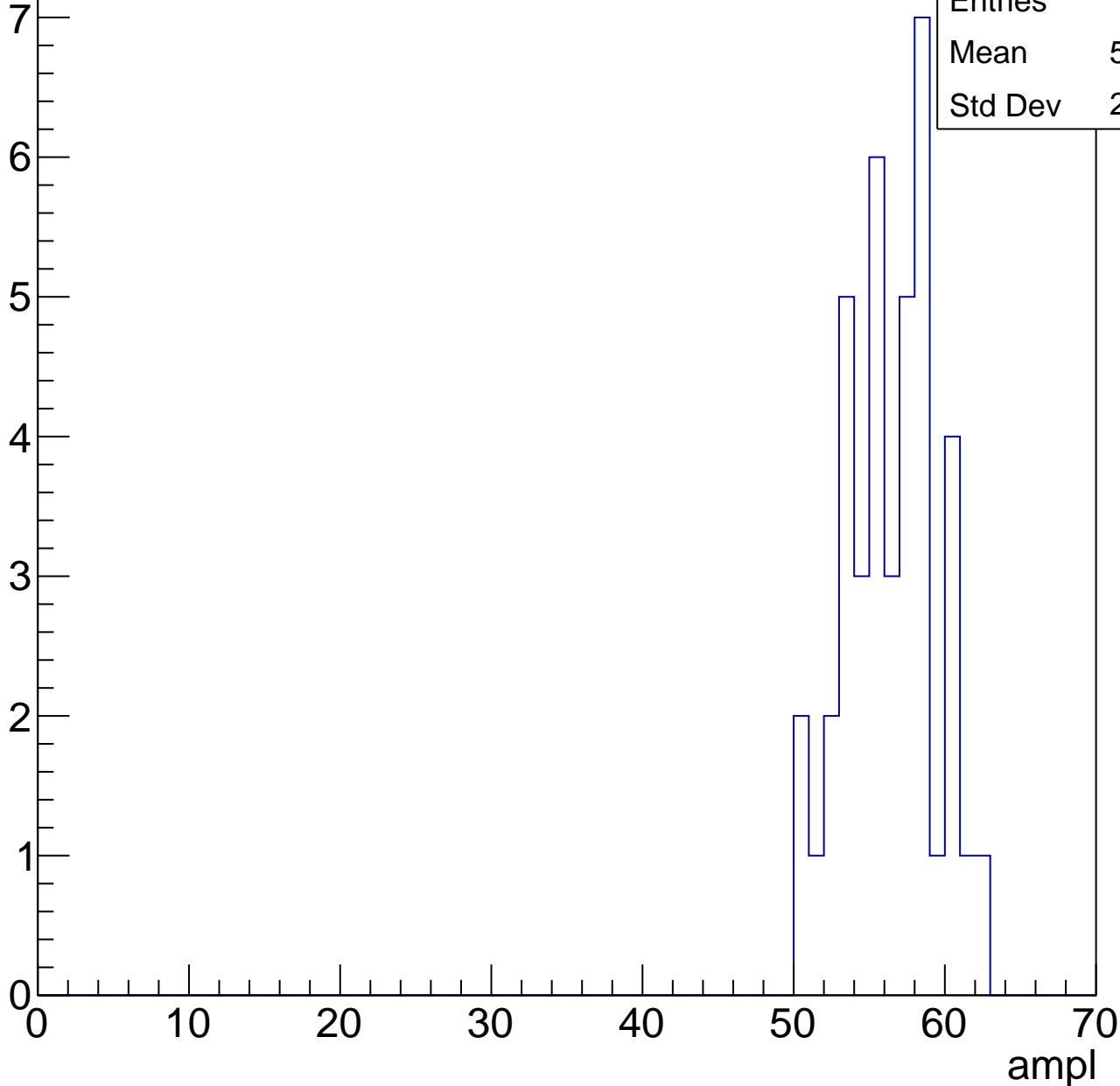


B1L102S, U26-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	55.93
Std Dev	2.958

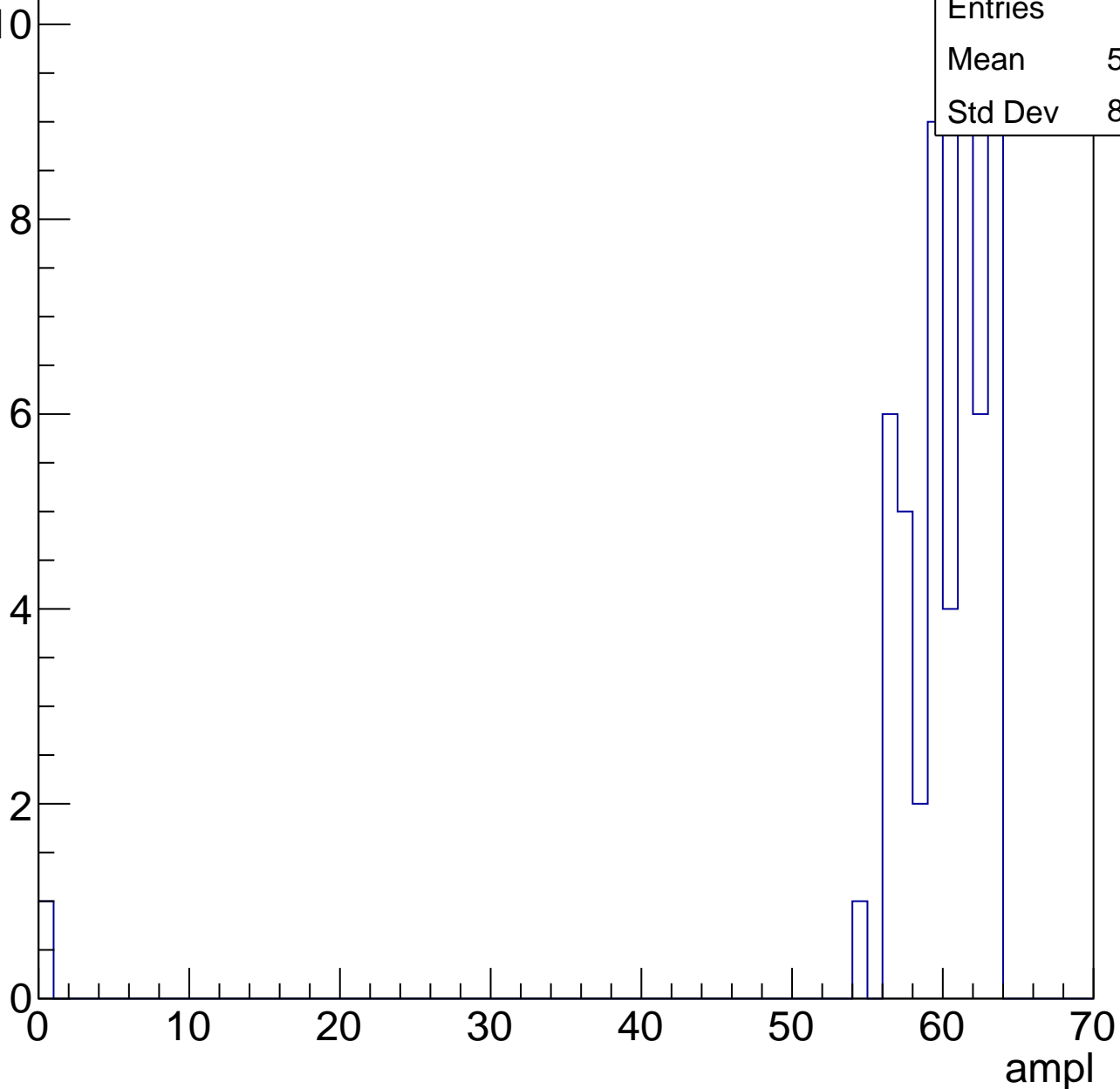


B1L102S, U26-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

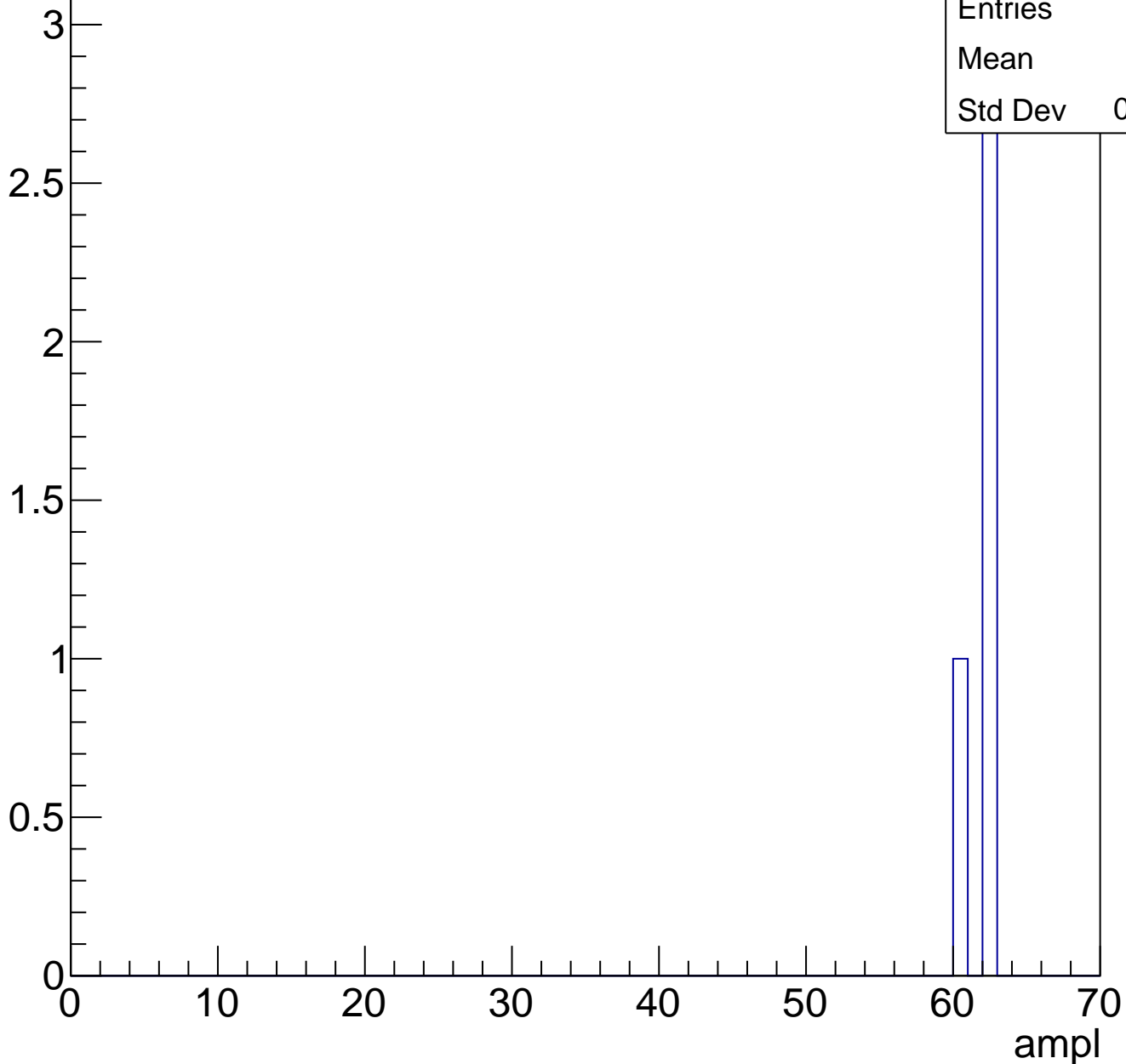
Entries	53
Mean	58.74
Std Dev	8.503



B1L102S, U26-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch1, adc0

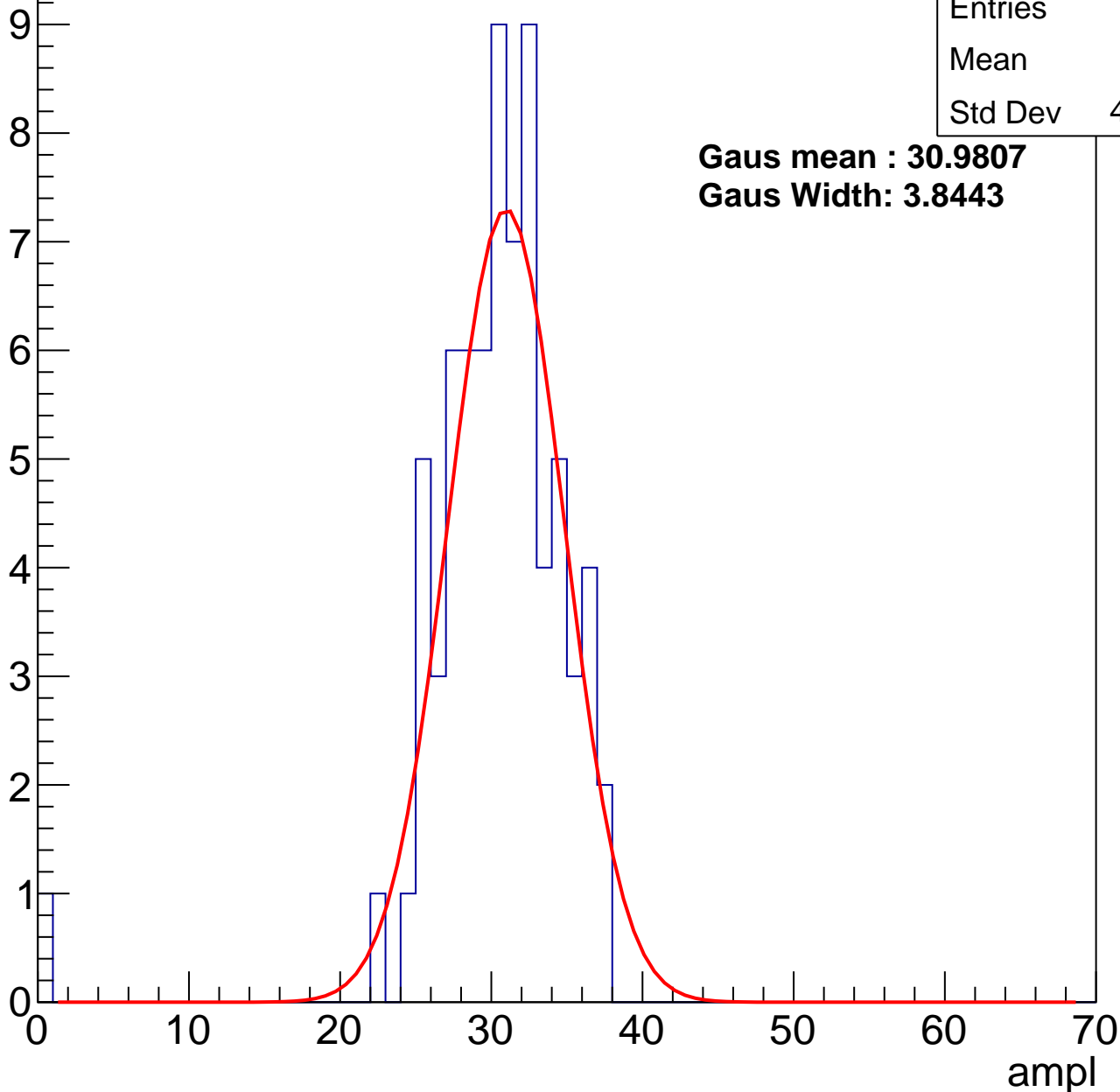
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.9
Std Dev	4.905

Gaus mean : 30.9807

Gaus Width: 3.8443



B1L102S, U26-ch1, adc1

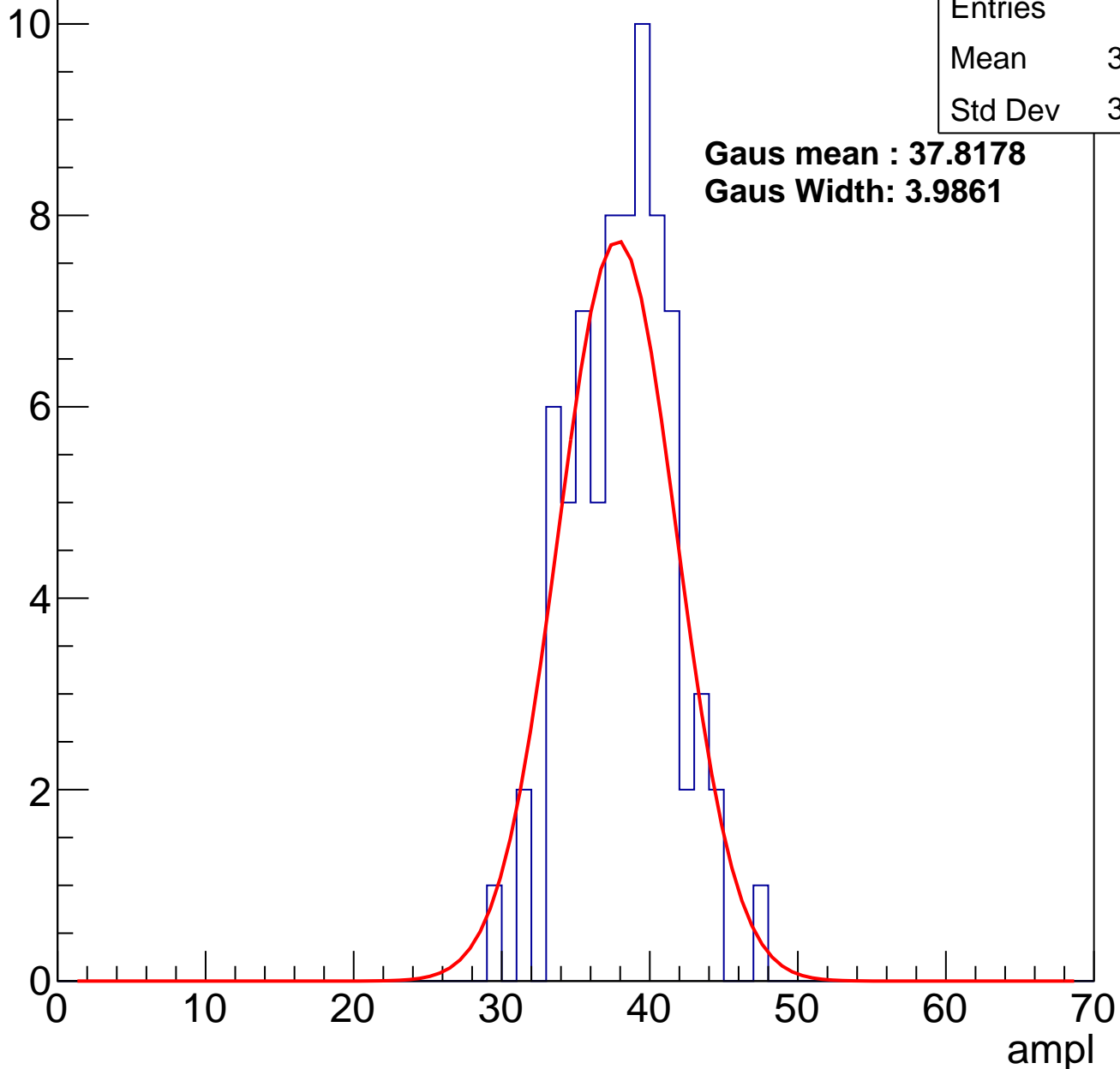
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	37.72
Std Dev	3.385

Gaus mean : 37.8178

Gaus Width: 3.9861

Entry



B1L102S, U26-ch1, adc2

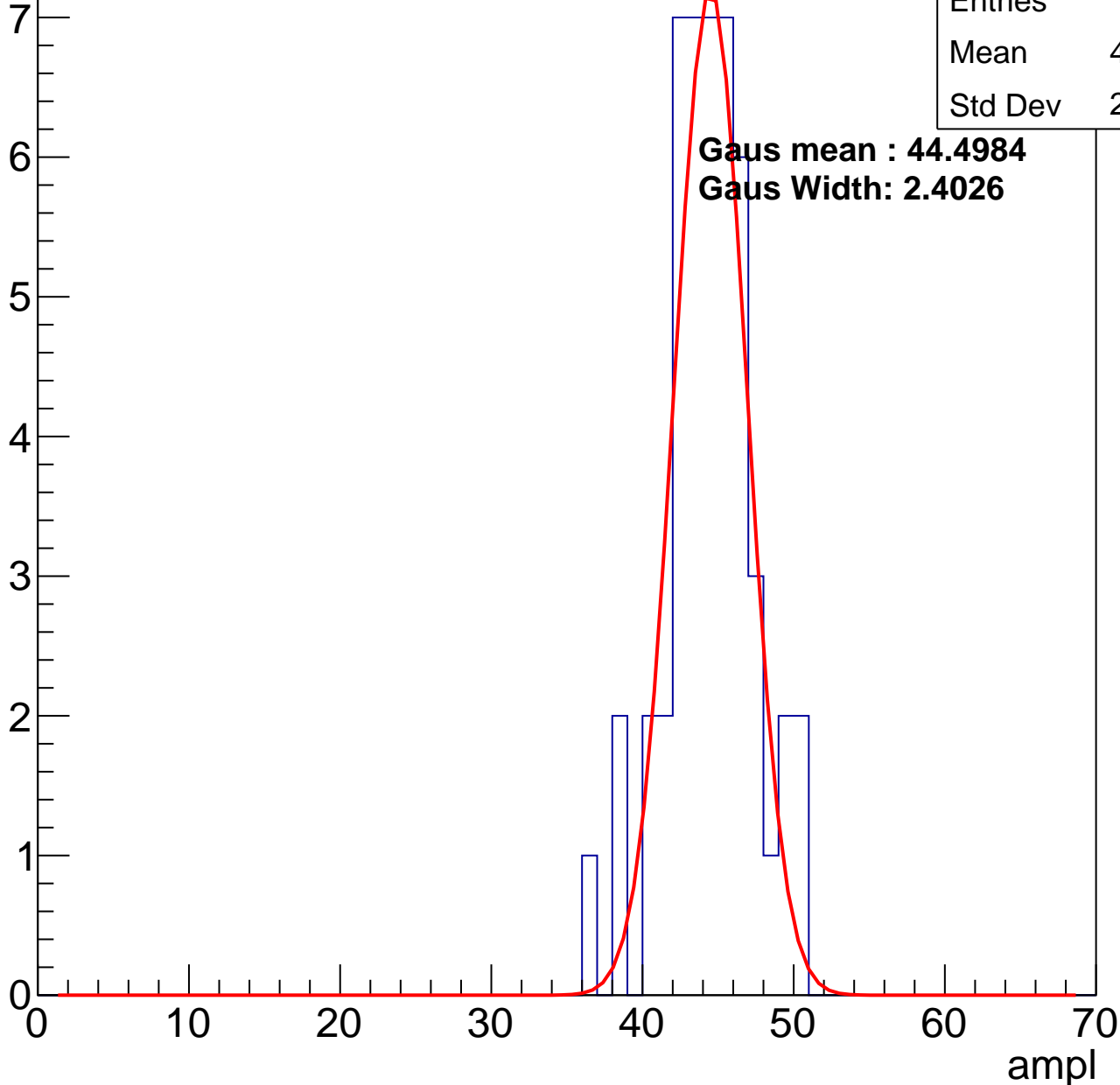
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.98
Std Dev	2.917

Gaus mean : 44.4984

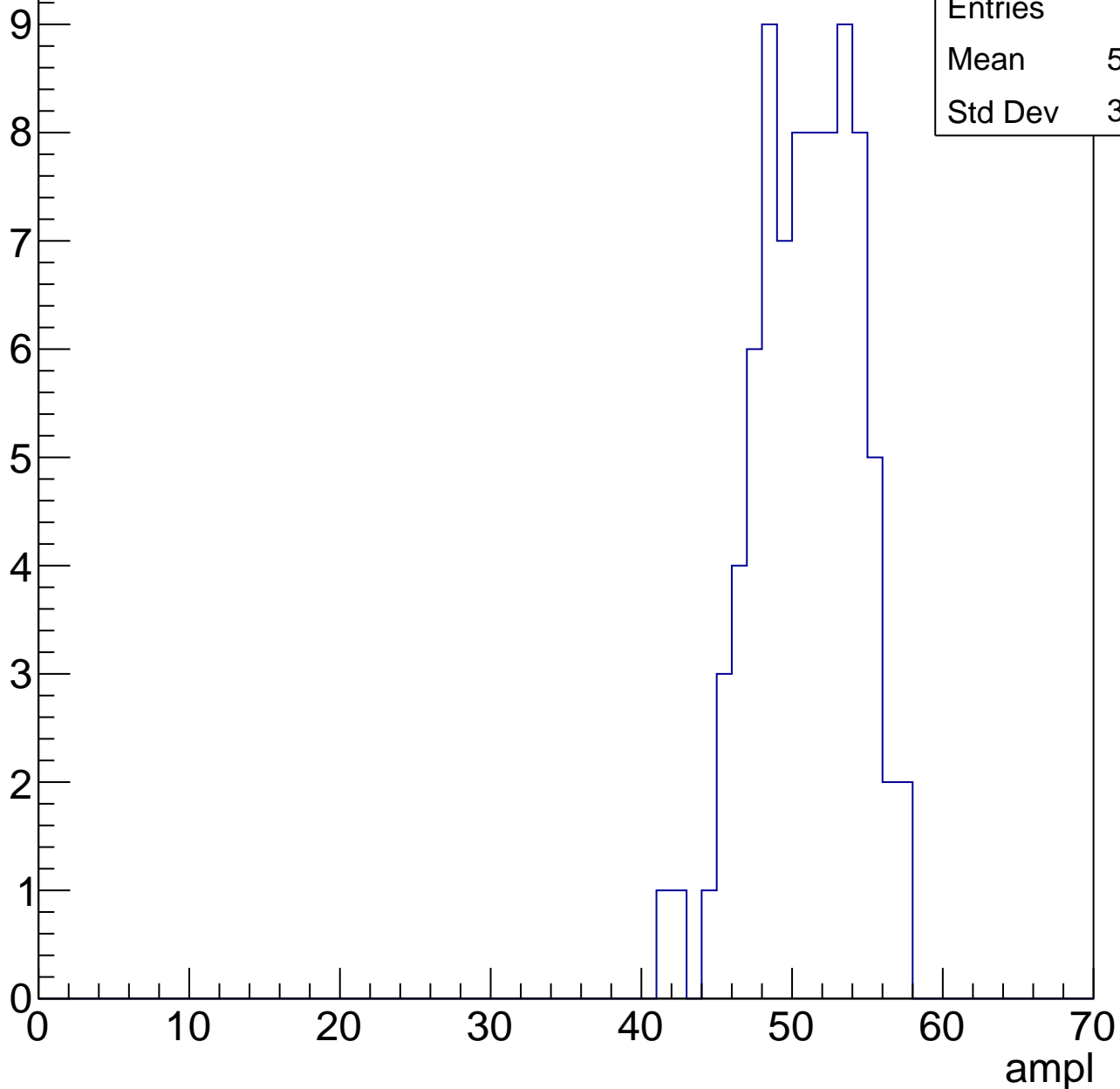
Gaus Width: 2.4026



B1L102S, U26-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	82
Mean	50.45
Std Dev	3.394

B1L102S, U26-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

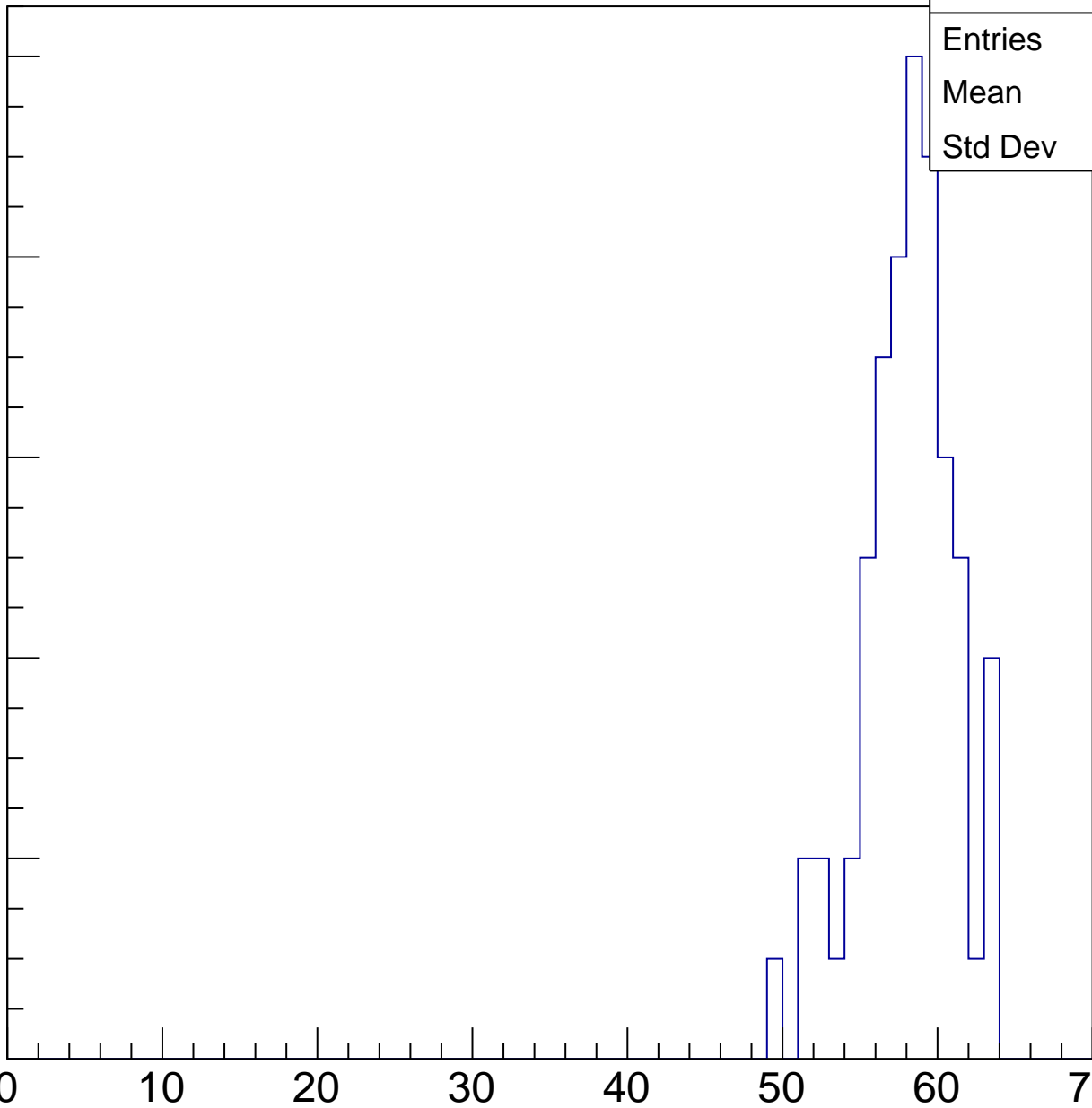
40

50

60

ampl

Entries	63
Mean	57.6
Std Dev	3.016

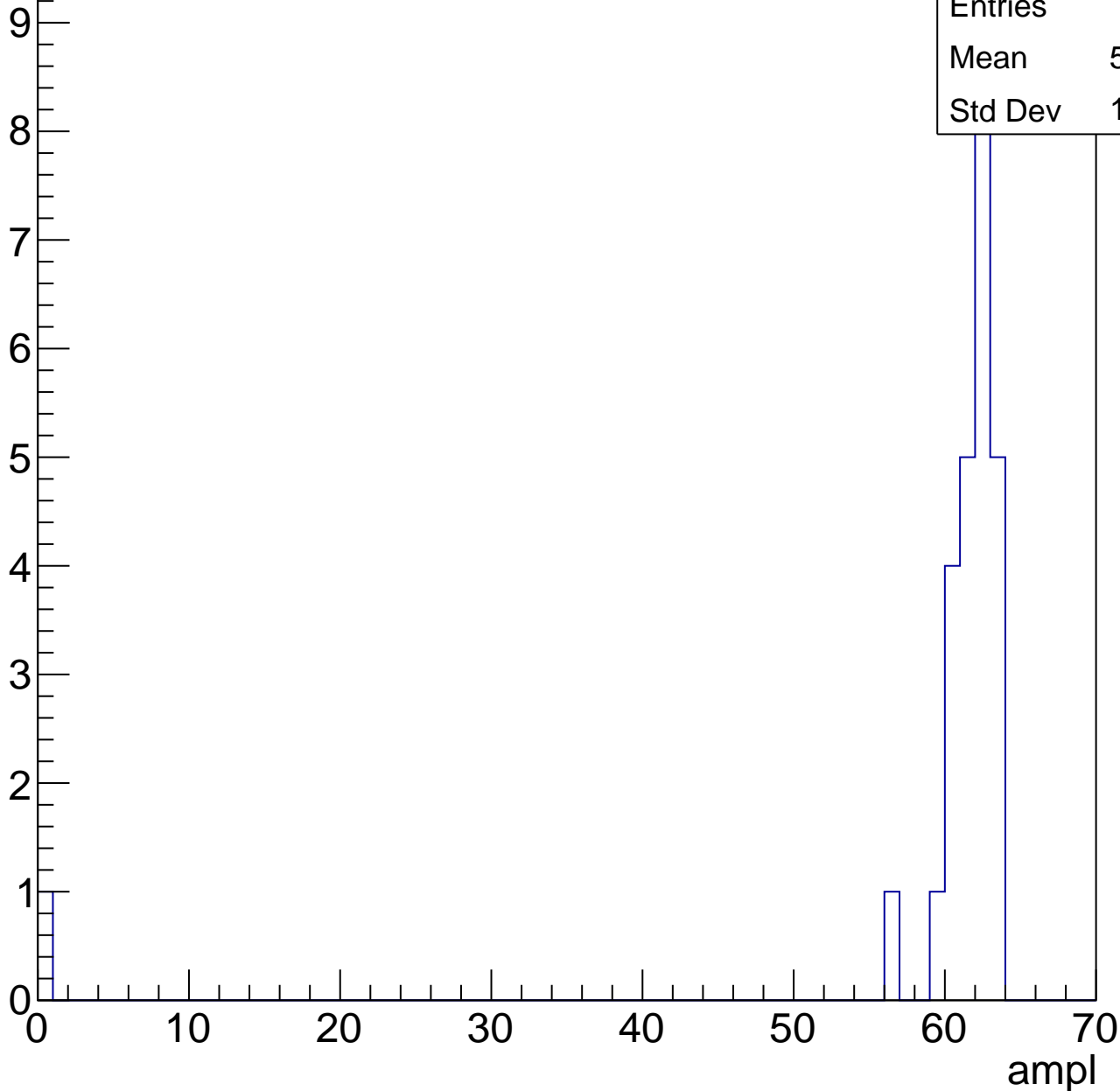


B1L102S, U26-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	58.96
Std Dev	11.89



B1L102S, U26-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch2, adc0

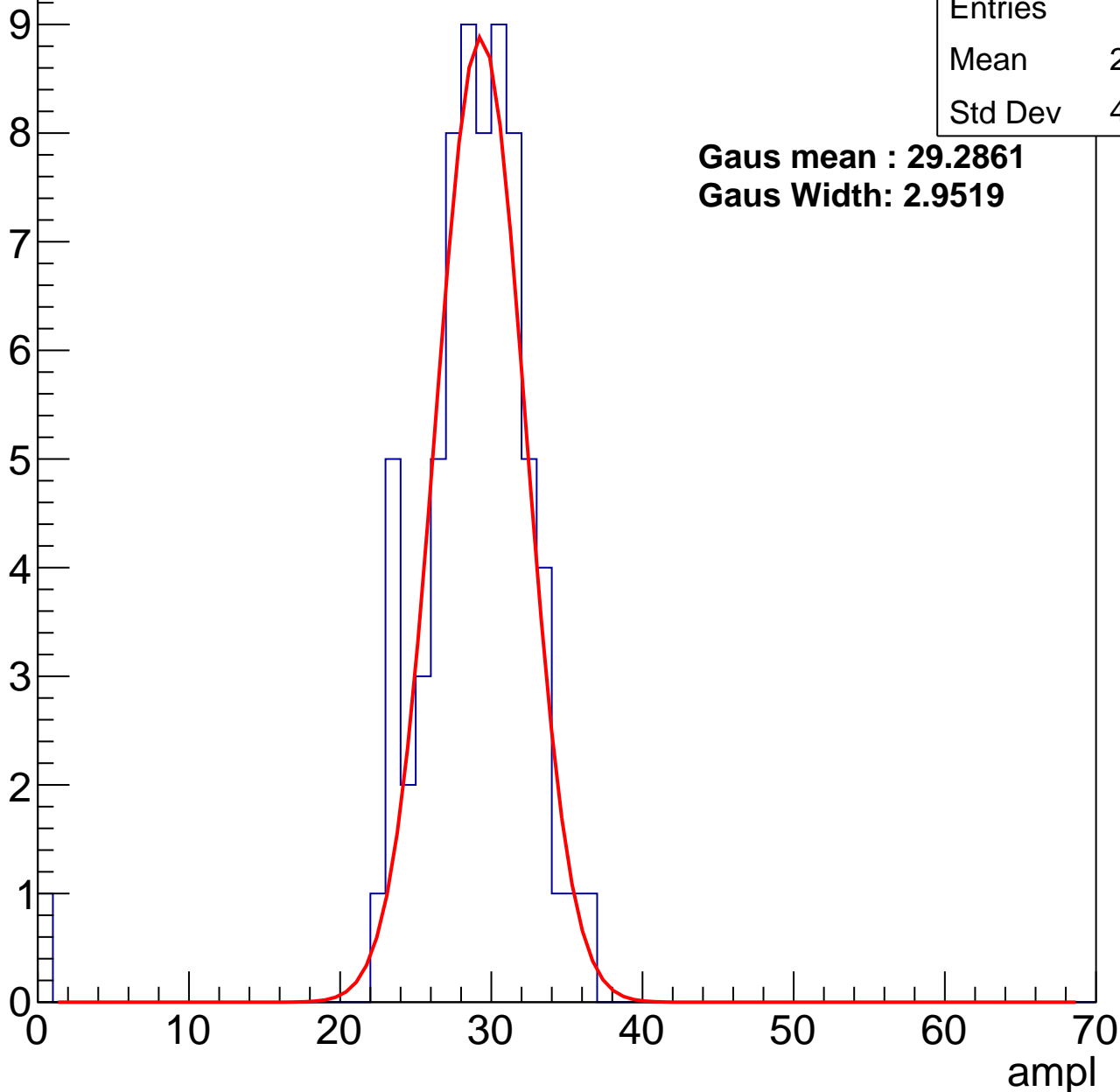
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.24
Std Dev	4.555

Gaus mean : 29.2861

Gaus Width: 2.9519



B1L102S, U26-ch2, adc1

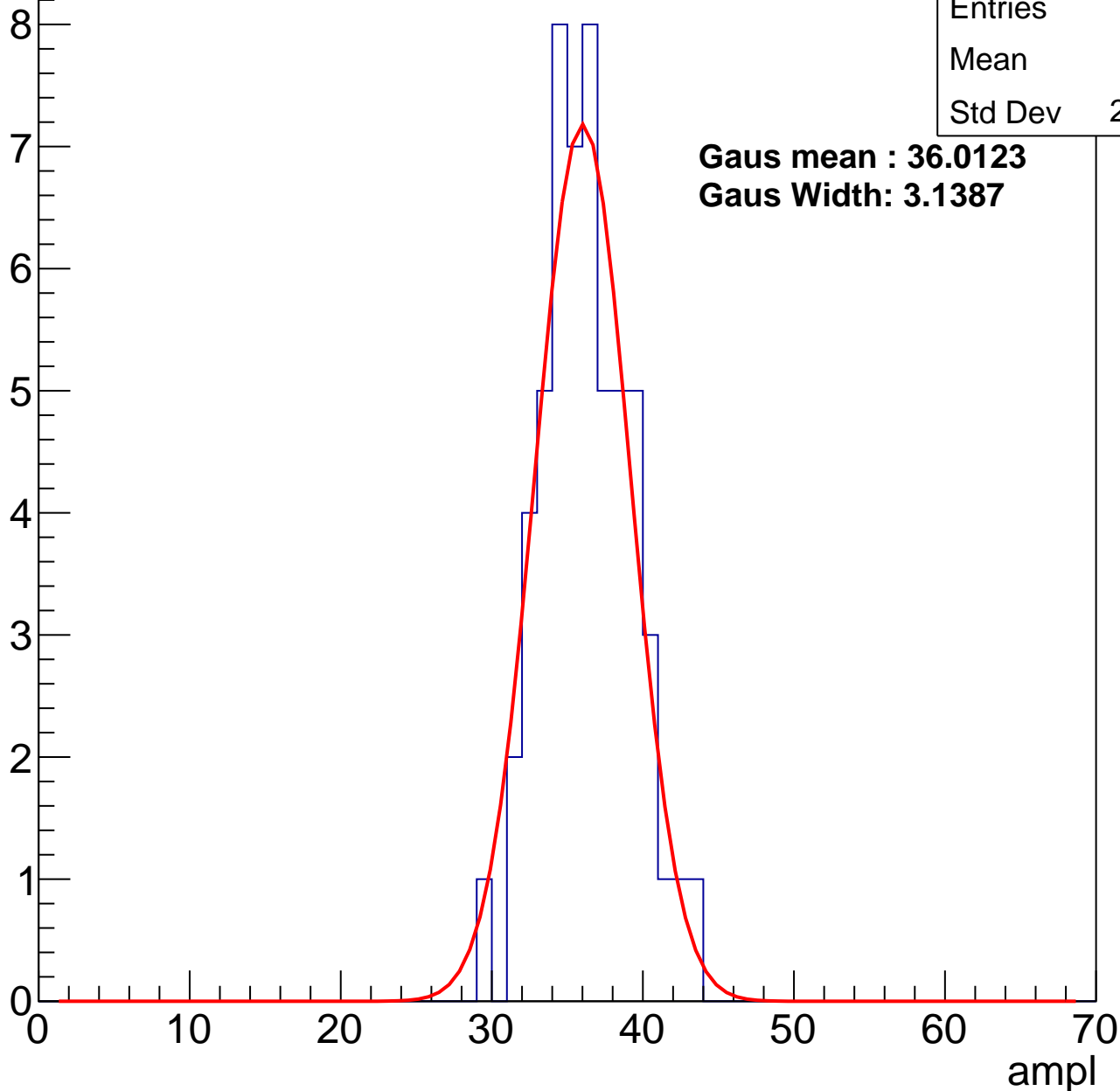
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.8
Std Dev	2.906

Gaus mean : 36.0123

Gaus Width: 3.1387



B1L102S, U26-ch2, adc2

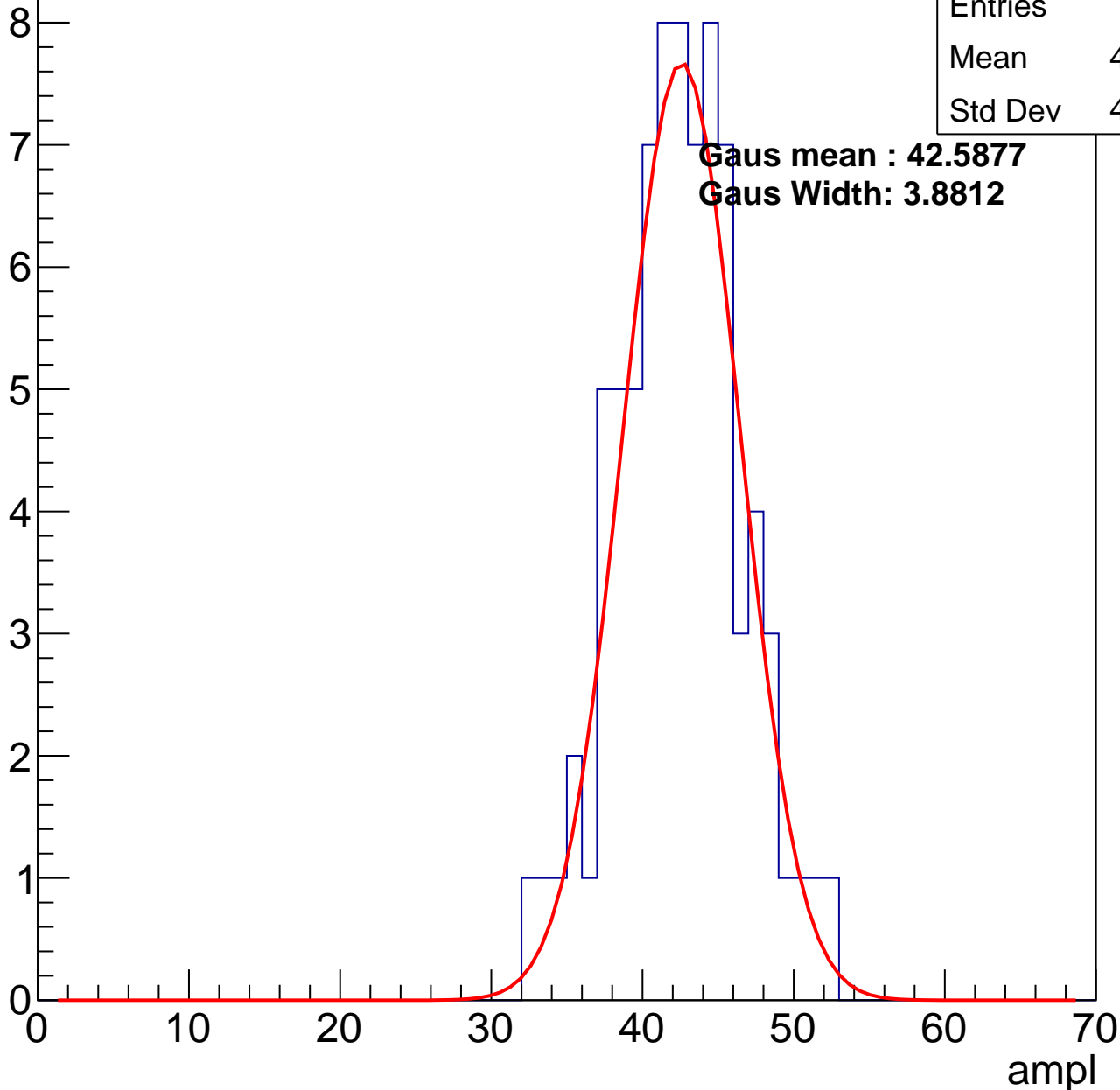
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.99
Std Dev	4.057

Gaus mean : 42.5877

Gaus Width: 3.8812

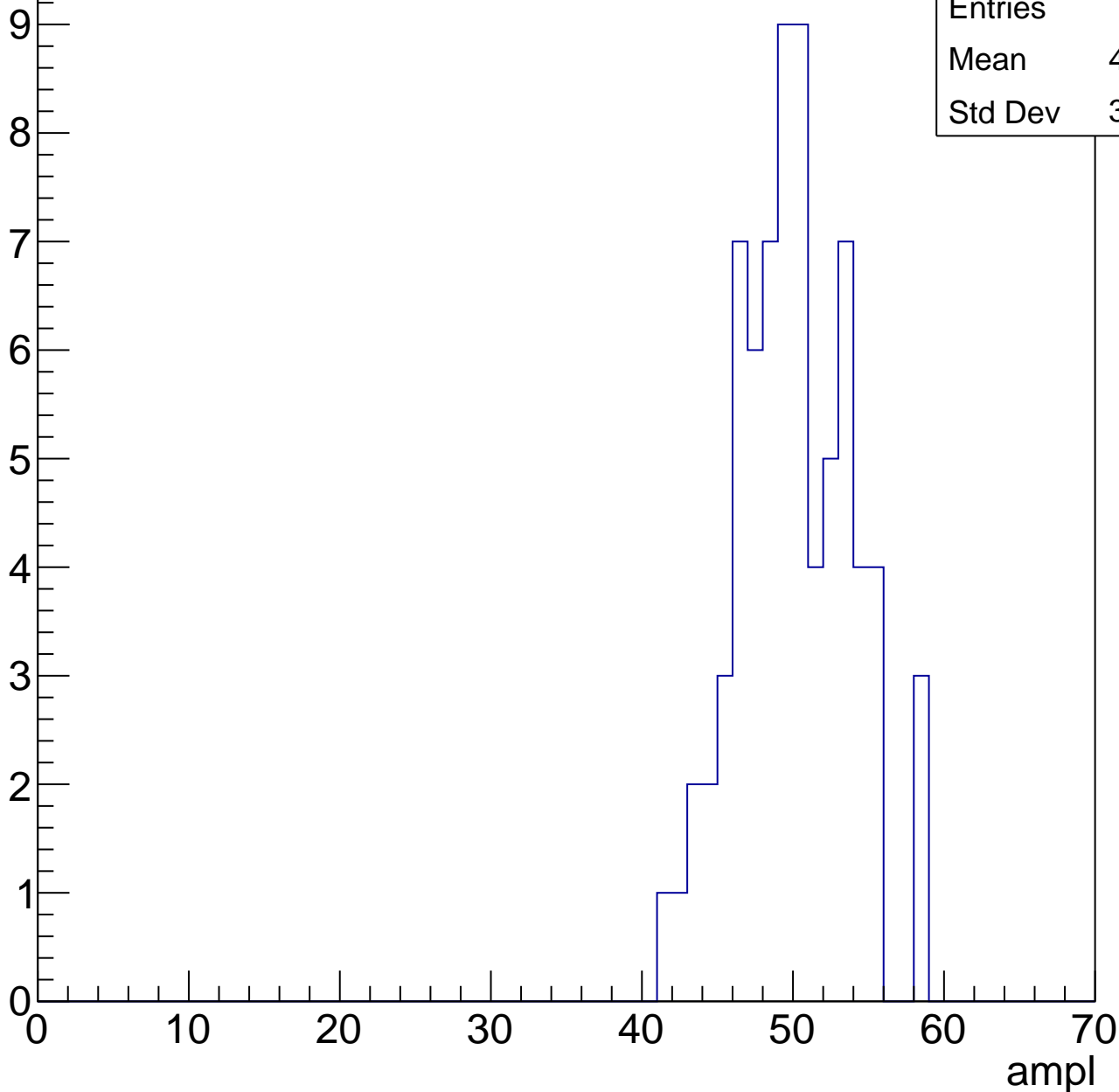


B1L102S, U26-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

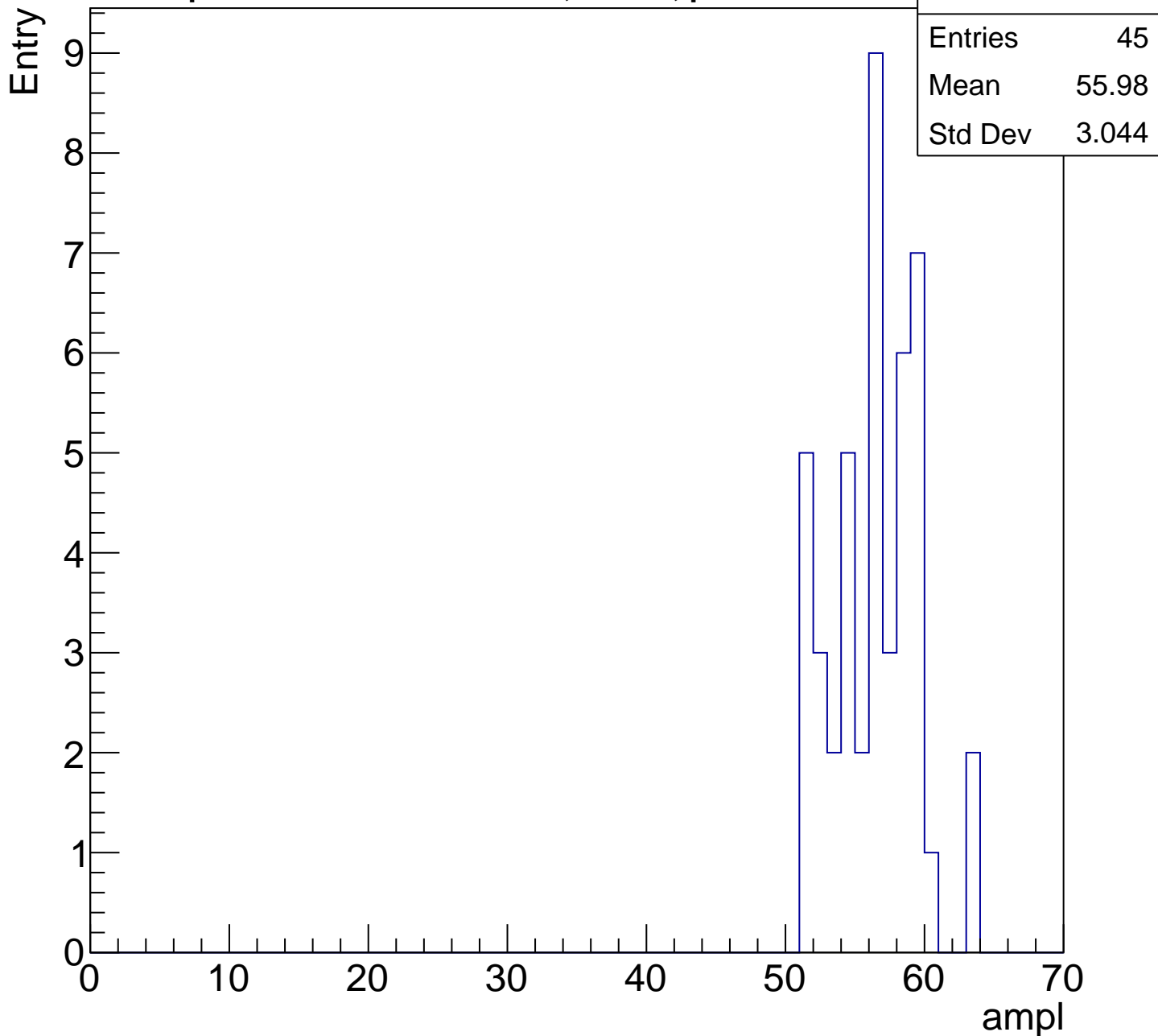
Entry

Entries	74
Mean	49.57
Std Dev	3.717



B1L102S, U26-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U26-ch2, adc5

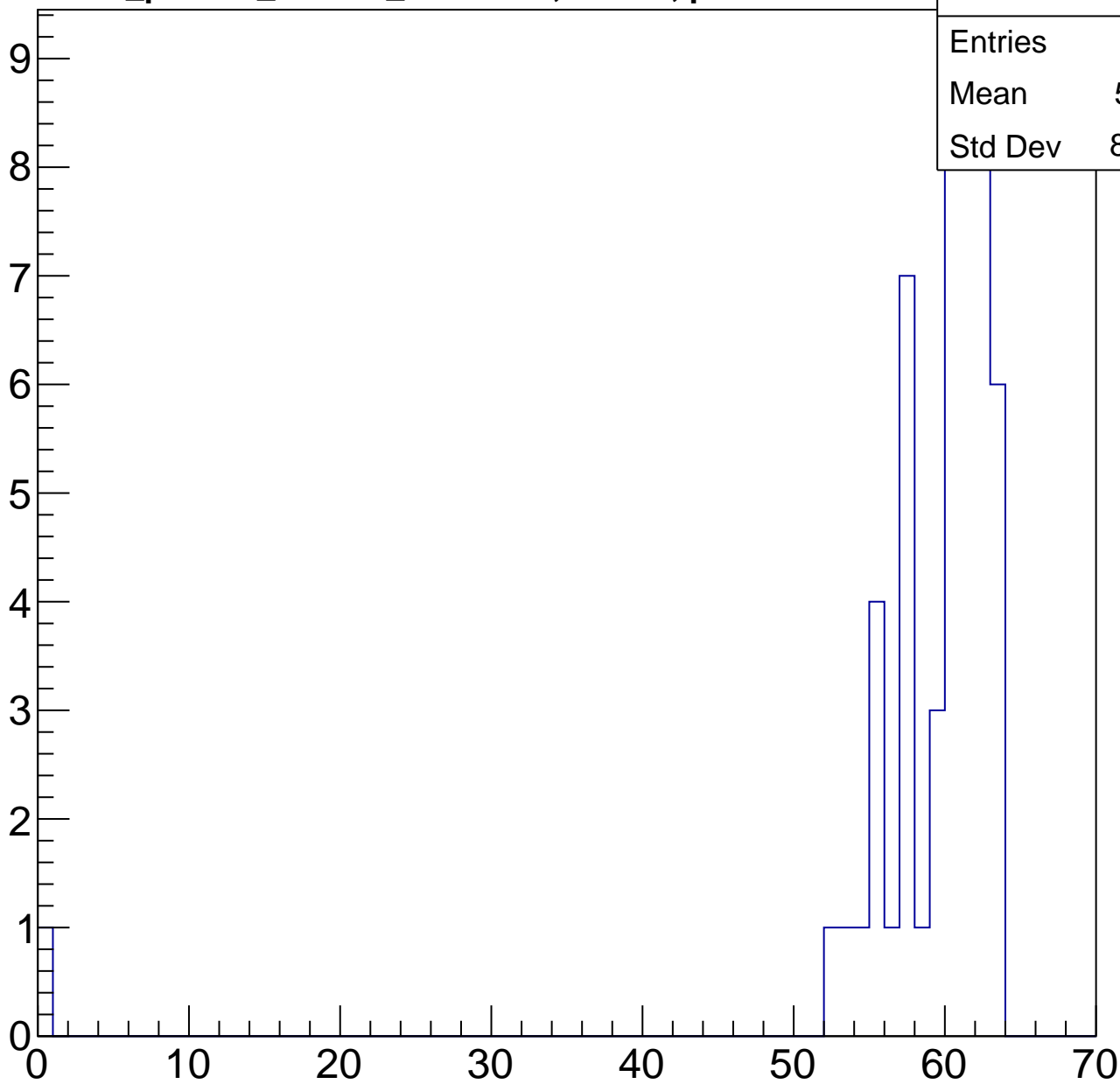
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.31
Std Dev	8.639

ampl



B1L102S, U26-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch3, adc0

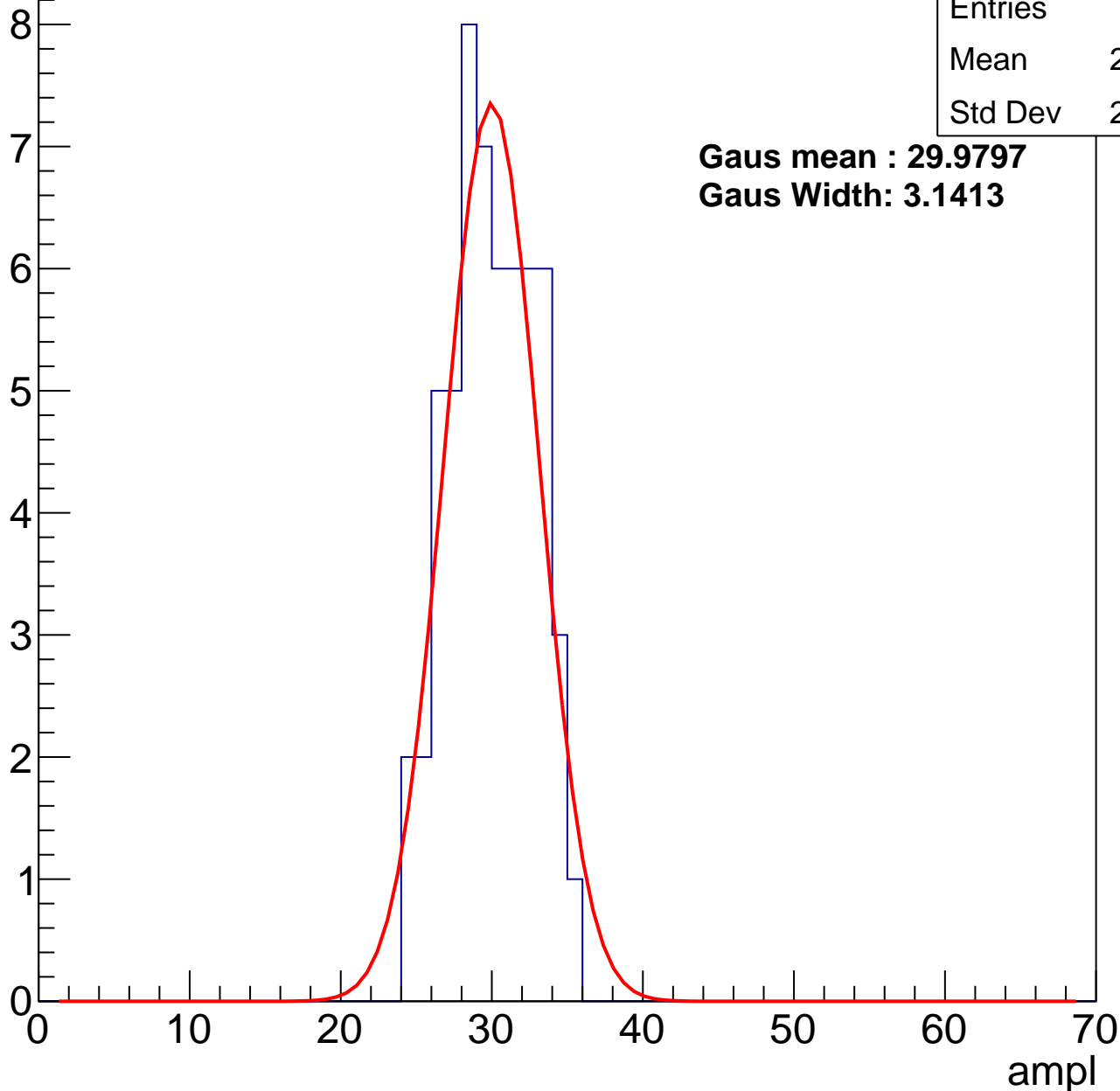
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	29.53
Std Dev	2.747

Gaus mean : 29.9797

Gaus Width: 3.1413



B1L102S, U26-ch3, adc1

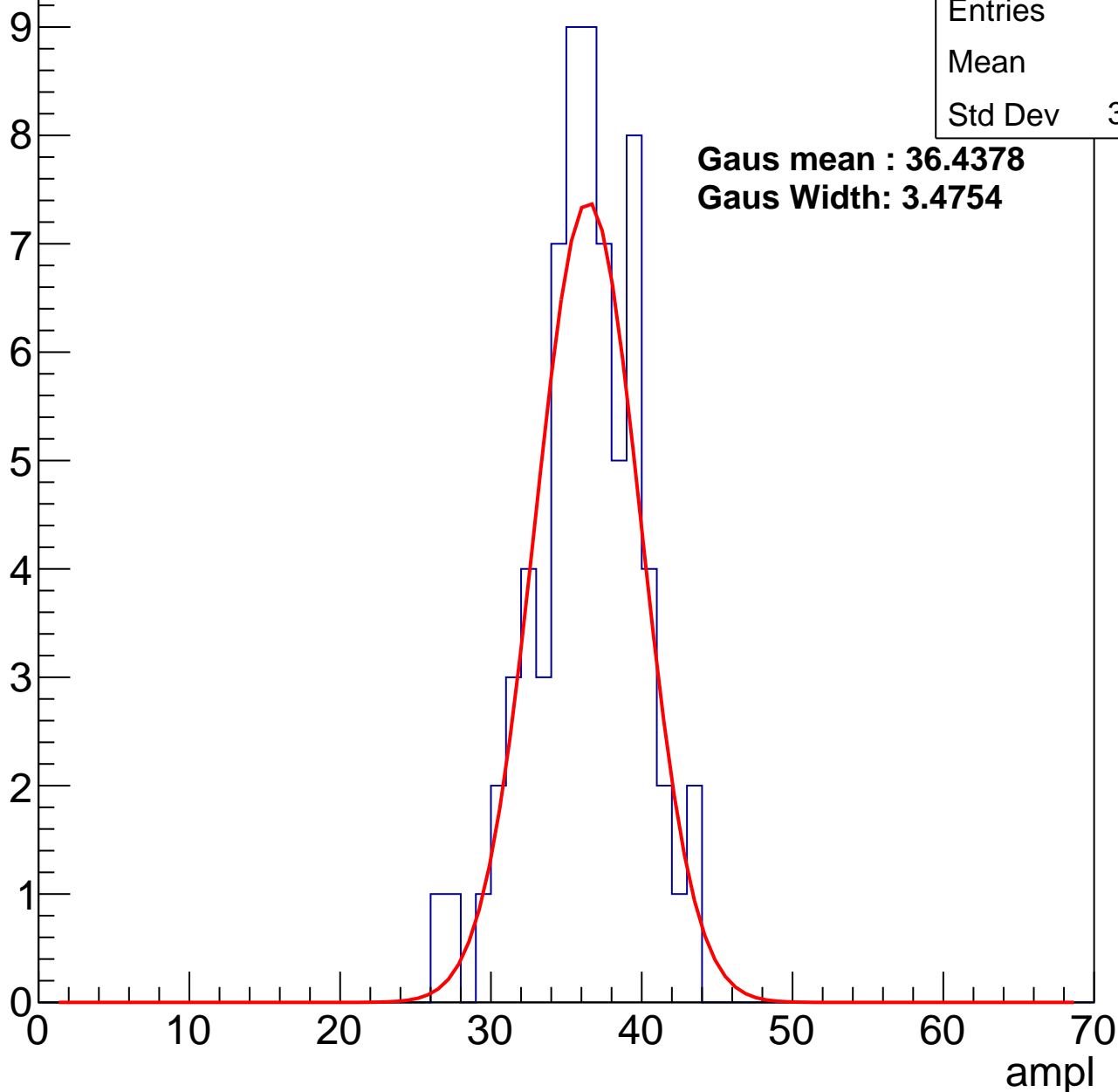
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.8
Std Dev	3.508

Gaus mean : 36.4378

Gaus Width: 3.4754



B1L102S, U26-ch3, adc2

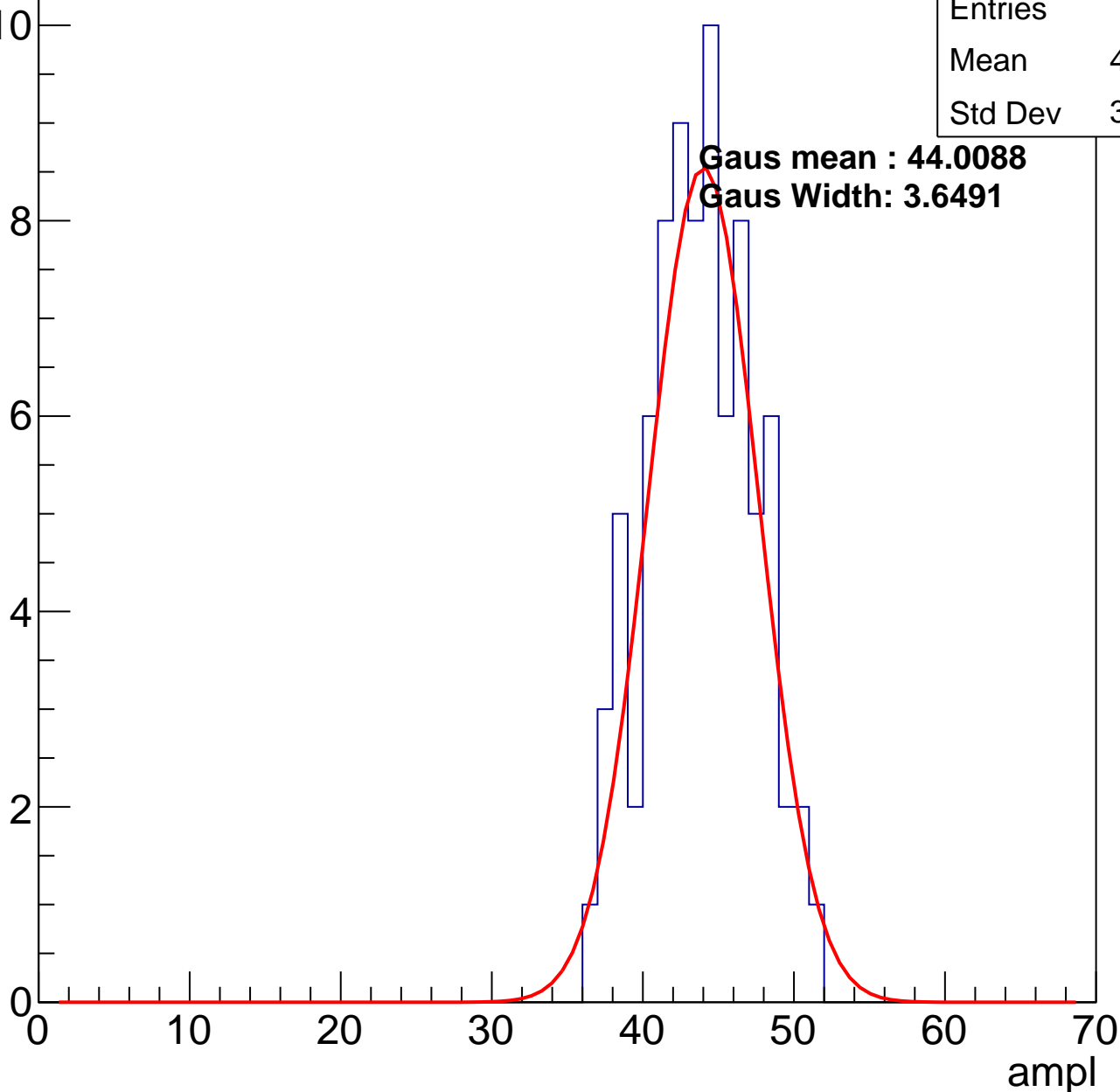
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	43.35
Std Dev	3.437

Gaus mean : 44.0088

Gaus Width: 3.6491

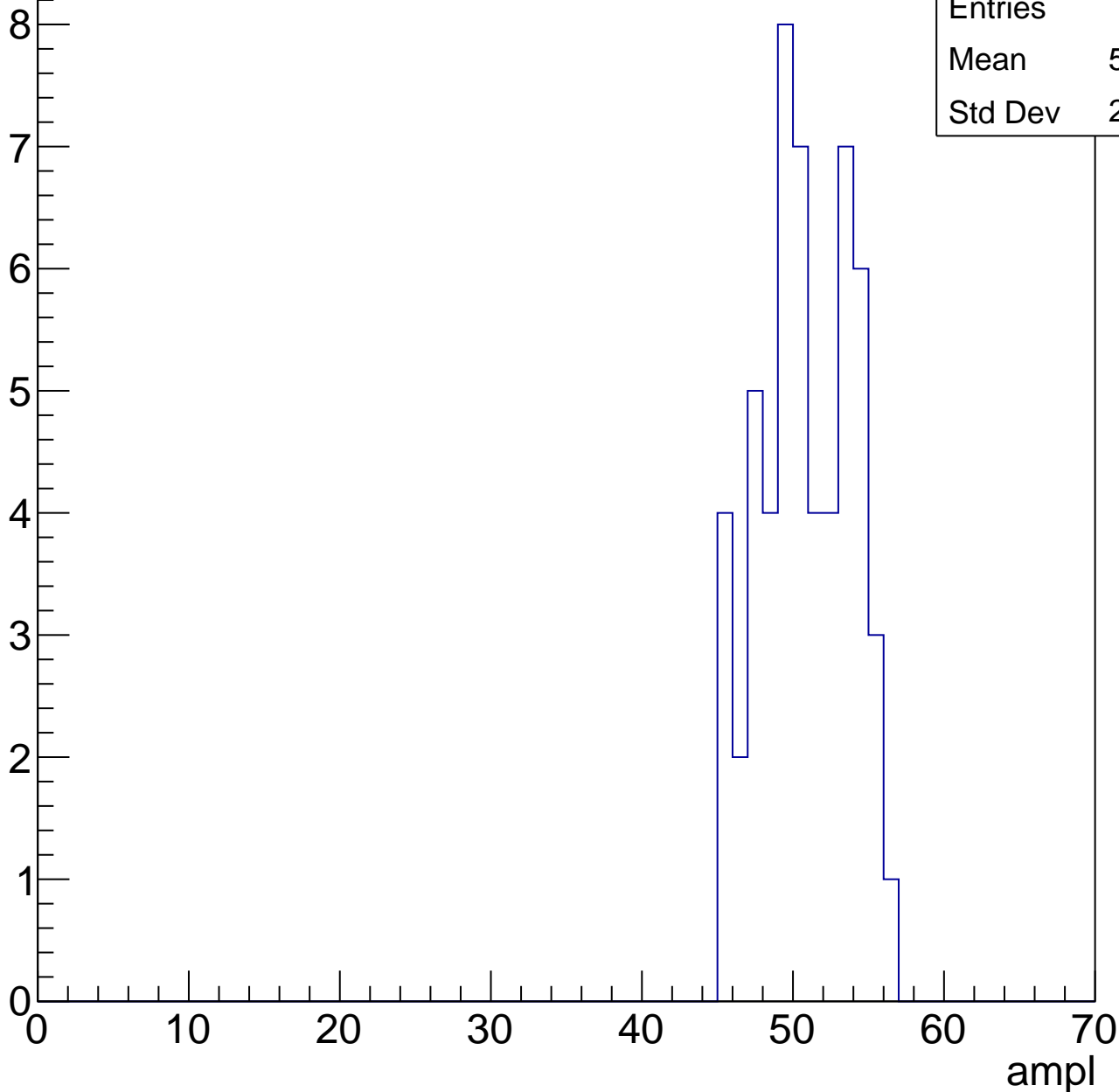


B1L102S, U26-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

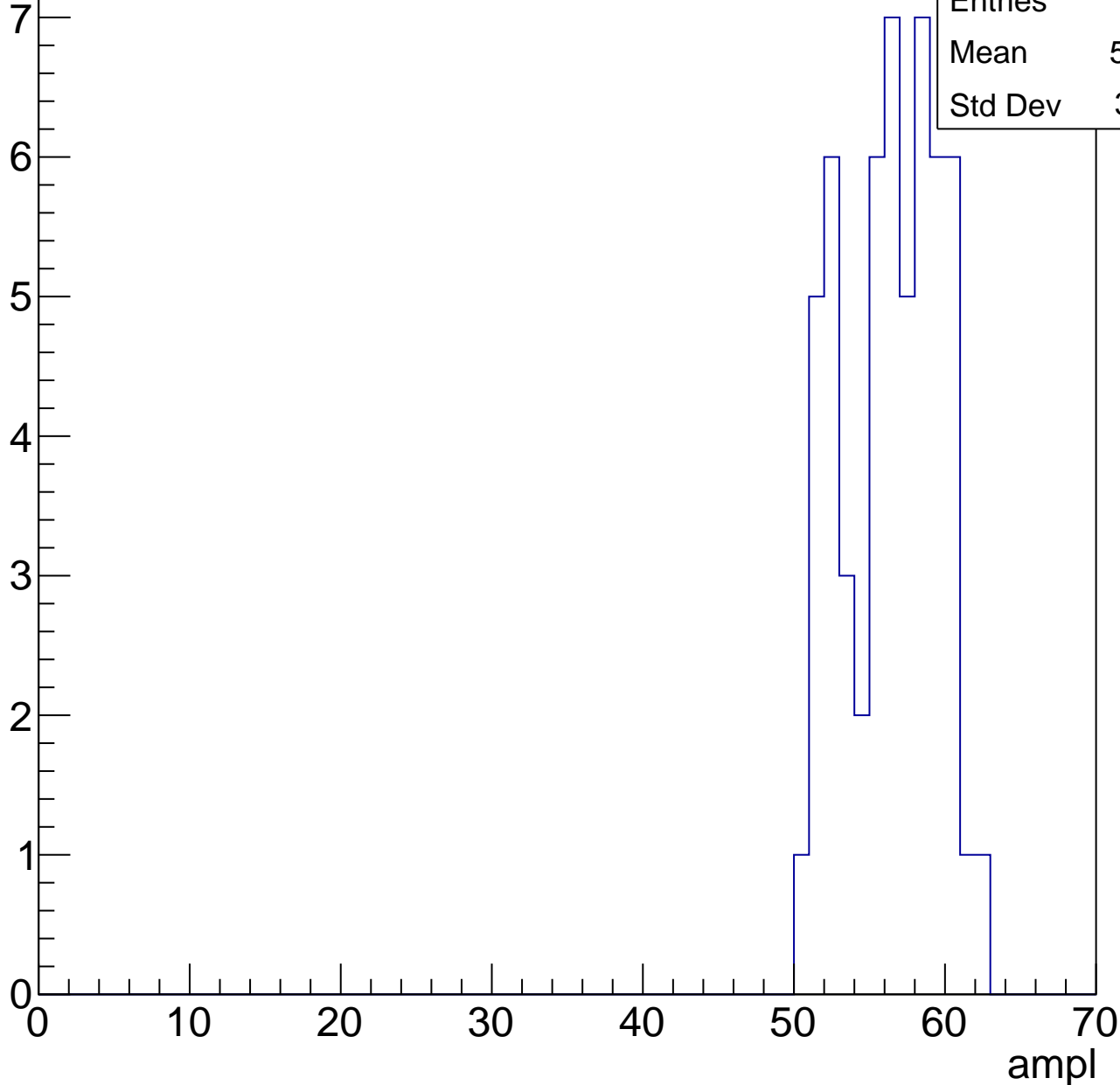
Entries	55
Mean	50.35
Std Dev	2.968



B1L102S, U26-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

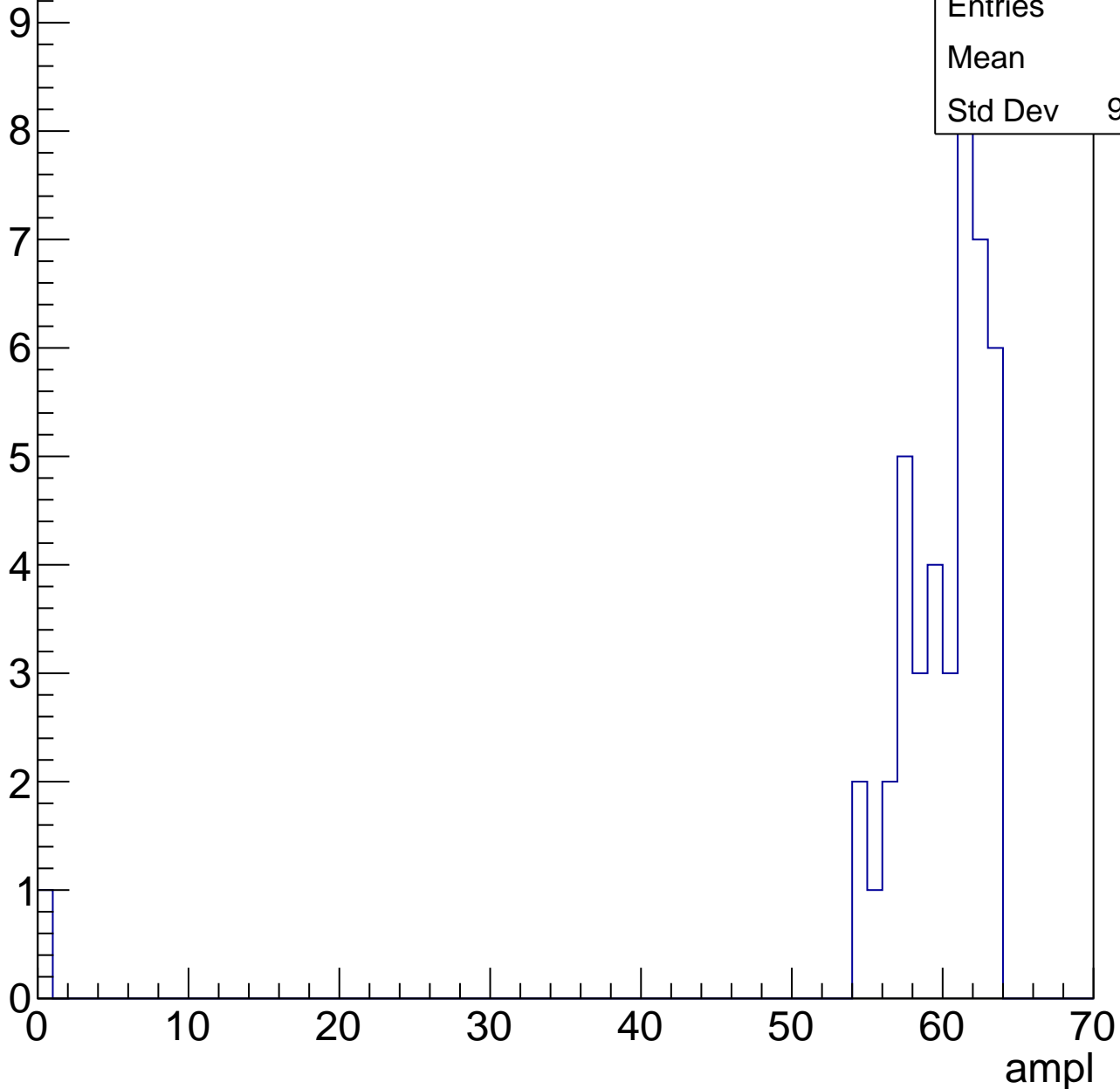


B1L102S, U26-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

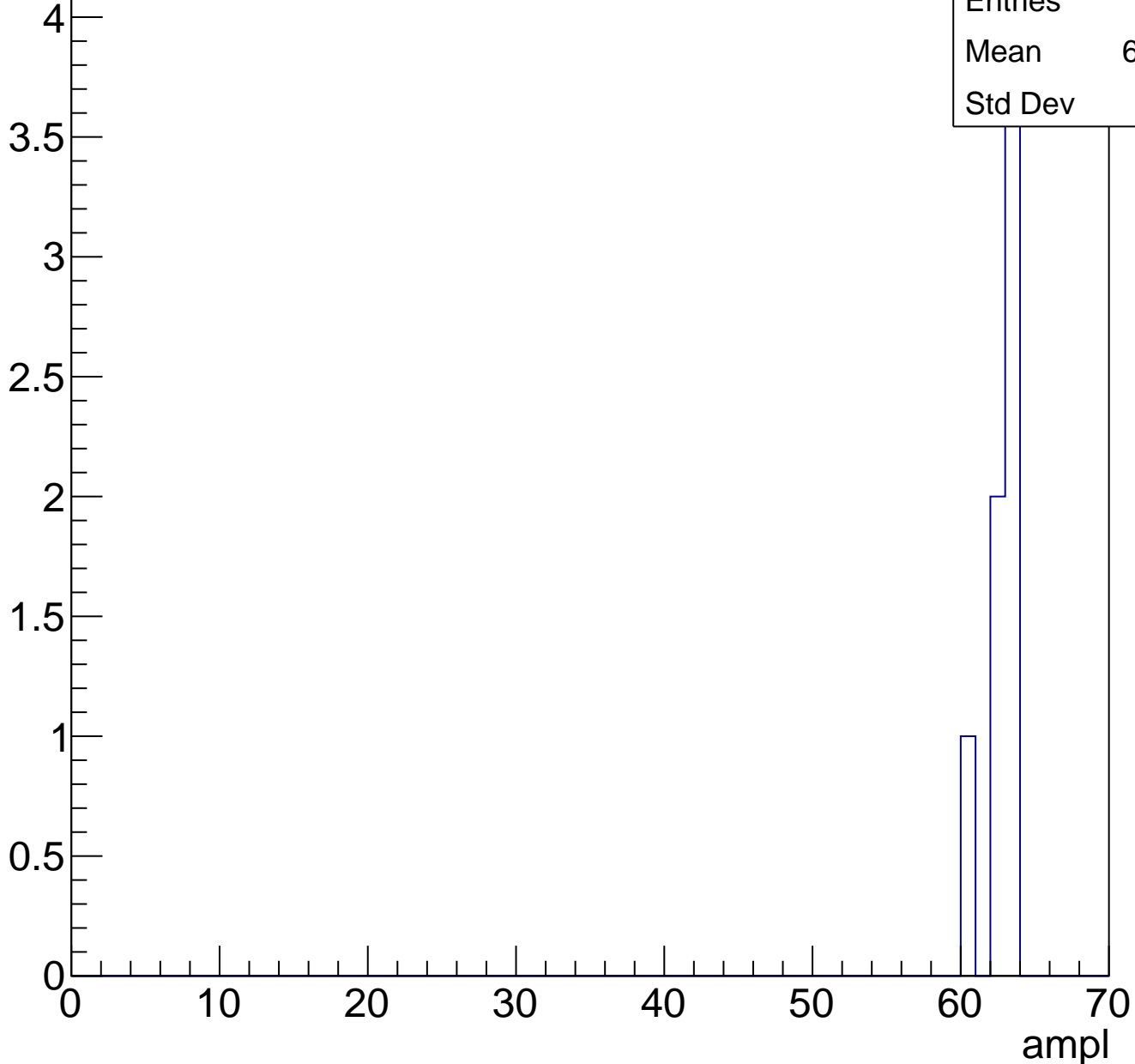
Entries	43
Mean	58.4
Std Dev	9.364



B1L102S, U26-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch4, adc0

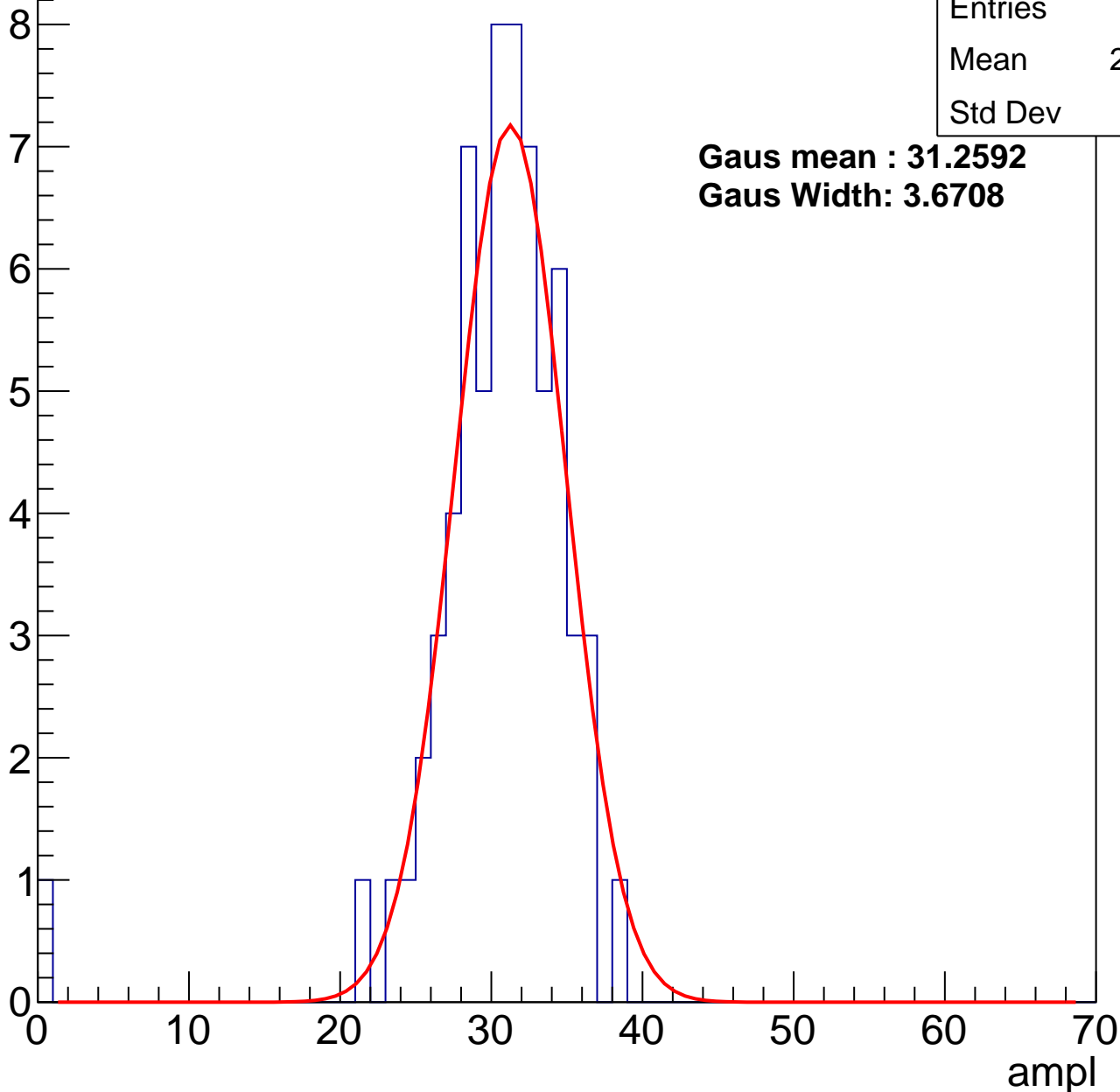
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.95
Std Dev	5.01

Gaus mean : 31.2592

Gaus Width: 3.6708



B1L102S, U26-ch4, adc1

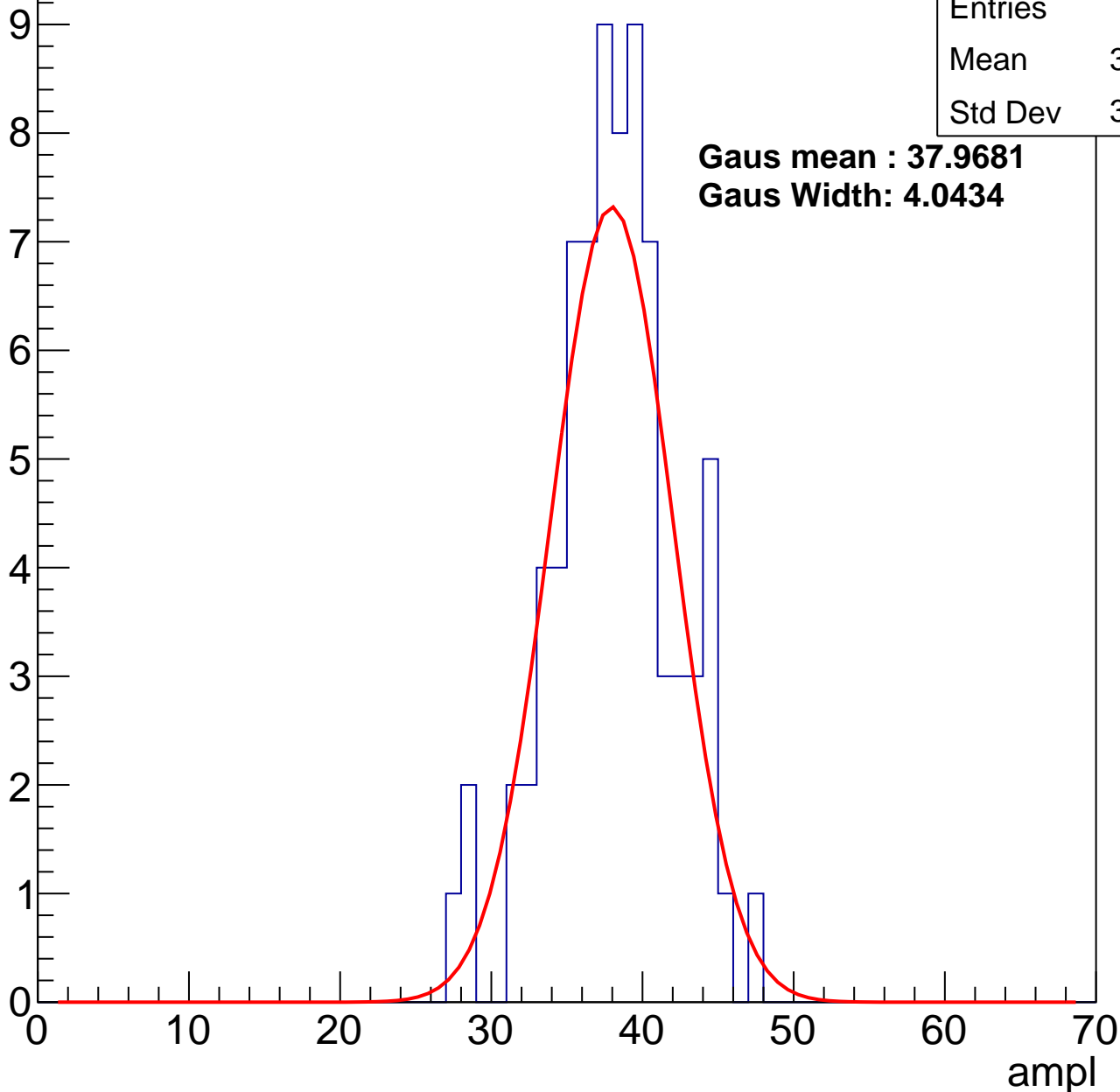
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	37.59
Std Dev	3.985

Gaus mean : 37.9681

Gaus Width: 4.0434



B1L102S, U26-ch4, adc2

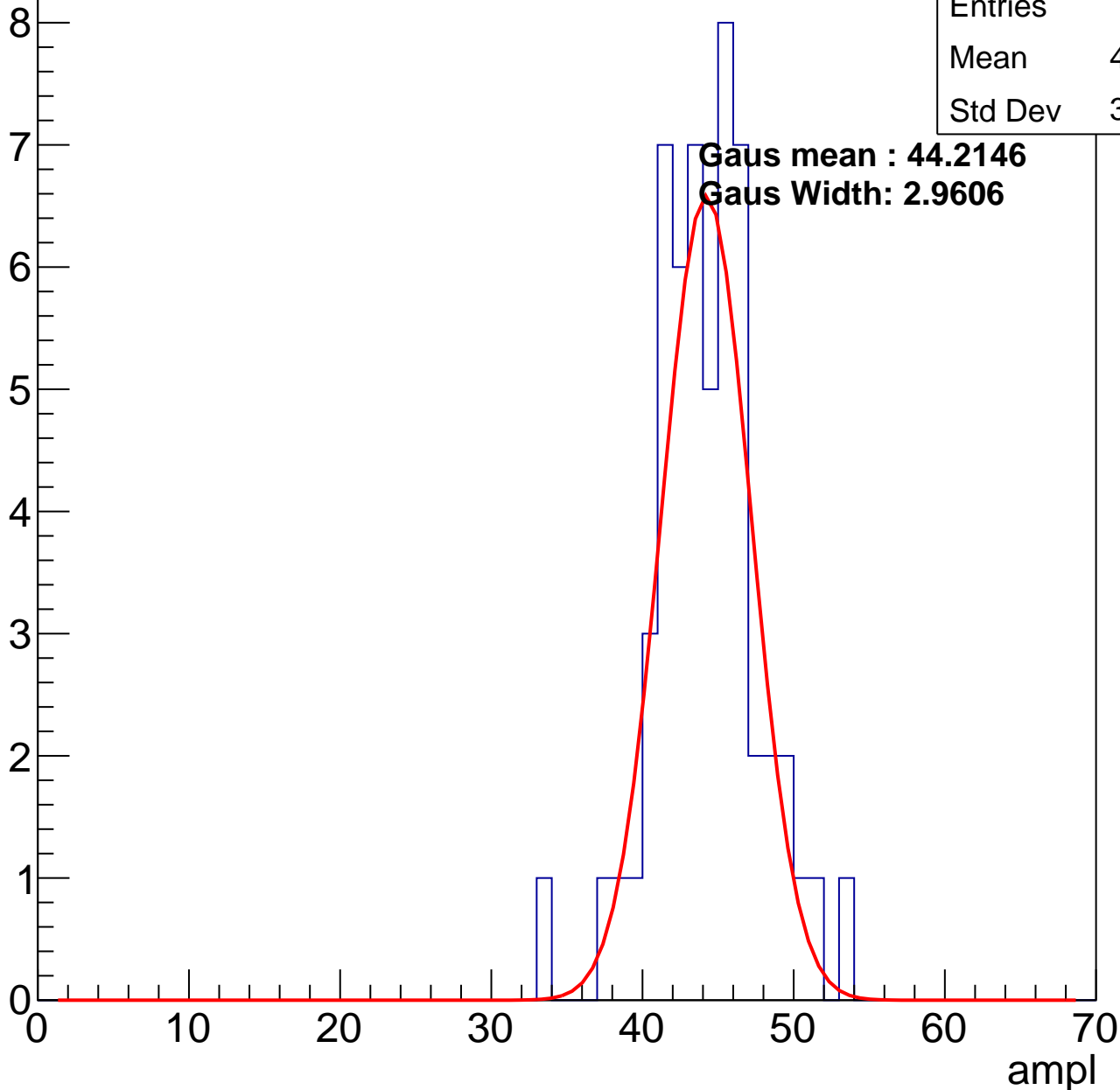
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.77
Std Dev	3.464

Gaus mean : 44.2146

Gaus Width: 2.9606

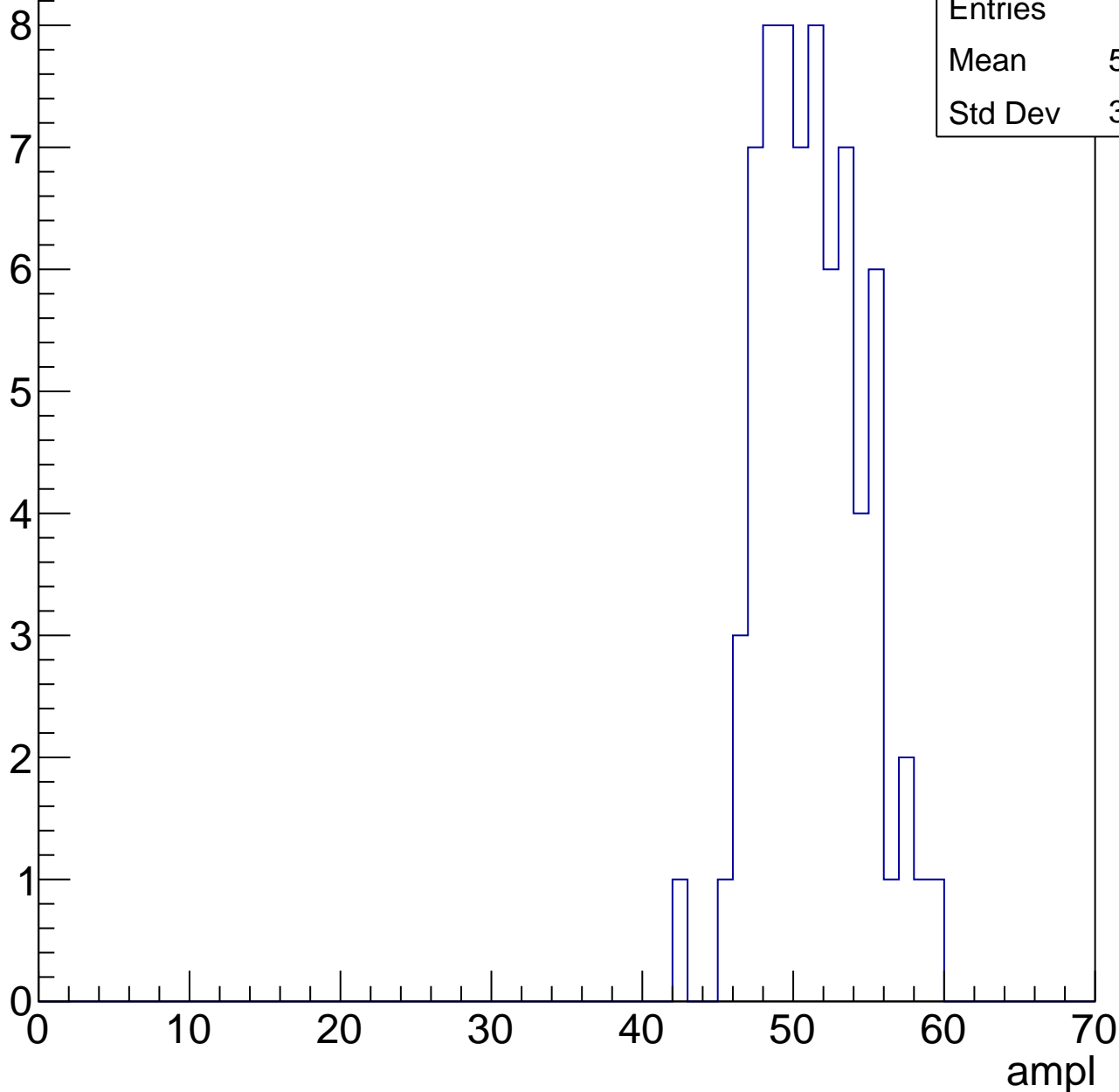


B1L102S, U26-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.76
Std Dev	3.329

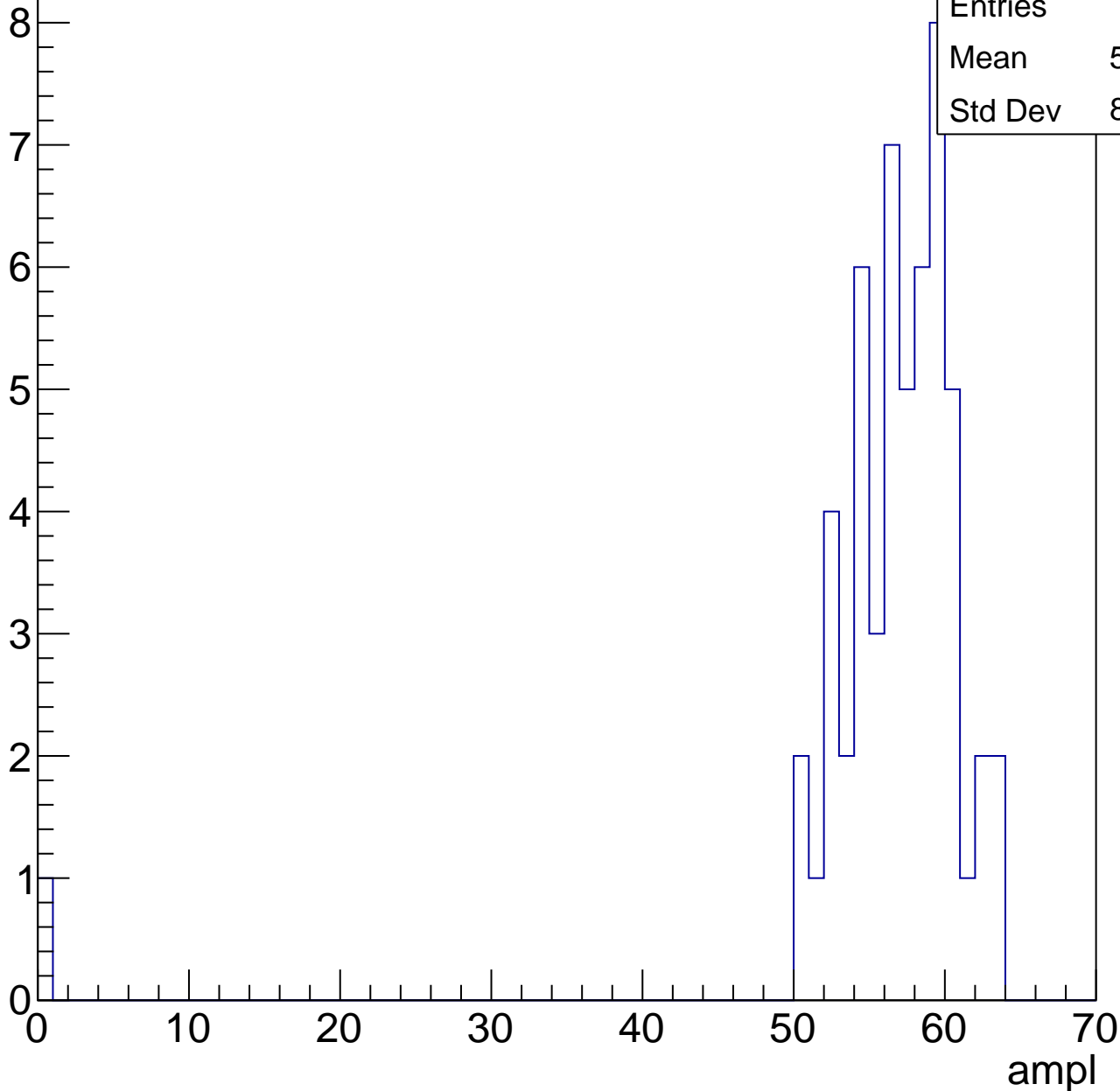


B1L102S, U26-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

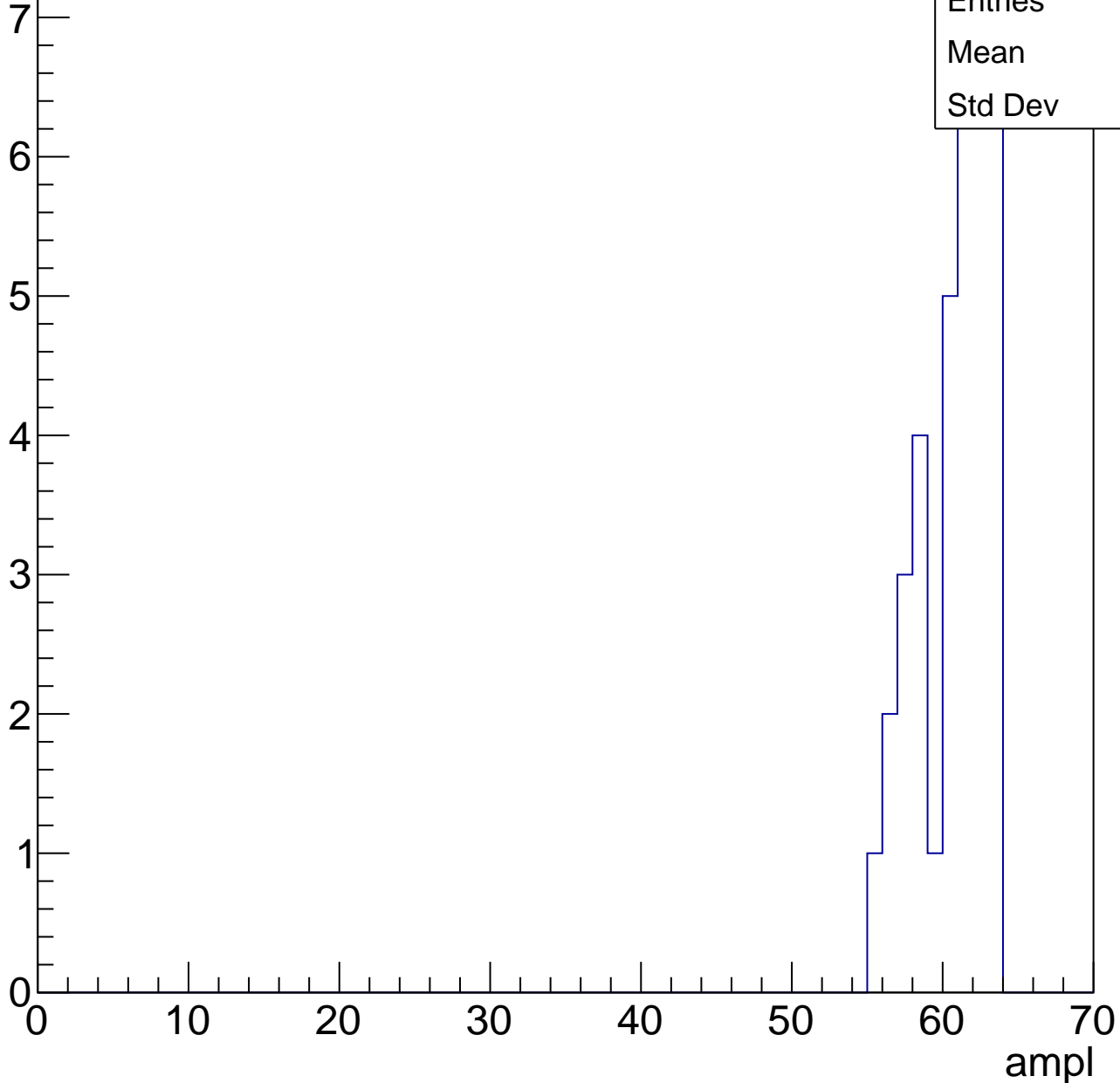
Entries	55
Mean	55.67
Std Dev	8.215



B1L102S, U26-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch5, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	29.91
Std Dev	3.619

Gaus mean : 30.4193

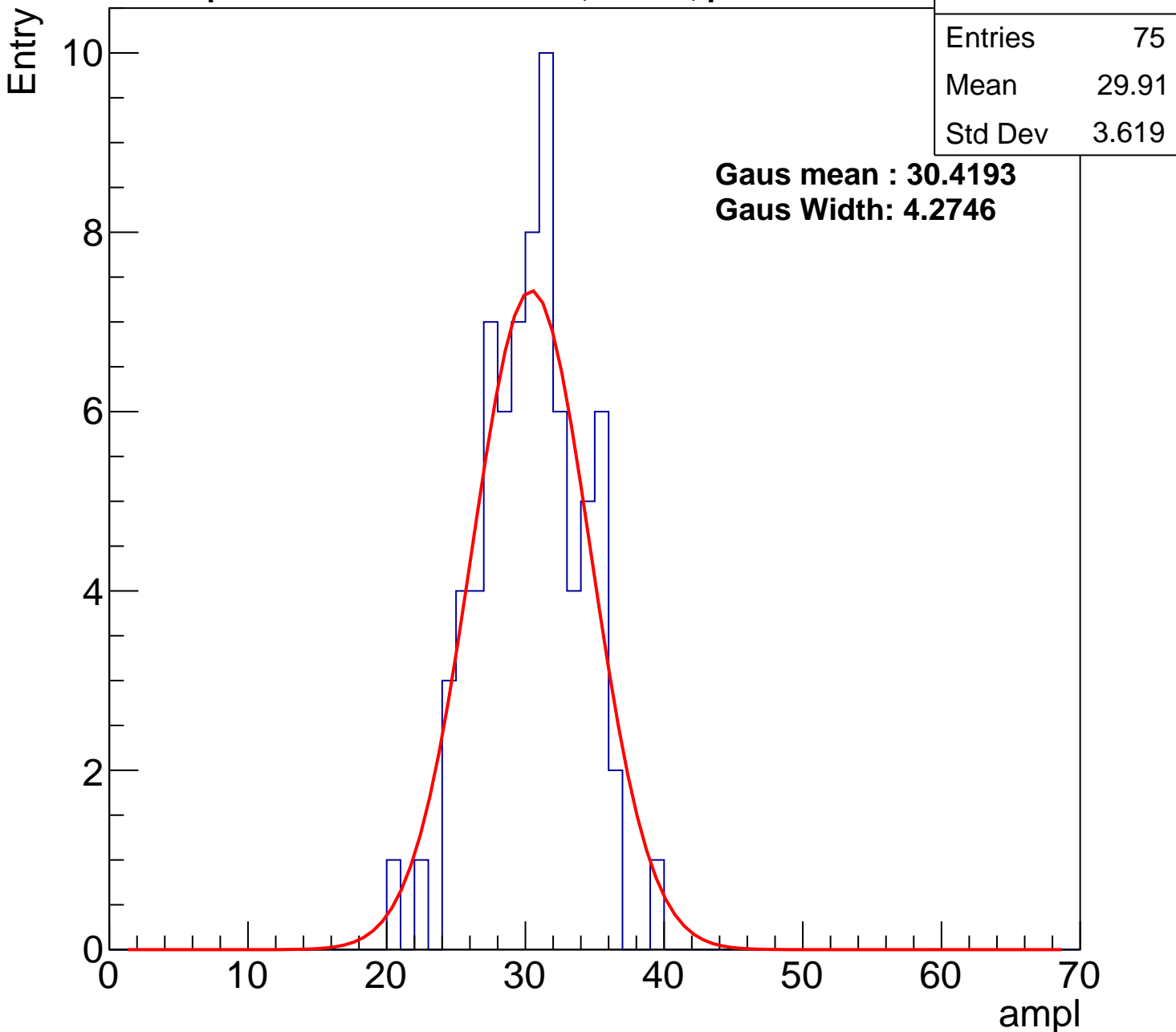
Gaus Width: 4.2746

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch5, adc1

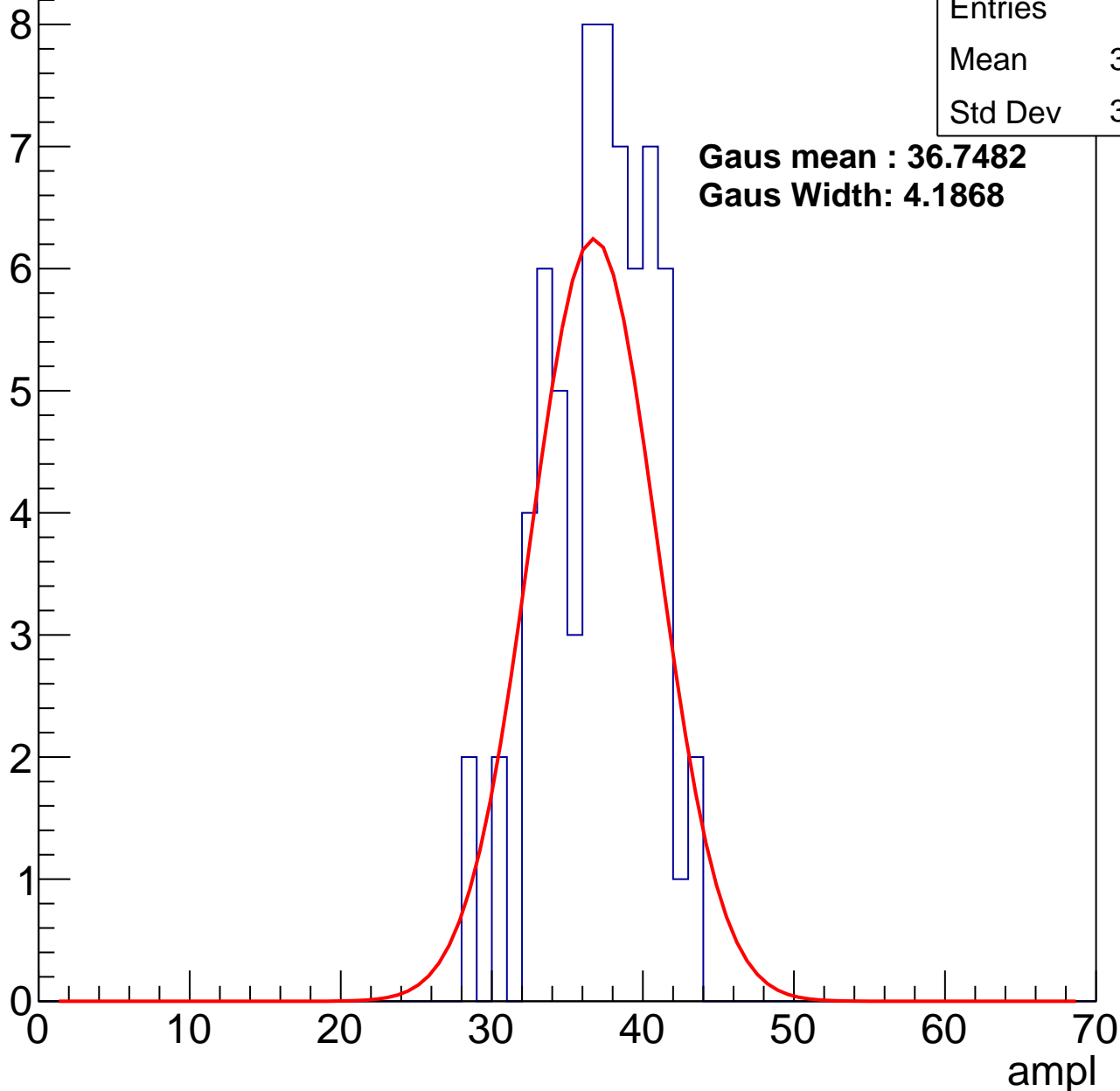
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.64
Std Dev	3.454

Gaus mean : 36.7482

Gaus Width: 4.1868



B1L102S, U26-ch5, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	44.29
Std Dev	3.797

Gaus mean : 44.7558

Gaus Width: 4.1949

10

8

6

4

2

0

ampl

0

10

20

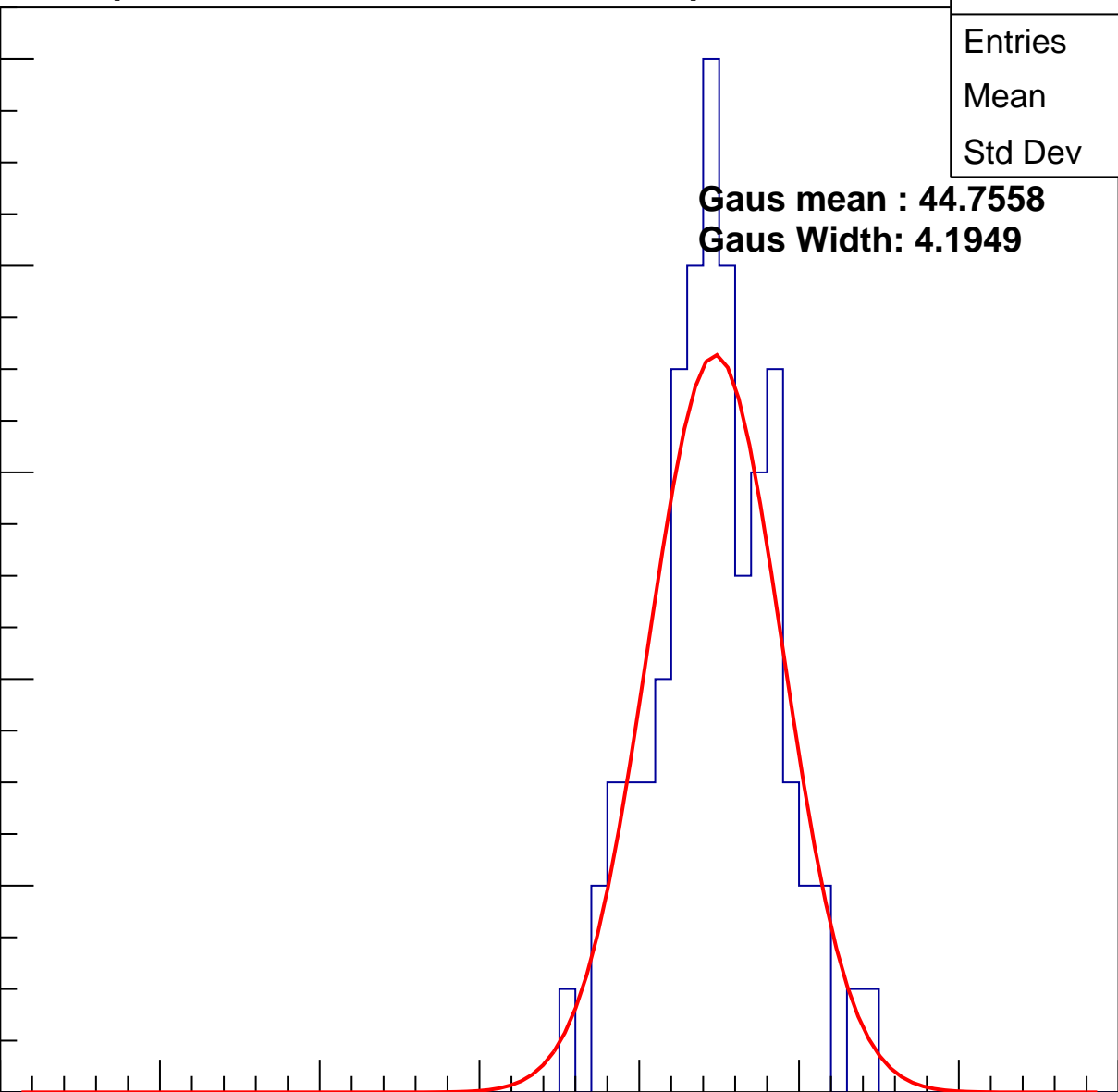
30

40

50

60

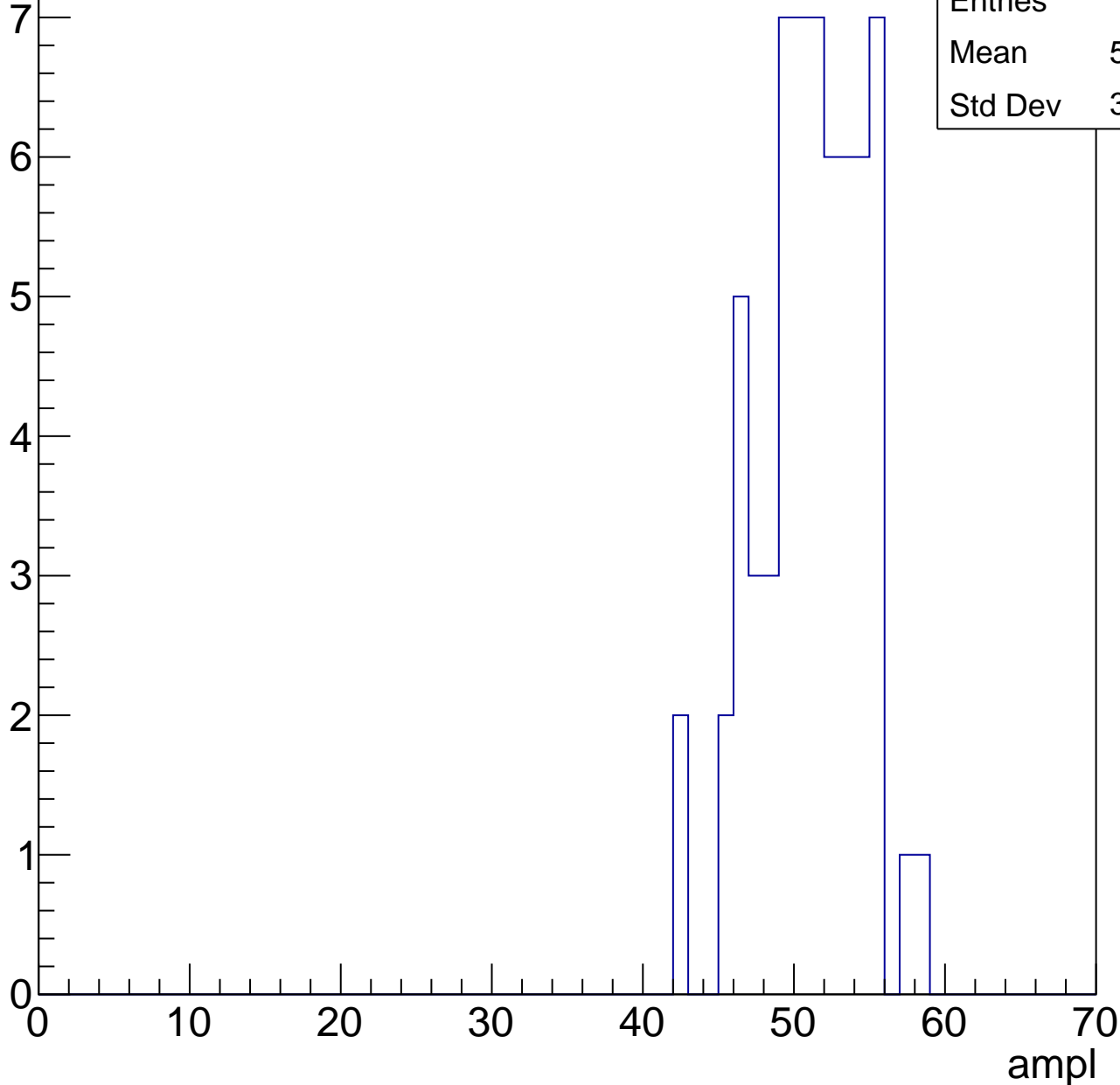
70



B1L102S, U26-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	51
Mean	56.43
Std Dev	3.303

Entry

10

8

6

4

2

0

0

10

20

30

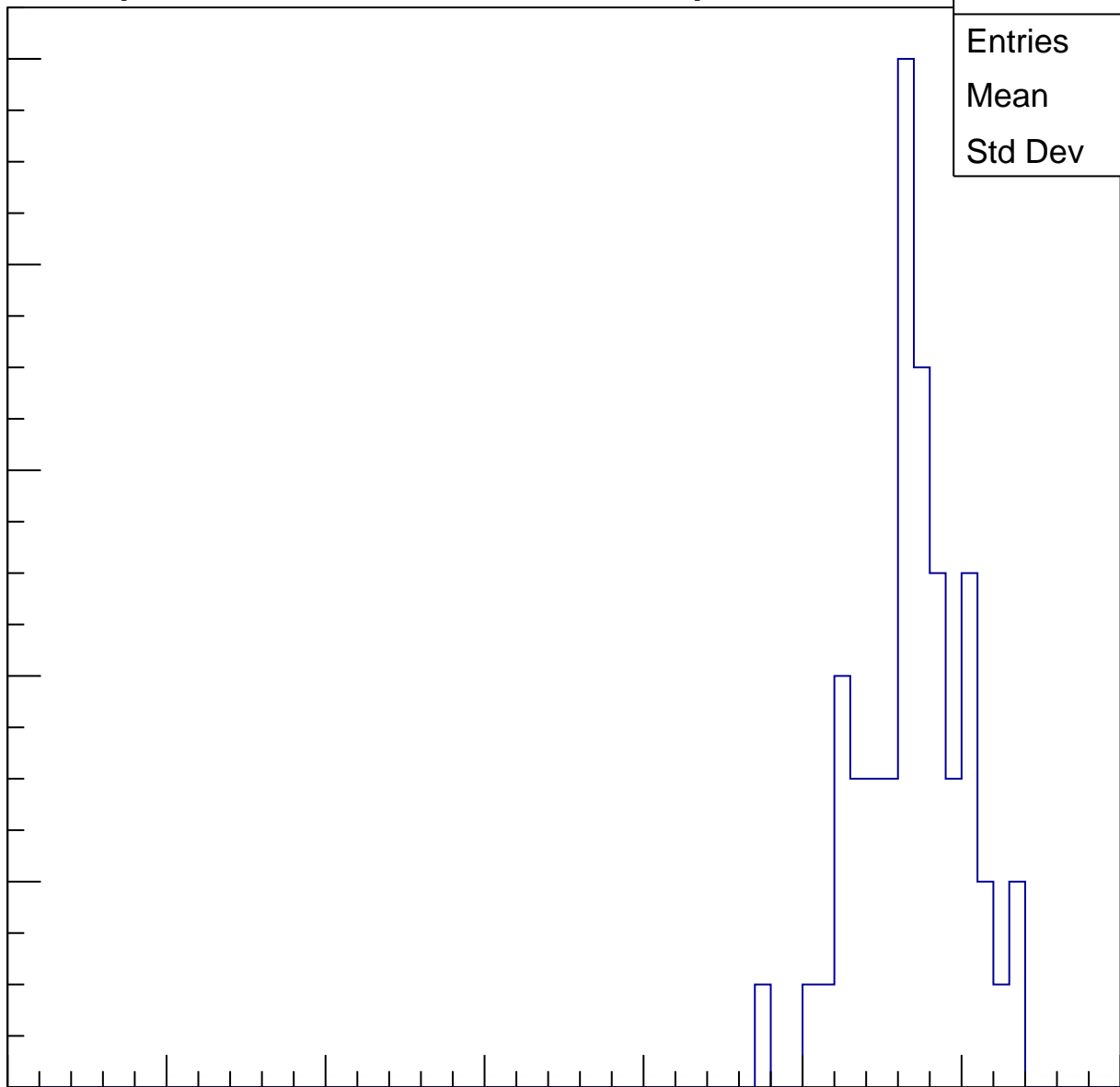
40

50

60

70

ampl



B1L102S, U26-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

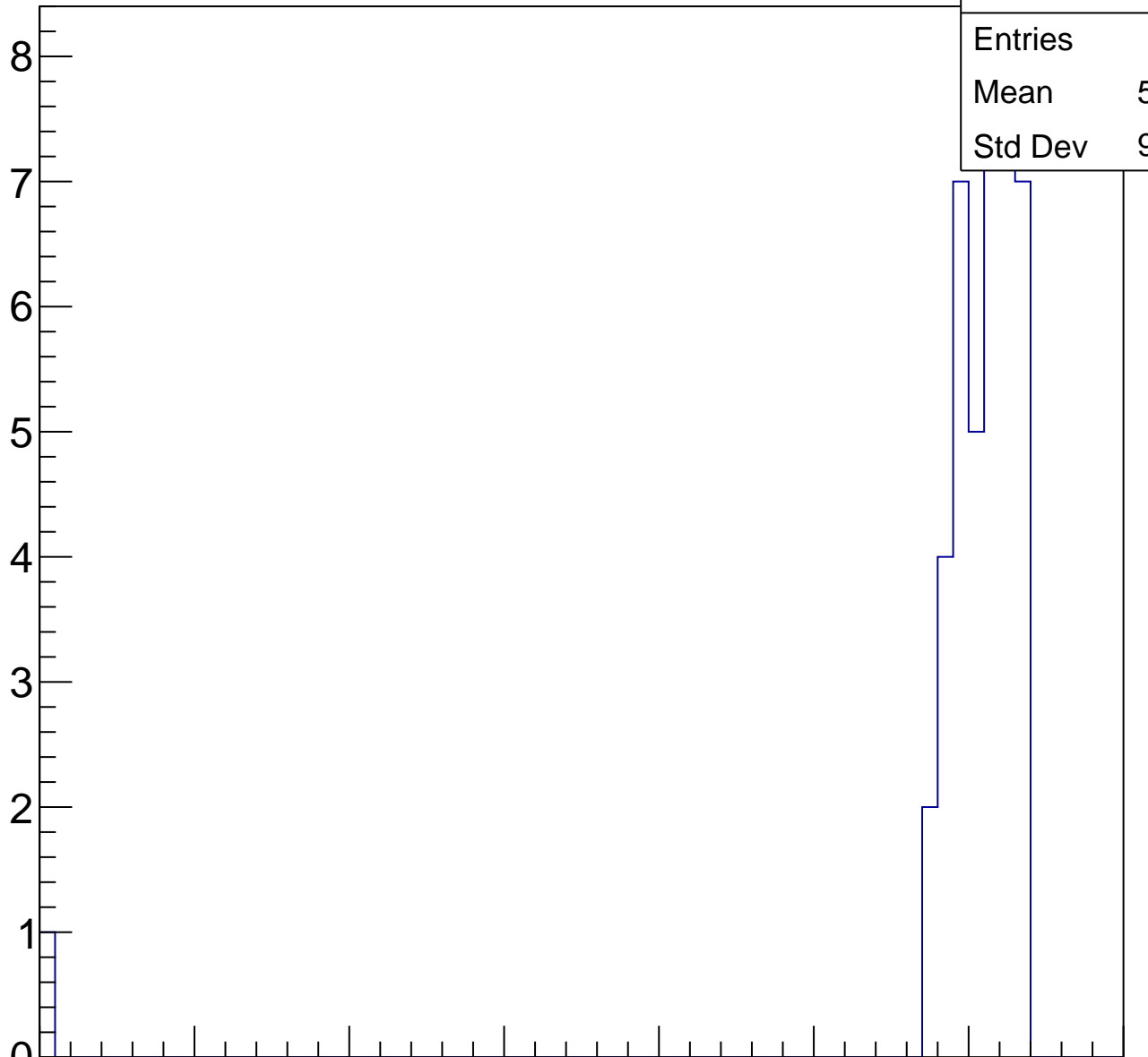
Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.14
Std Dev	9.403

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch6, adc0

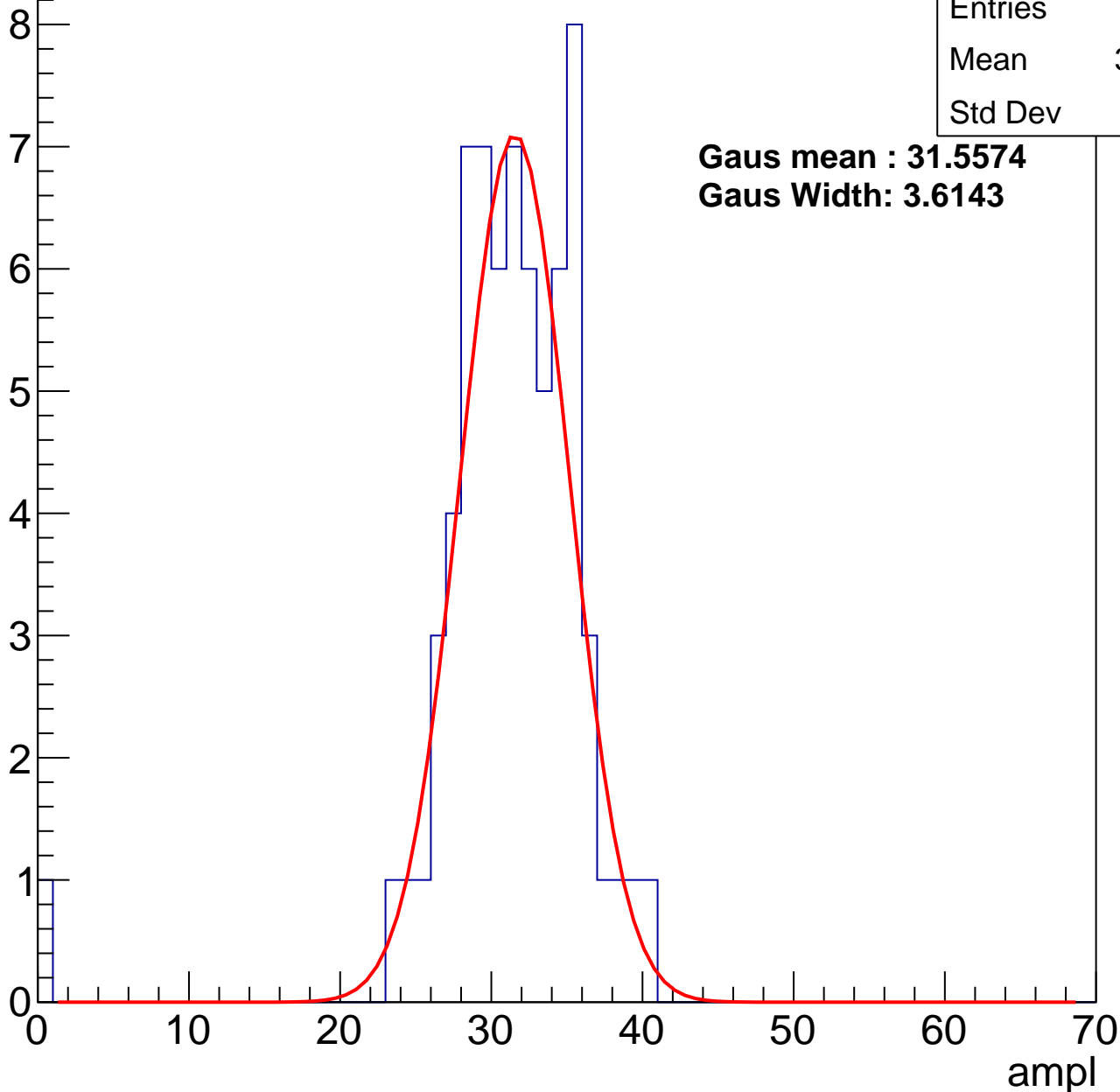
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	30.81
Std Dev	5.15

Gaus mean : 31.5574

Gaus Width: 3.6143



B1L102S, U26-ch6, adc1

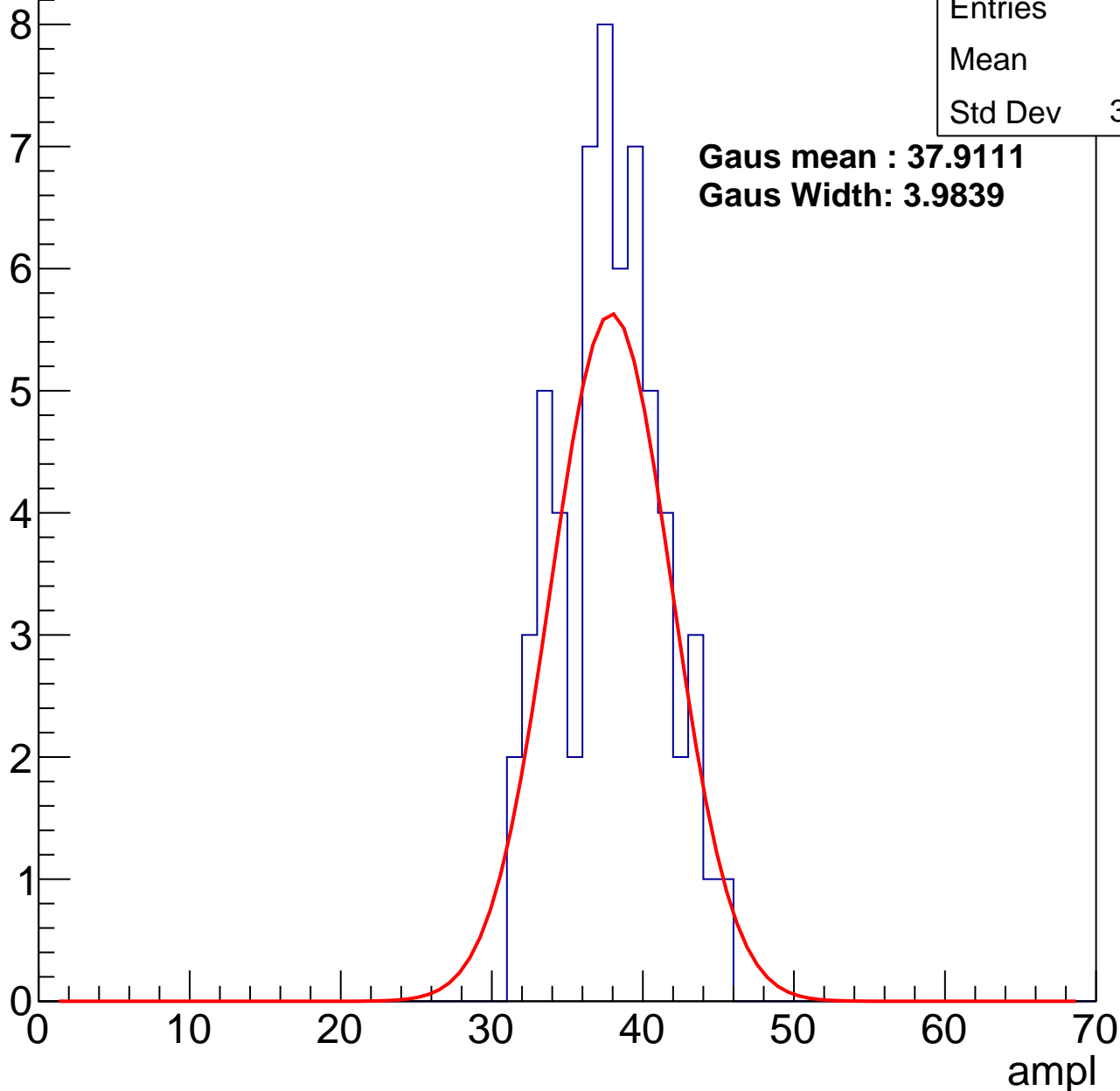
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	37.4
Std Dev	3.372

Gaus mean : 37.9111

Gaus Width: 3.9839



B1L102S, U26-ch6, adc2

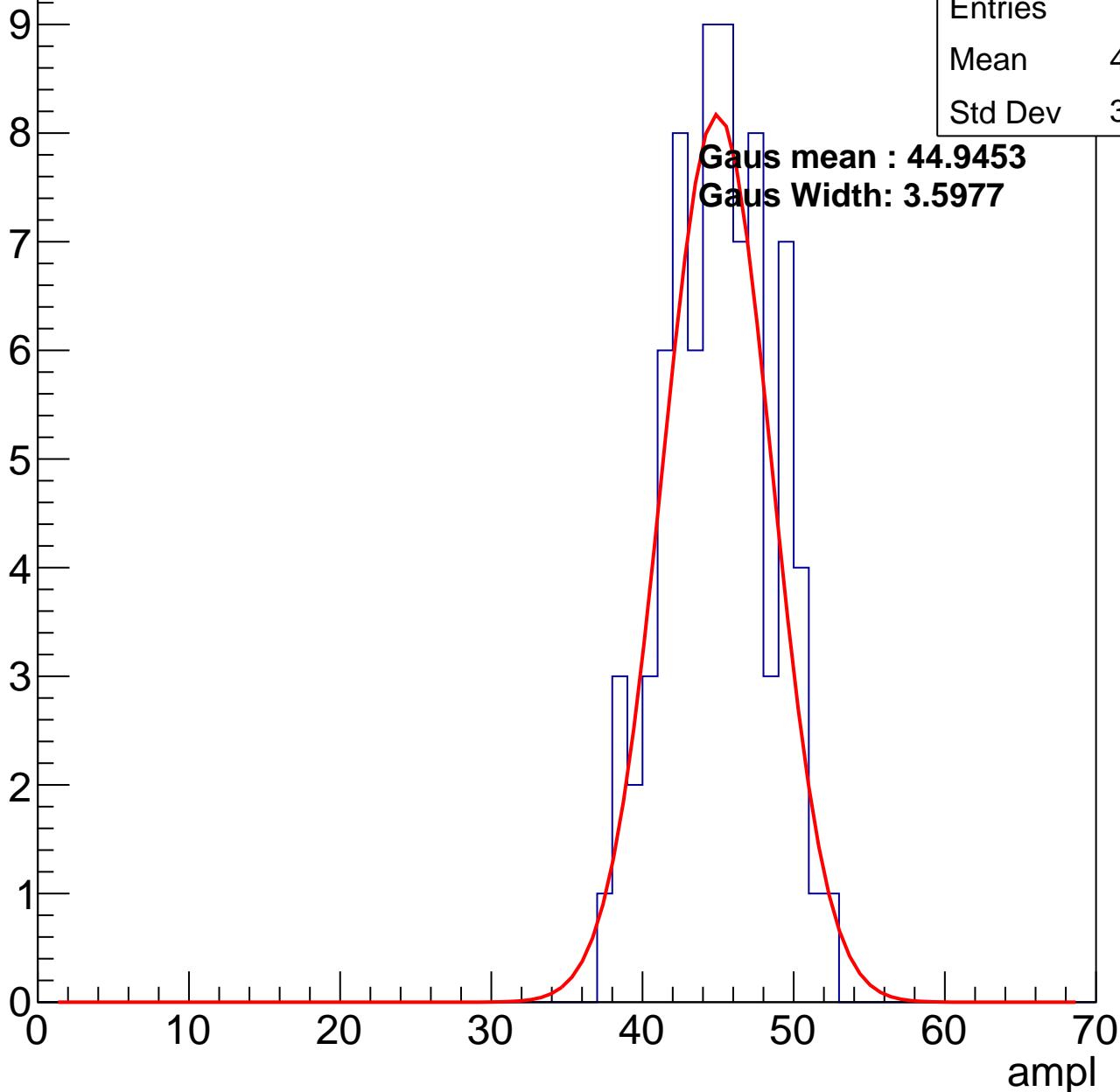
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	44.59
Std Dev	3.414

Gaus mean : 44.9453

Gaus Width: 3.5977

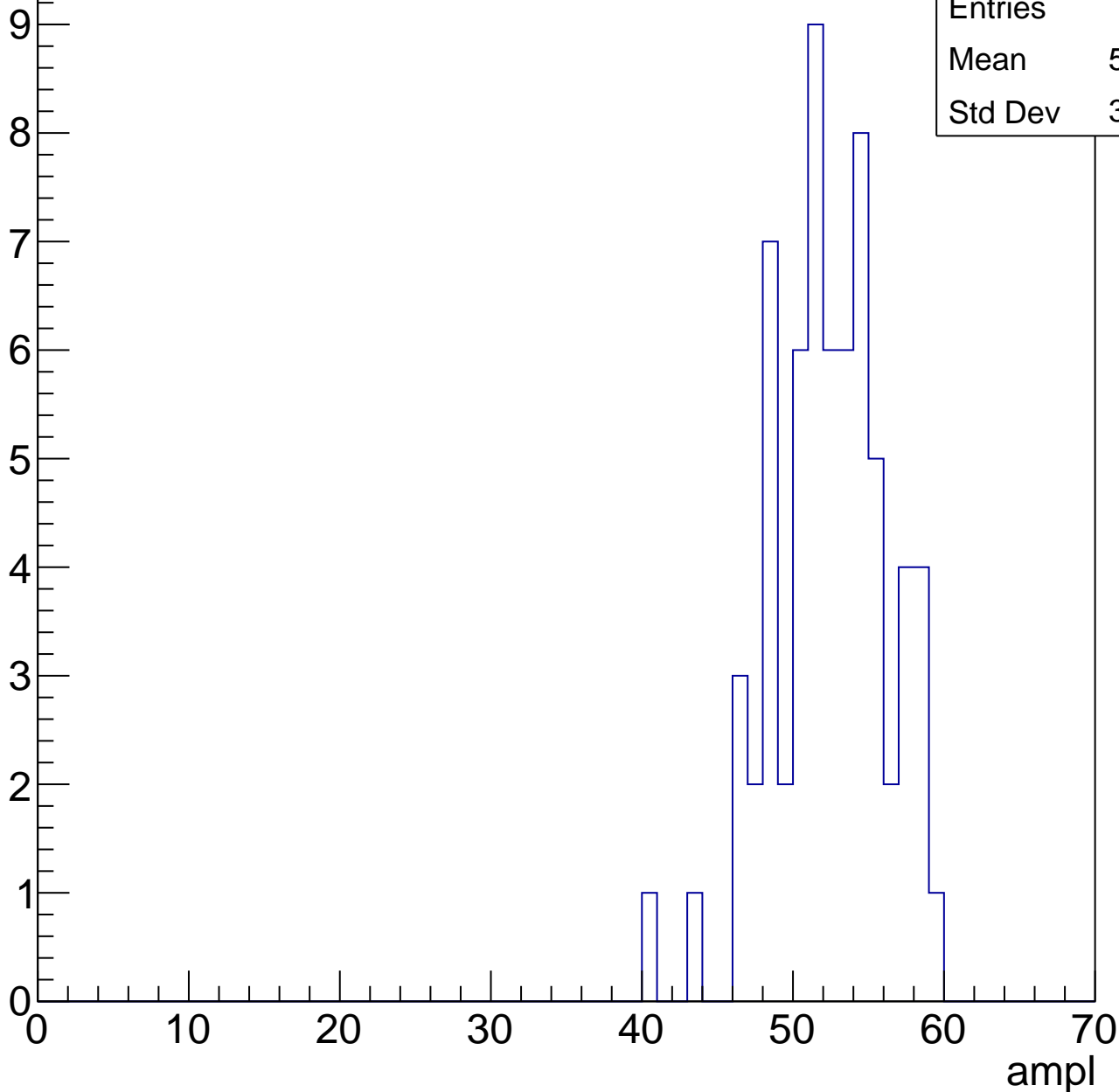


B1L102S, U26-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	51.88
Std Dev	3.772



B1L102S, U26-ch6, adc4

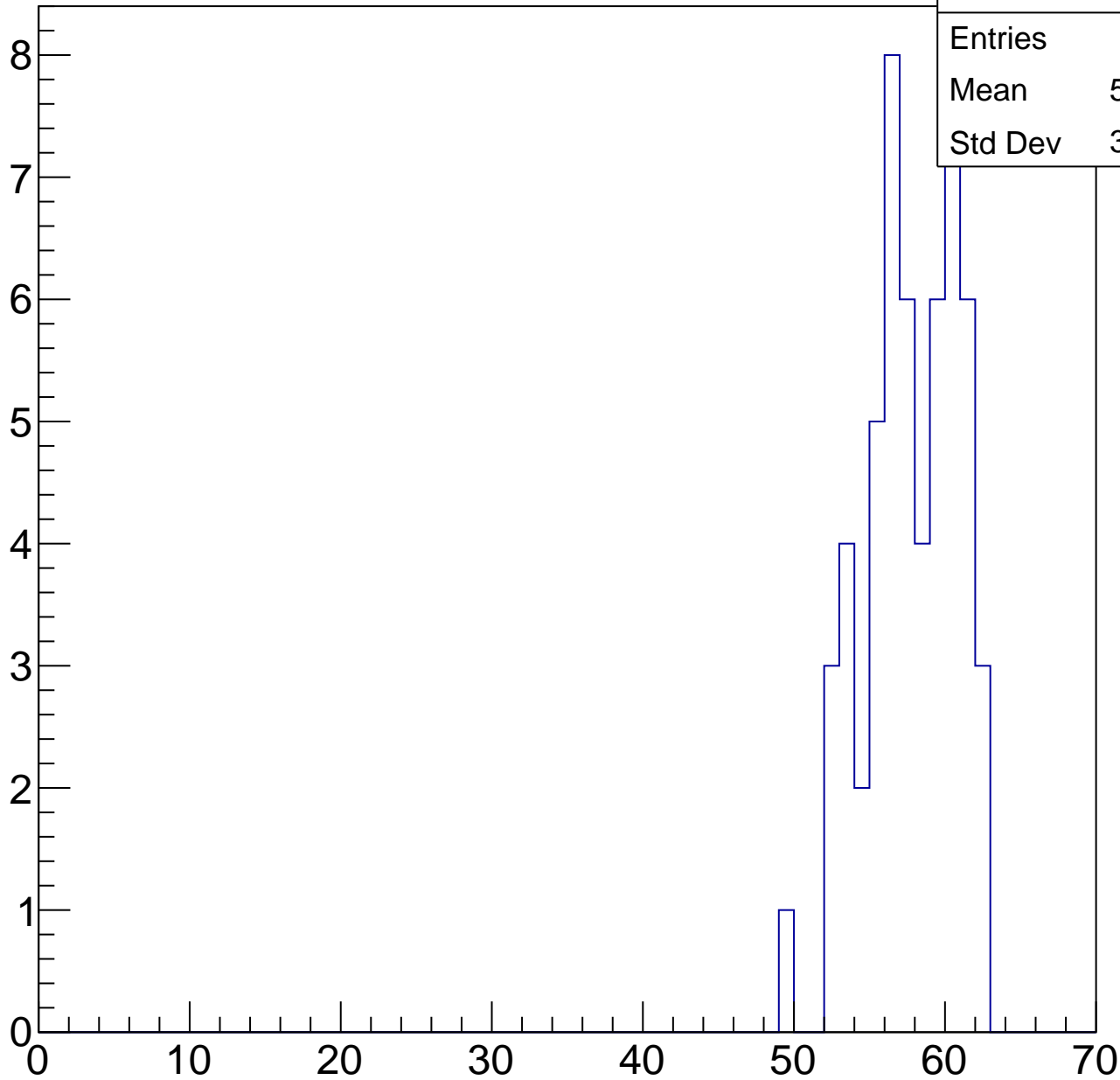
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	57.29
Std Dev	3.034

ampl

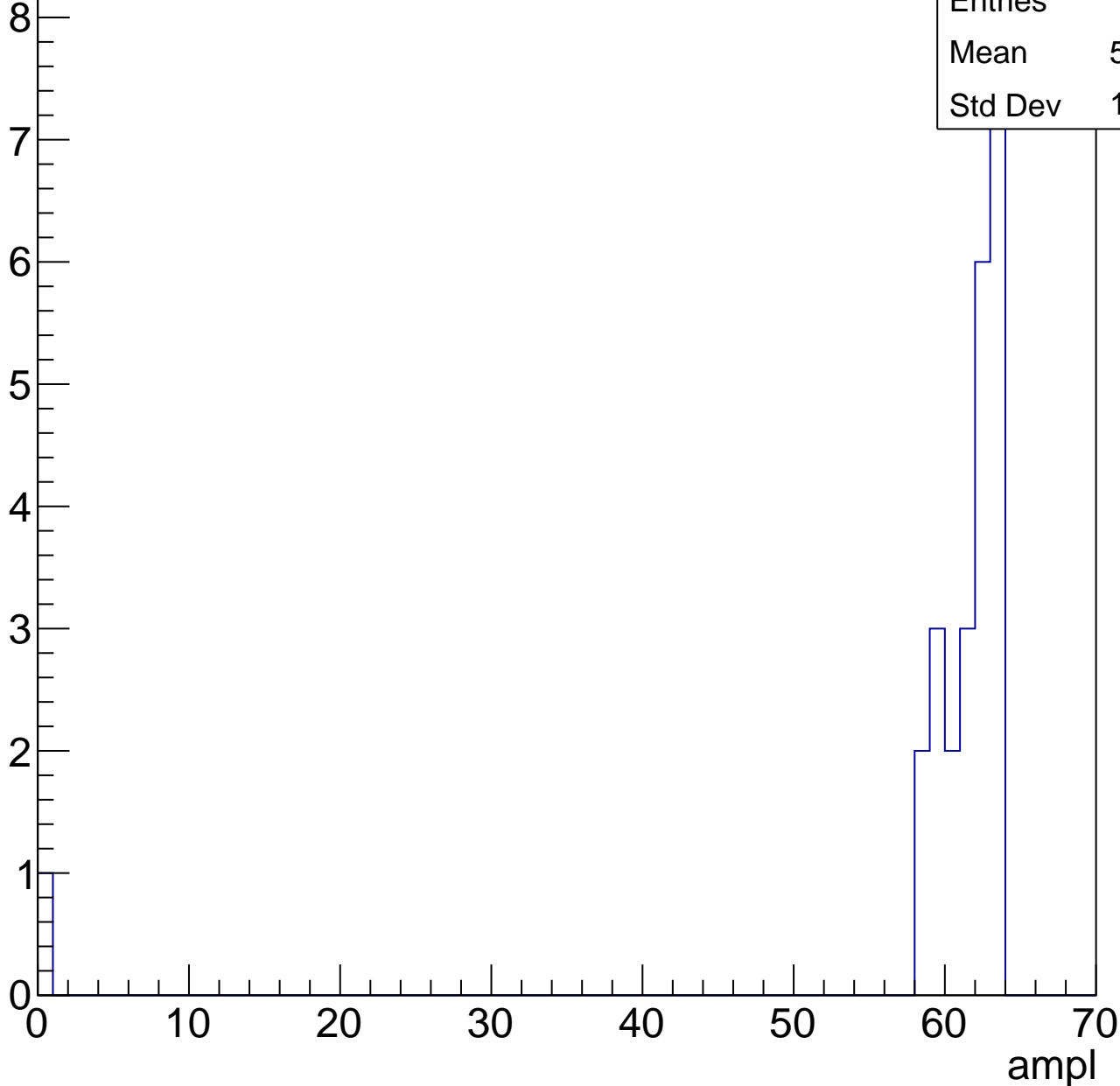


B1L102S, U26-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.88
Std Dev	12.13



B1L102S, U26-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch6, adc7

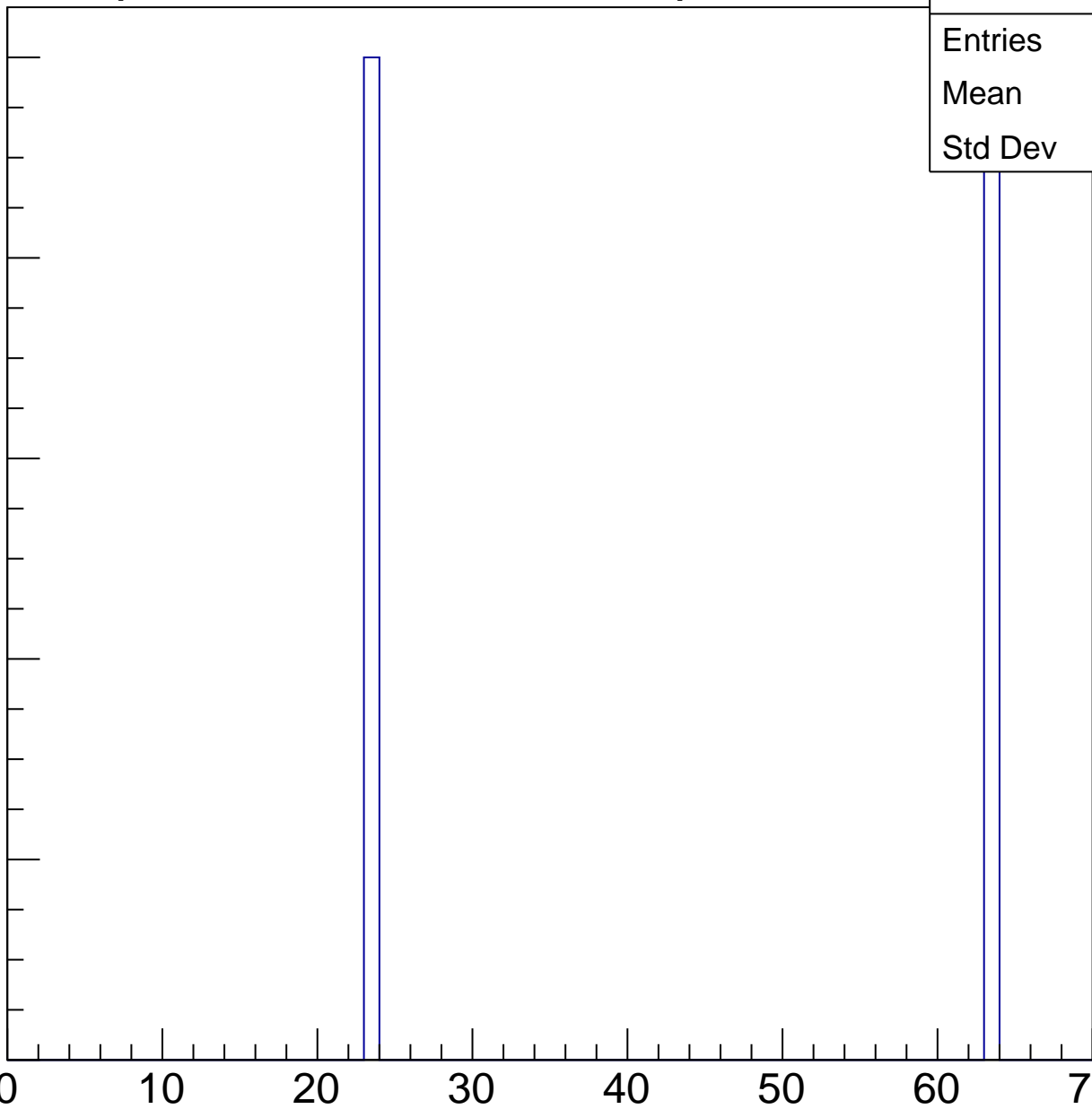
calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl



B1L102S, U26-ch7, adc0

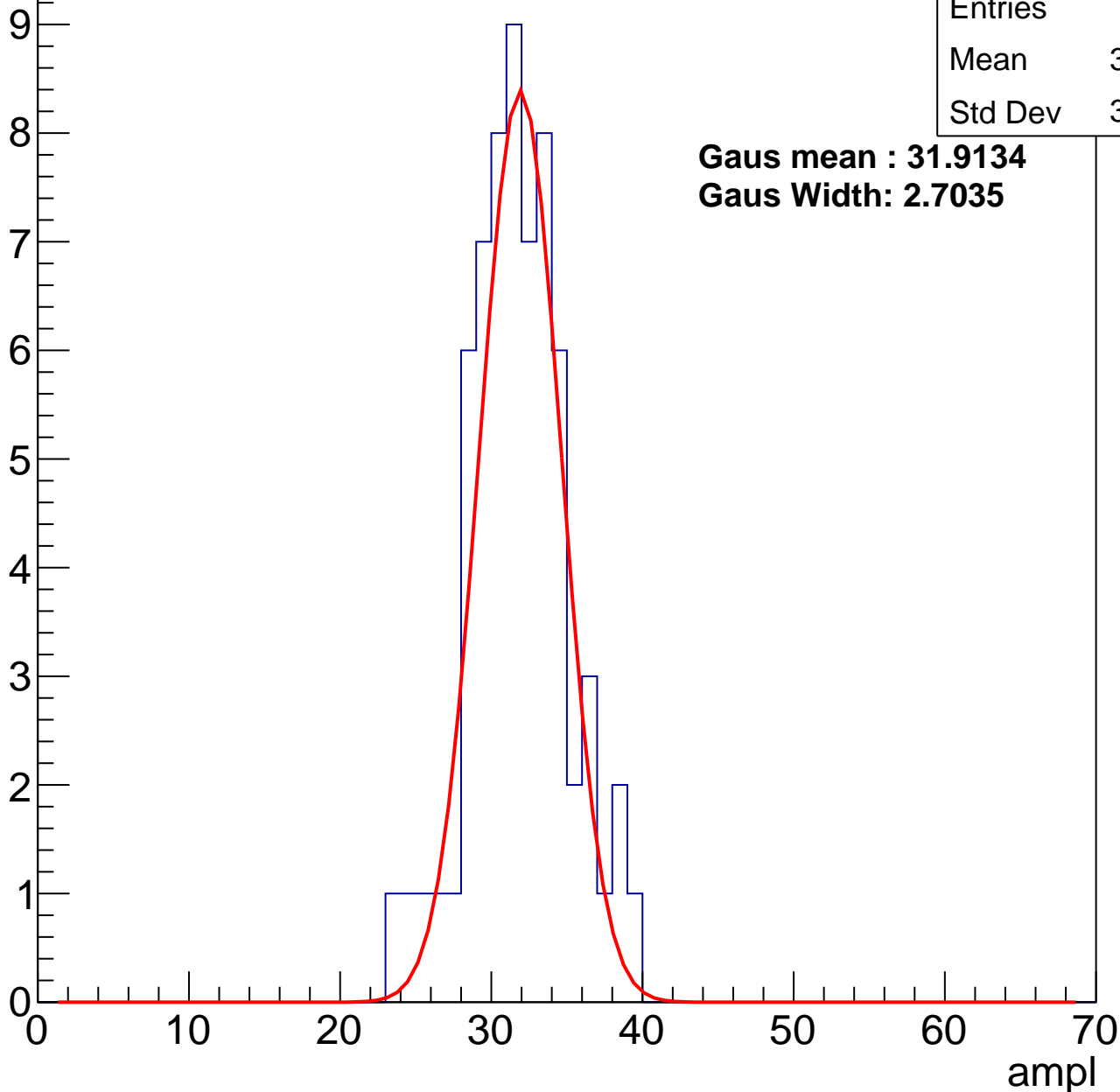
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	31.34
Std Dev	3.212

Gaus mean : 31.9134

Gaus Width: 2.7035



B1L102S, U26-ch7, adc1

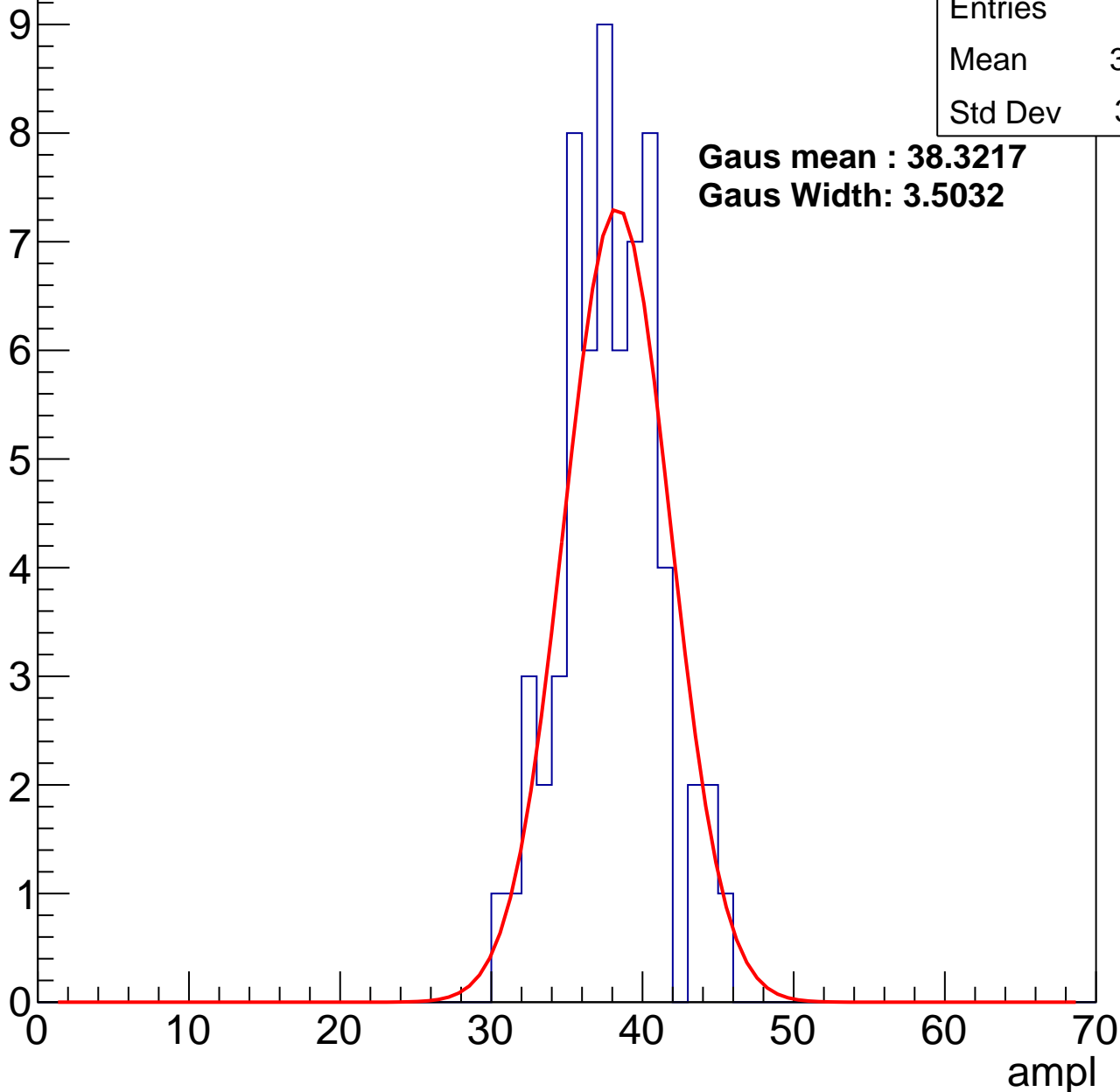
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.43
Std Dev	3.211

Gaus mean : 38.3217

Gaus Width: 3.5032



B1L102S, U26-ch7, adc2

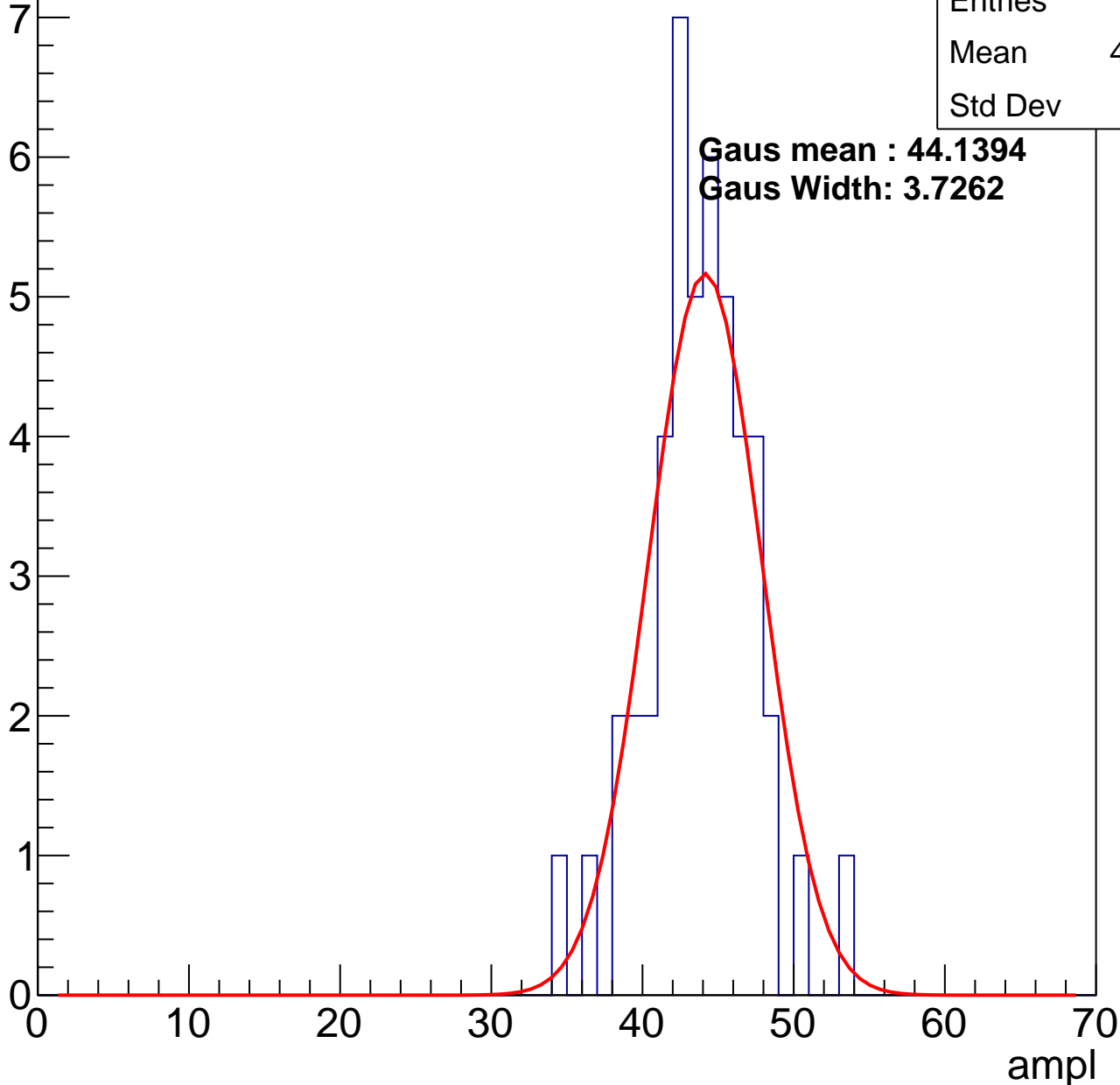
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.34
Std Dev	3.49

Gaus mean : 44.1394

Gaus Width: 3.7262



B1L102S, U26-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

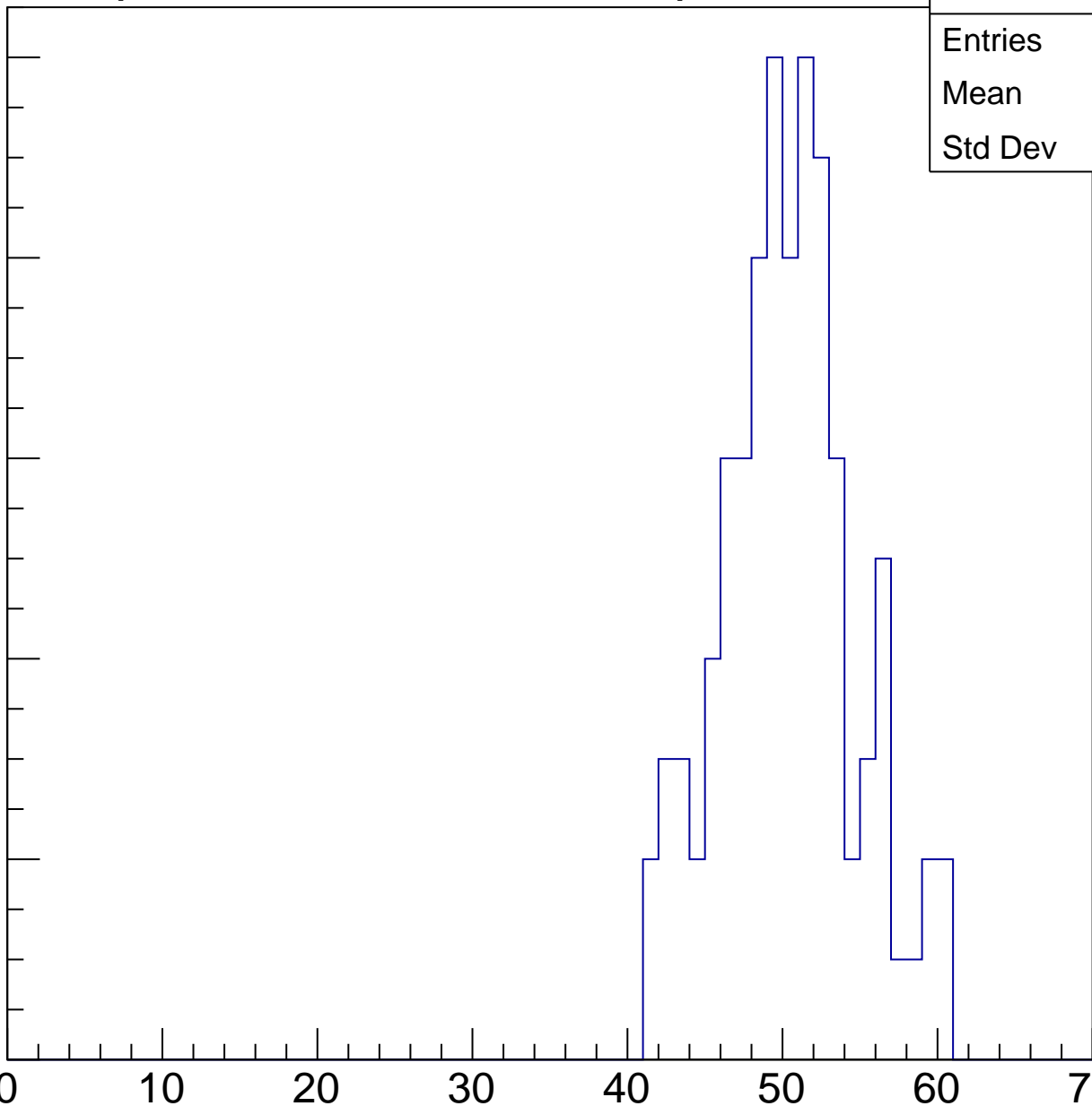
Entries	93
Mean	49.88
Std Dev	4.308

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

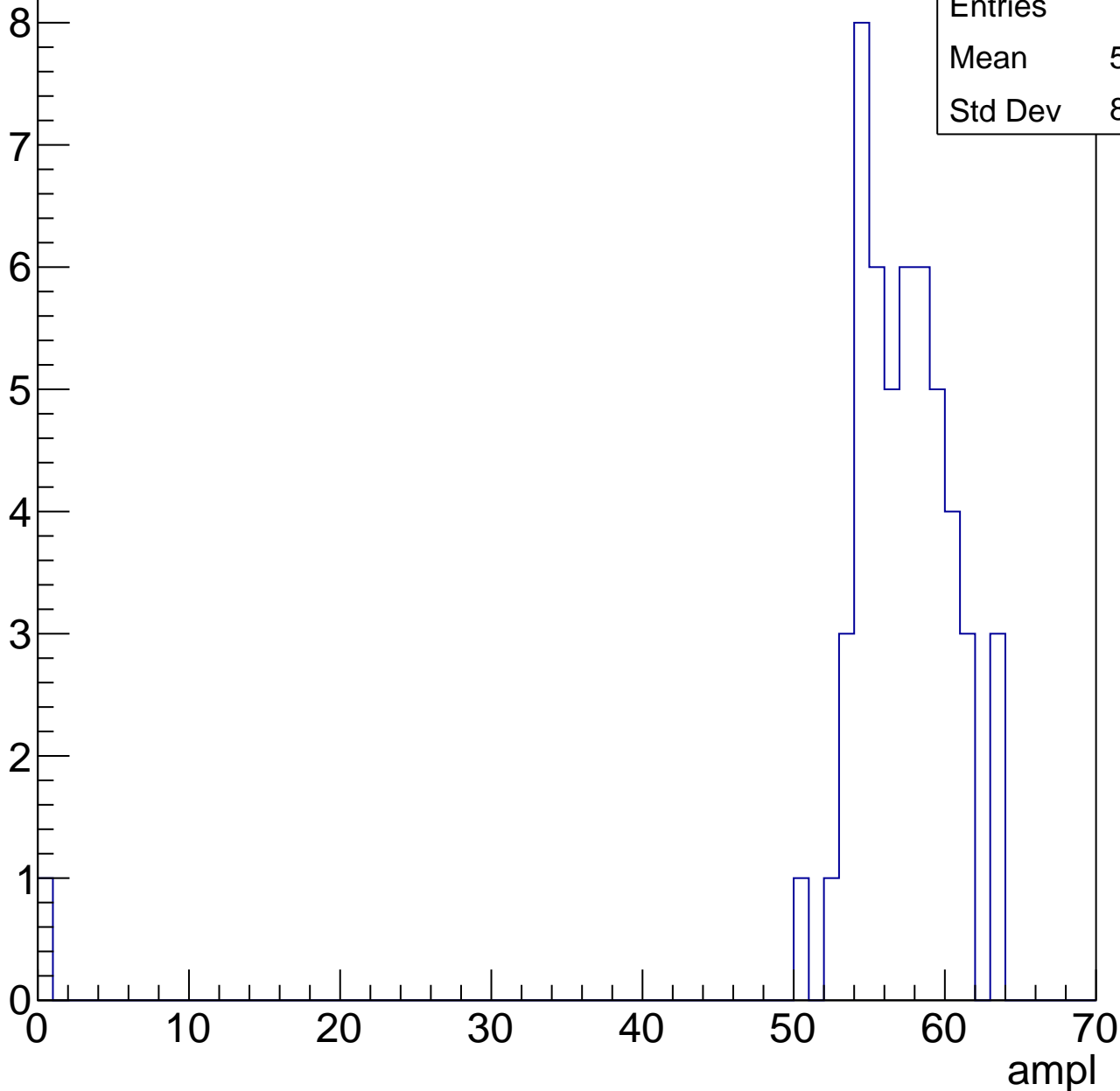


B1L102S, U26-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.77
Std Dev	8.333

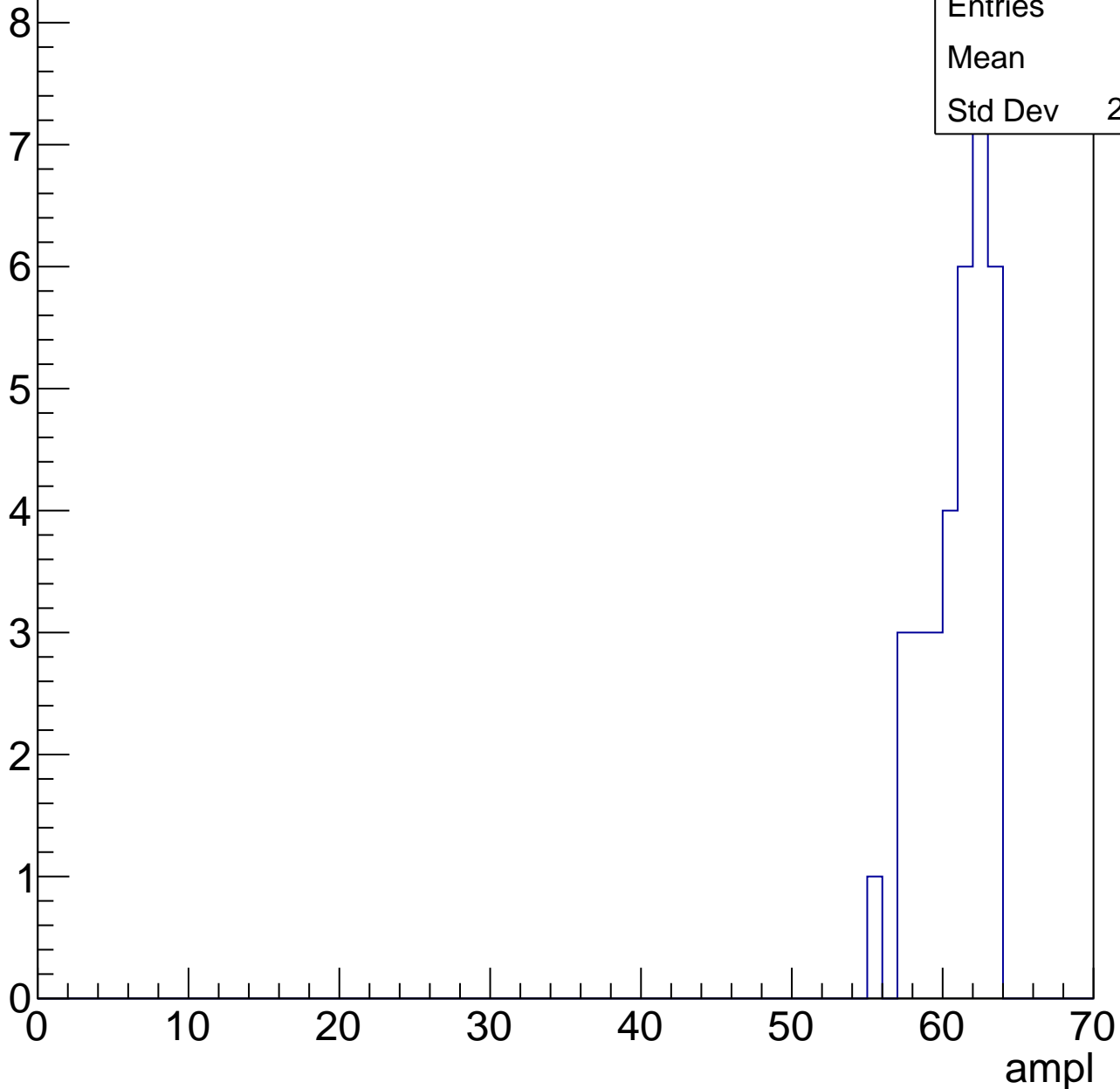


B1L102S, U26-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	60.5
Std Dev	2.104



B1L102S, U26-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L102S, U26-ch8, adc0

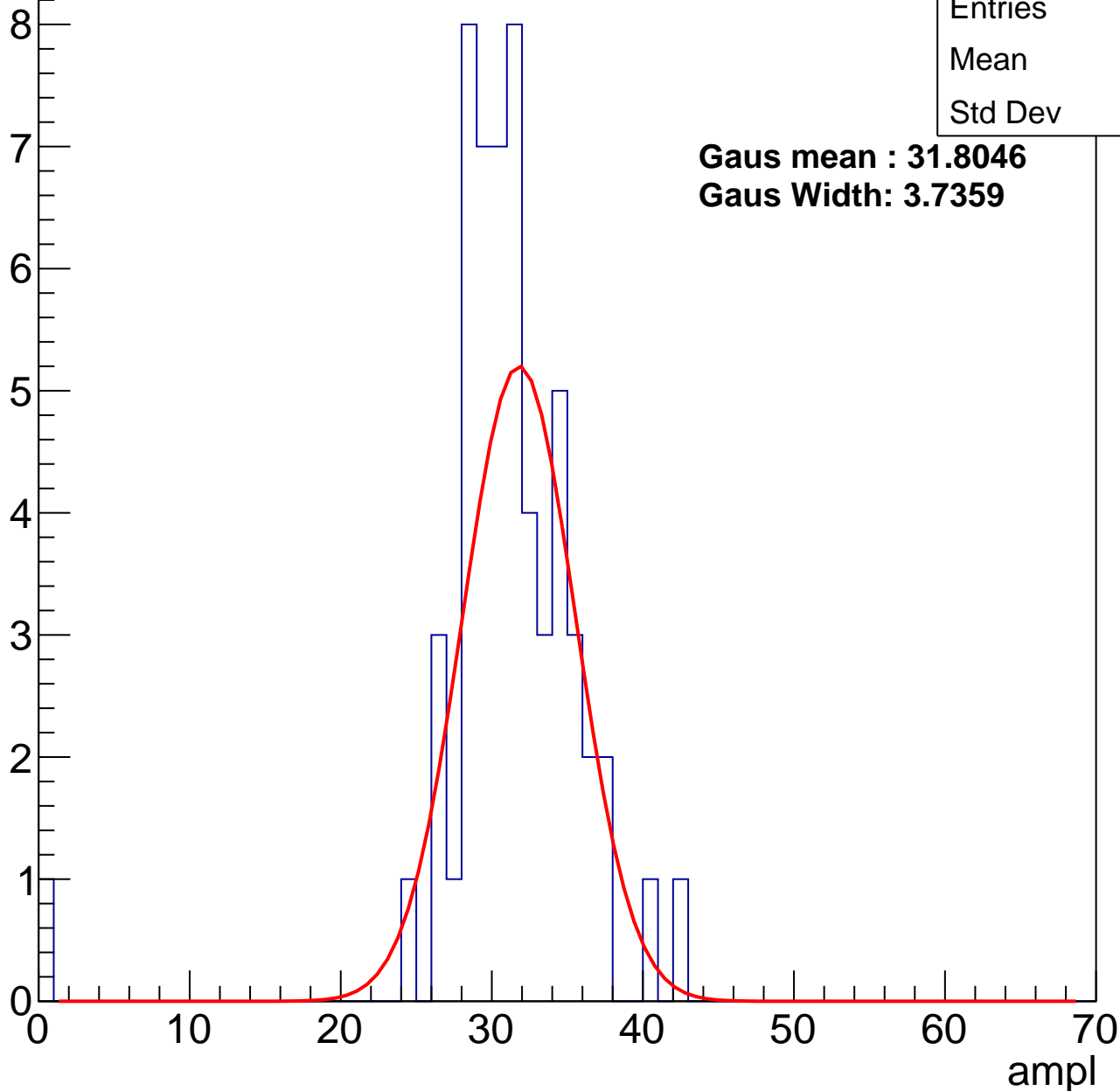
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	30.6
Std Dev	5.35

Gaus mean : 31.8046

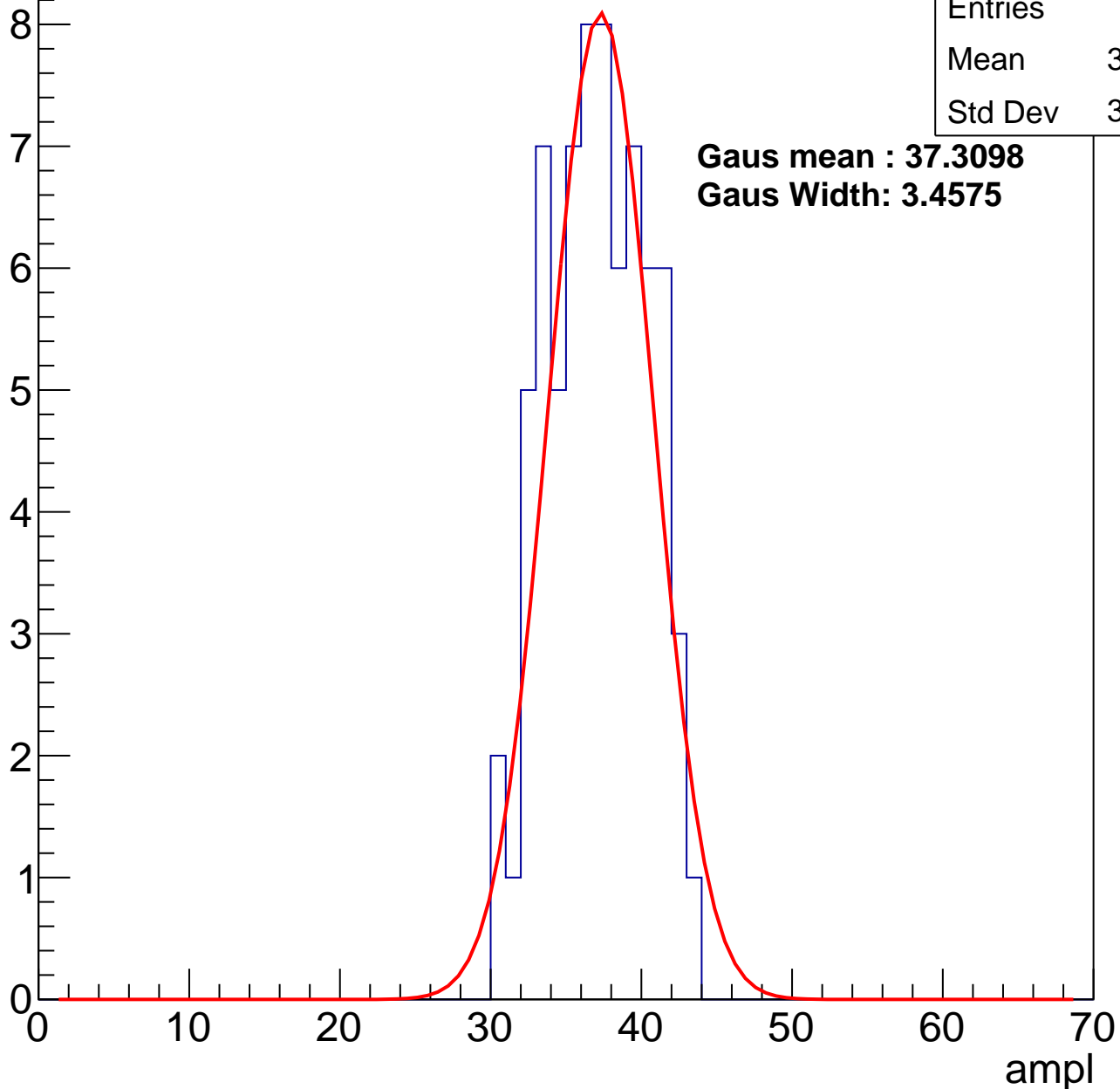
Gaus Width: 3.7359



B1L102S, U26-ch8, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch8, adc2

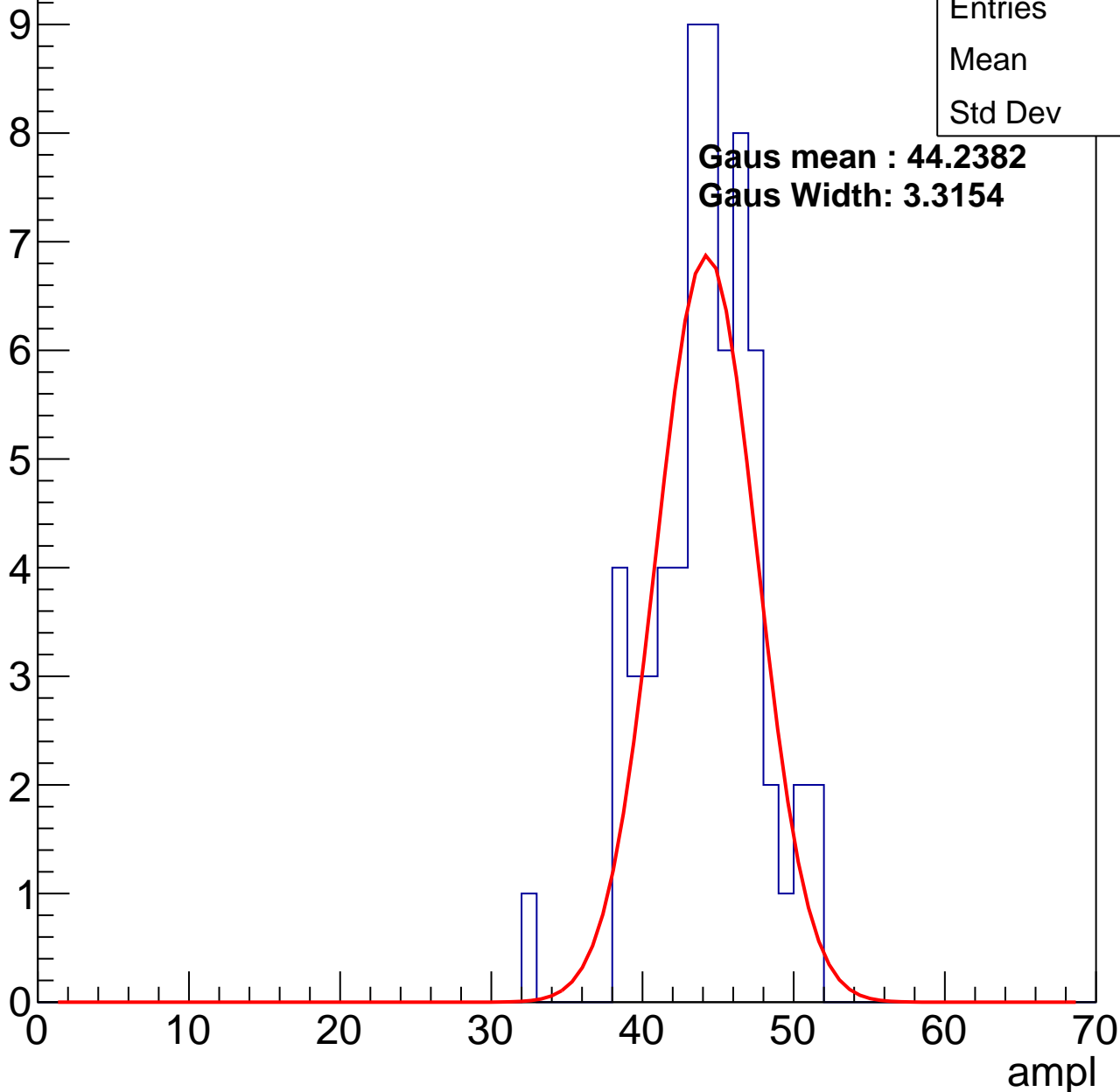
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.8
Std Dev	3.51

Gaus mean : 44.2382

Gaus Width: 3.3154

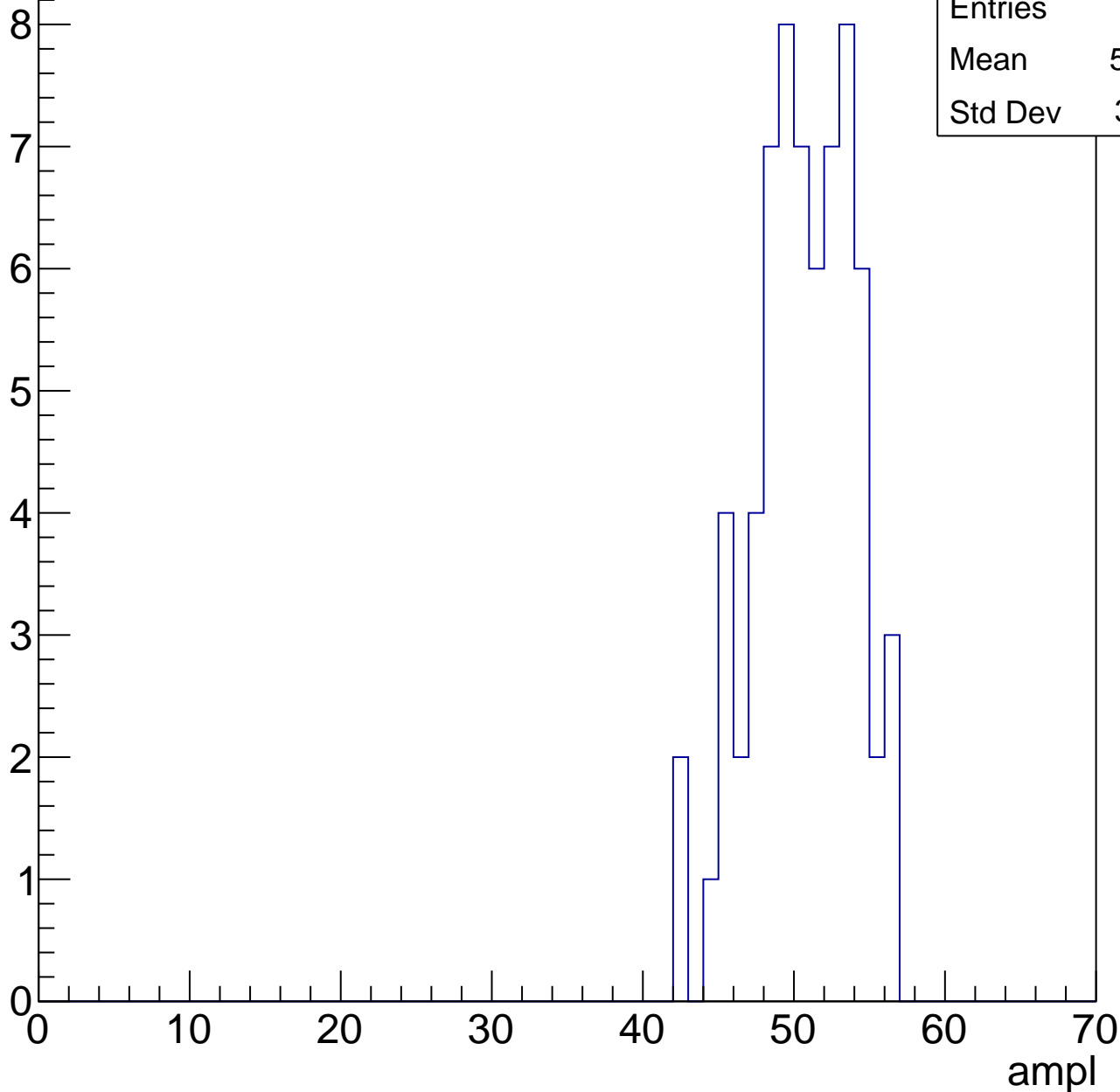


B1L102S, U26-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

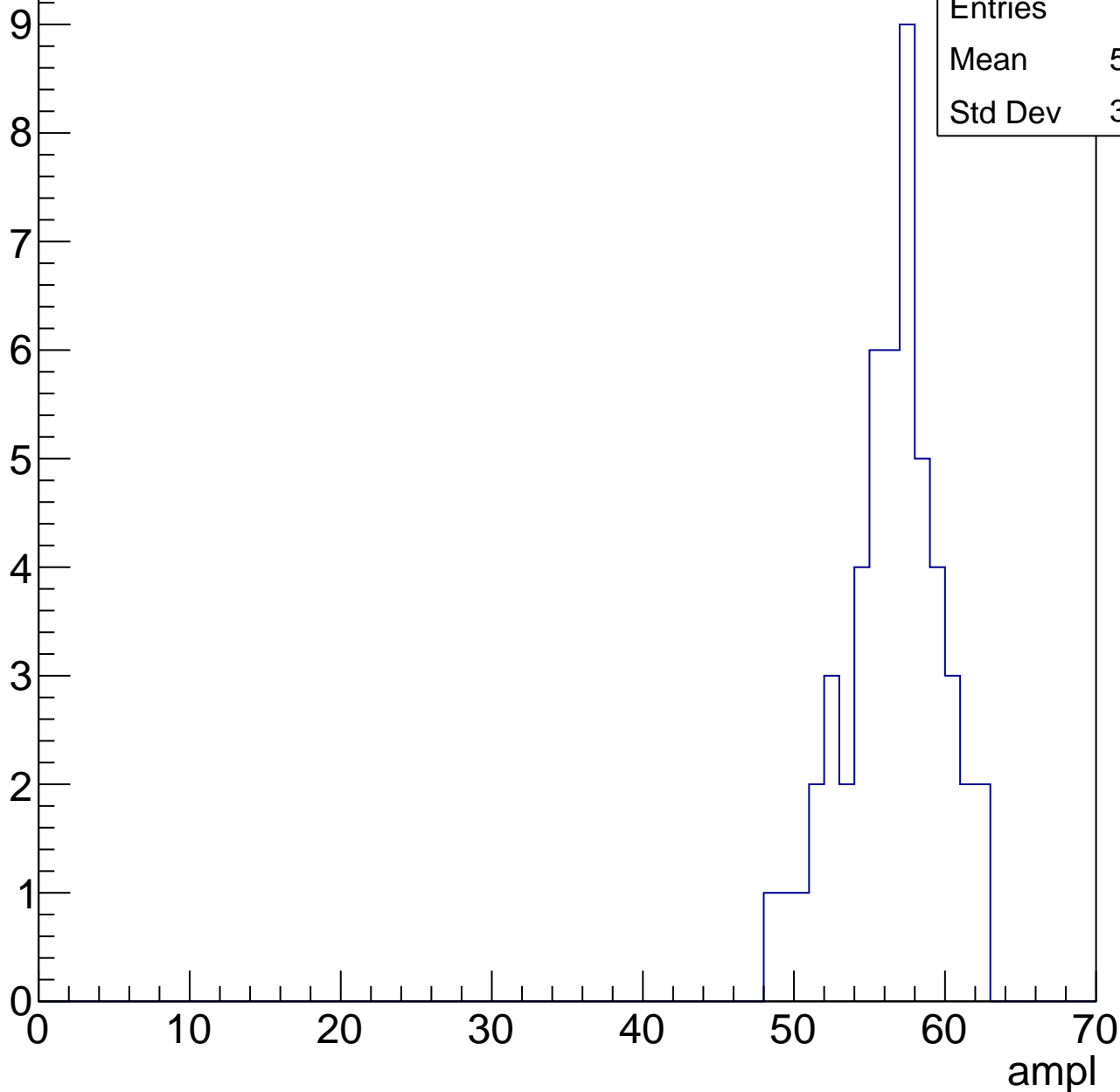
Entries	67
Mean	50.18
Std Dev	3.291



B1L102S, U26-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

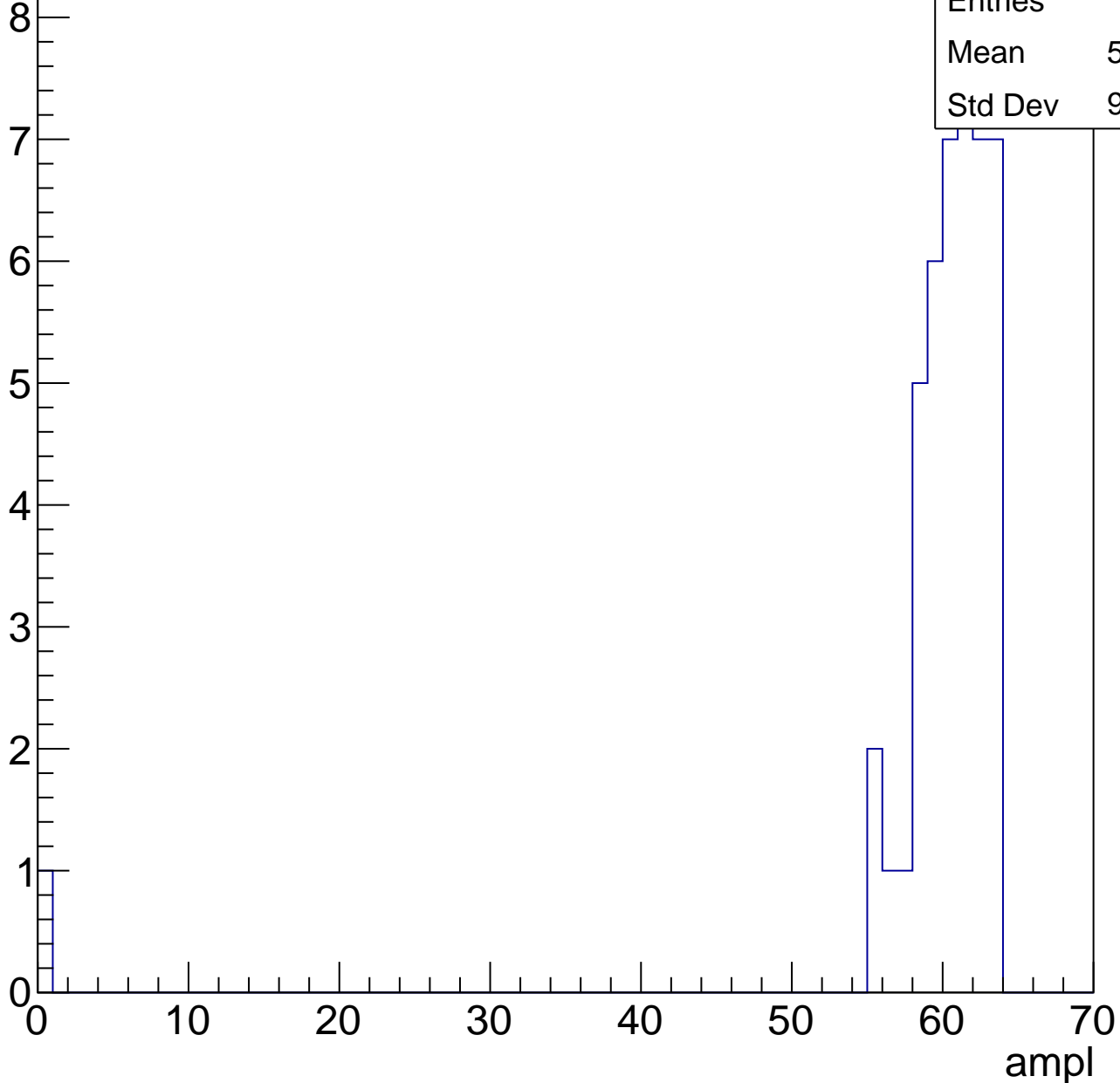


B1L102S, U26-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.89
Std Dev	9.122



B1L102S, U26-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch9, adc0

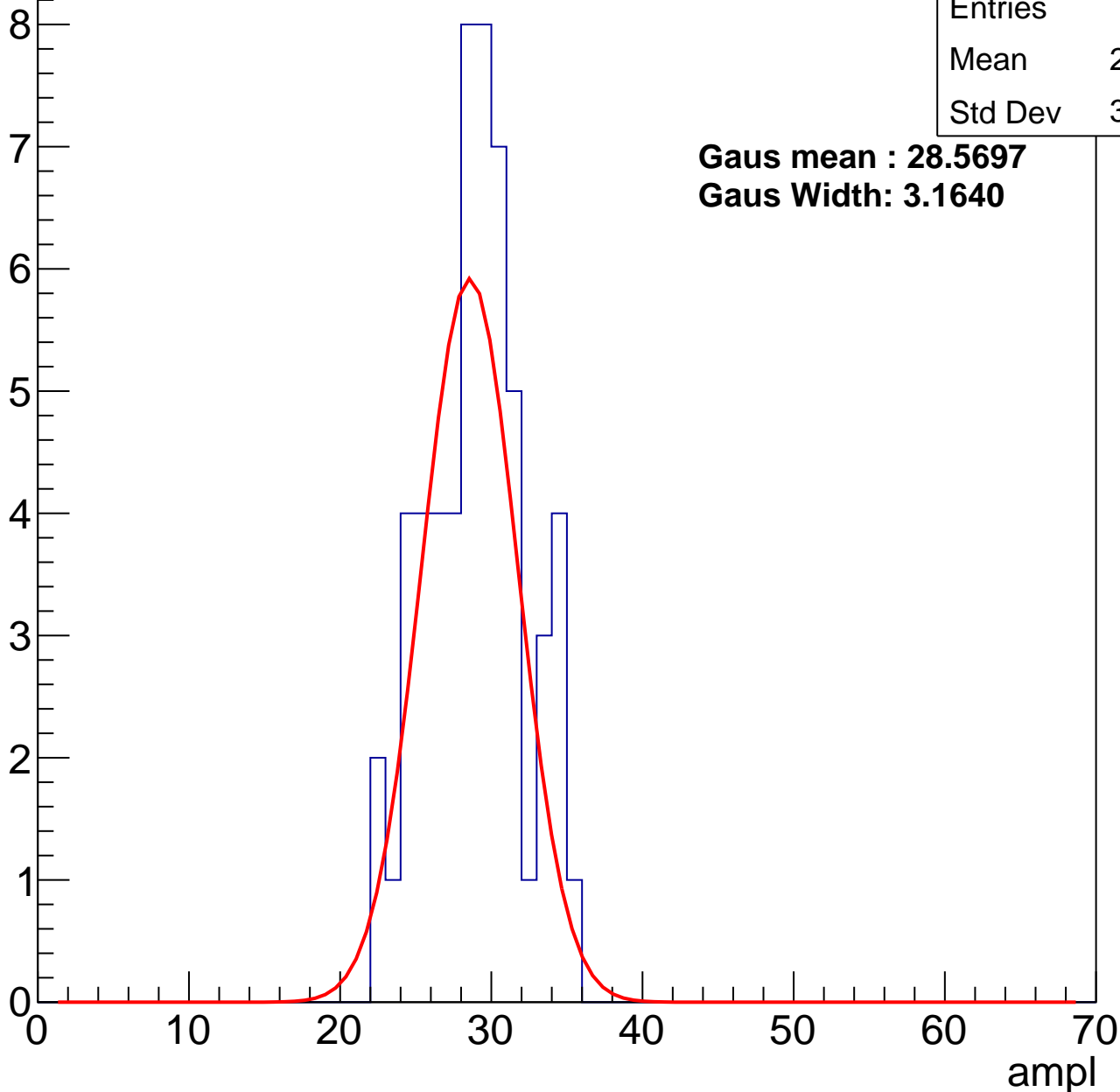
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.54
Std Dev	3.179

Gaus mean : 28.5697

Gaus Width: 3.1640



B1L102S, U26-ch9, adc1

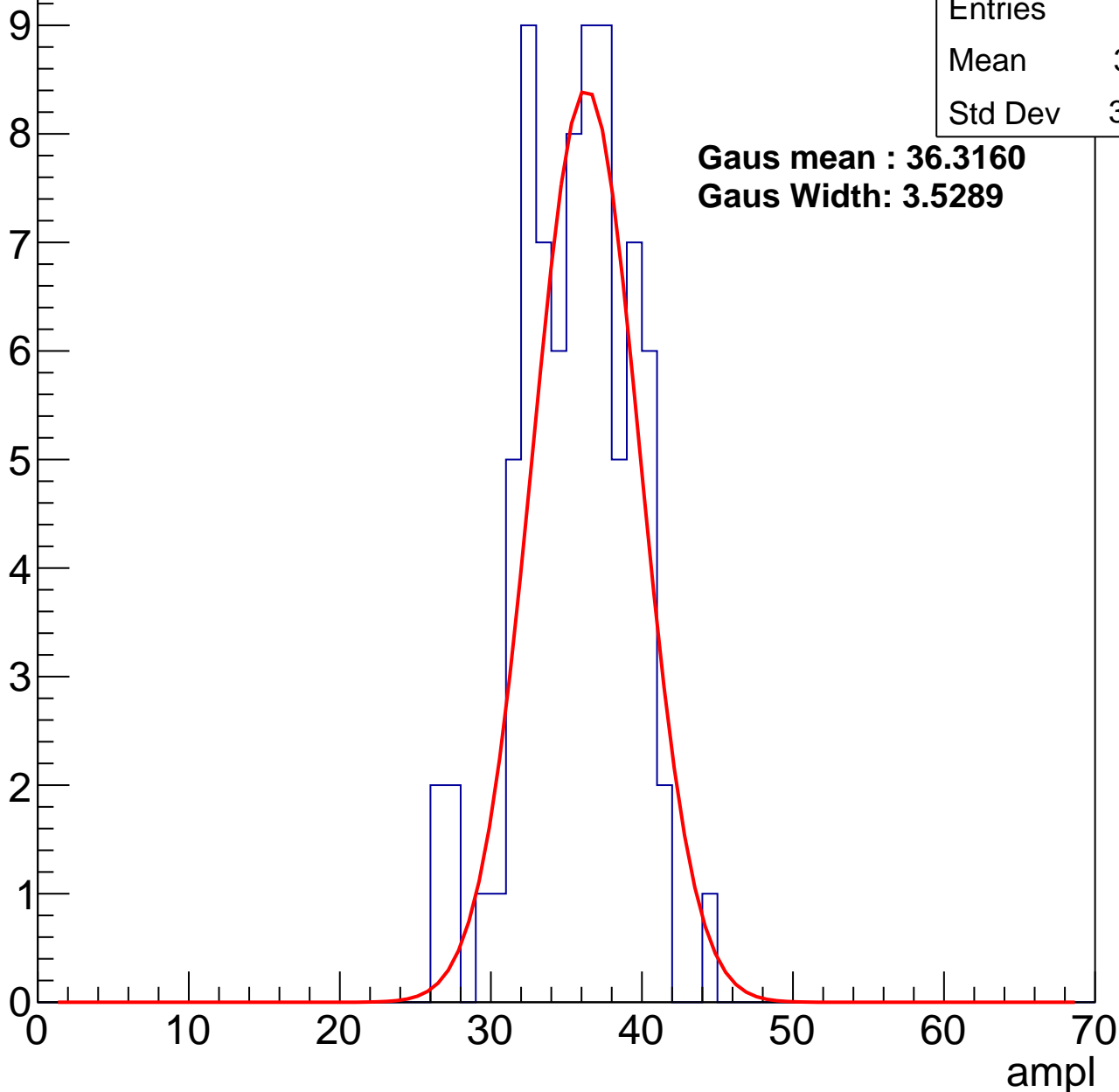
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.11
Std Dev	3.623

Gaus mean : 36.3160

Gaus Width: 3.5289



B1L102S, U26-ch9, adc2

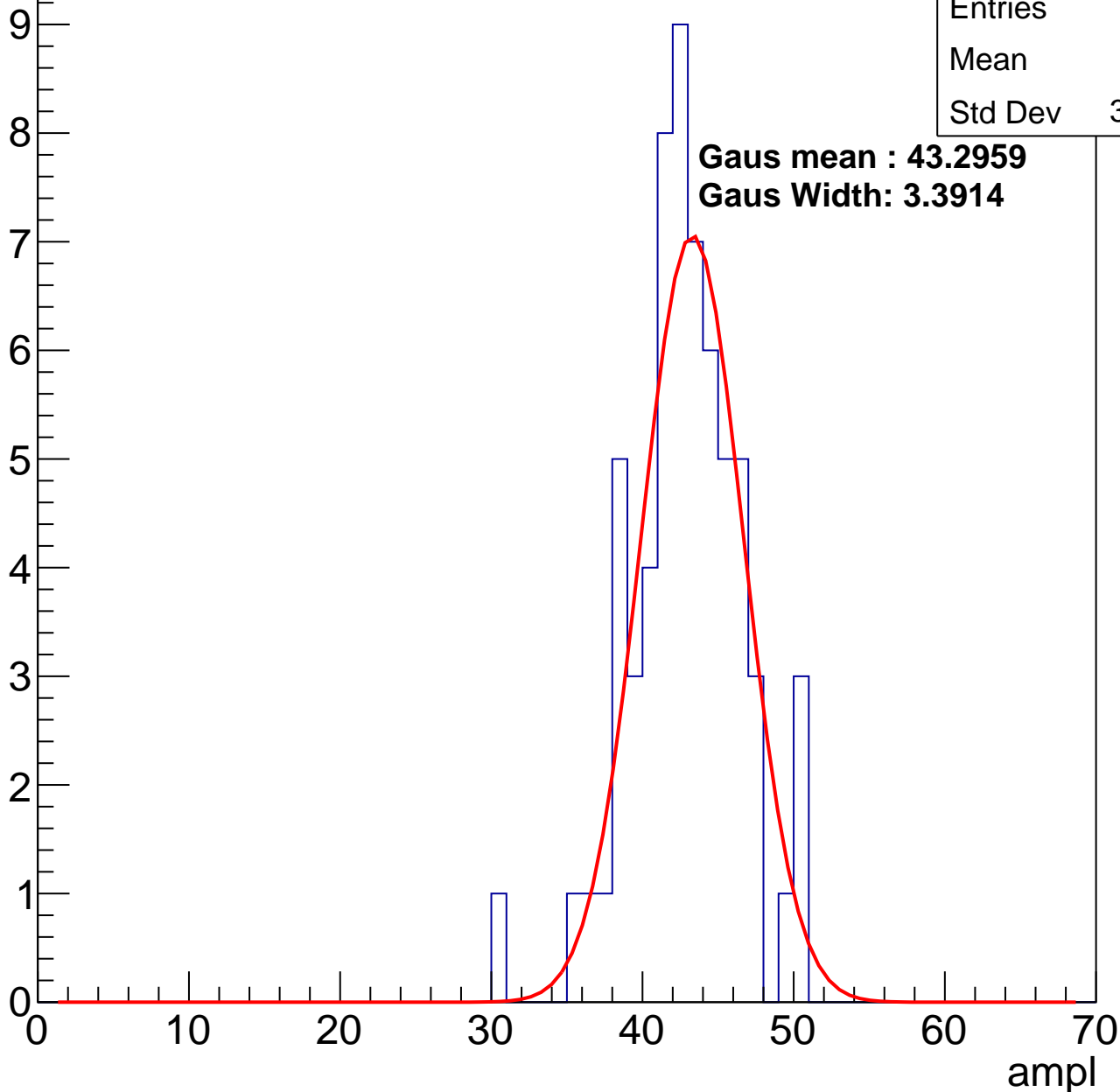
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.4
Std Dev	3.667

Gaus mean : 43.2959

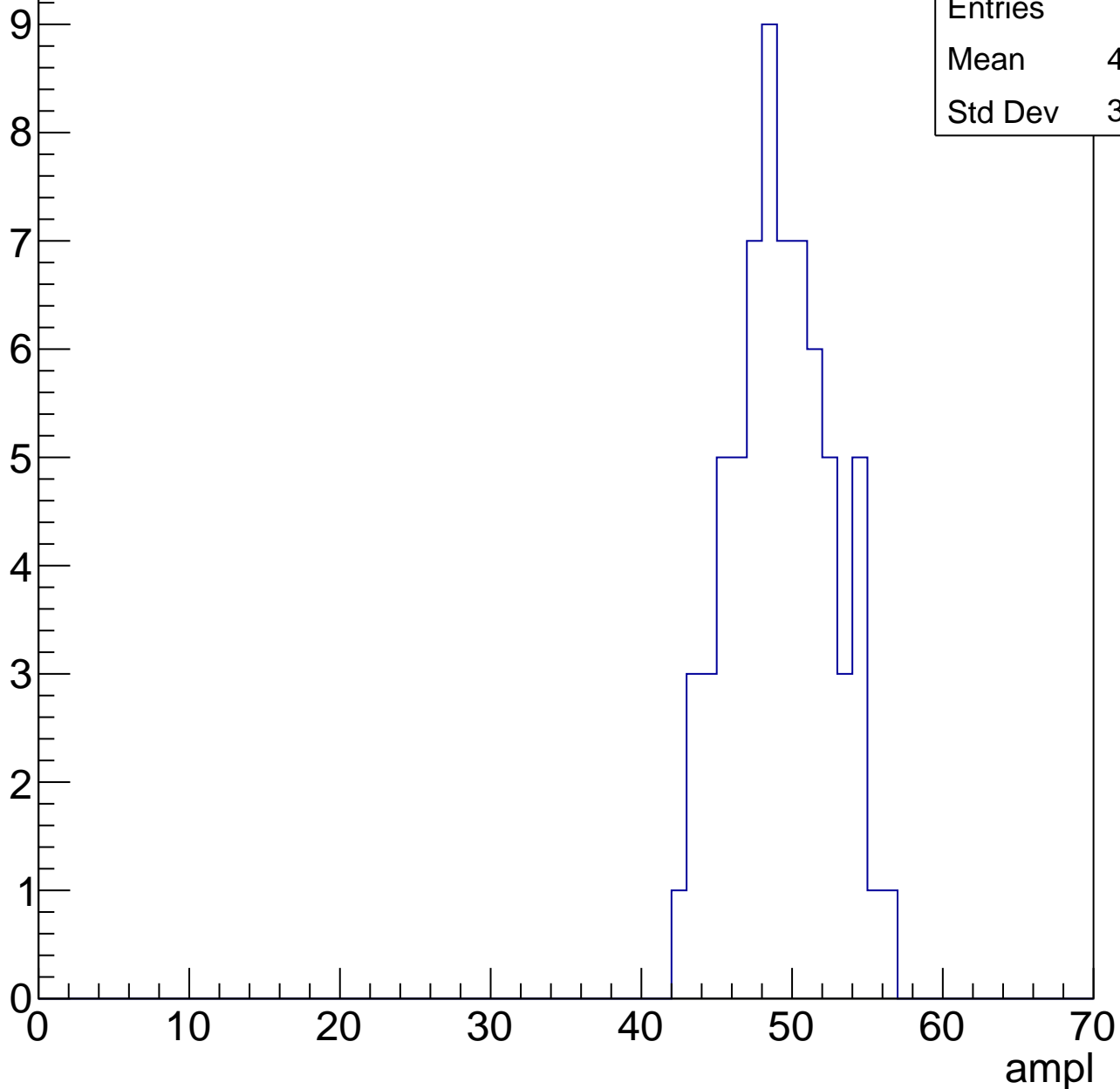
Gaus Width: 3.3914



B1L102S, U26-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



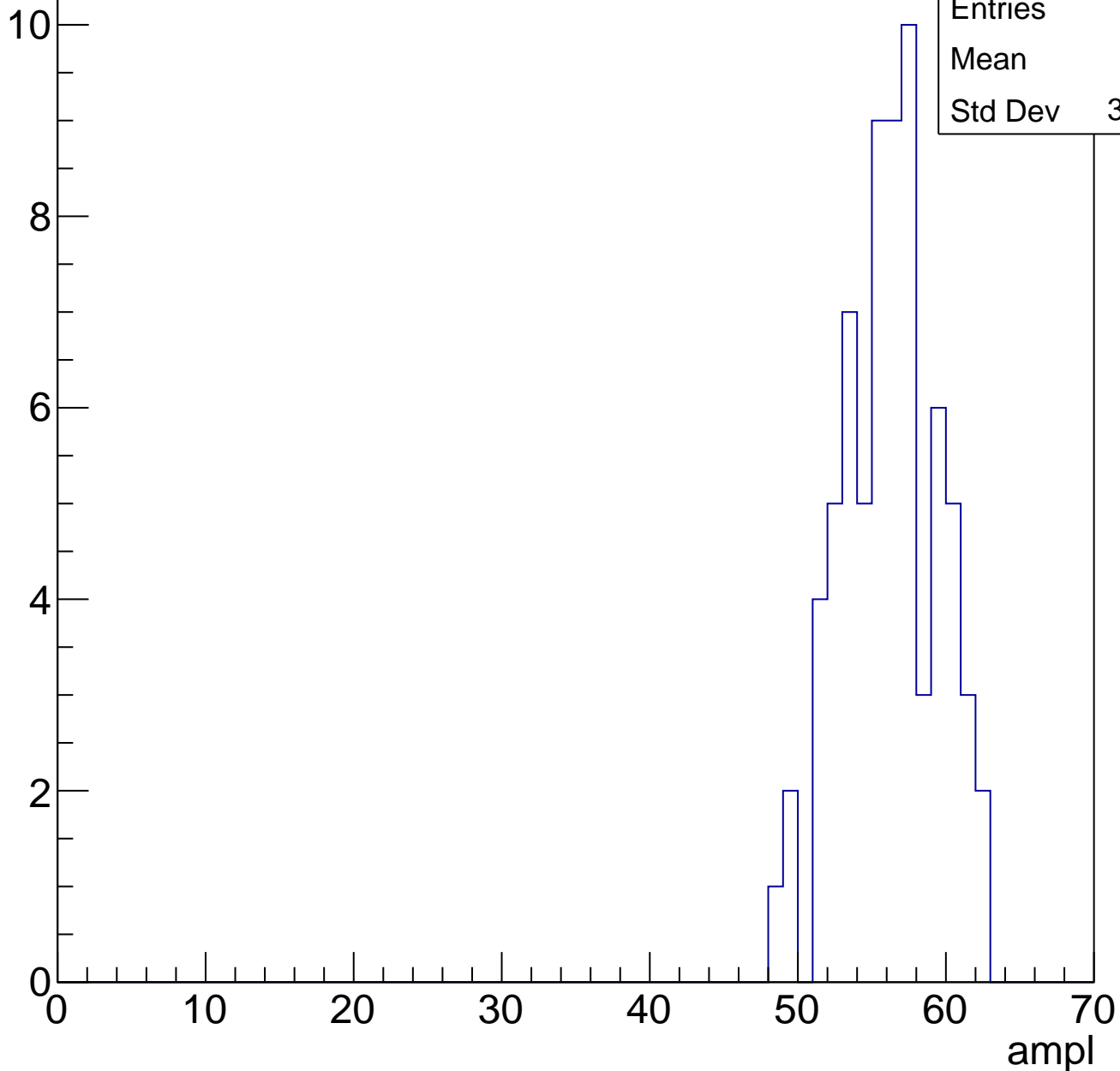
Entries	68
Mean	48.79
Std Dev	3.266

B1L102S, U26-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	55.7
Std Dev	3.208

Entry

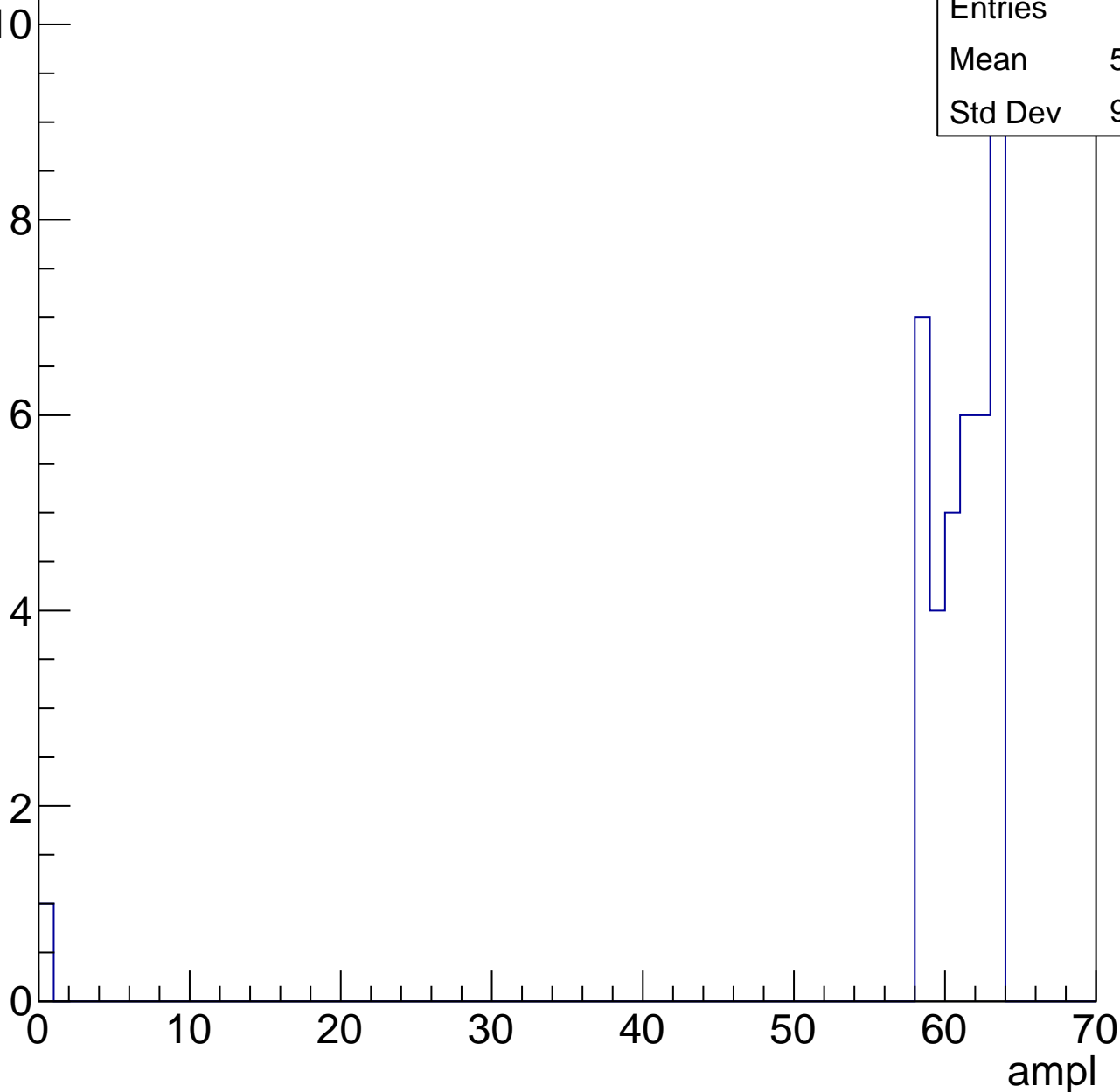


B1L102S, U26-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.23
Std Dev	9.778



B1L102S, U26-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch10, adc0

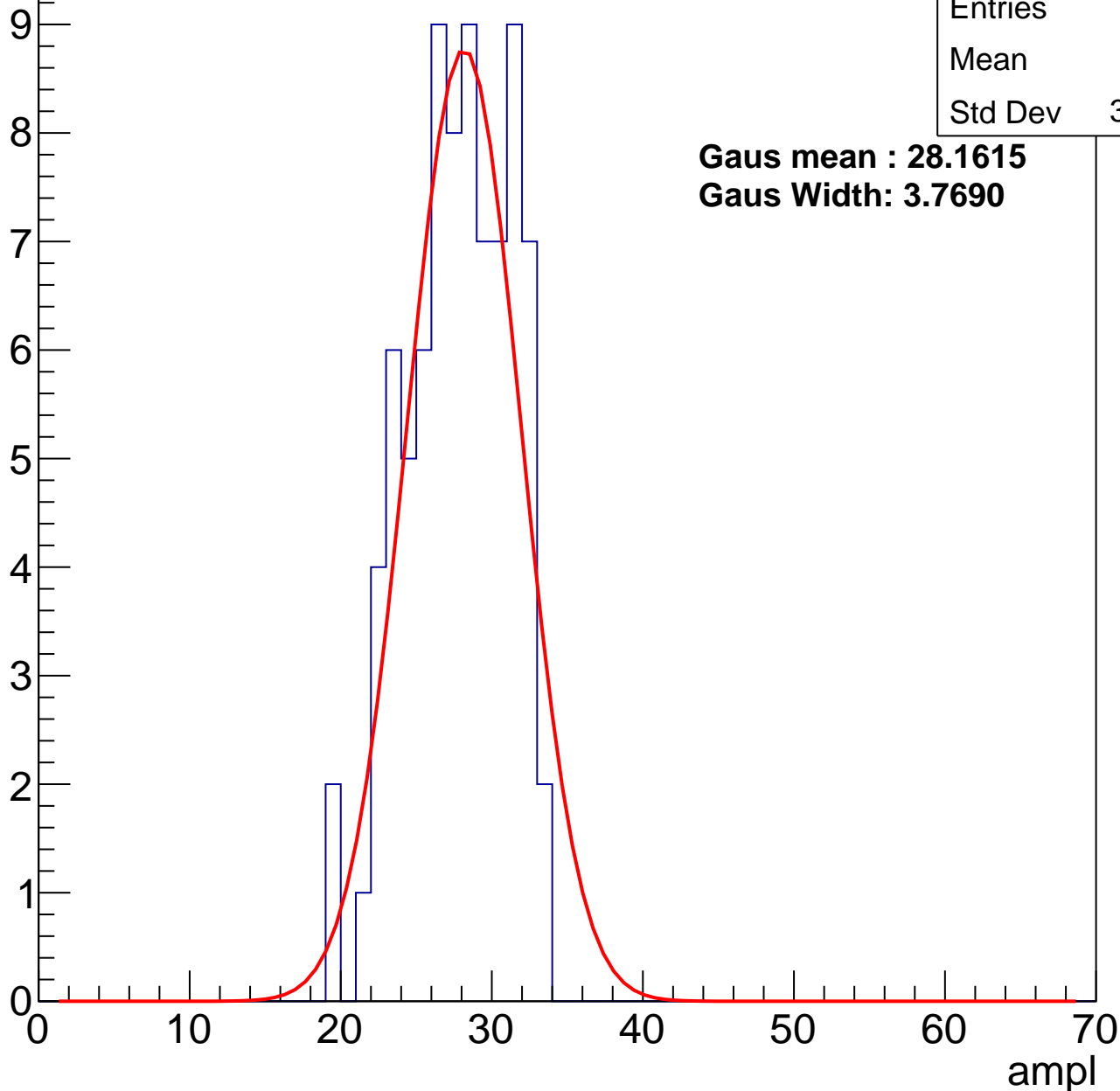
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	27.3
Std Dev	3.352

Gaus mean : 28.1615

Gaus Width: 3.7690

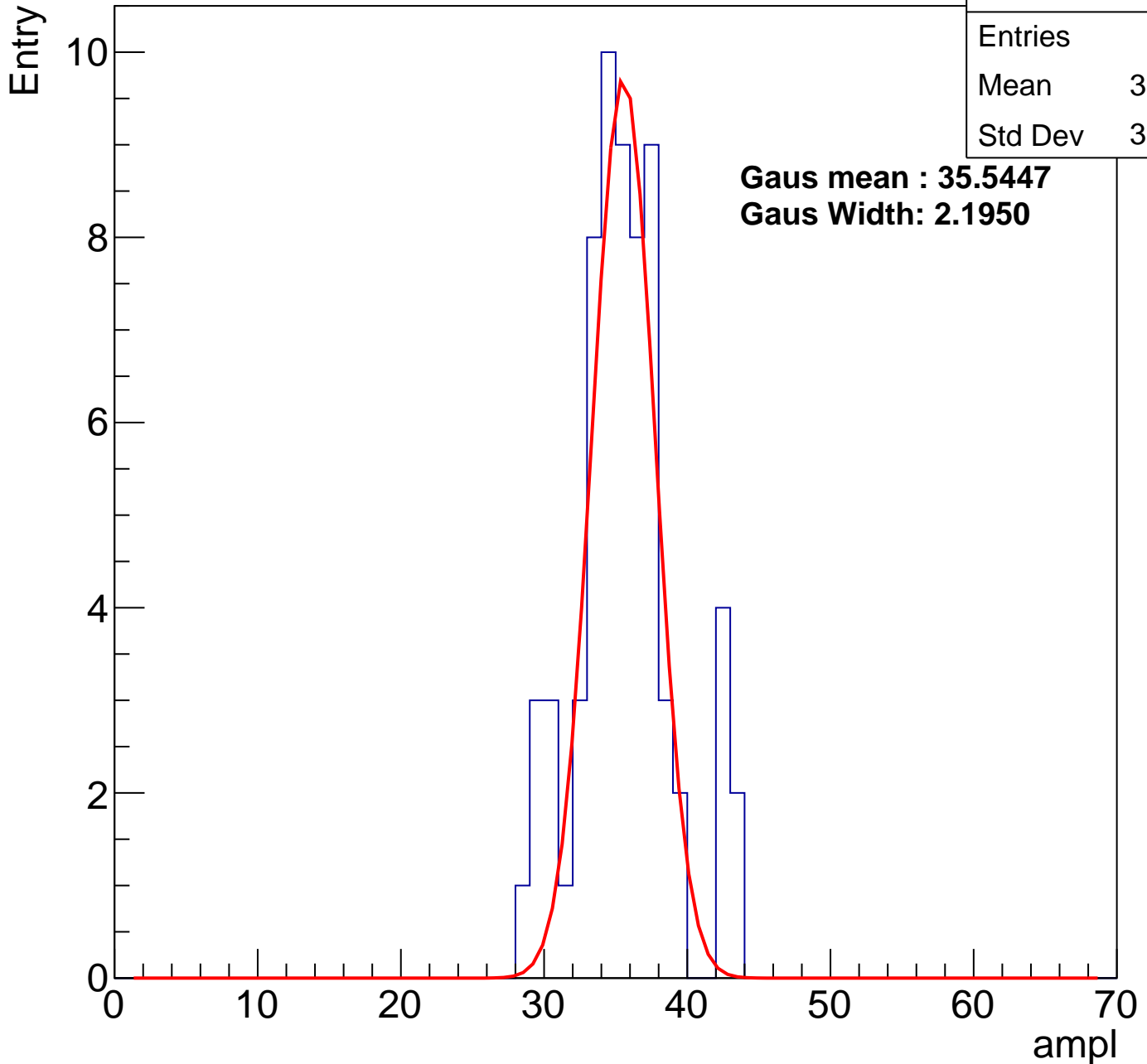


B1L102S, U26-ch10, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	35.12
Std Dev	3.355

Gaus mean : 35.5447
Gaus Width: 2.1950



B1L102S, U26-ch10, adc2

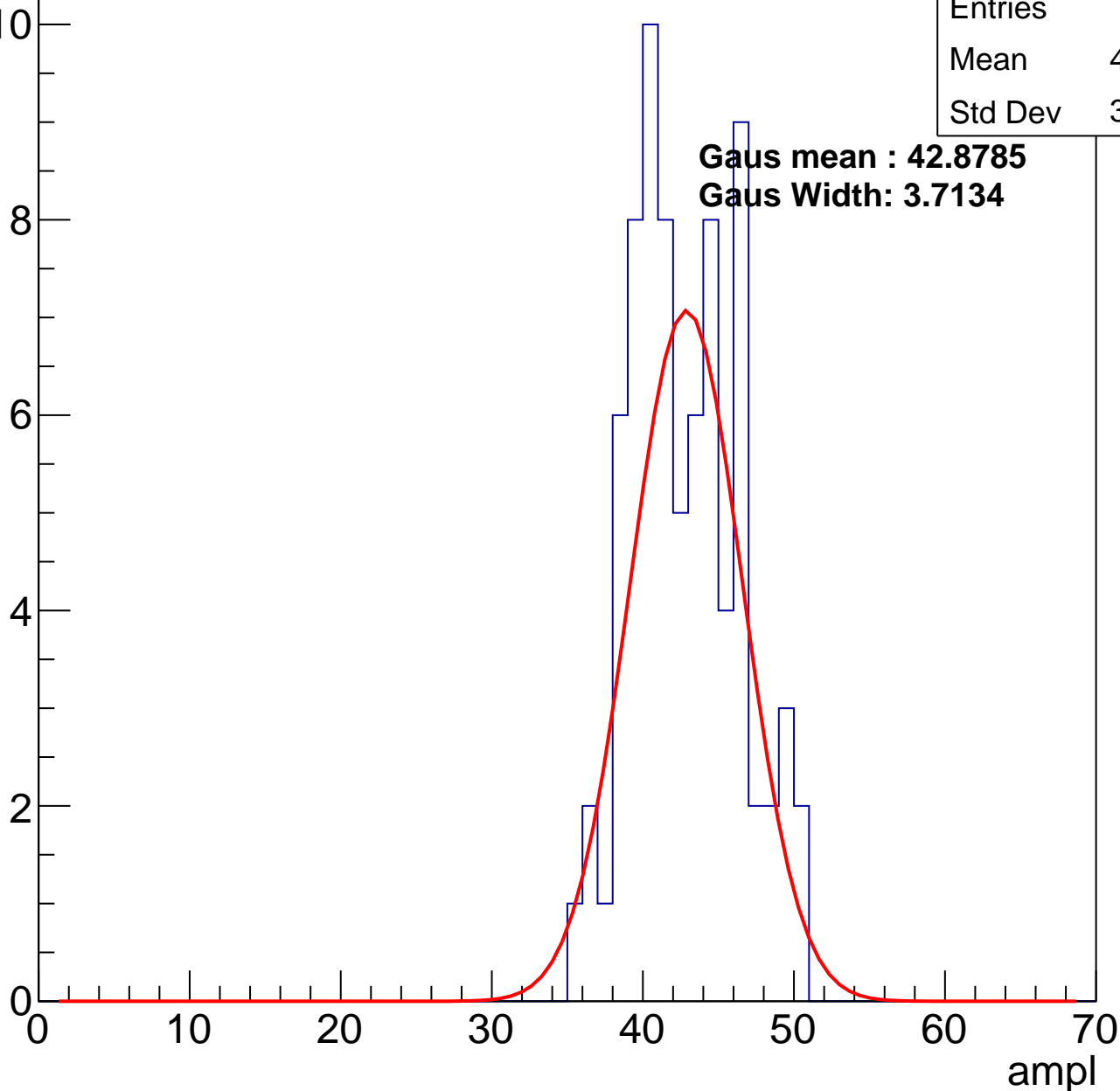
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.38
Std Dev	3.542

Gaus mean : 42.8785

Gaus Width: 3.7134

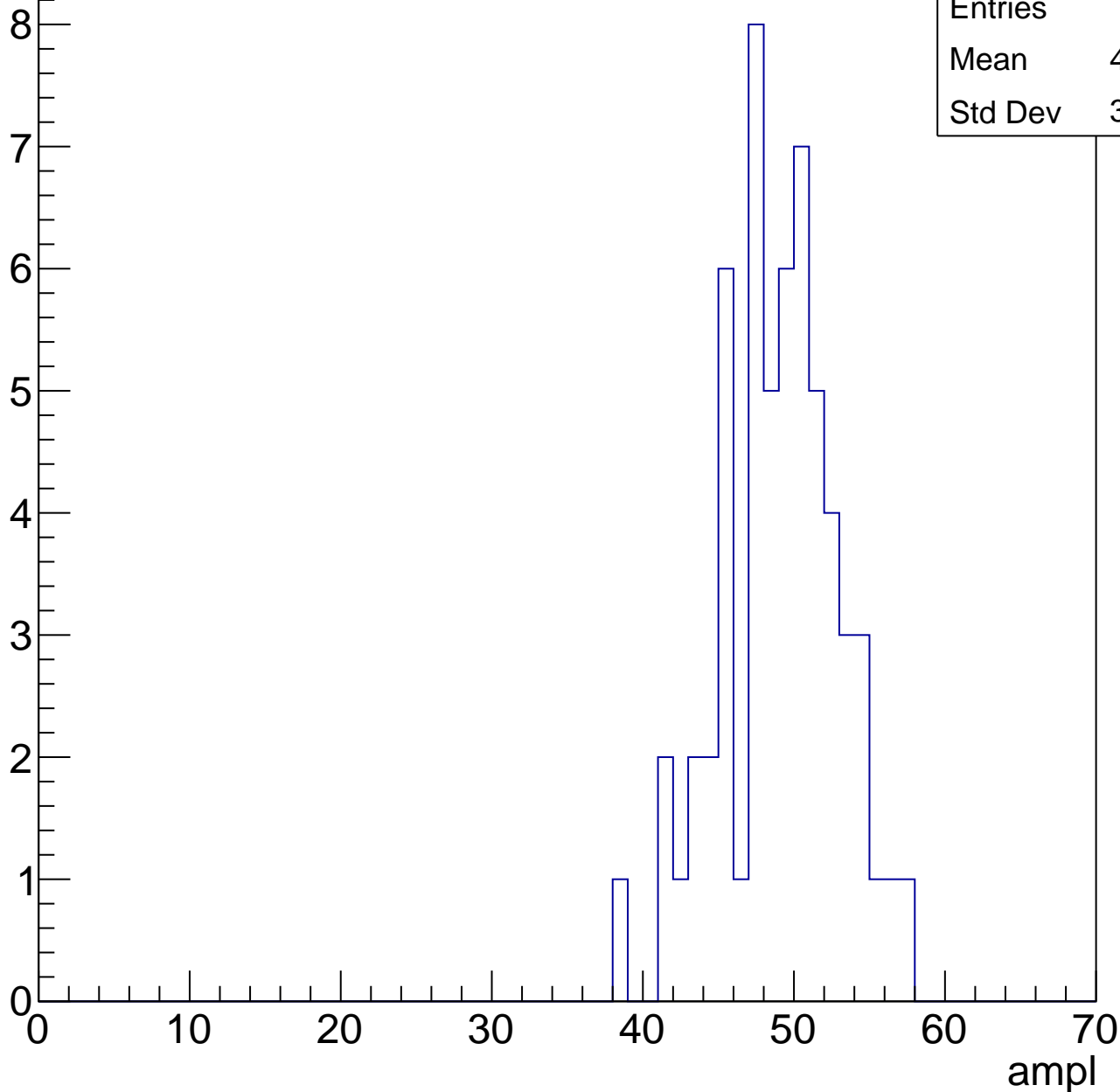


B1L102S, U26-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.54
Std Dev	3.872

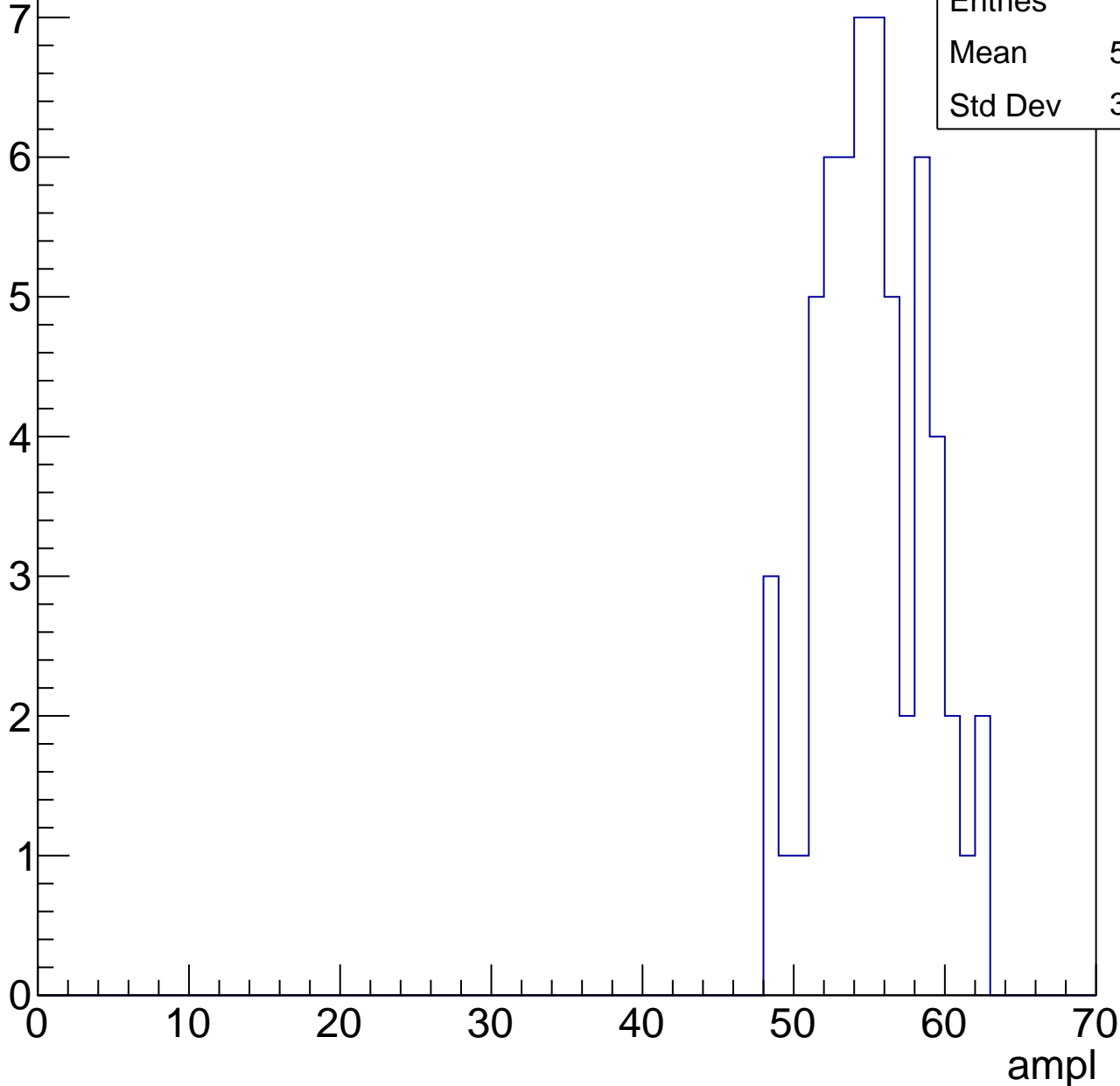


B1L102S, U26-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.72
Std Dev	3.433

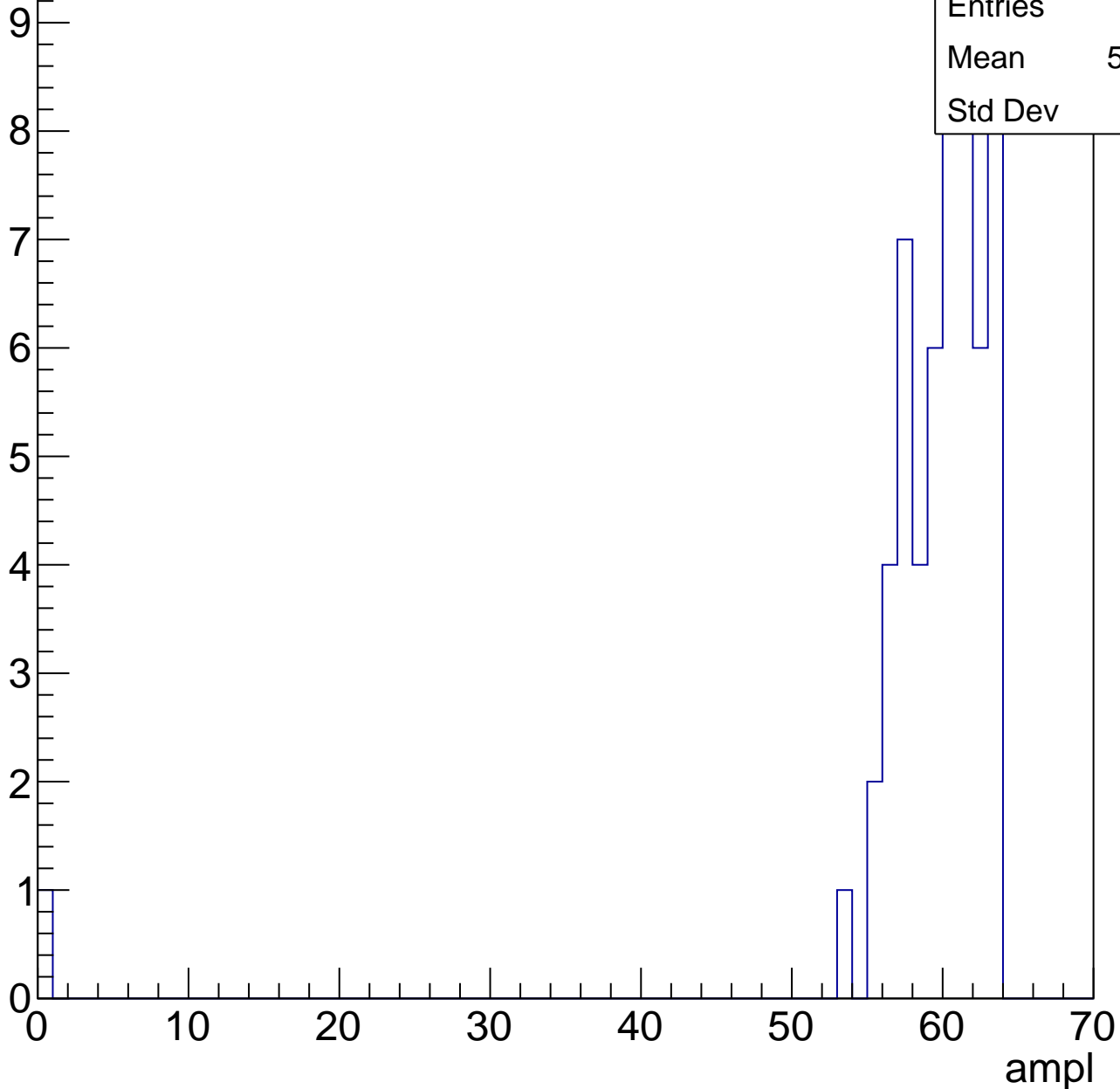


B1L102S, U26-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.52
Std Dev	8.27



B1L102S, U26-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch11, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	28.93
Std Dev	3.983

Gaus mean : 29.4409

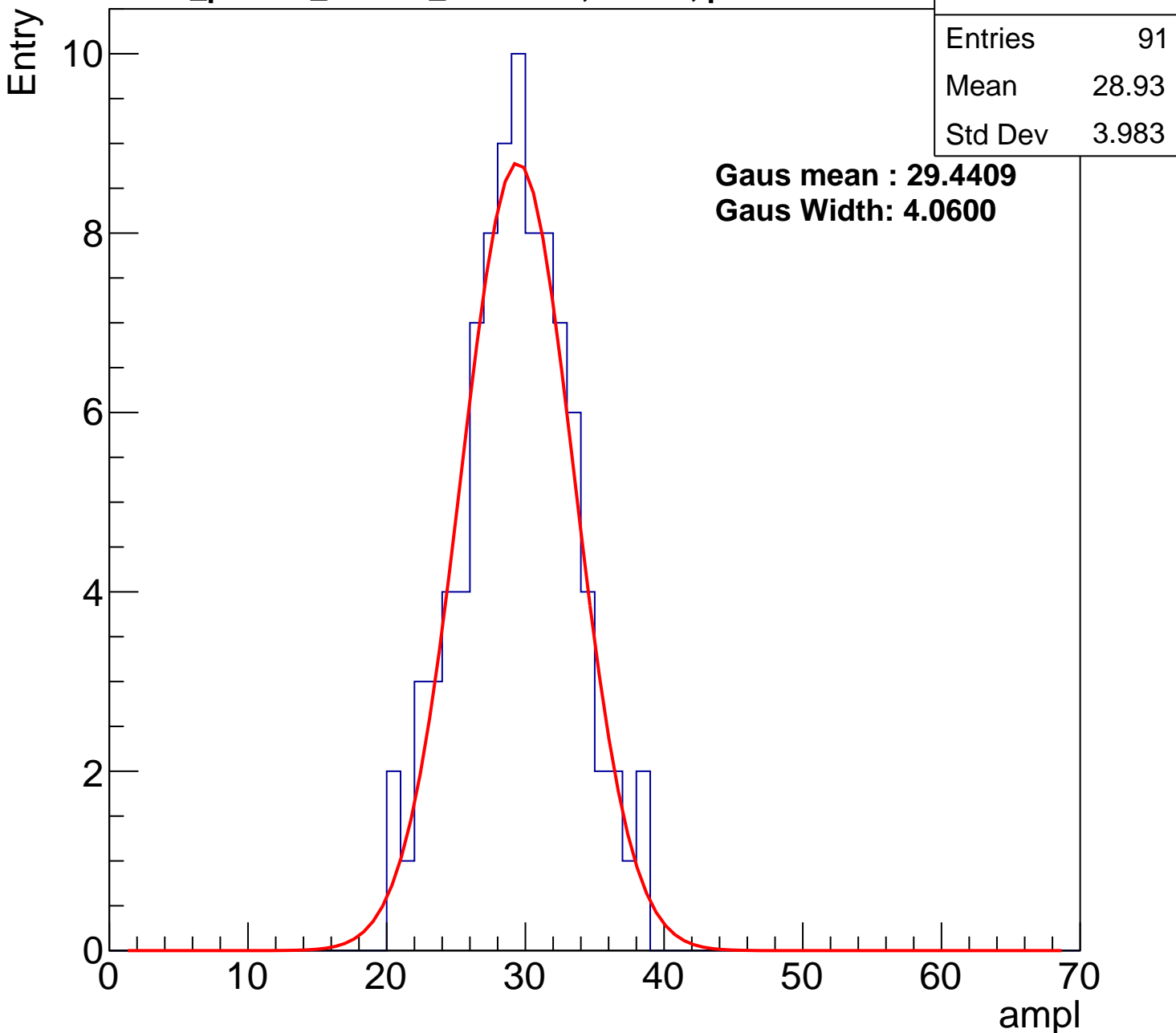
Gaus Width: 4.0600

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch11, adc1

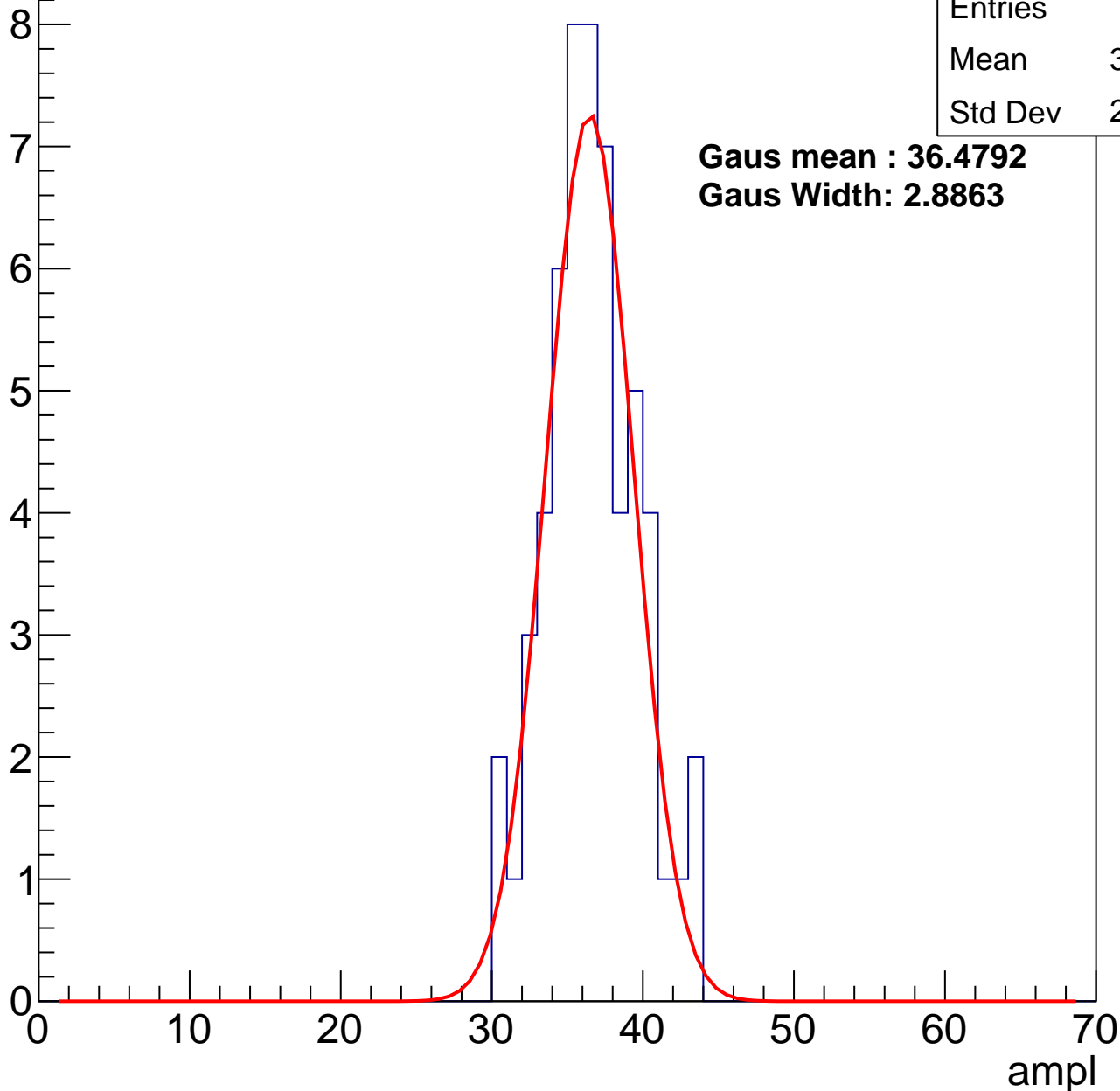
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	36.18
Std Dev	2.995

Gaus mean : 36.4792

Gaus Width: 2.8863



B1L102S, U26-ch11, adc2

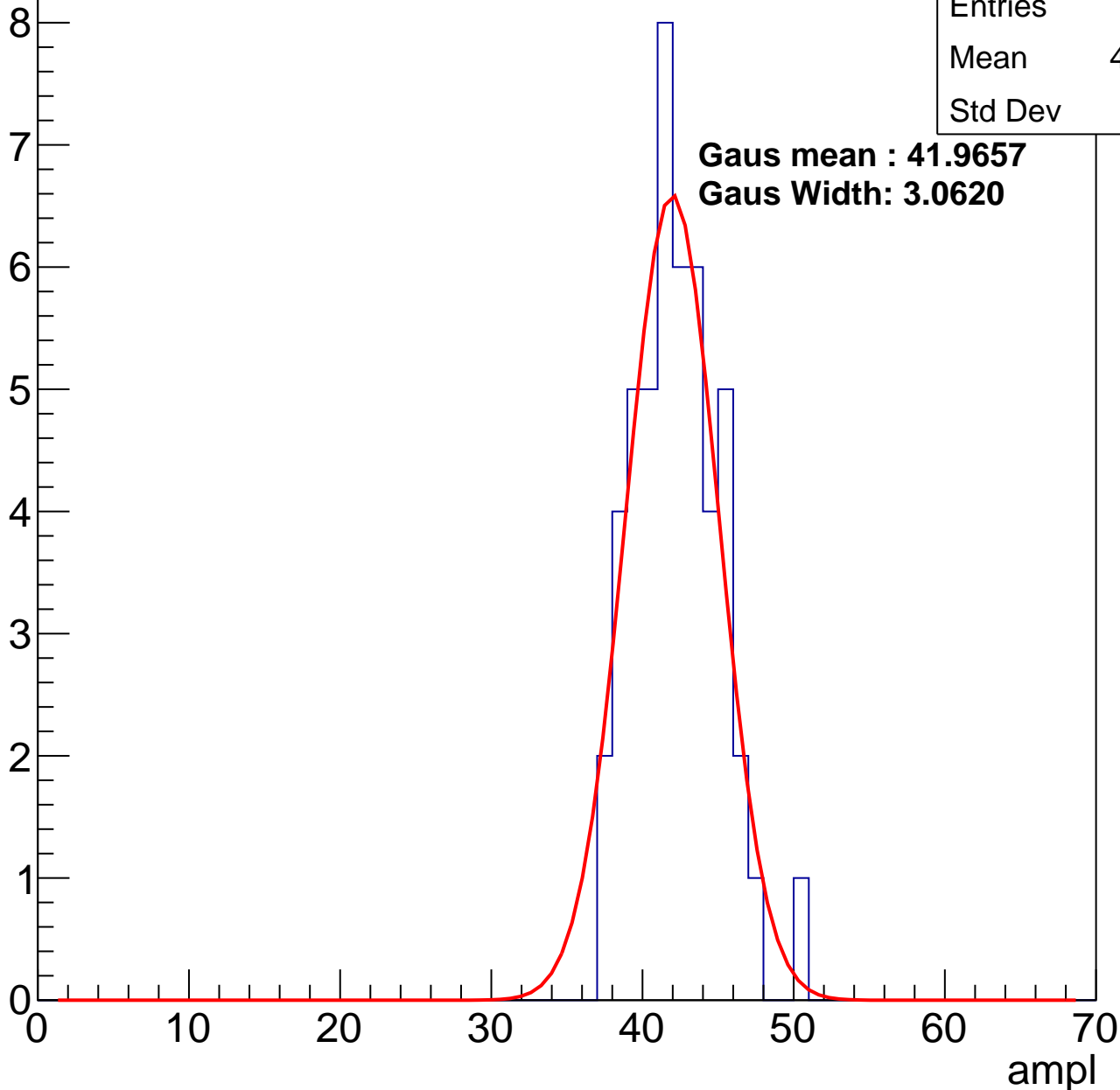
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.82
Std Dev	2.76

Gaus mean : 41.9657

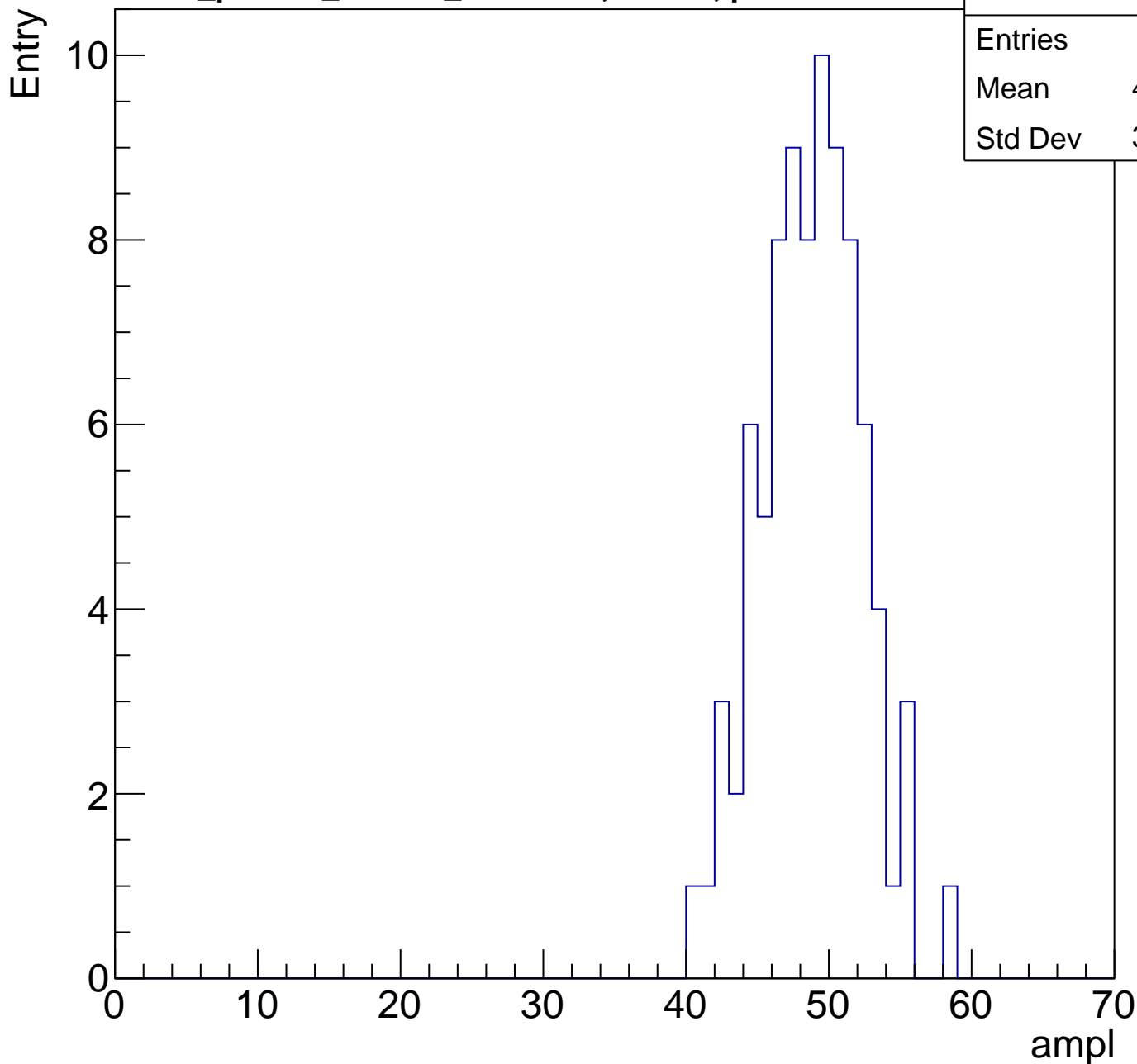
Gaus Width: 3.0620



B1L102S, U26-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	48.31
Std Dev	3.501

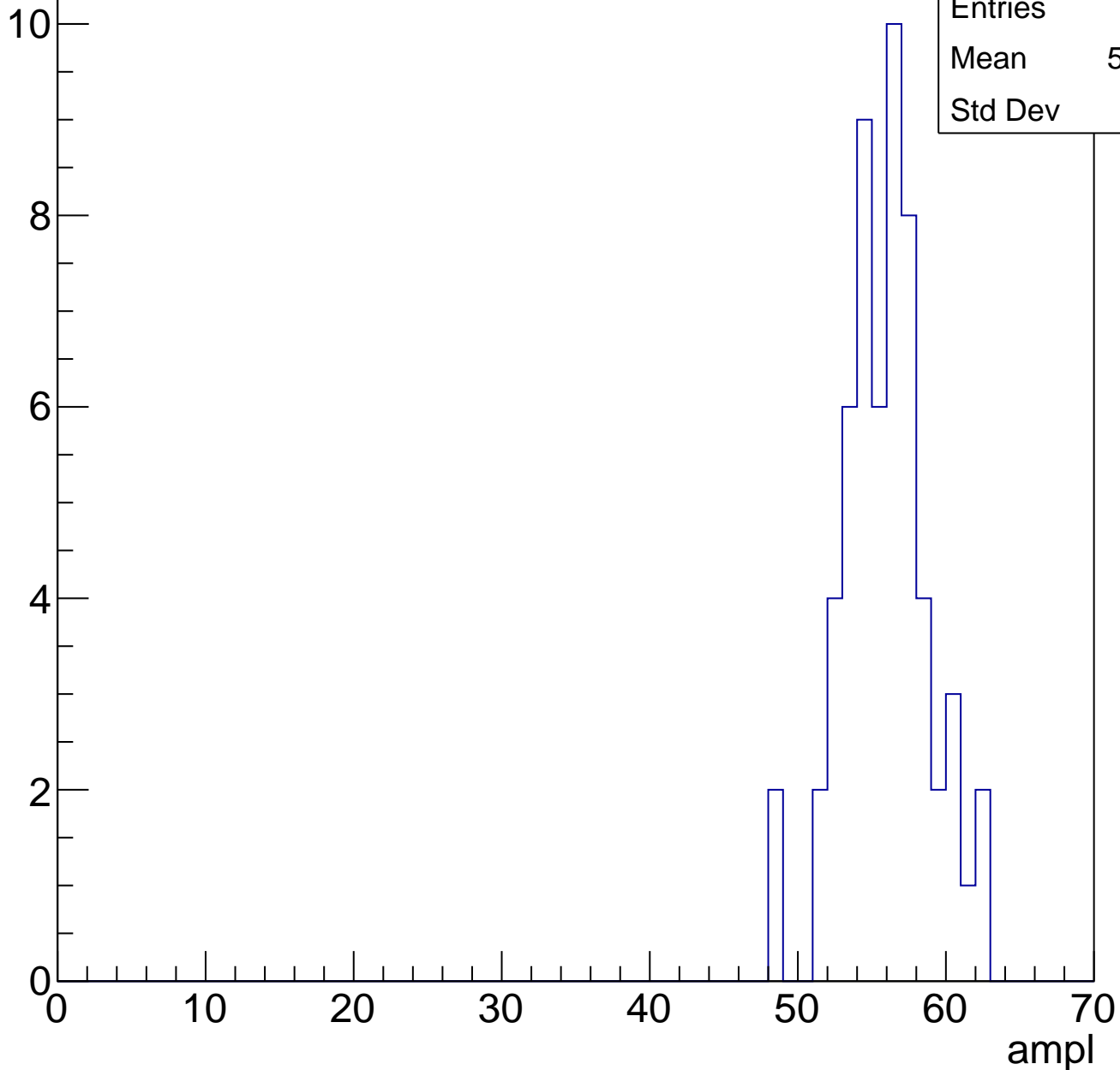


B1L102S, U26-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	55.44
Std Dev	2.93

Entry

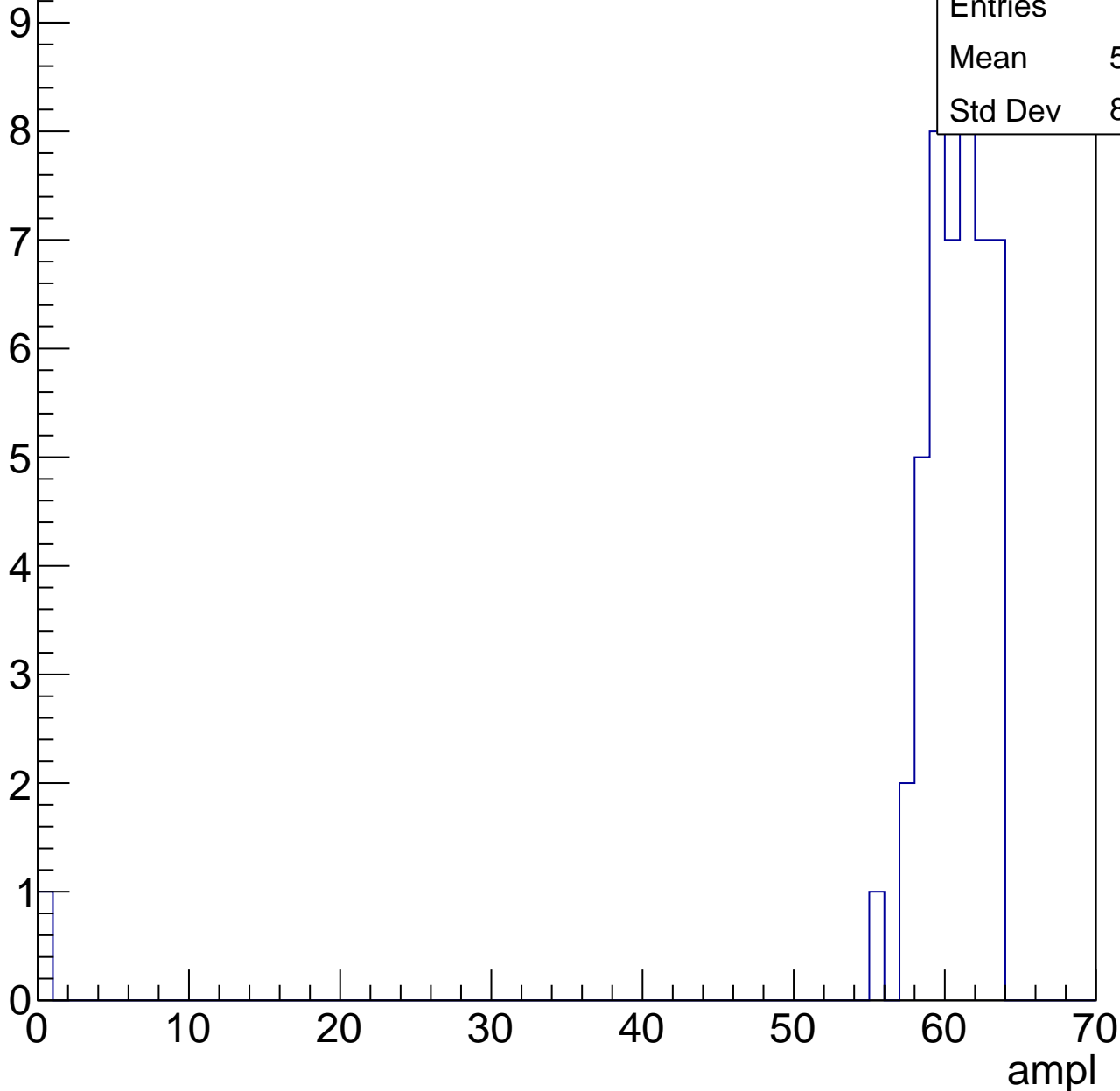


B1L102S, U26-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.04
Std Dev	8.906



B1L102S, U26-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch12, adc0

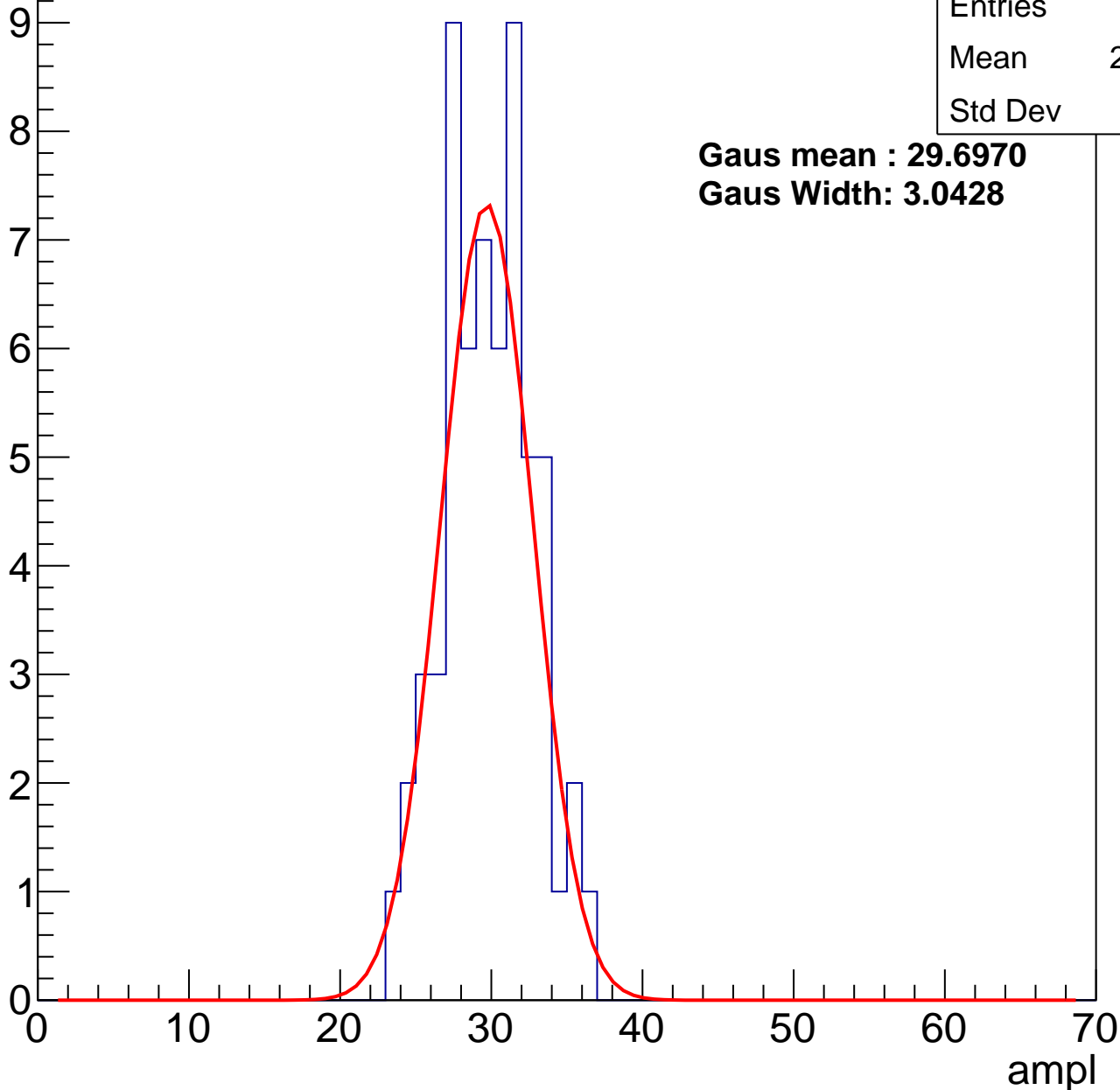
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.37
Std Dev	2.91

Gaus mean : 29.6970

Gaus Width: 3.0428



B1L102S, U26-ch12, adc1

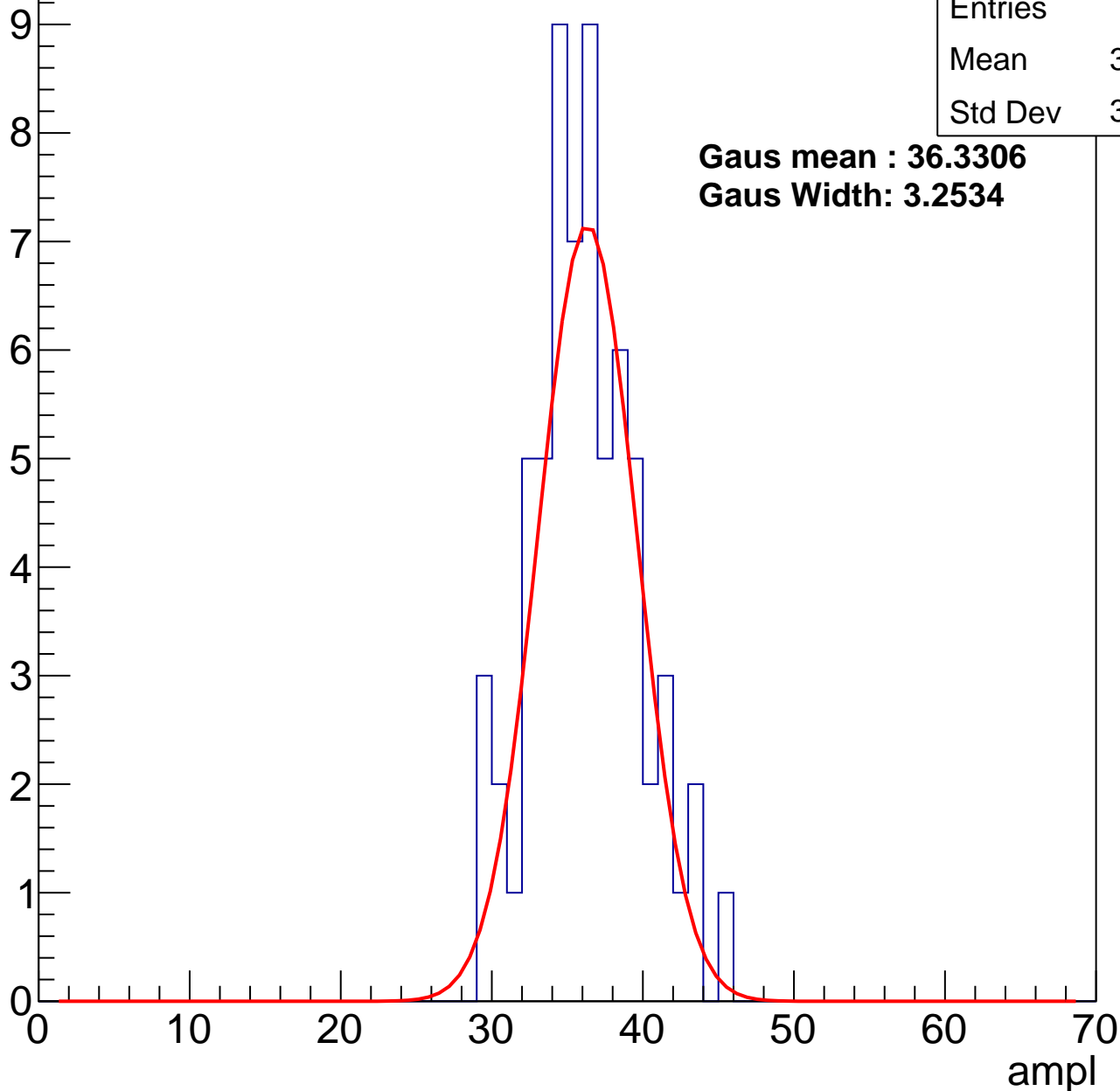
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.79
Std Dev	3.492

Gaus mean : 36.3306

Gaus Width: 3.2534



B1L102S, U26-ch12, adc2

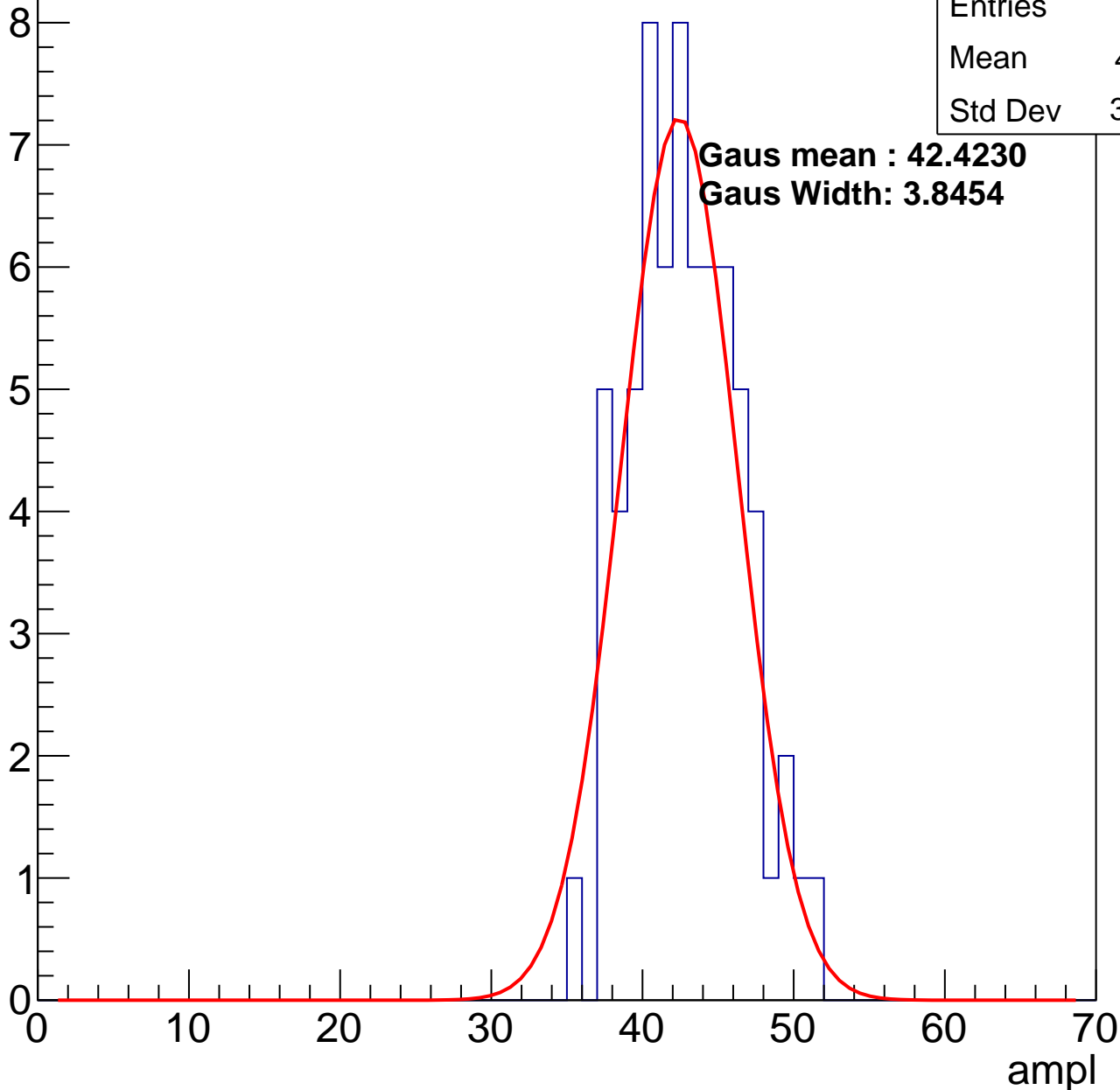
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.41
Std Dev	3.515

Gaus mean : 42.4230

Gaus Width: 3.8454

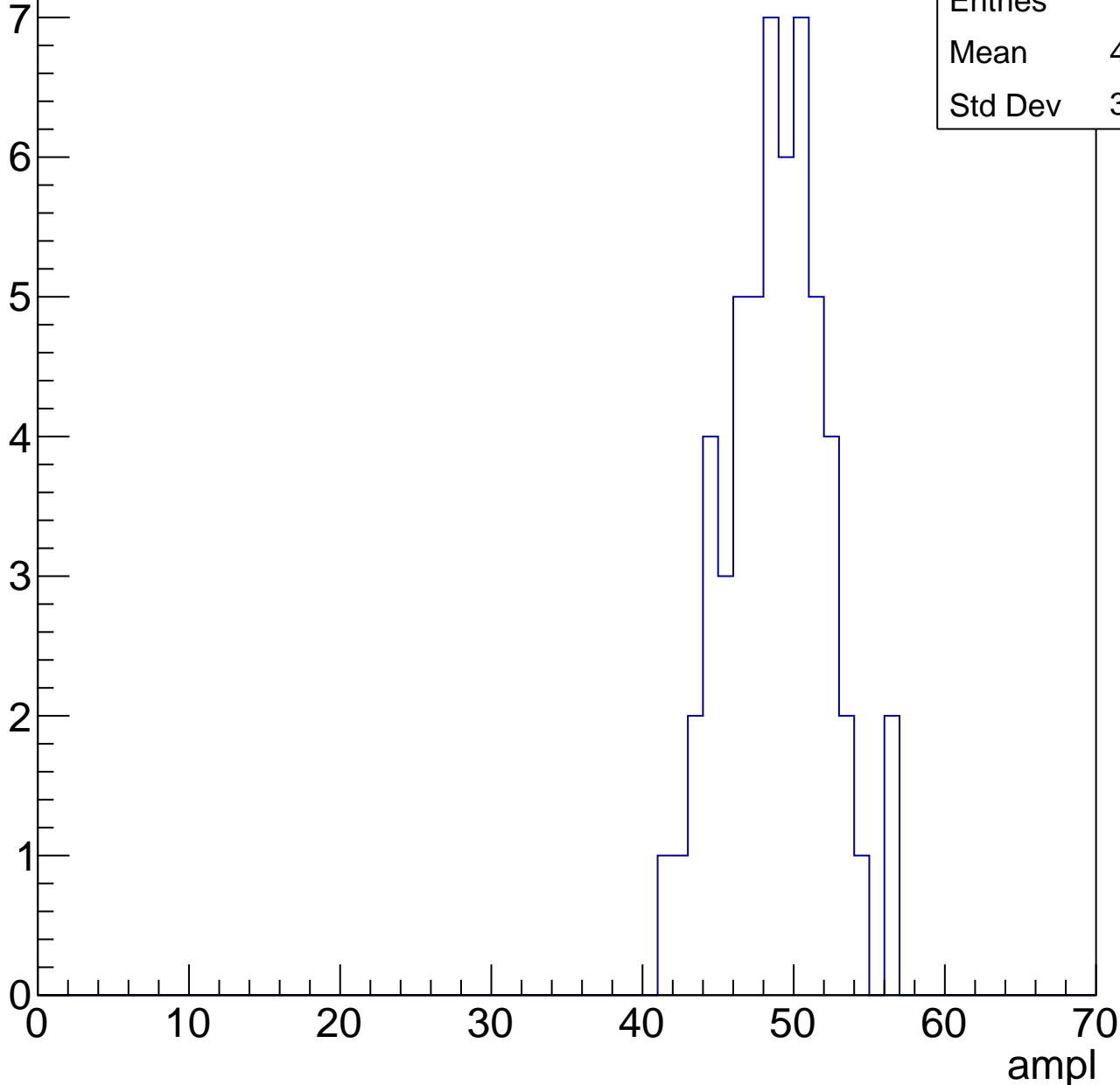


B1L102S, U26-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

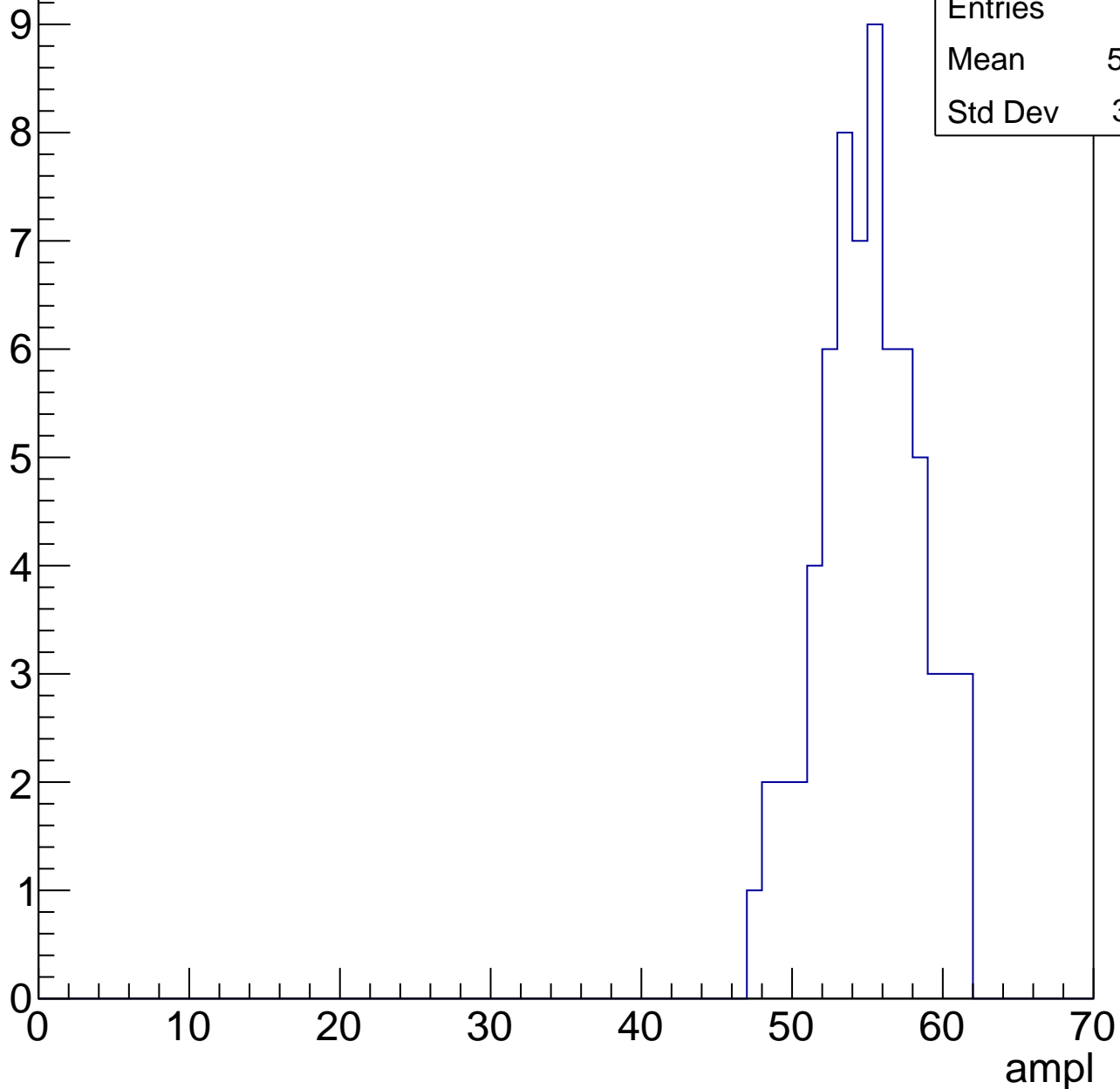
Entries	55
Mean	48.36
Std Dev	3.305



B1L102S, U26-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

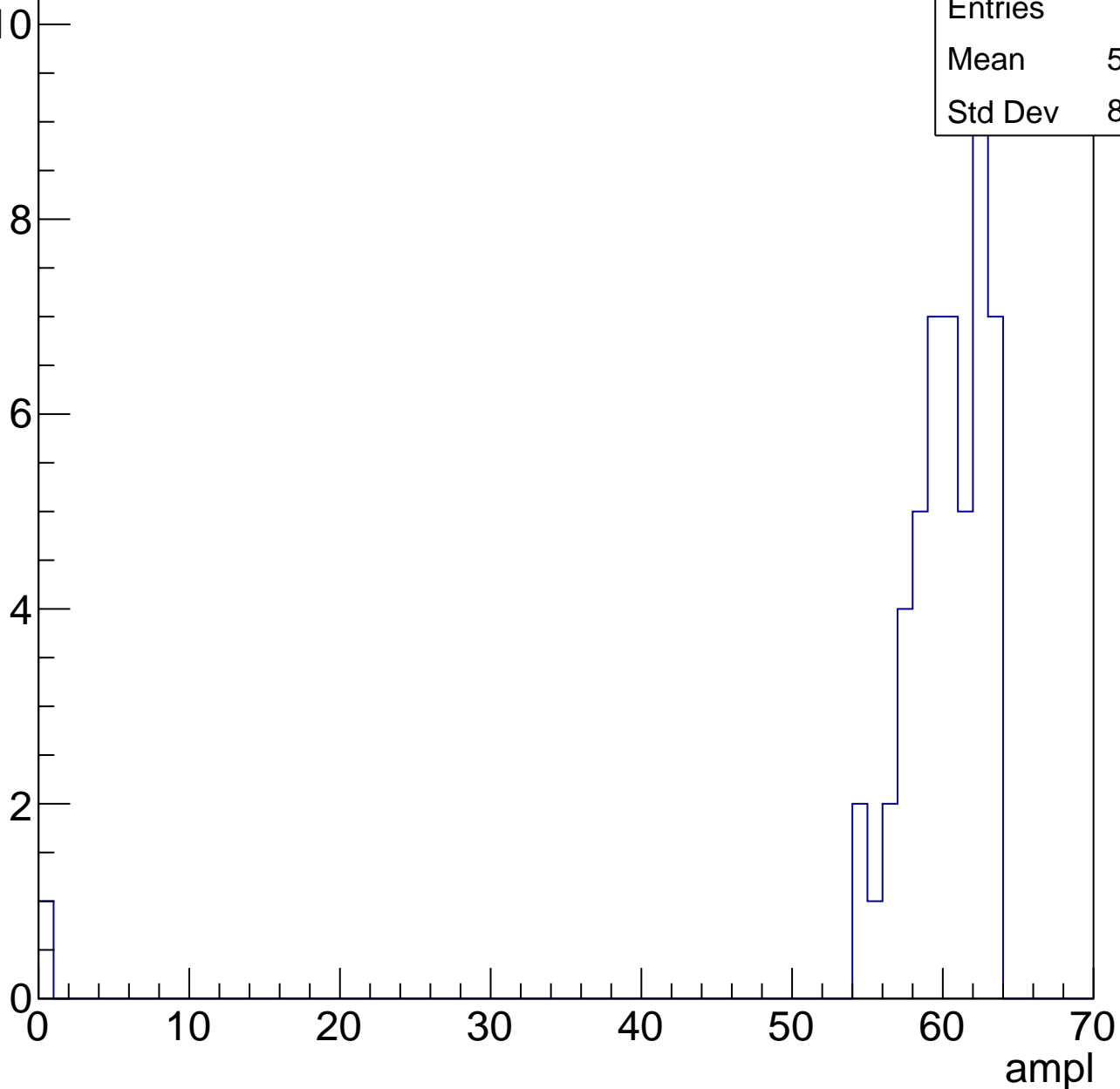


B1L102S, U26-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.67
Std Dev	8.643



B1L102S, U26-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

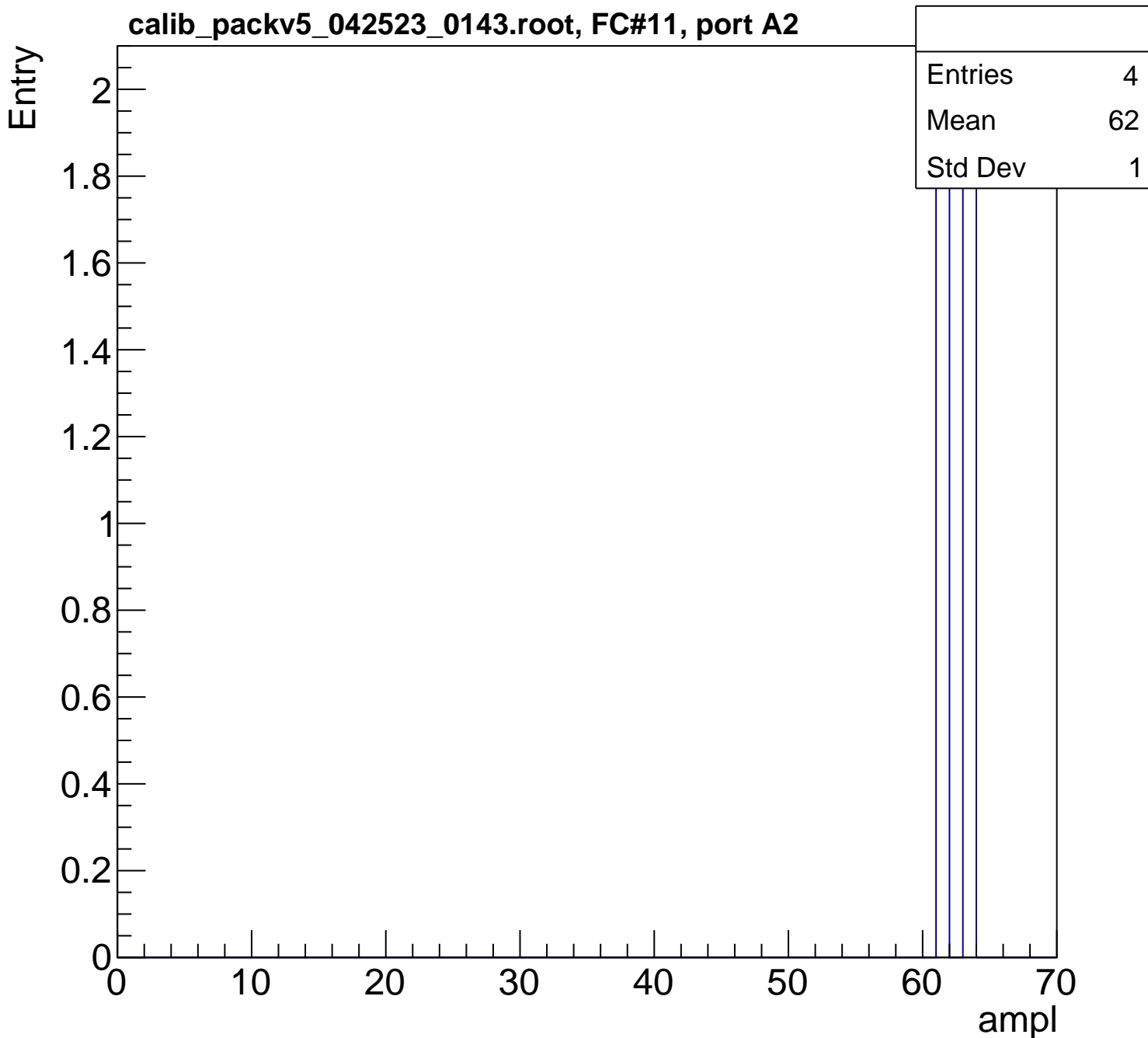
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch13, adc0

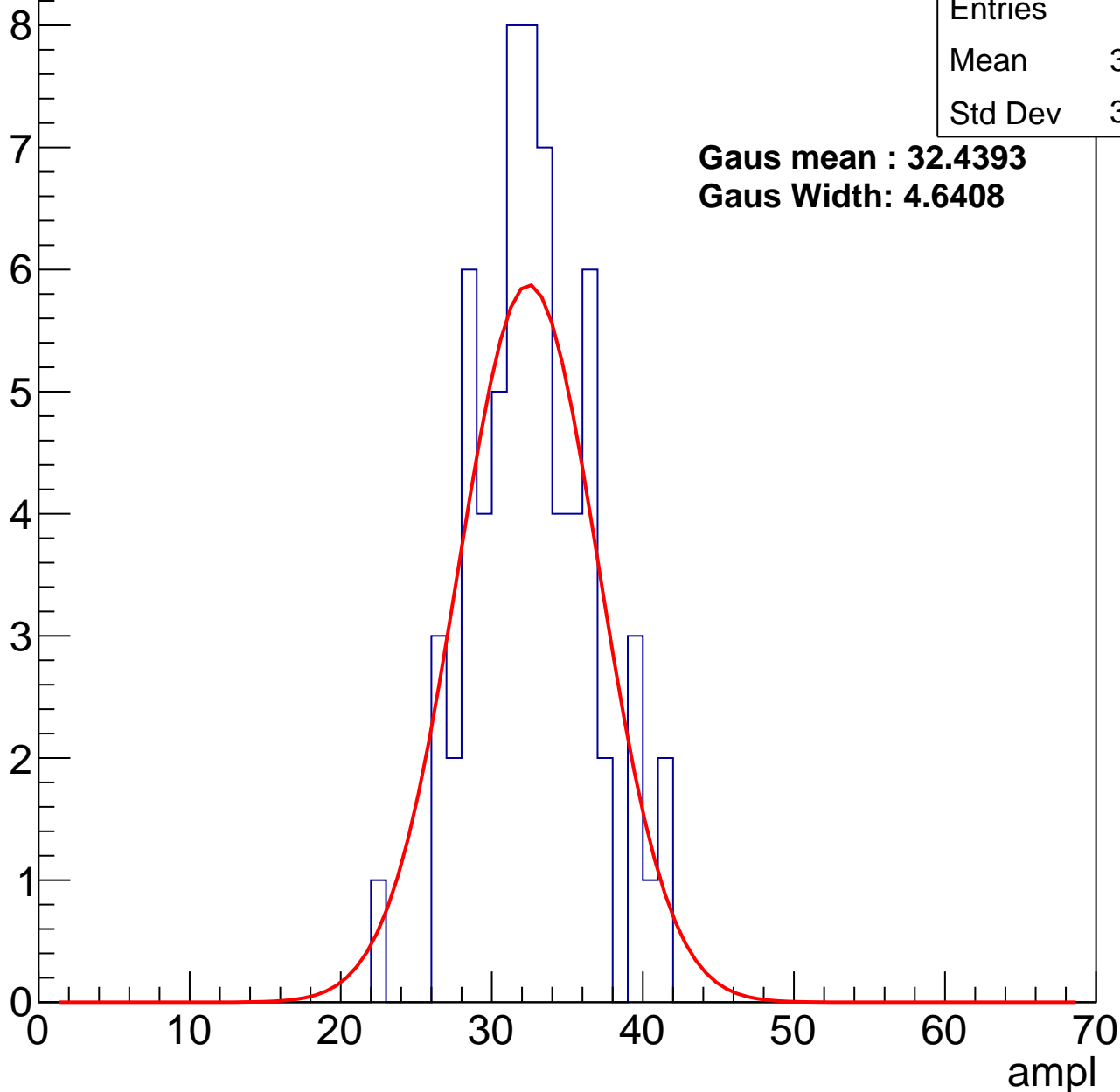
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	32.24
Std Dev	3.877

Gaus mean : 32.4393

Gaus Width: 4.6408



B1L102S, U26-ch13, adc1

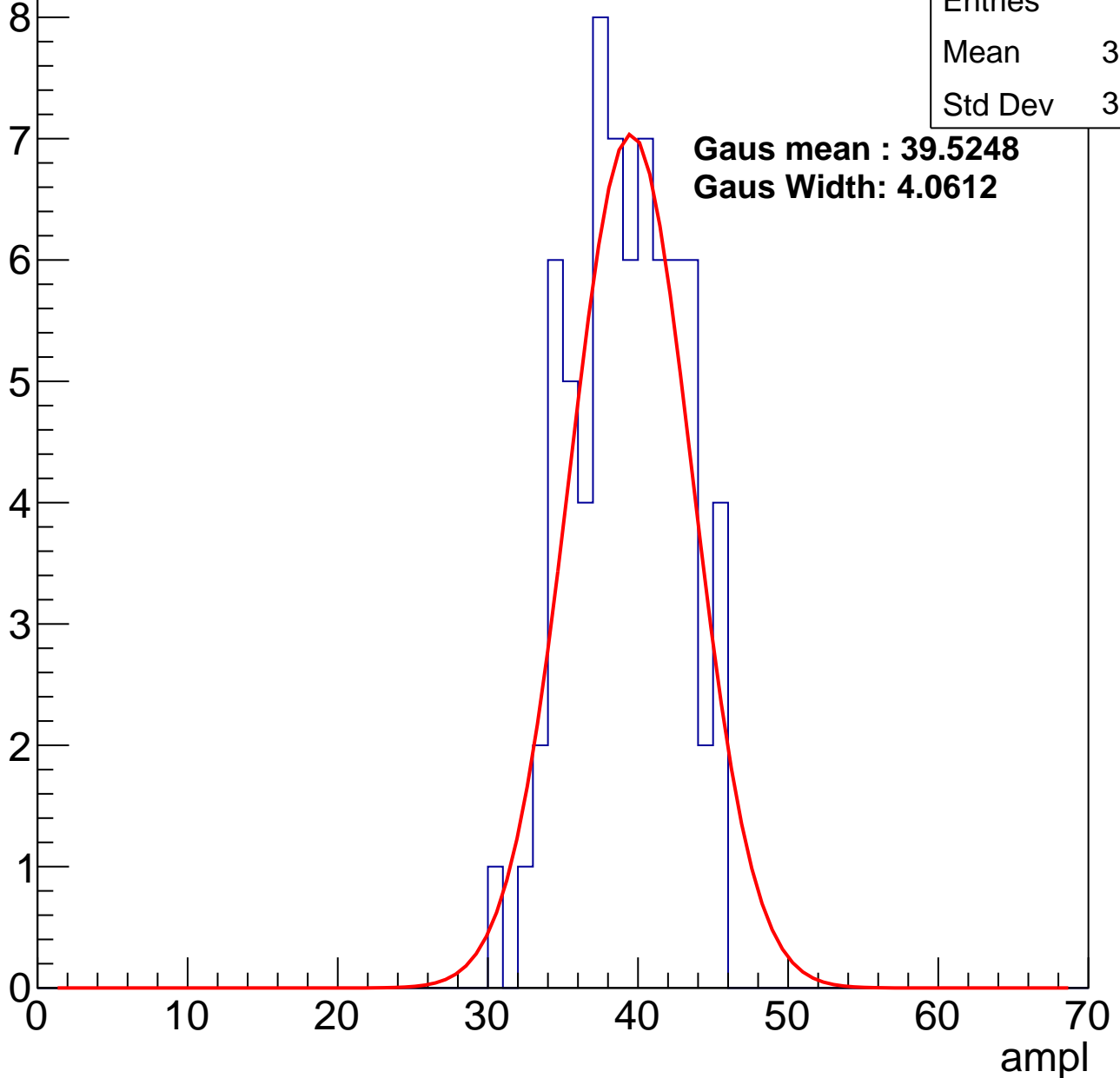
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	38.75
Std Dev	3.515

Gaus mean : 39.5248

Gaus Width: 4.0612



B1L102S, U26-ch13, adc2

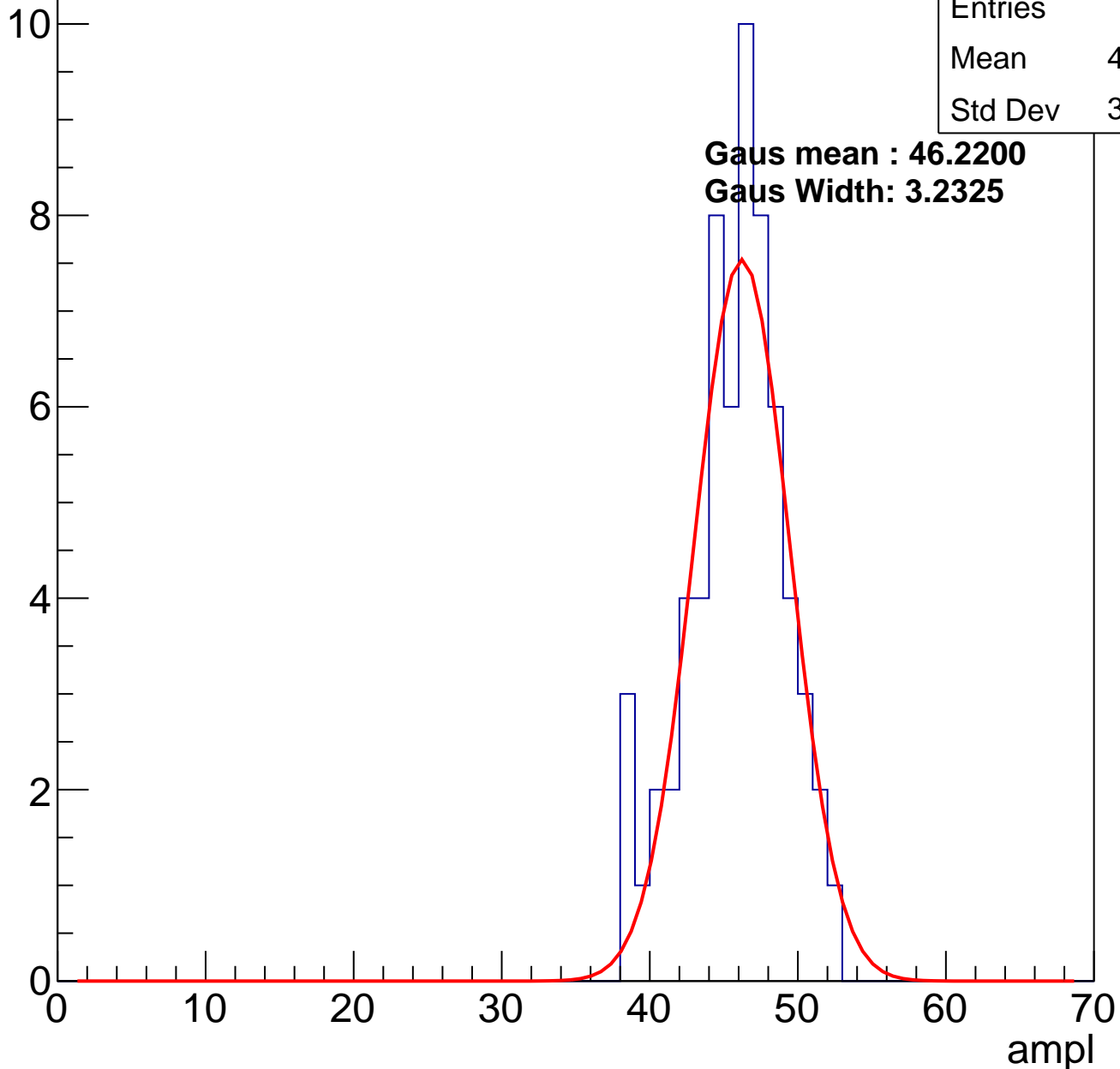
calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	45.33
Std Dev	3.245

Gaus mean : 46.2200

Gaus Width: 3.2325

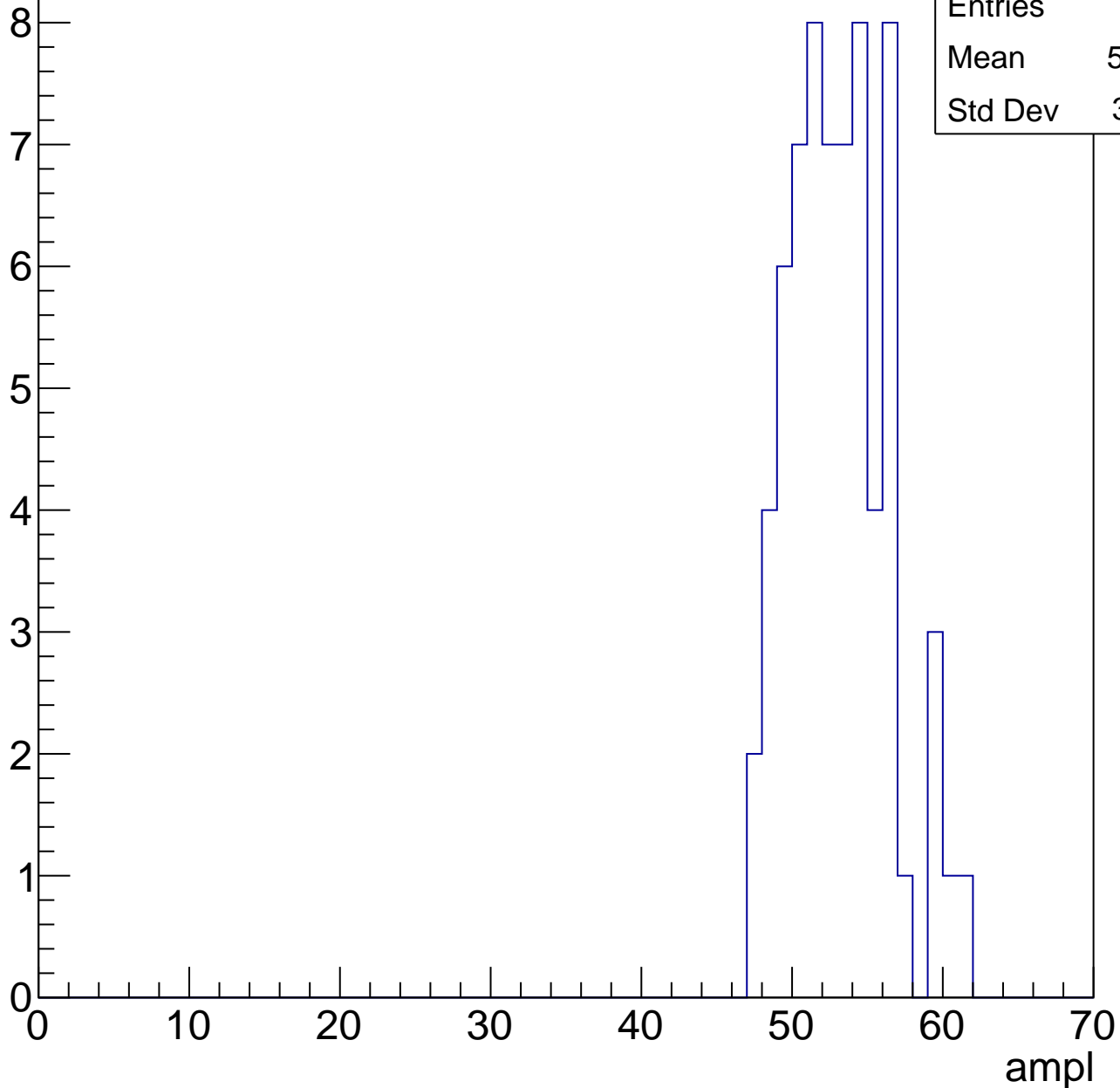
Entry



B1L102S, U26-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

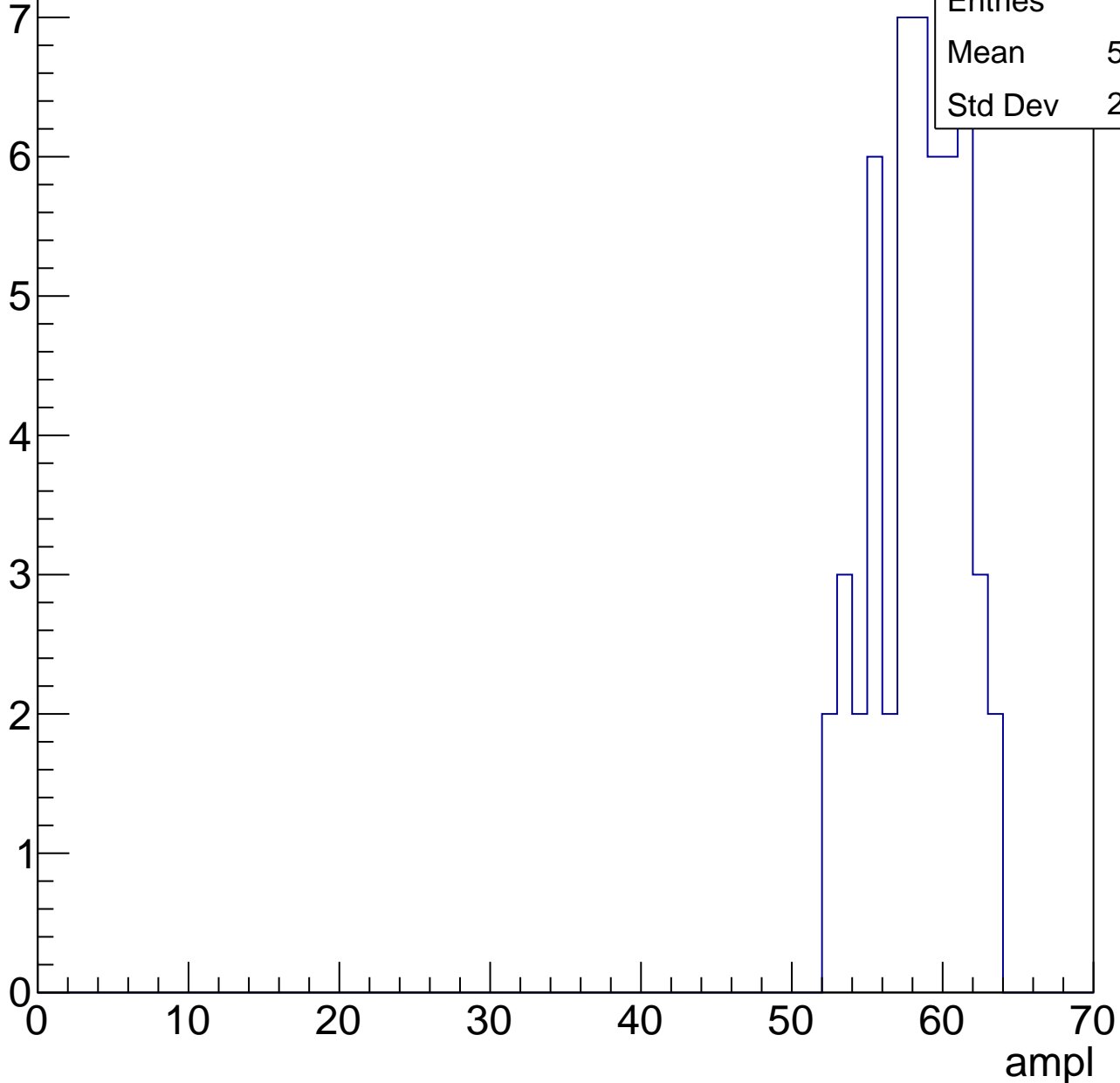


B1L102S, U26-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.94
Std Dev	2.877

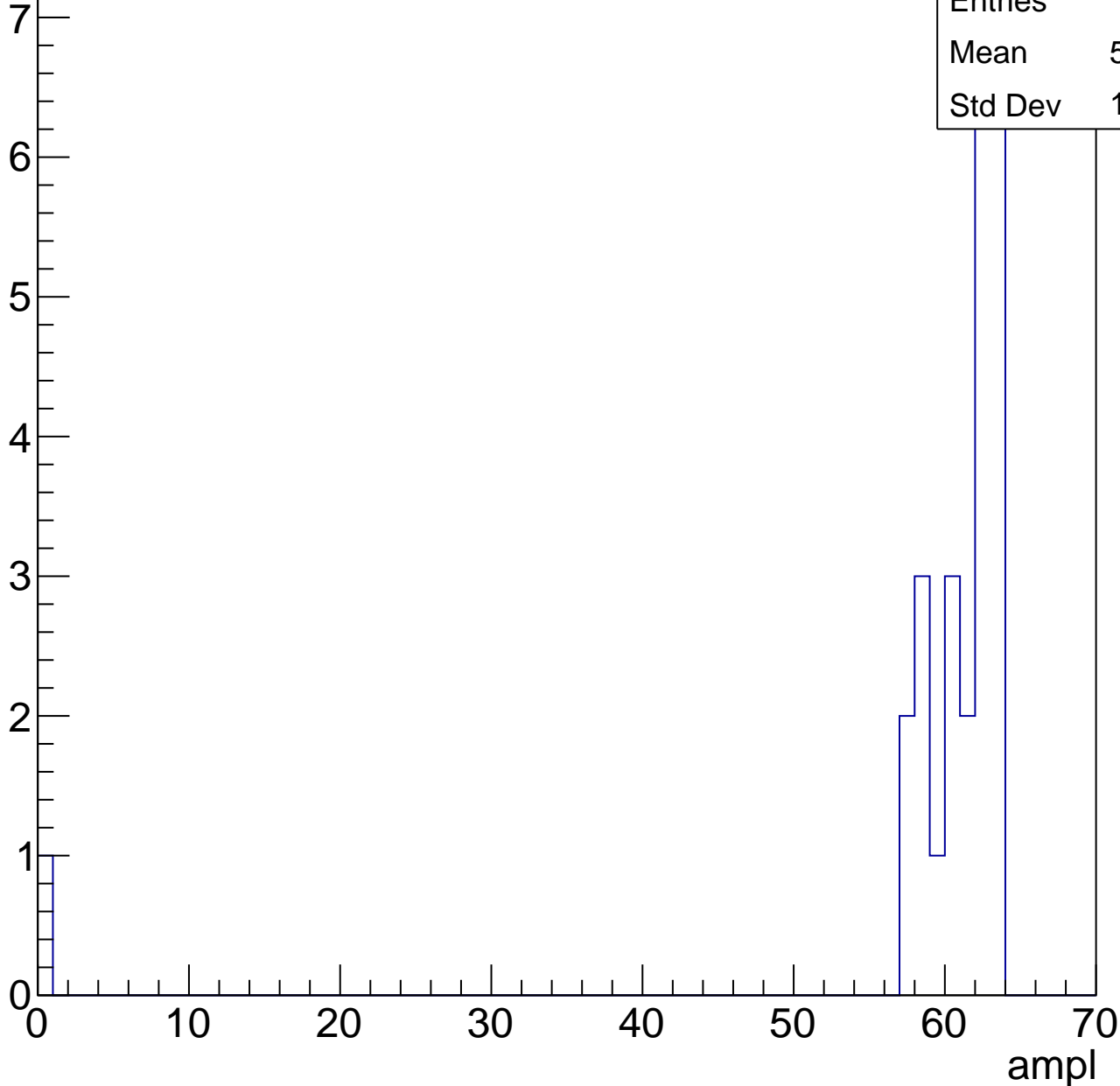


B1L102S, U26-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	58.62
Std Dev	11.89



B1L102S, U26-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch14, adc0

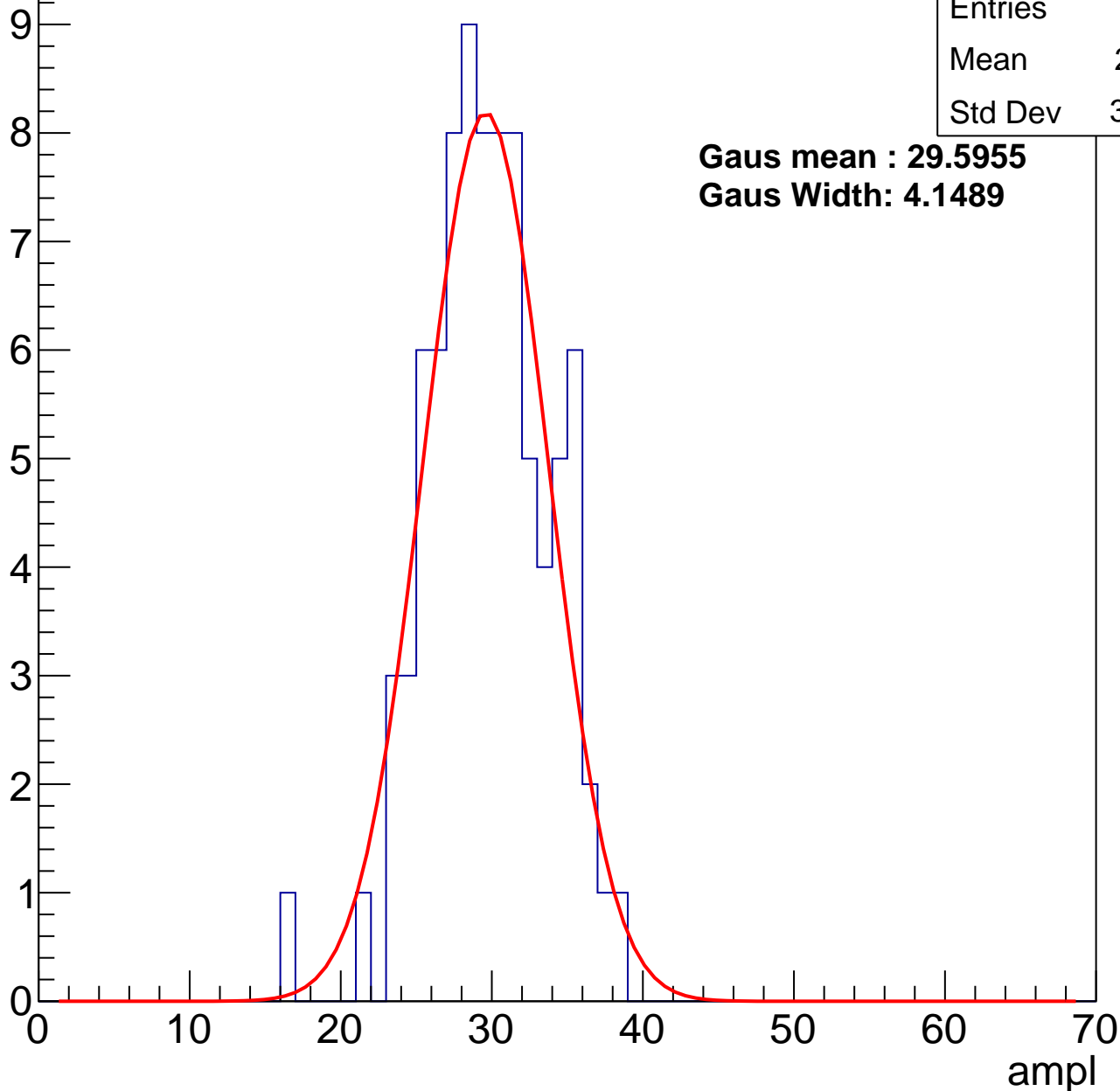
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.31
Std Dev	3.959

Gaus mean : 29.5955

Gaus Width: 4.1489



B1L102S, U26-ch14, adc1

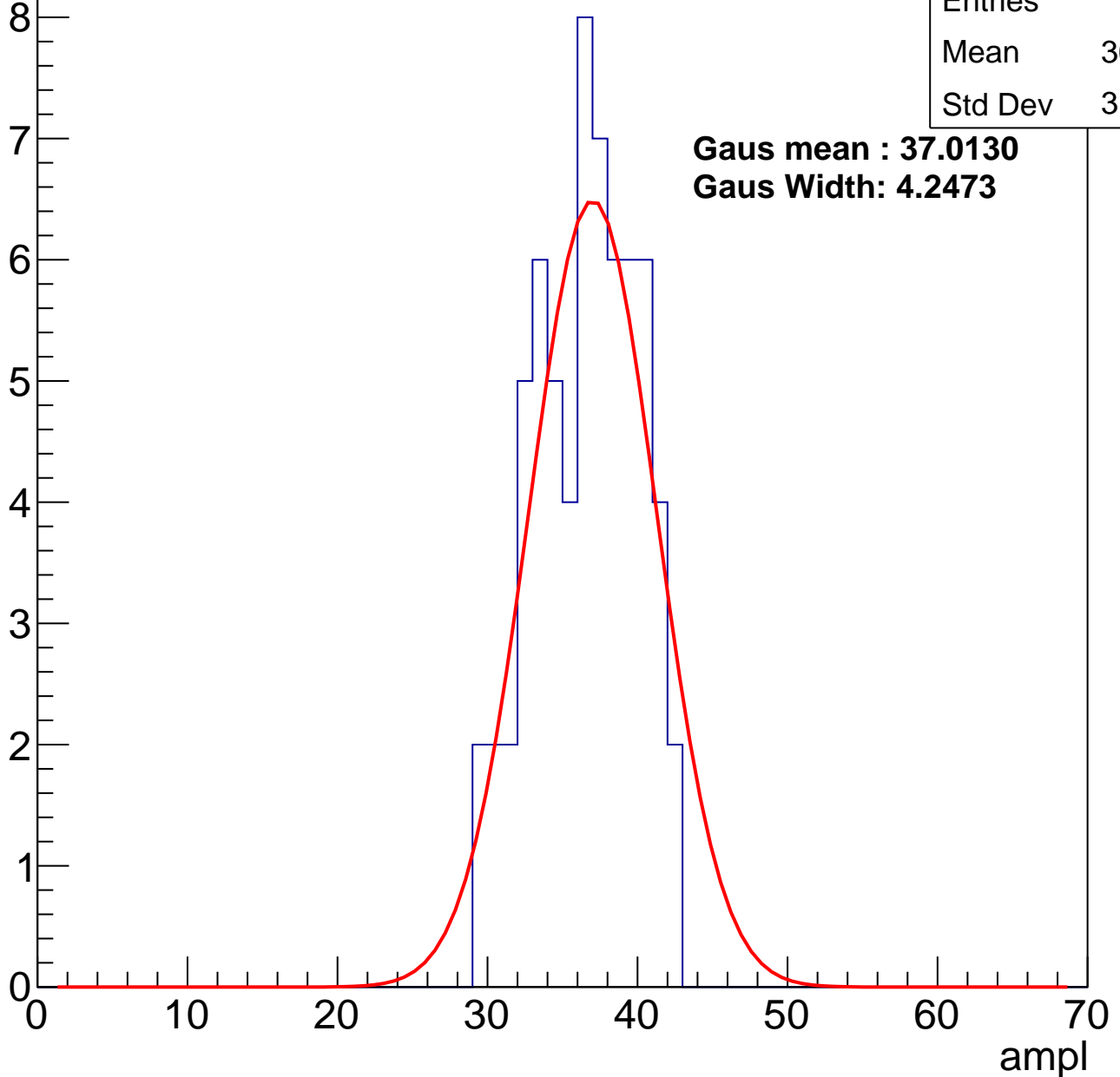
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.08
Std Dev	3.353

Gaus mean : 37.0130

Gaus Width: 4.2473



B1L102S, U26-ch14, adc2

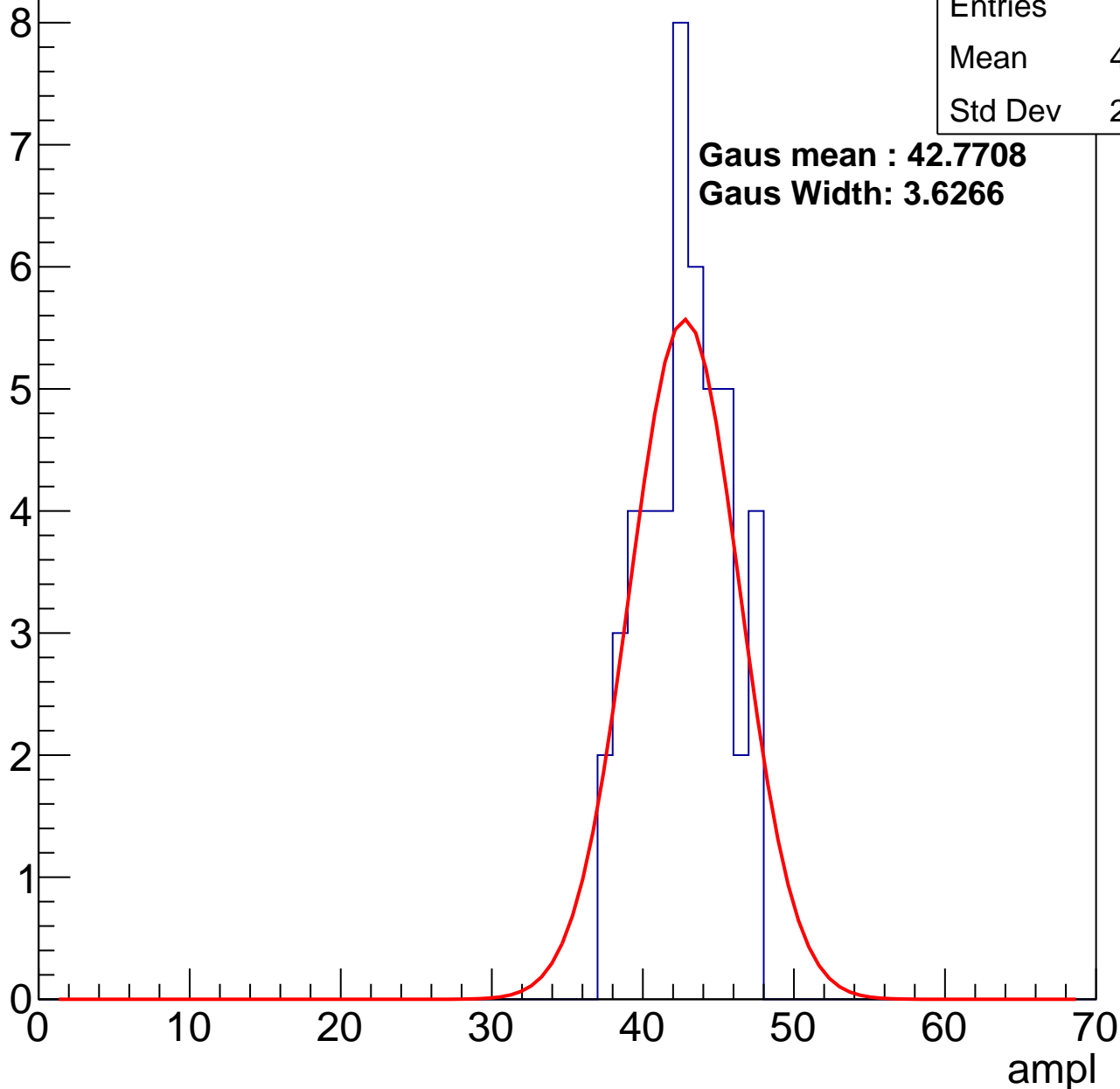
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	42.28
Std Dev	2.742

Gaus mean : 42.7708

Gaus Width: 3.6266

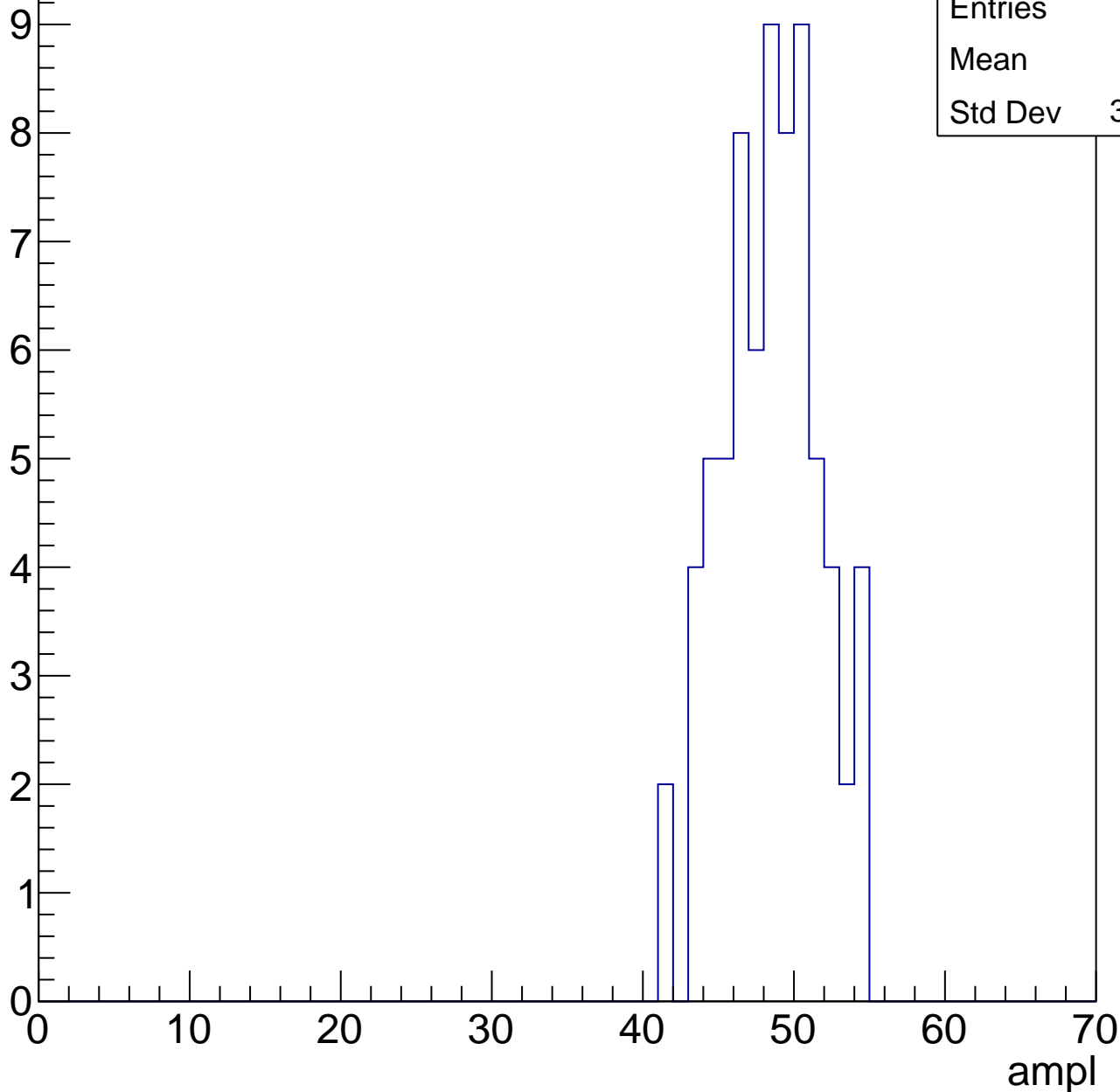


B1L102S, U26-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48
Std Dev	3.158



B1L102S, U26-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

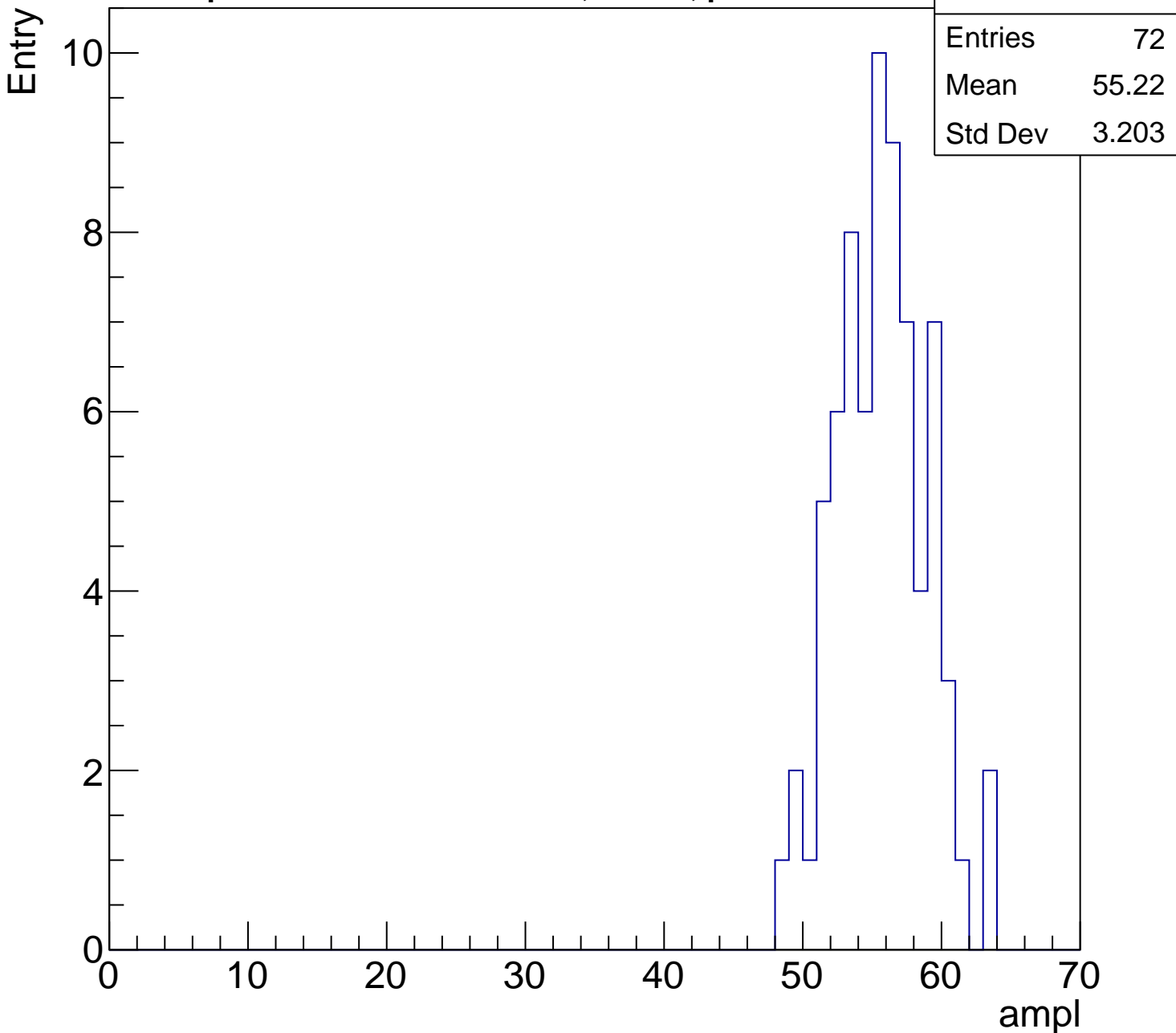
Entries	72
Mean	55.22
Std Dev	3.203

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

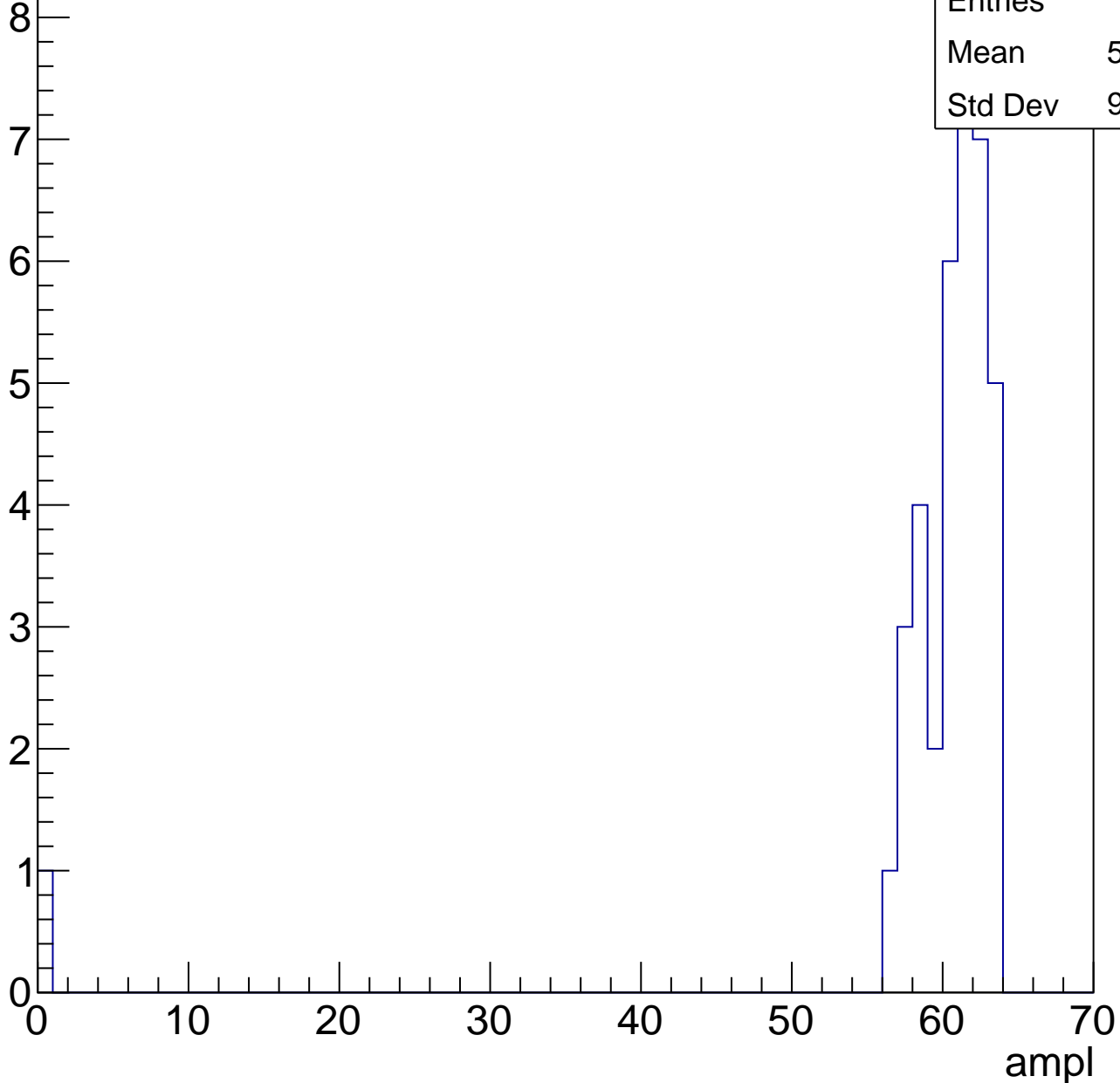


B1L102S, U26-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.76
Std Dev	9.979



B1L102S, U26-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61
Std Dev	1.859

B1L102S, U26-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch15, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	32.32
Std Dev	3.407

Gaus mean : 32.9610

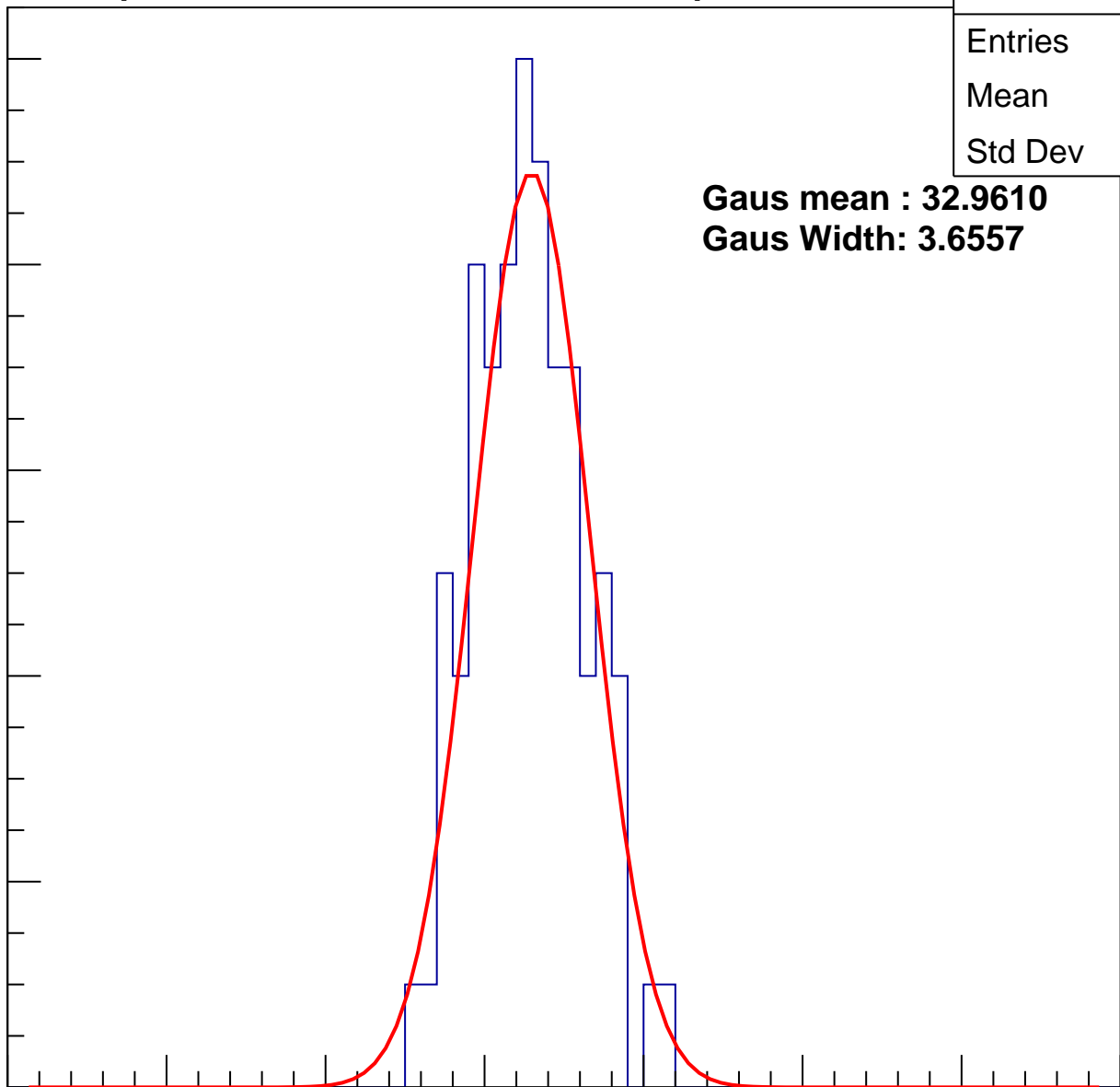
Gaus Width: 3.6557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch15, adc1

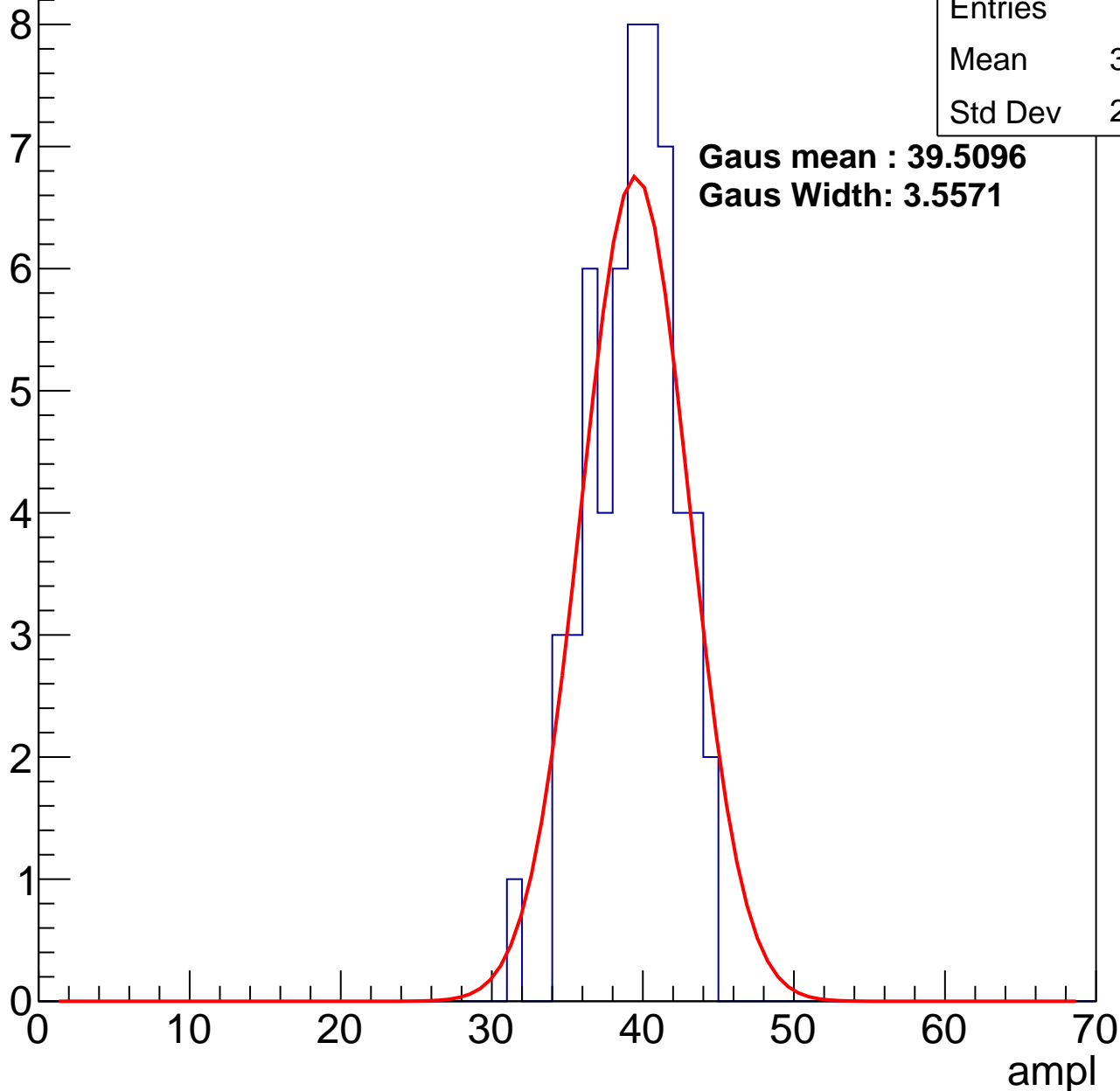
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	38.88
Std Dev	2.829

Gaus mean : 39.5096

Gaus Width: 3.5571



B1L102S, U26-ch15, adc2

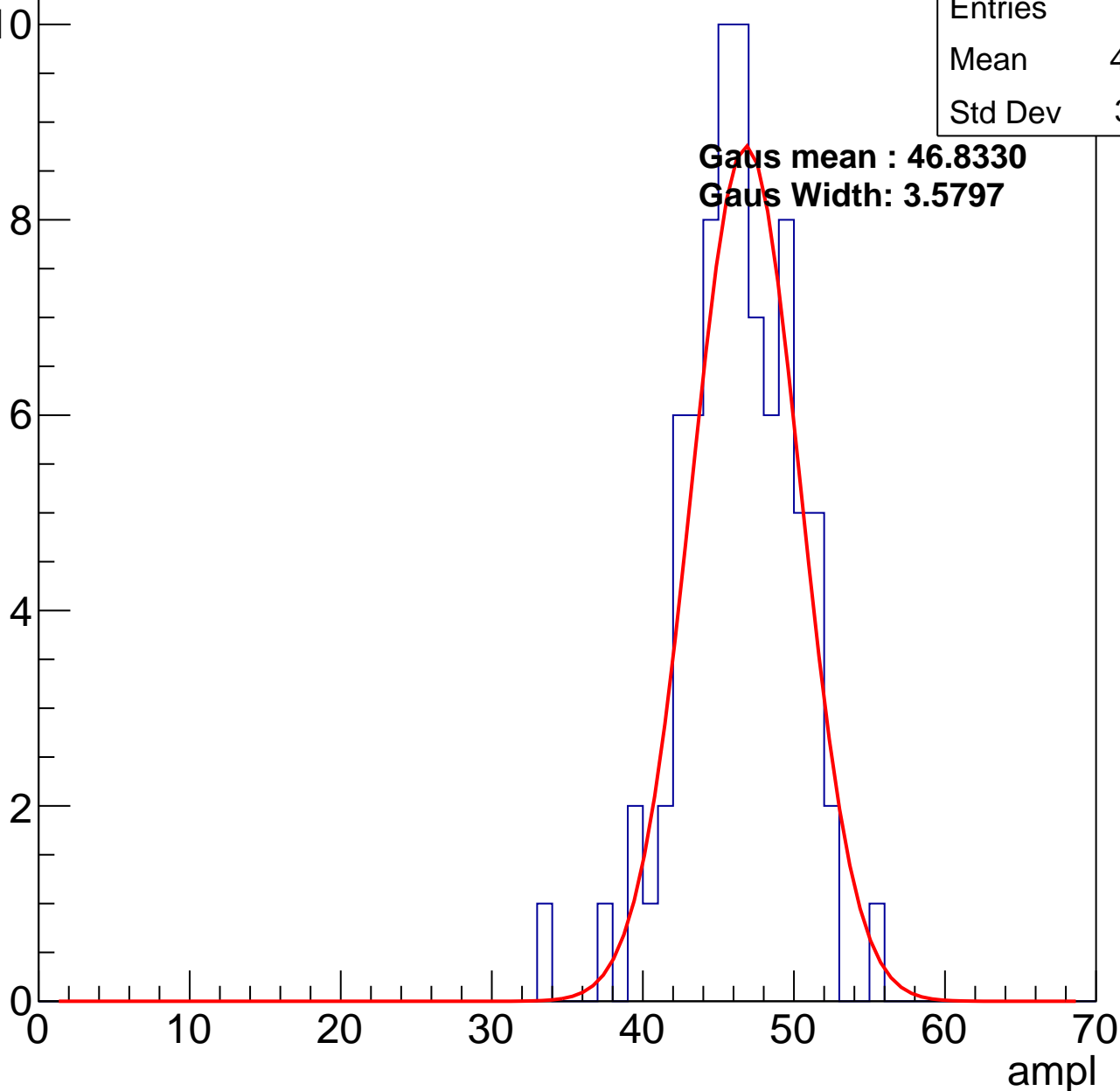
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	45.86
Std Dev	3.661

Gaus mean : 46.8330

Gaus Width: 3.5797

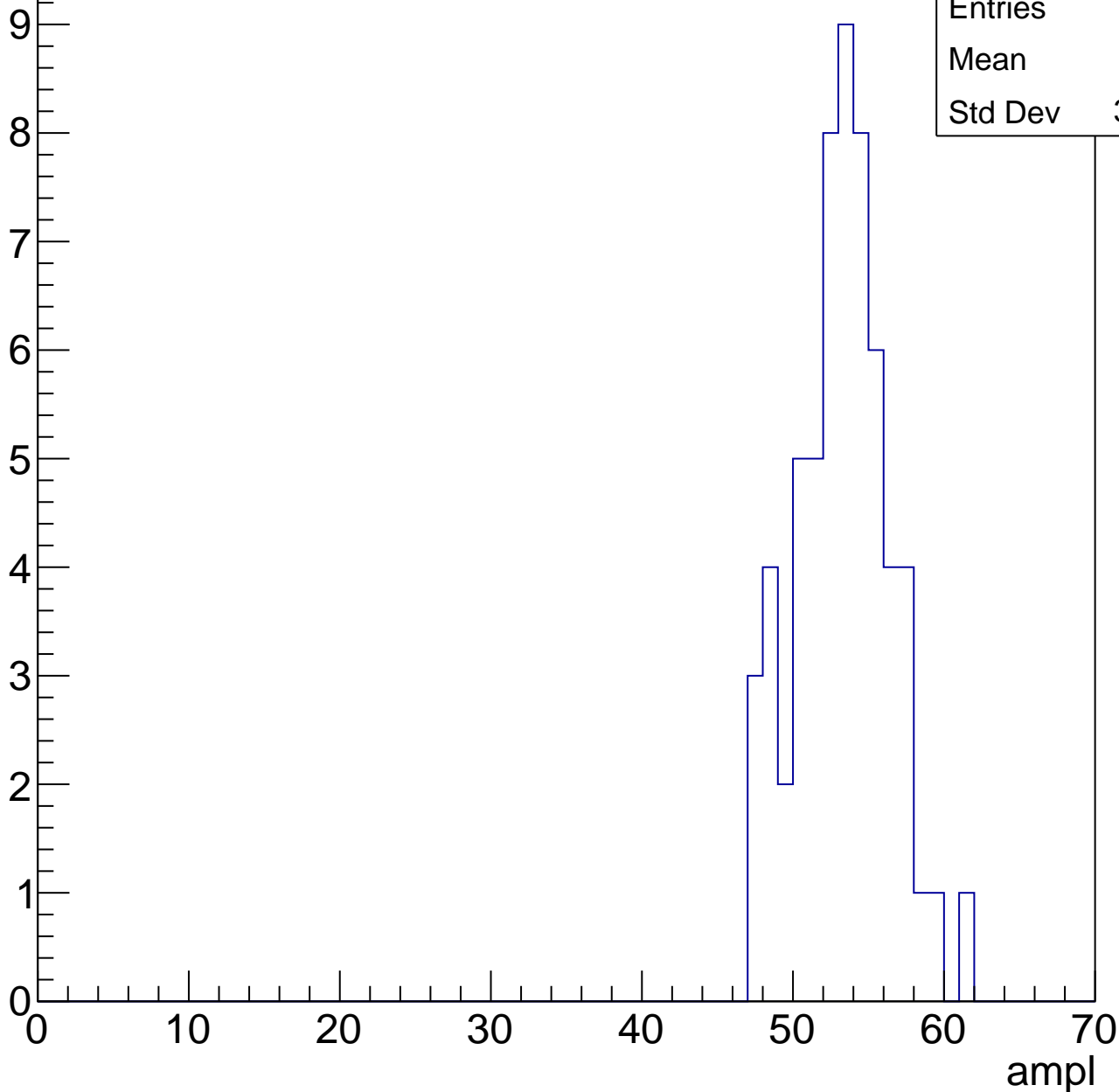


B1L102S, U26-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	52.8
Std Dev	3.051

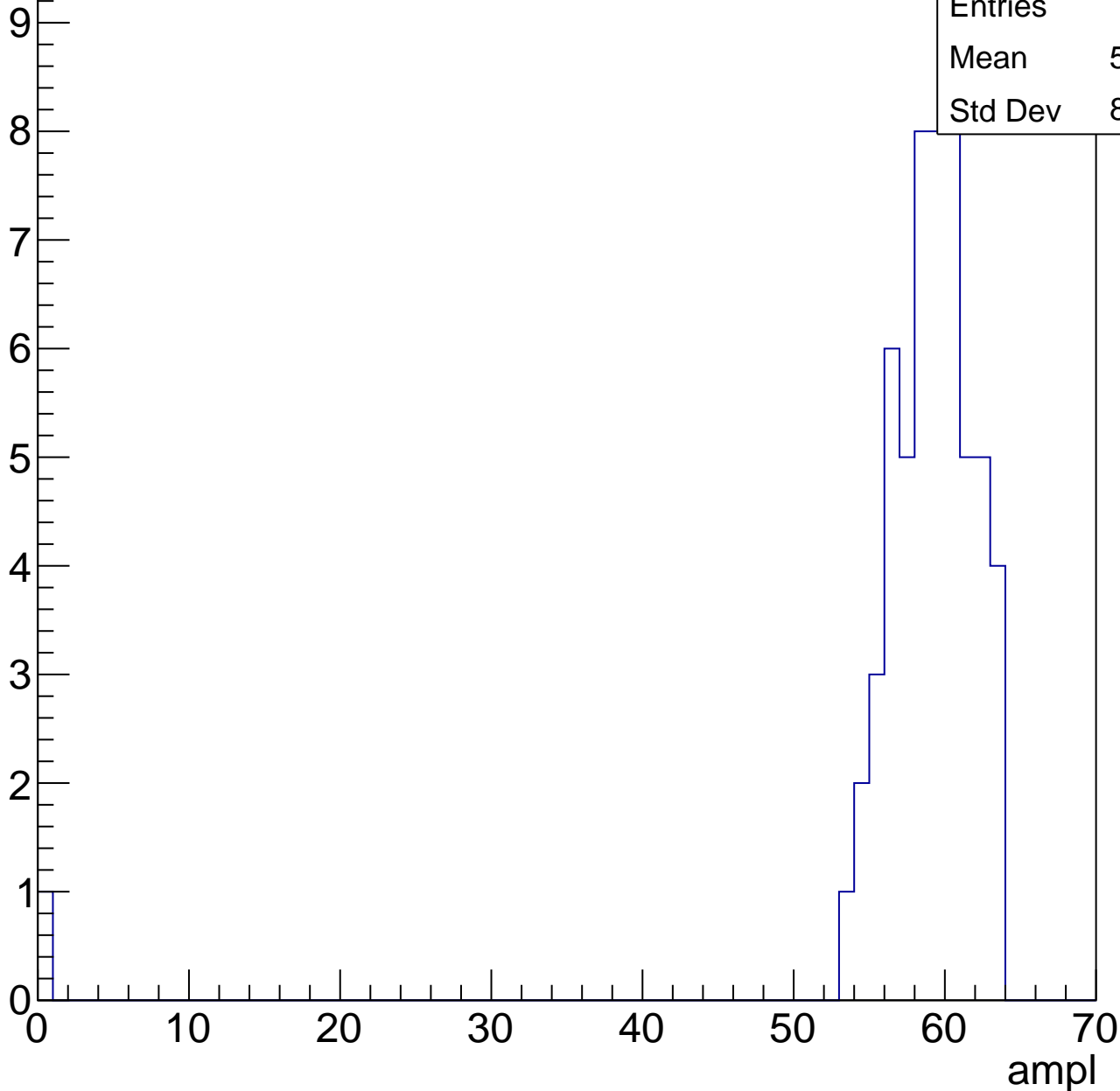


B1L102S, U26-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.72
Std Dev	8.102

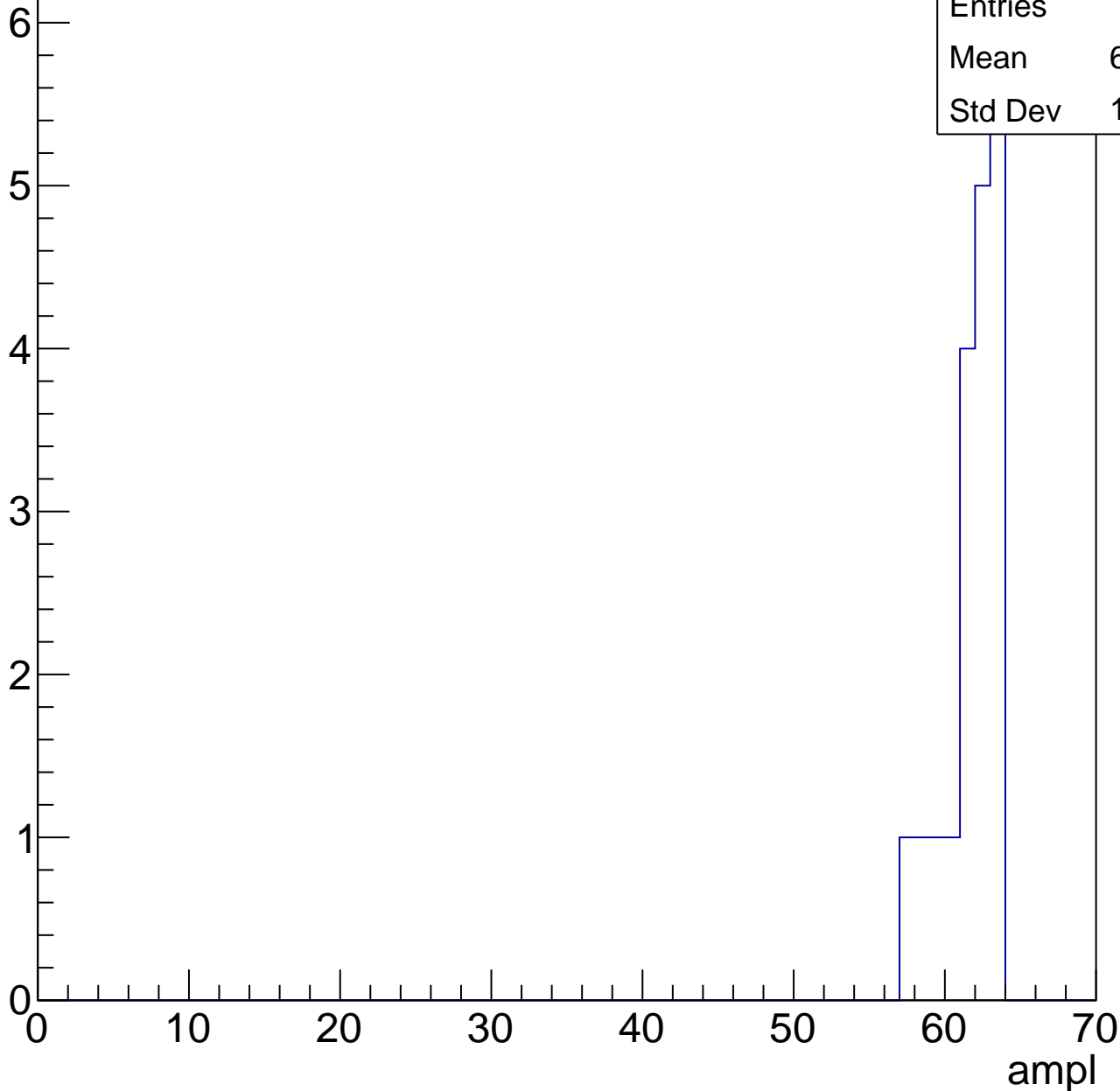


B1L102S, U26-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.37
Std Dev	1.723



B1L102S, U26-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

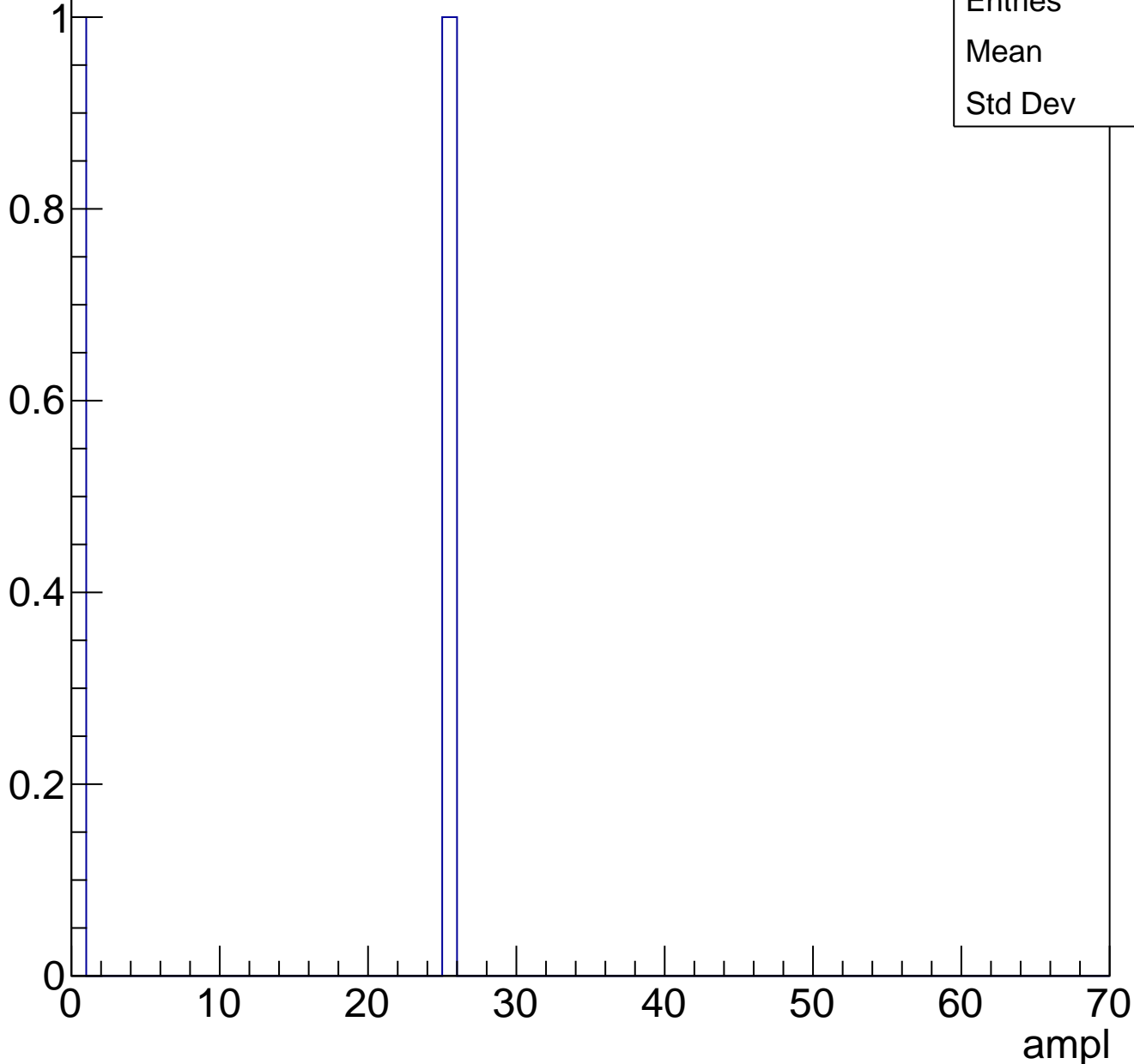


Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch16, adc0

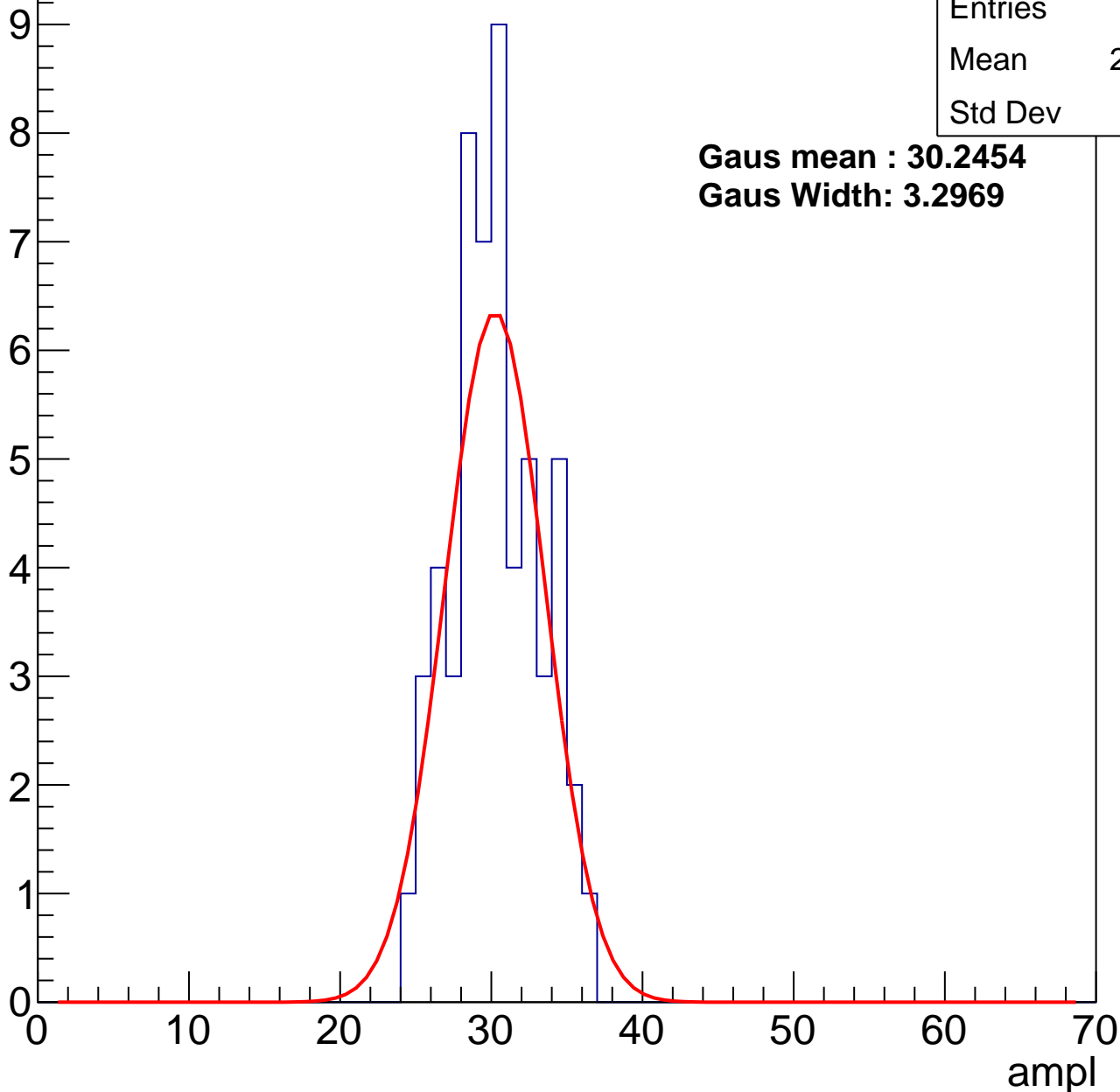
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	29.82
Std Dev	2.88

Gaus mean : 30.2454

Gaus Width: 3.2969



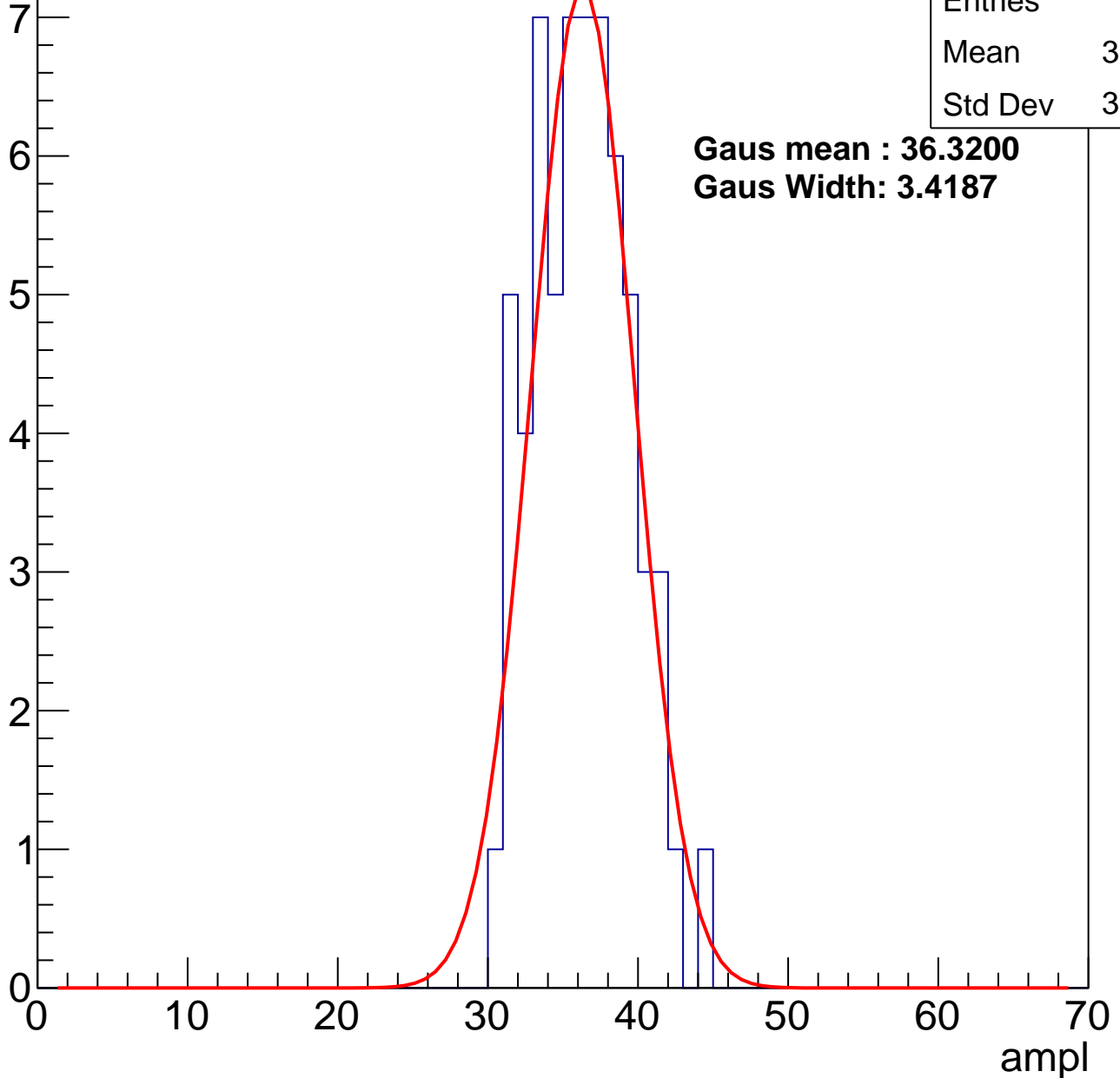
B1L102S, U26-ch16, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.84
Std Dev	3.143

Gaus mean : 36.3200
Gaus Width: 3.4187

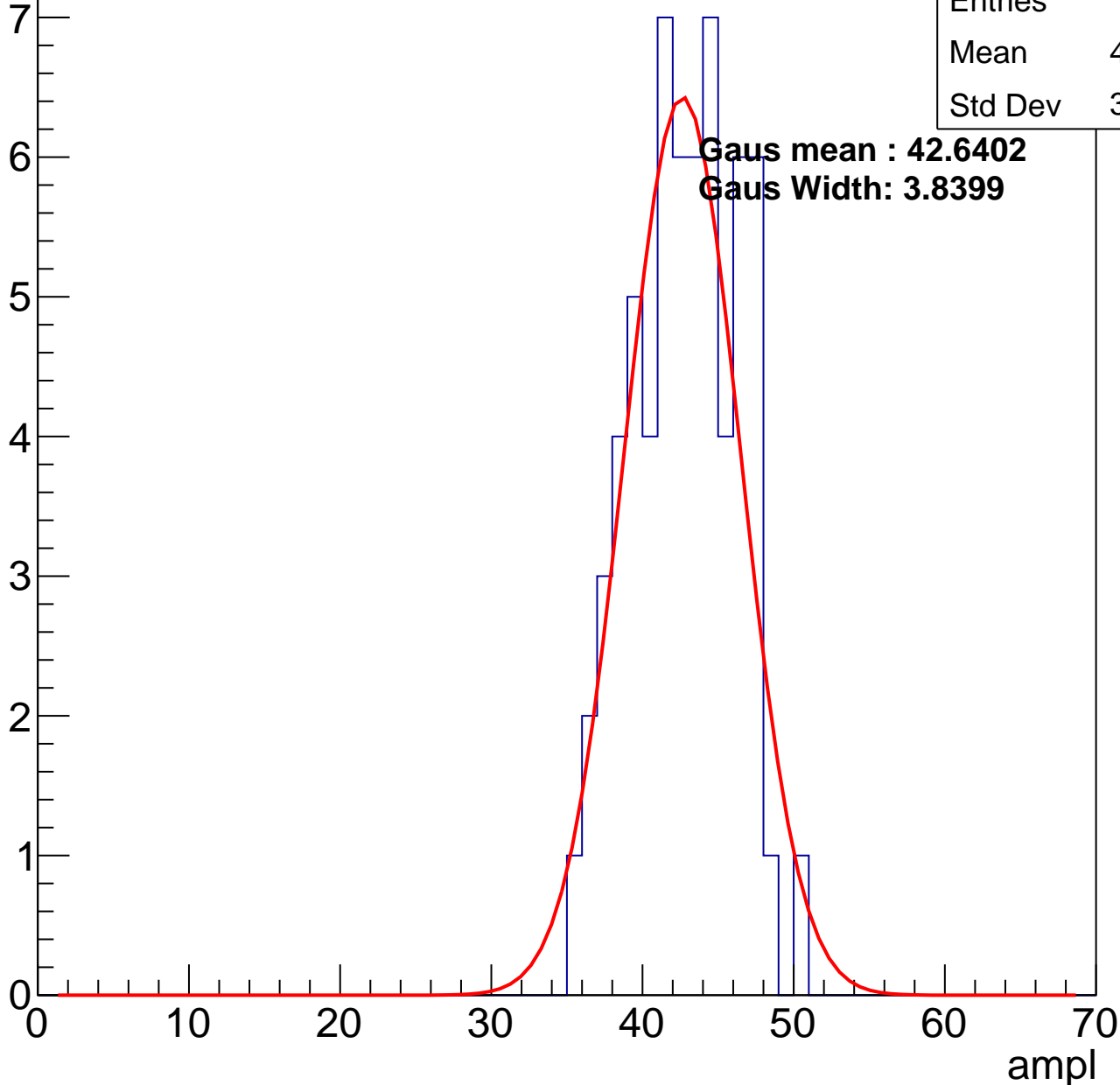


B1L102S, U26-ch16, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.32
Std Dev	3.422

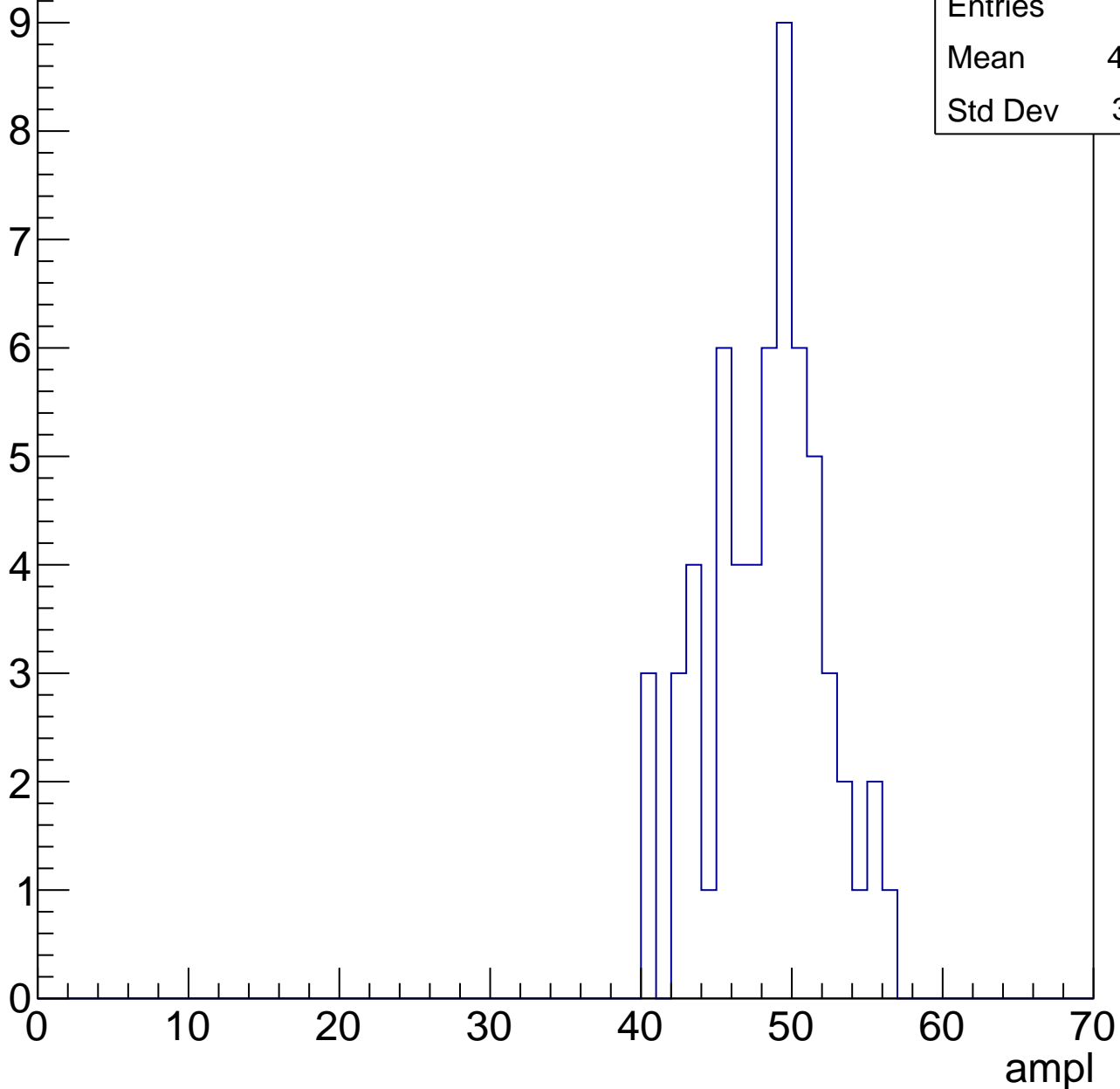


B1L102S, U26-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.83
Std Dev	3.791

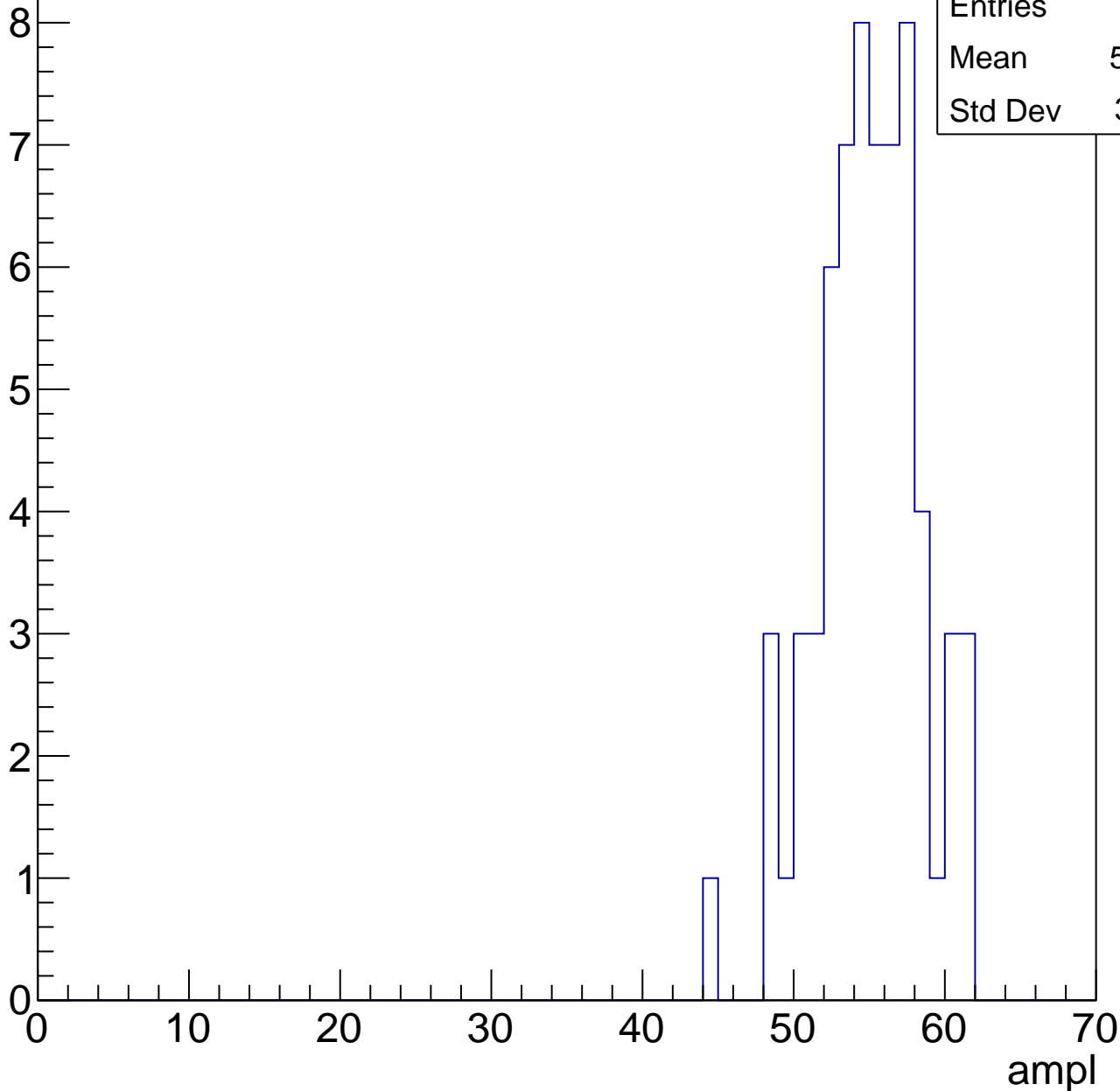


B1L102S, U26-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.49
Std Dev	3.451



B1L102S, U26-ch16, adc5

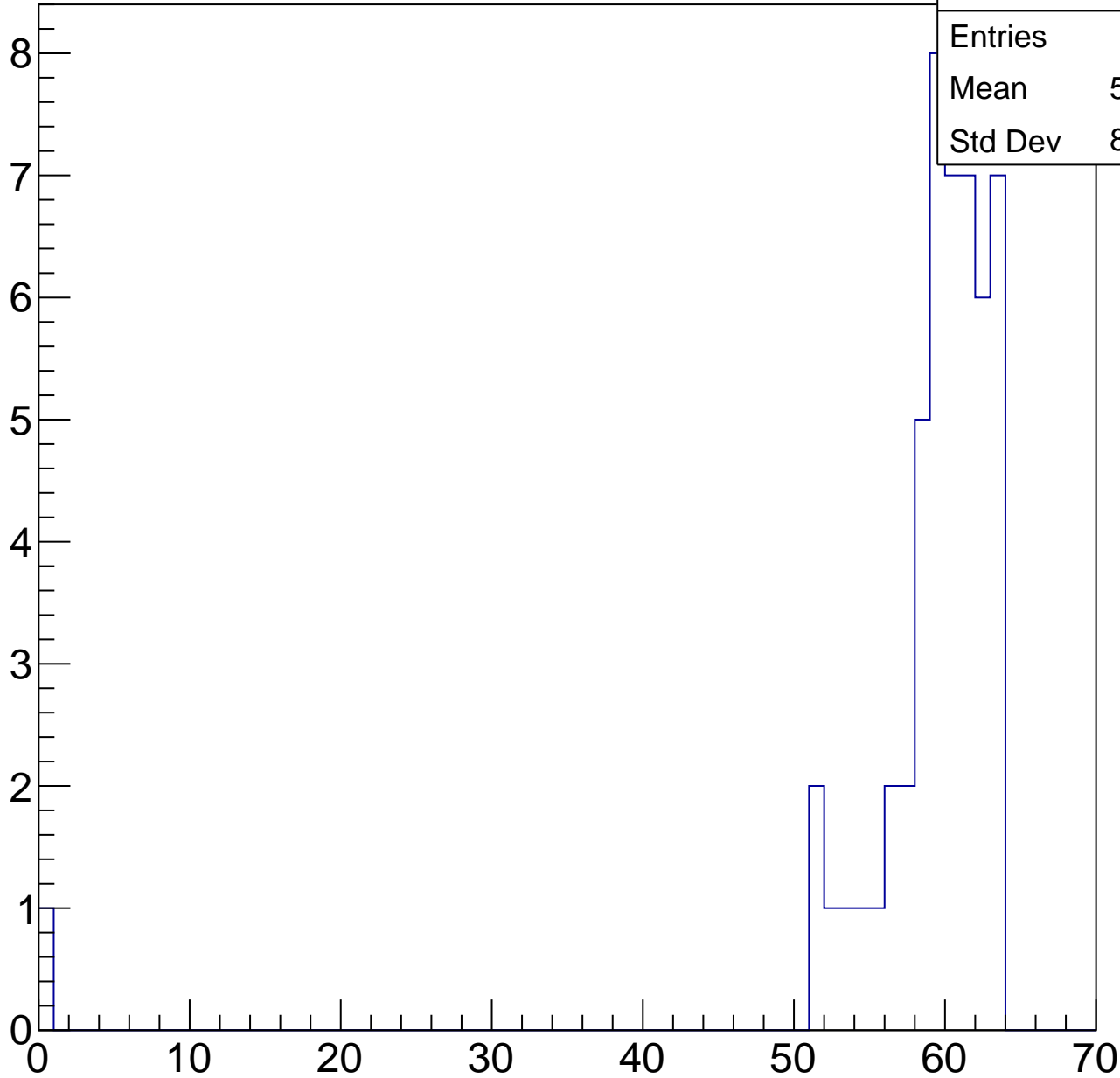
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.12
Std Dev	8.775

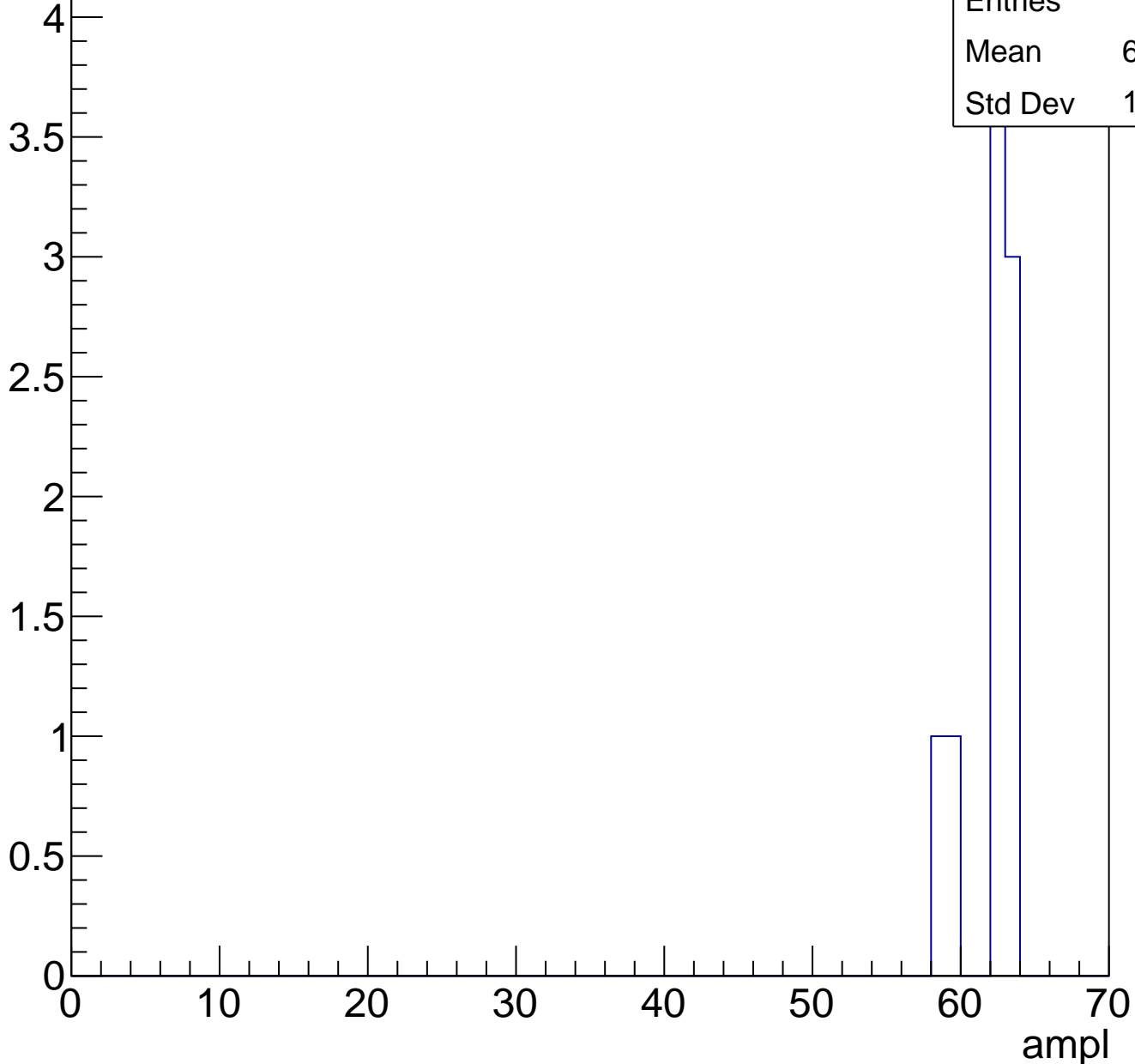
ampl



B1L102S, U26-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch17, adc0

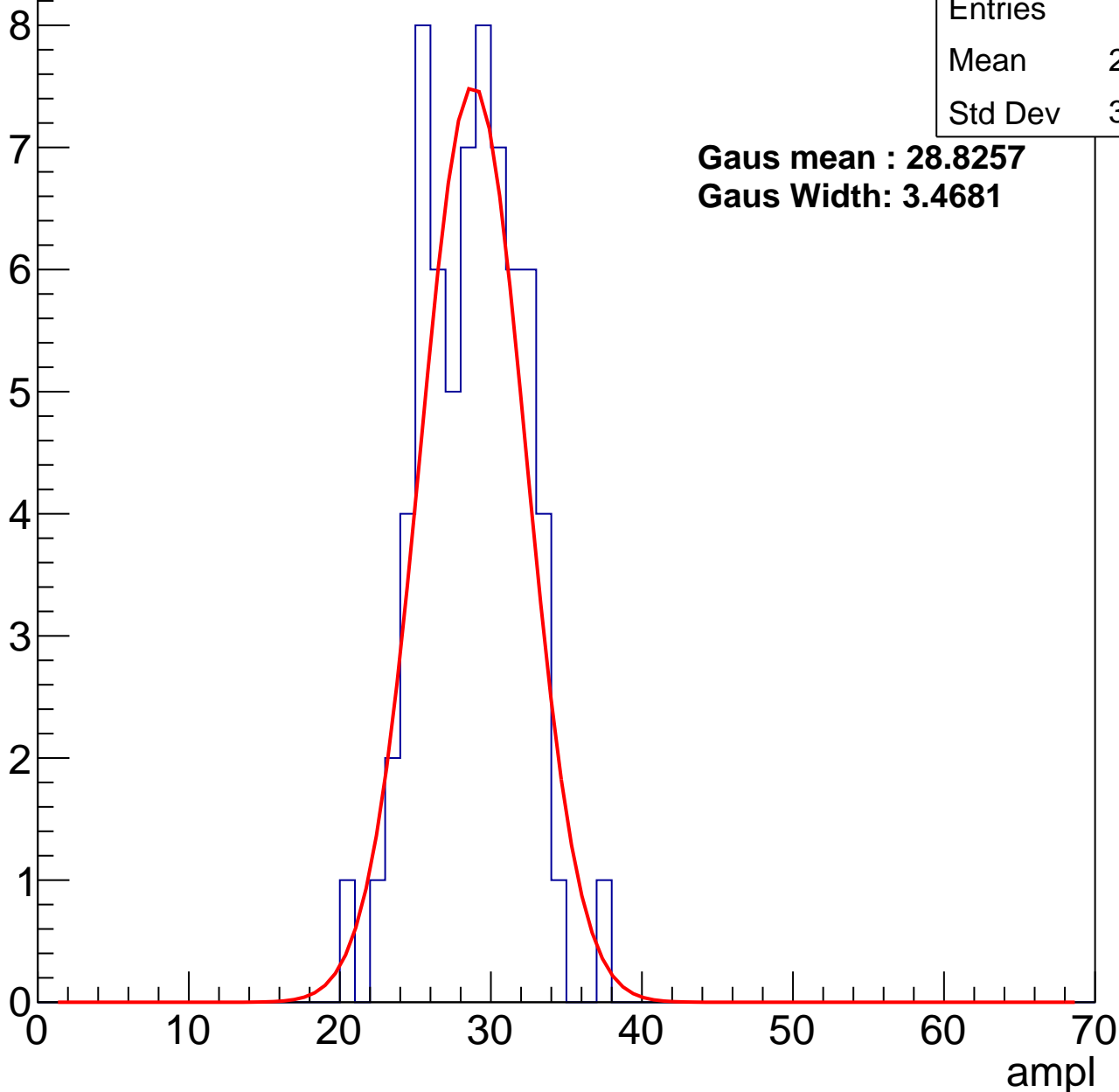
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.27
Std Dev	3.263

Gaus mean : 28.8257

Gaus Width: 3.4681



B1L102S, U26-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	35.05
Std Dev	3.397

Gaus mean : 35.6719

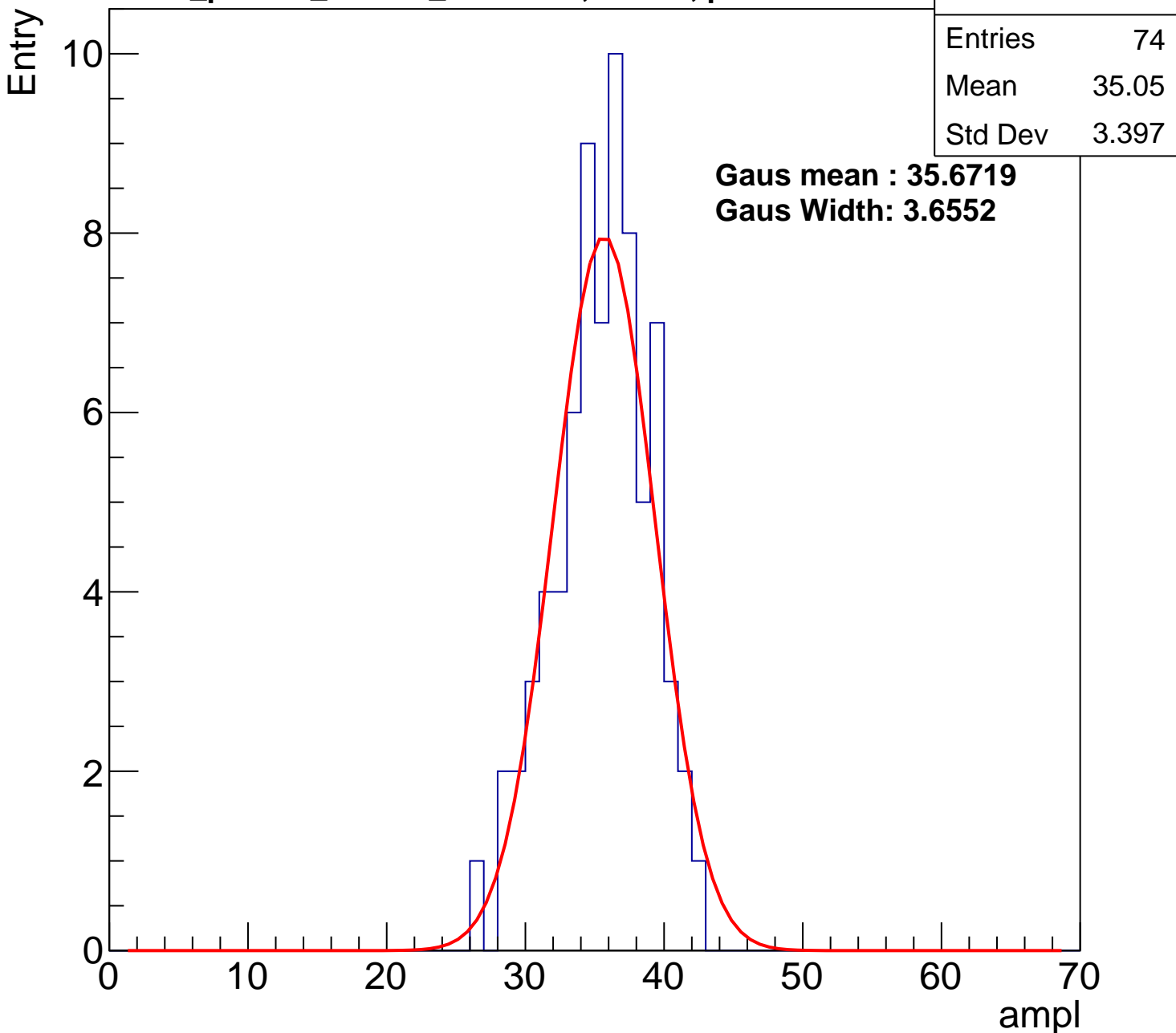
Gaus Width: 3.6552

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch17, adc2

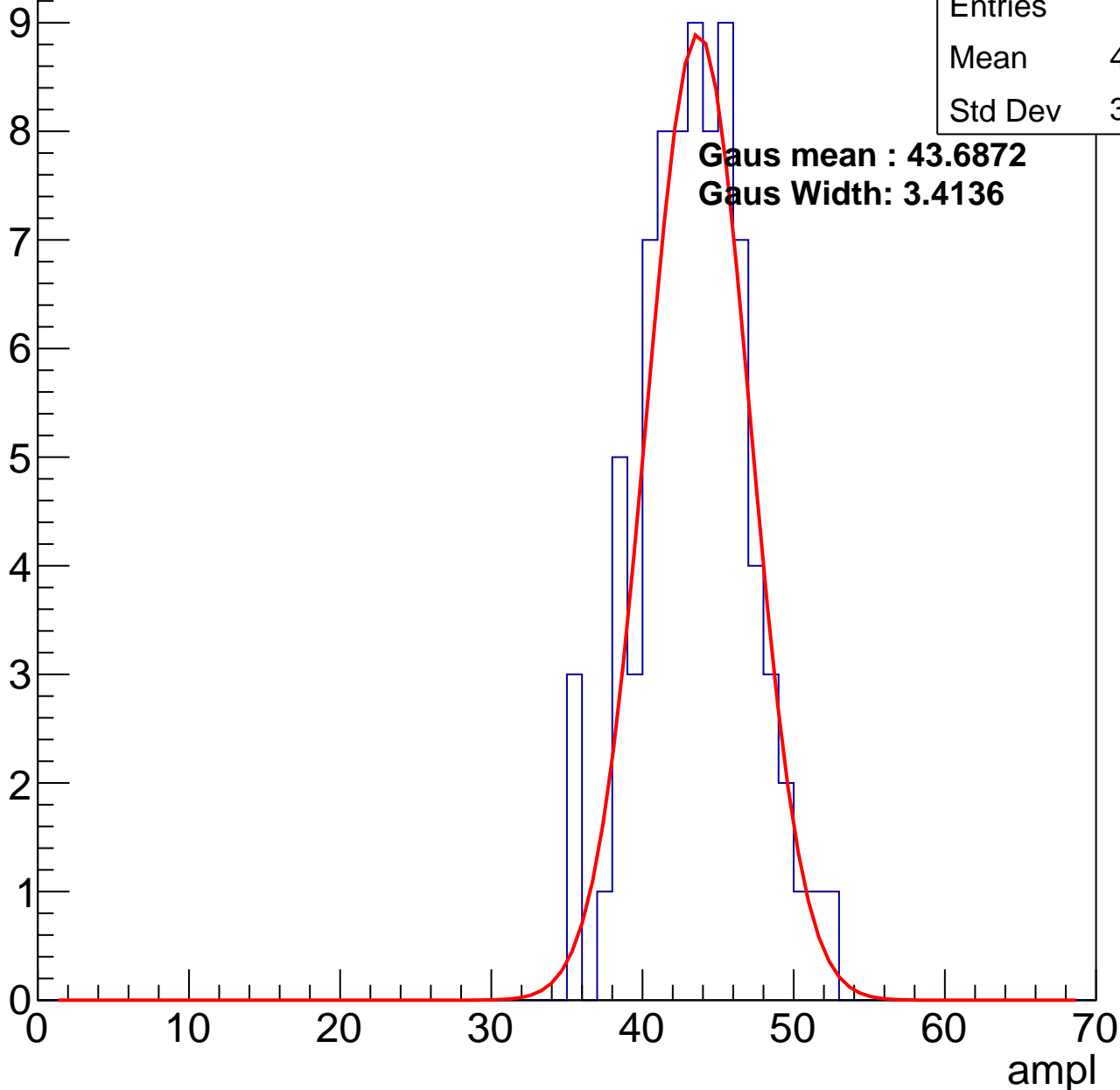
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.02
Std Dev	3.564

Gaus mean : 43.6872

Gaus Width: 3.4136

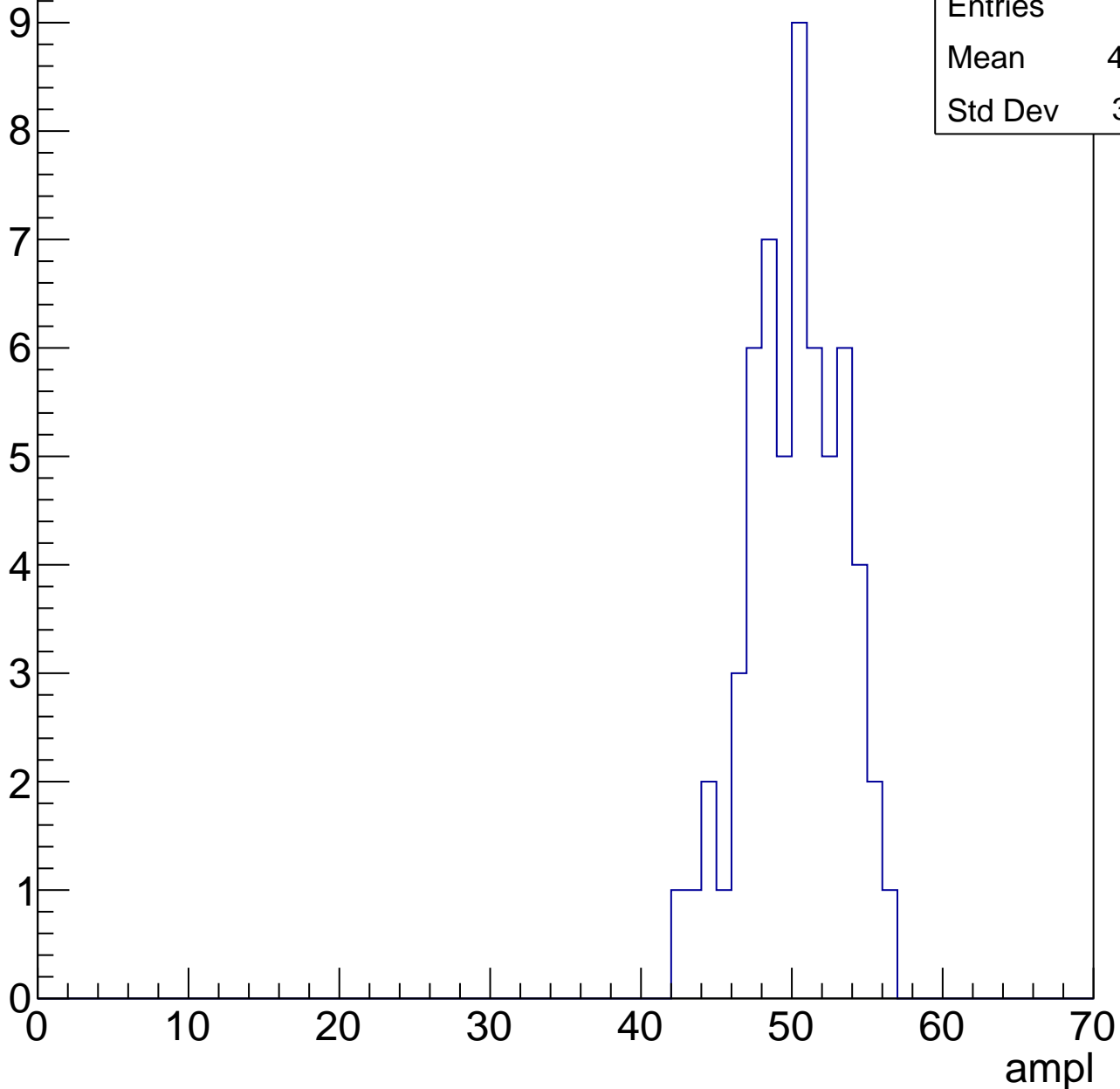


B1L102S, U26-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

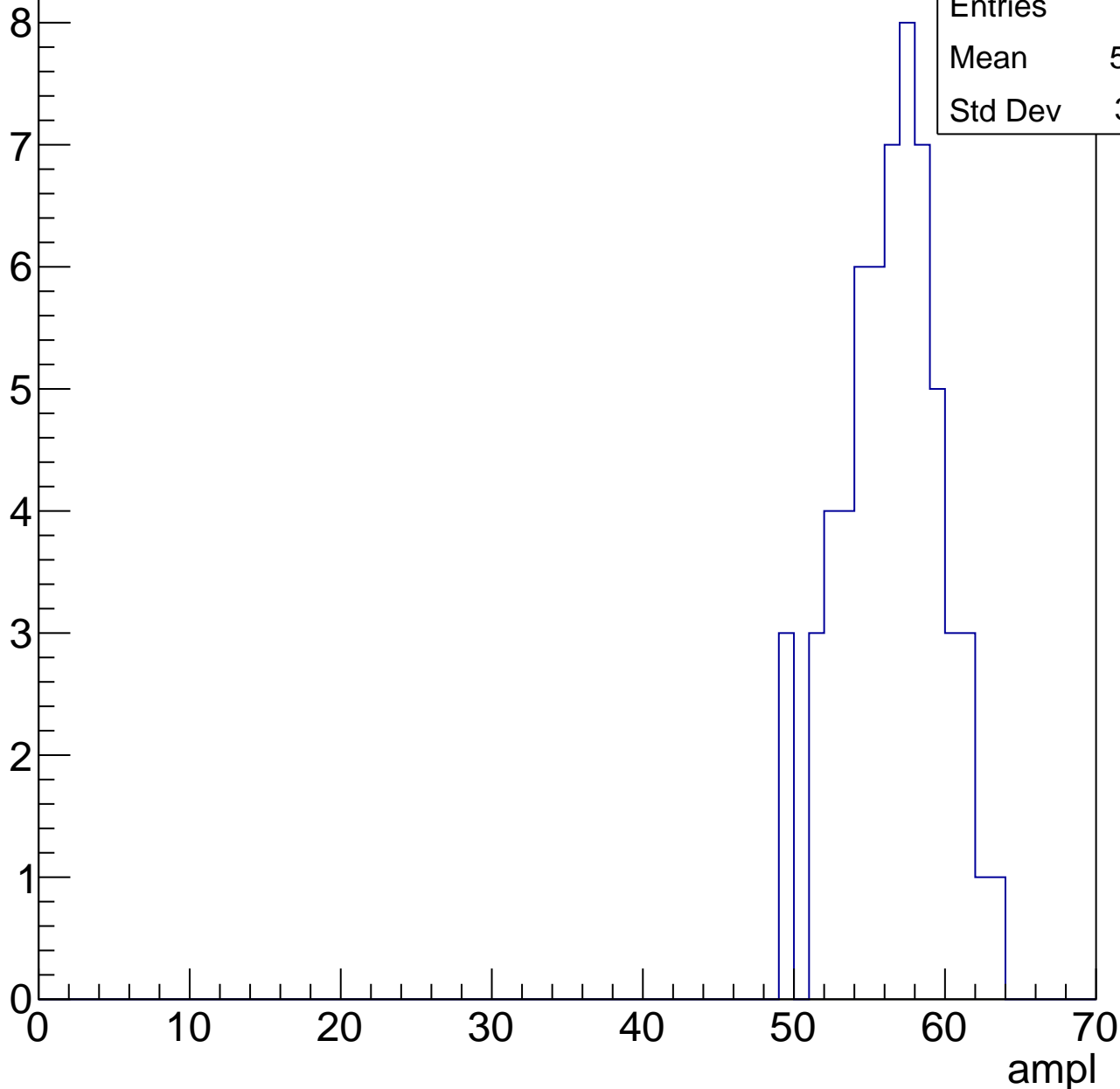
Entries	59
Mean	49.75
Std Dev	3.111



B1L102S, U26-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

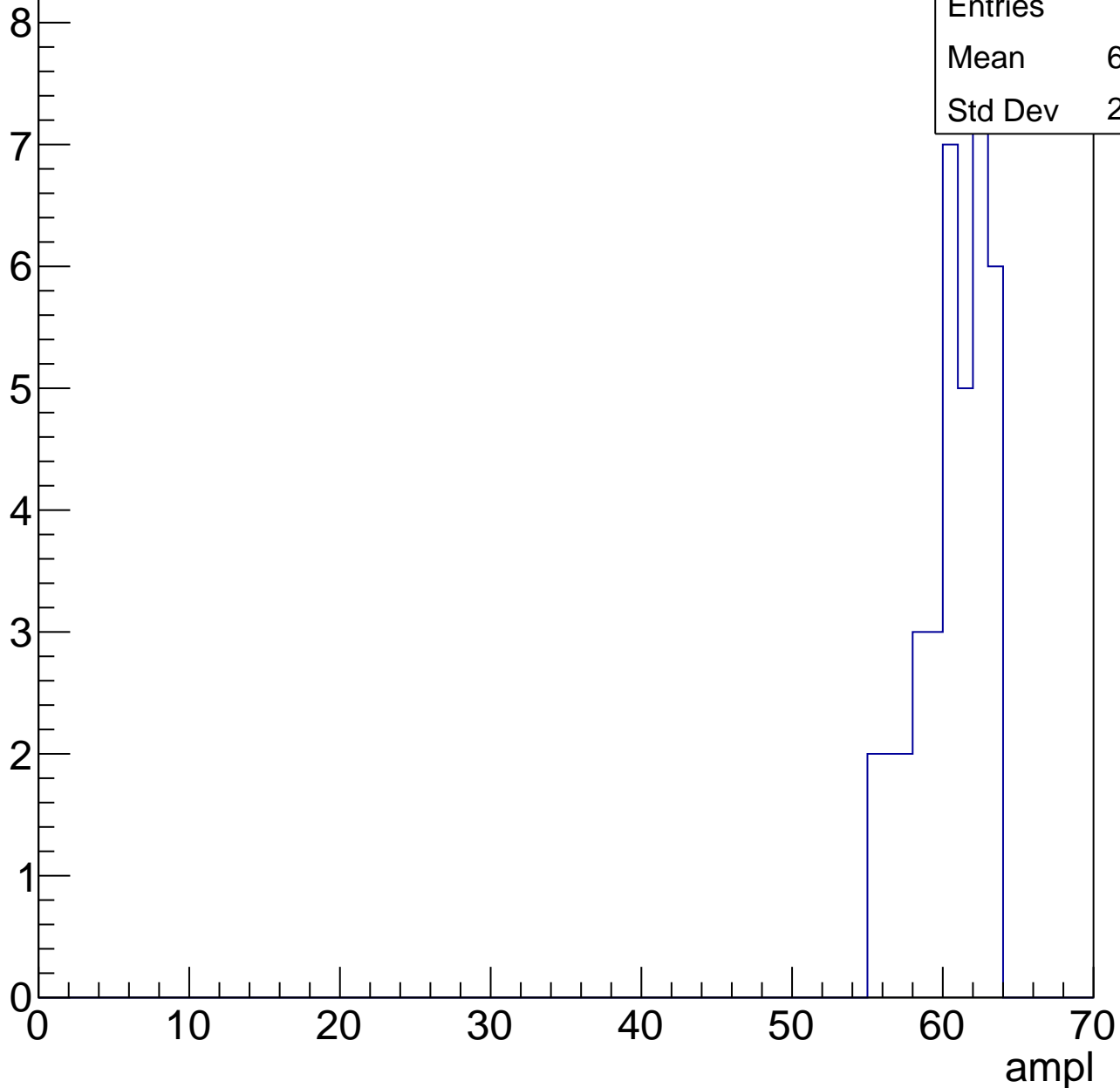
Entry



B1L102S, U26-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch17, adc6

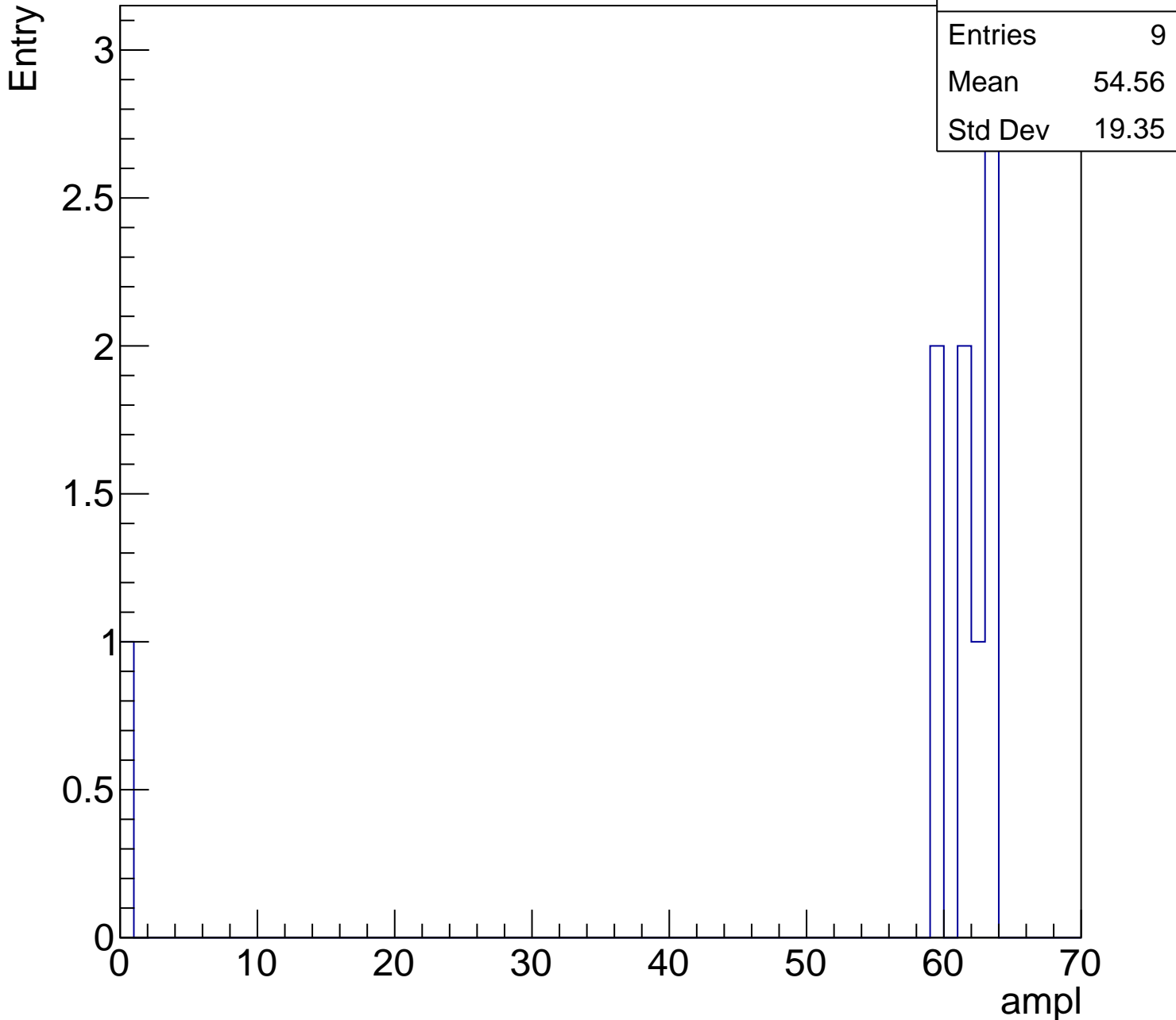
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.56
Std Dev	19.35

ampl



B1L102S, U26-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch18, adc0

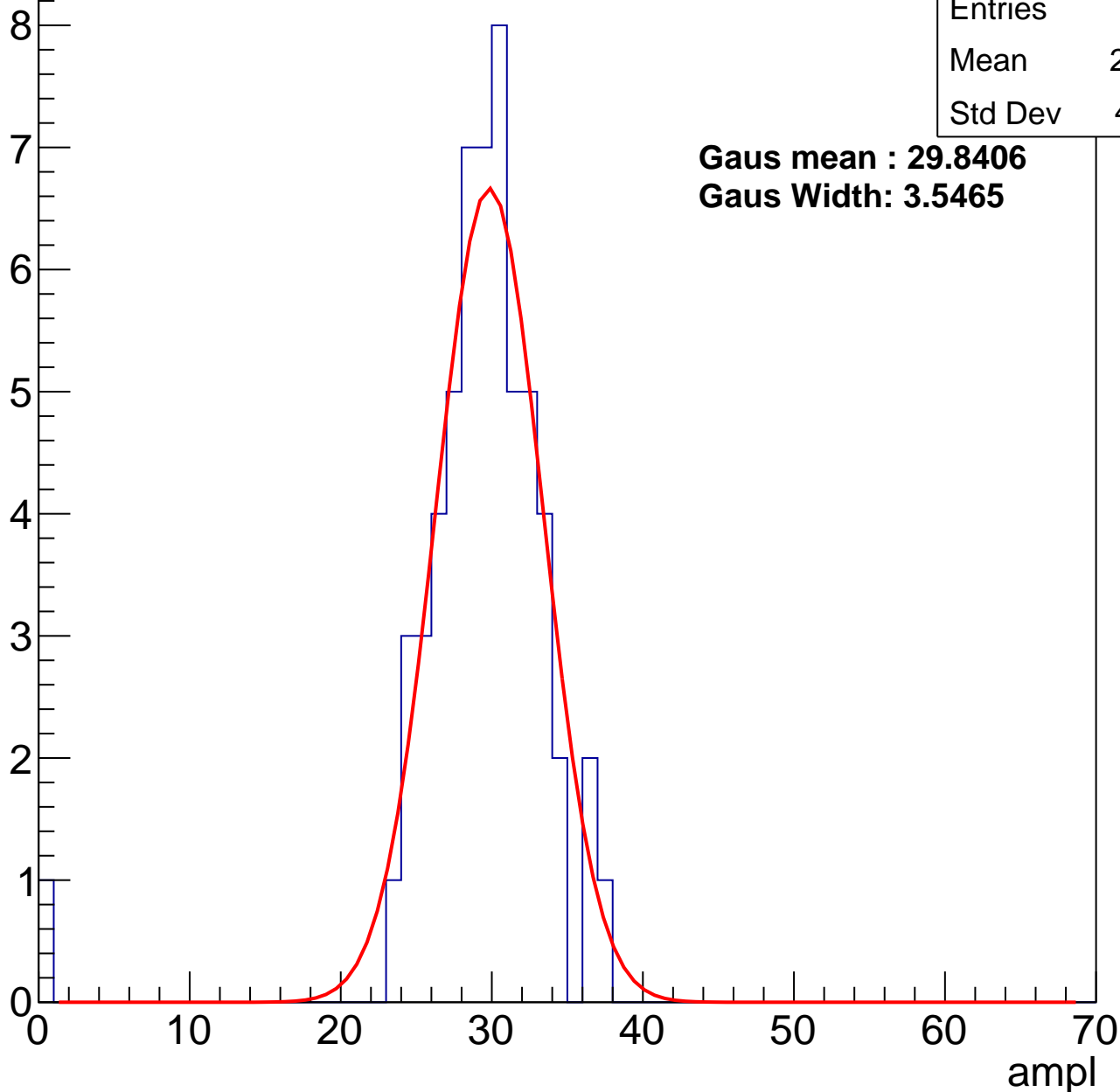
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.83
Std Dev	4.931

Gaus mean : 29.8406

Gaus Width: 3.5465



B1L102S, U26-ch18, adc1

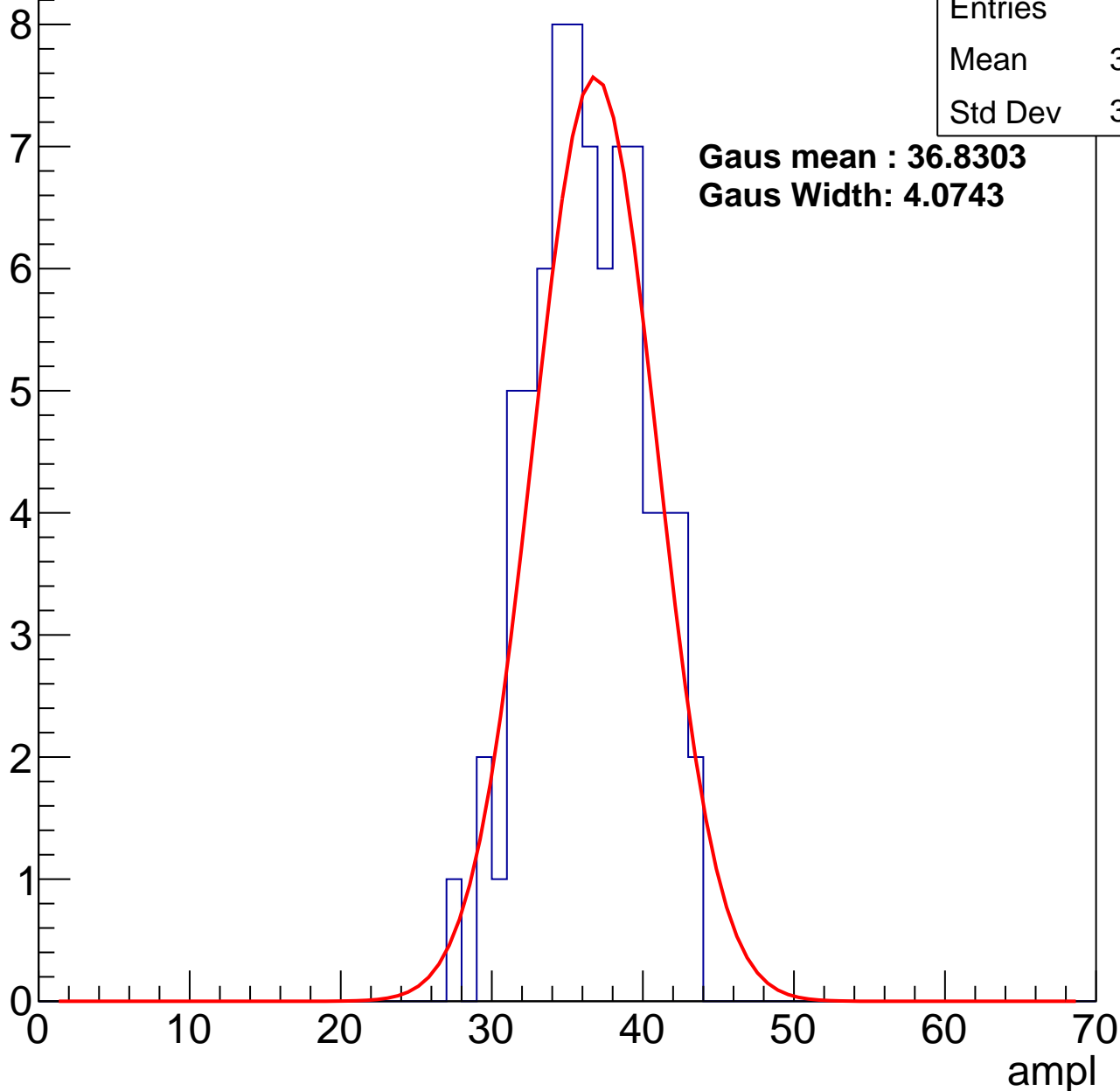
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.99
Std Dev	3.634

Gaus mean : 36.8303

Gaus Width: 4.0743



B1L102S, U26-ch18, adc2

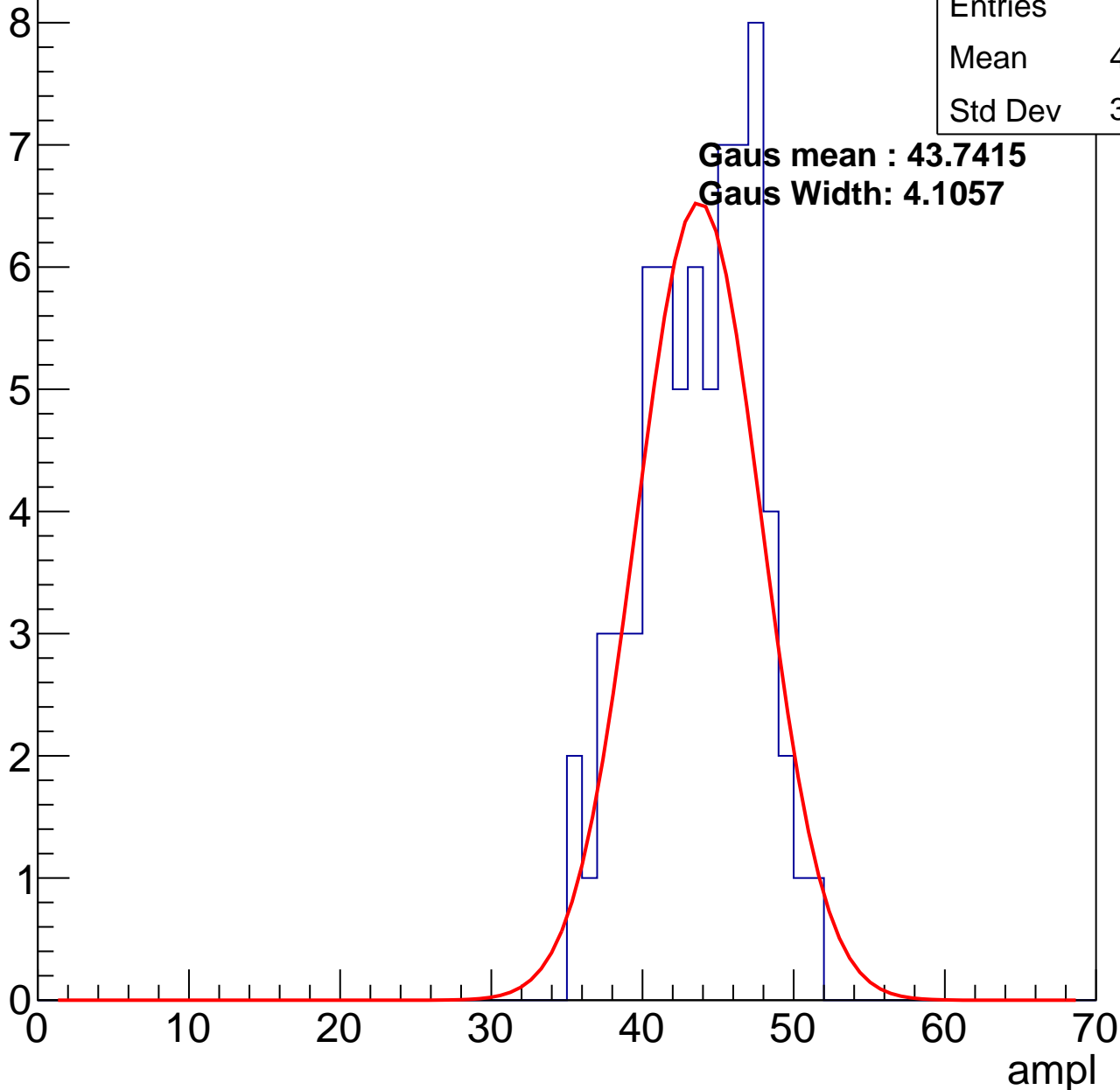
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.23
Std Dev	3.773

Gaus mean : 43.7415

Gaus Width: 4.1057

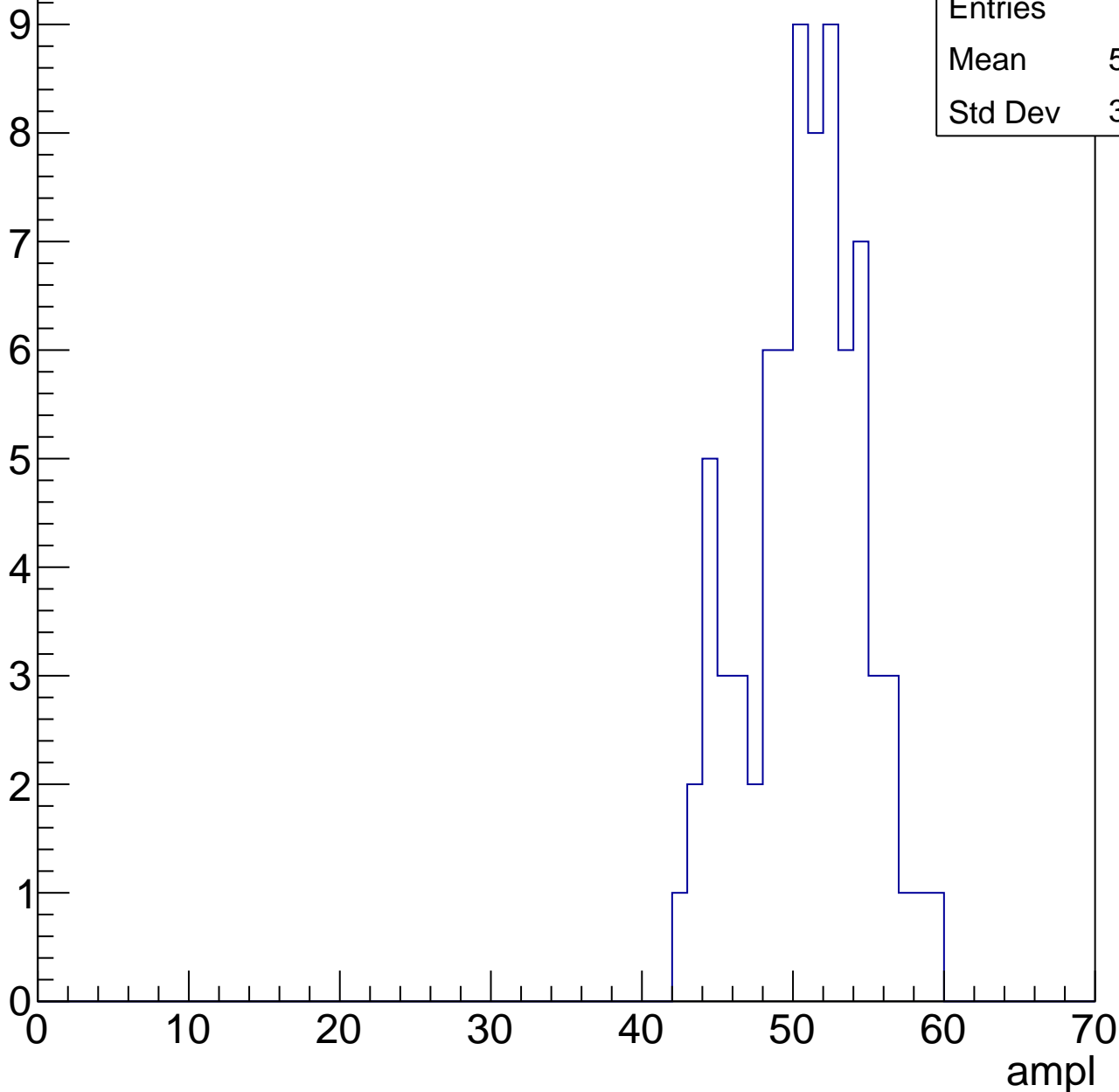


B1L102S, U26-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	50.34
Std Dev	3.789

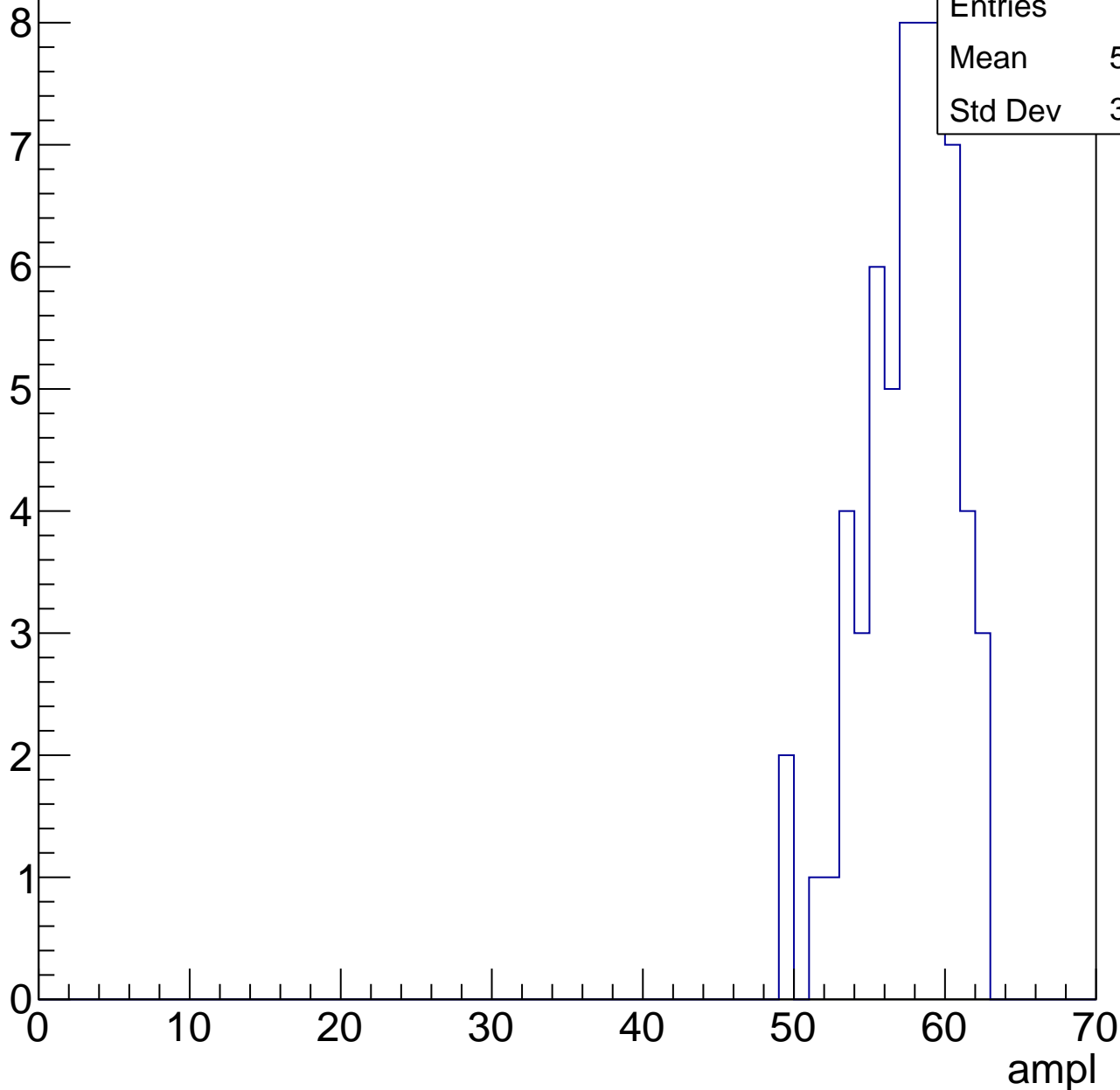


B1L102S, U26-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	57.12
Std Dev	3.028

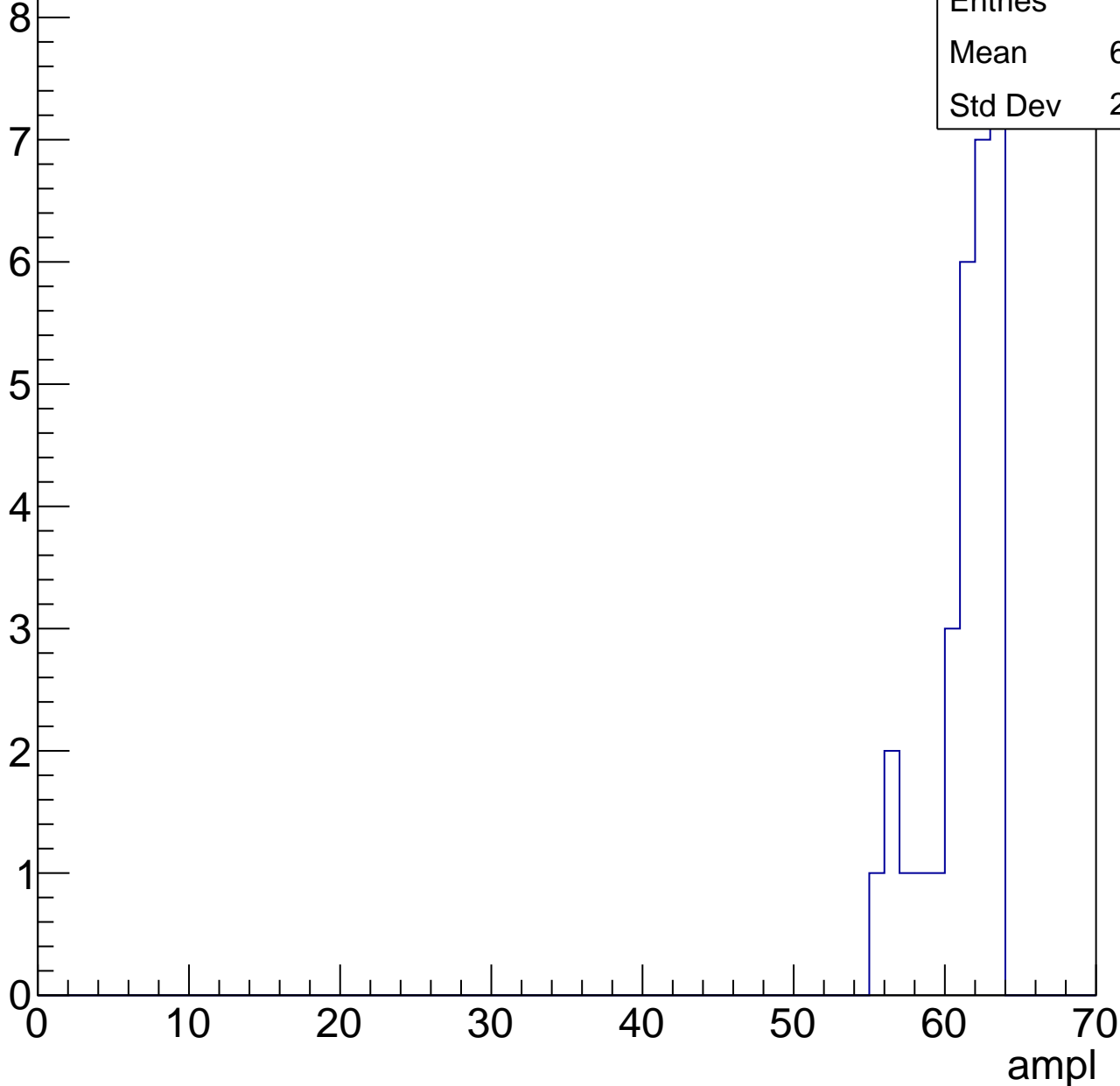


B1L102S, U26-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	60.83
Std Dev	2.282



B1L102S, U26-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

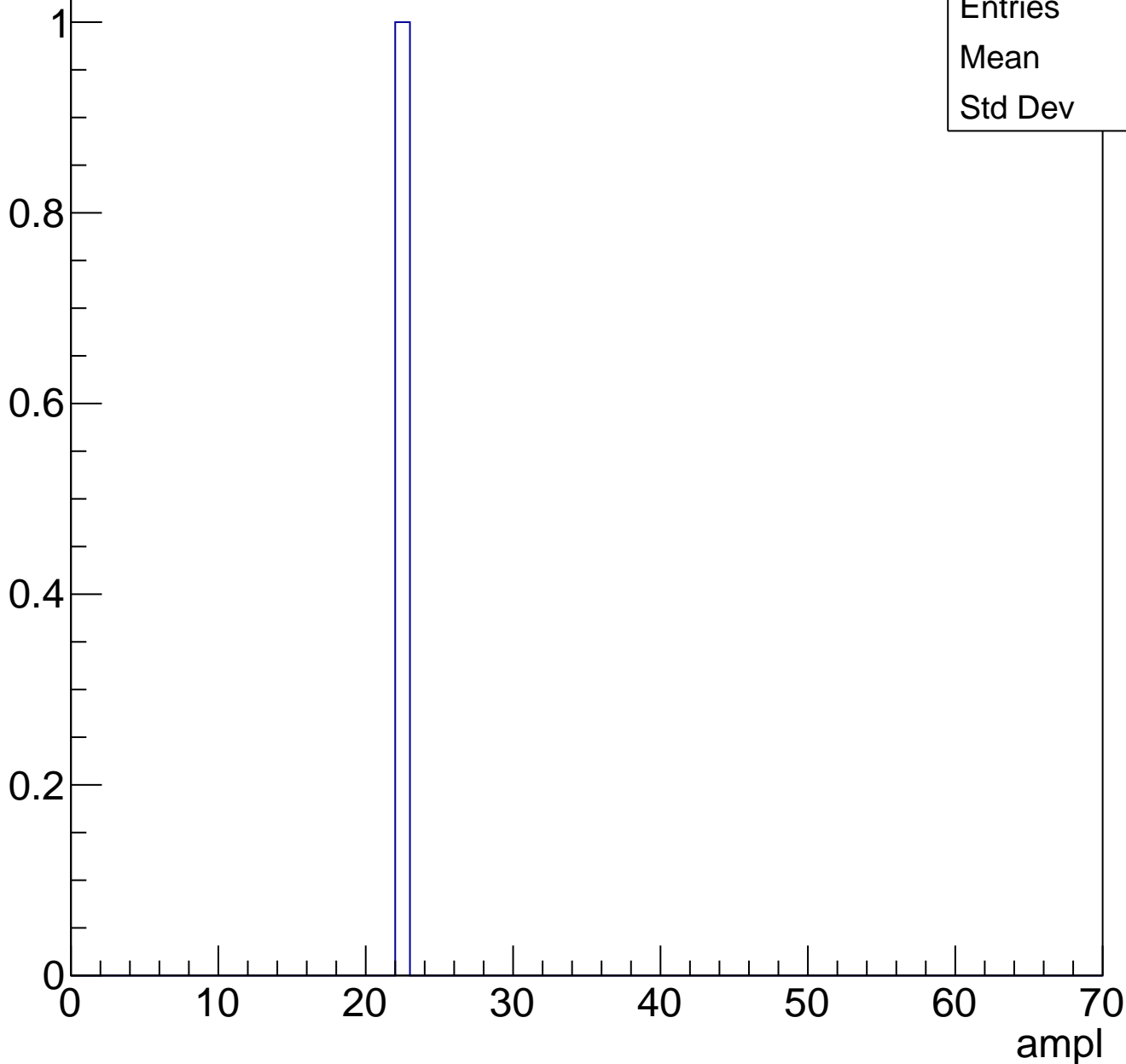
Entry



B1L102S, U26-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch19, adc0

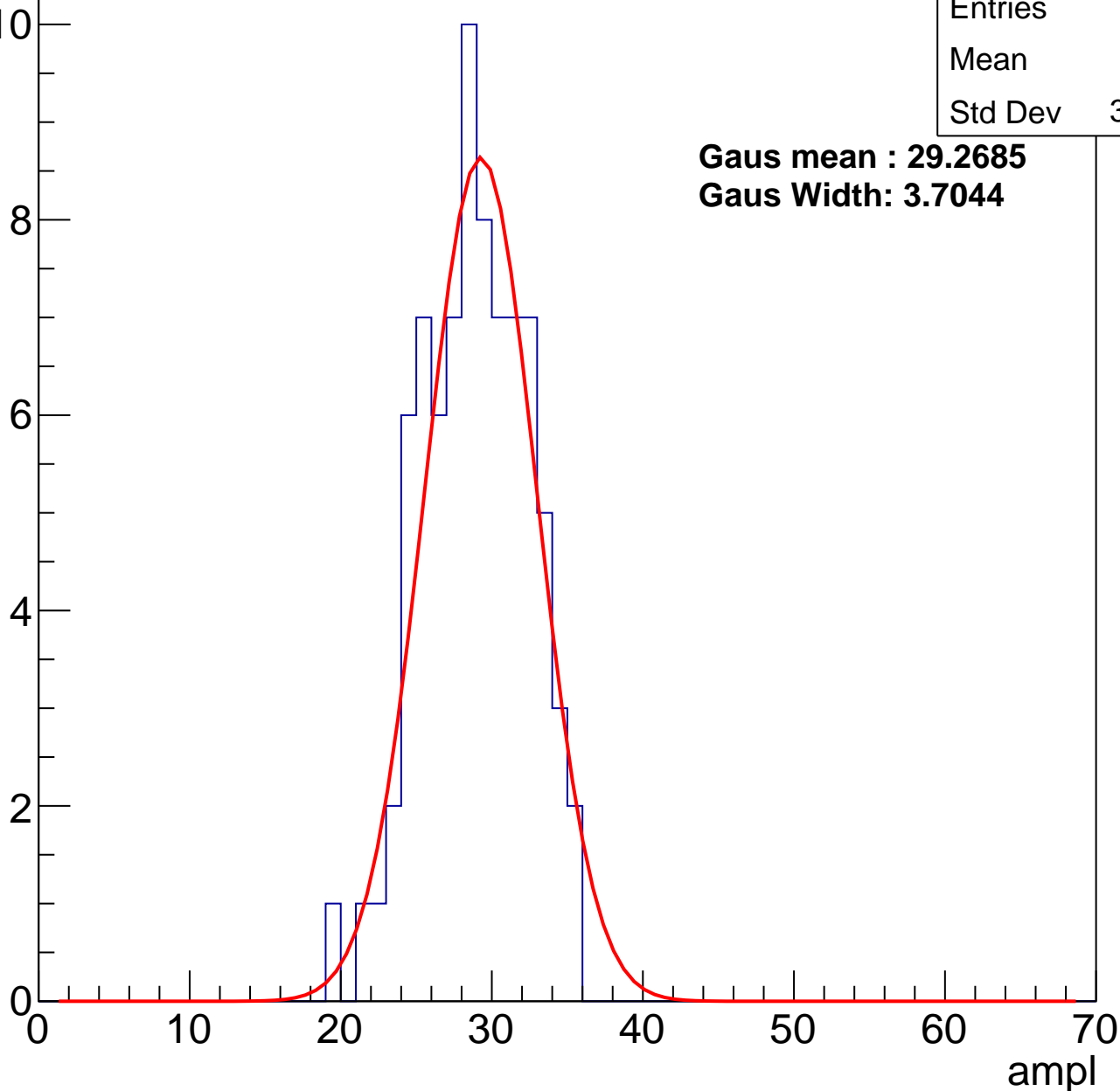
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.4
Std Dev	3.419

Gaus mean : 29.2685

Gaus Width: 3.7044



B1L102S, U26-ch19, adc1

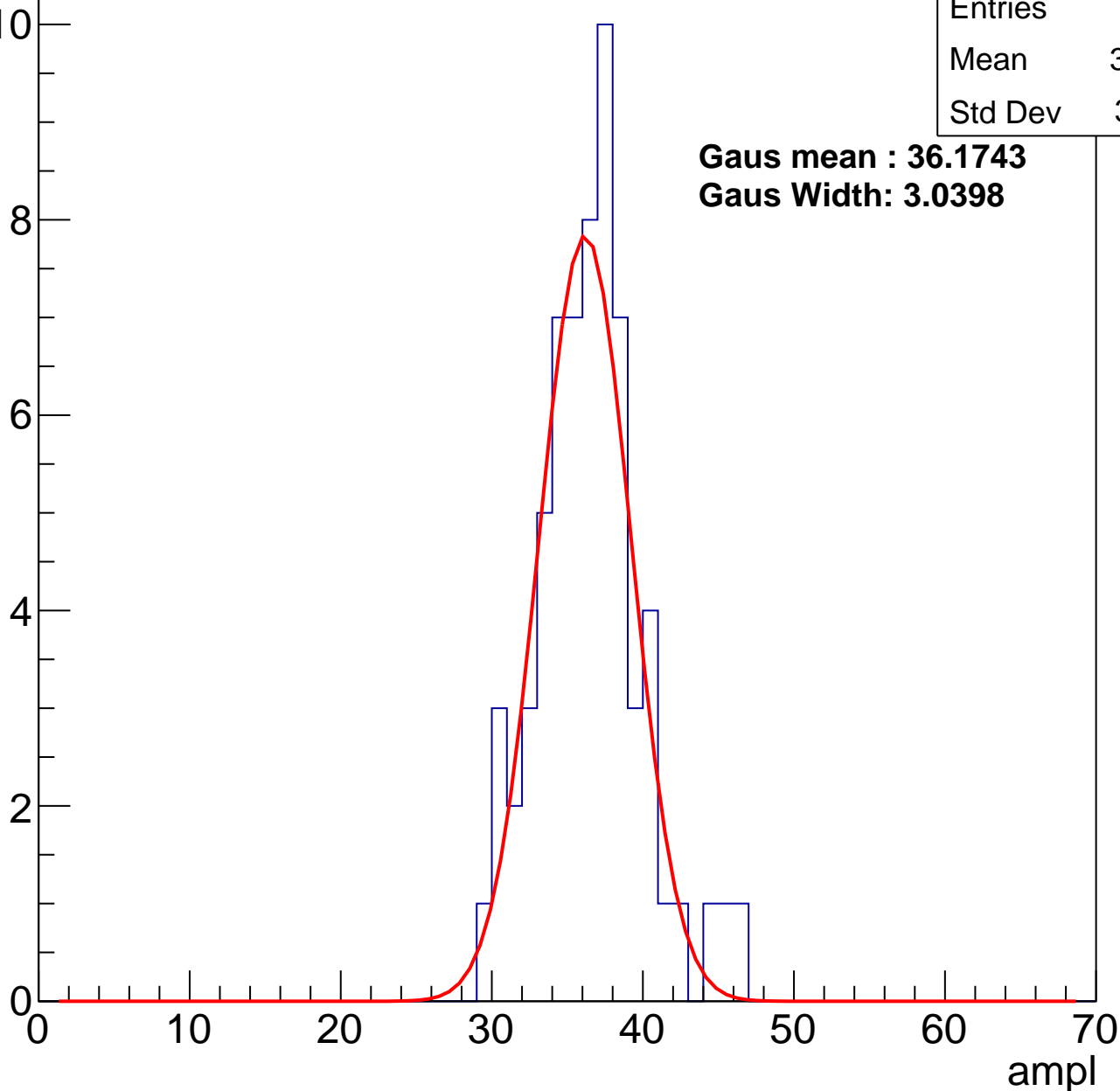
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.06
Std Dev	3.441

Gaus mean : 36.1743

Gaus Width: 3.0398



B1L102S, U26-ch19, adc2

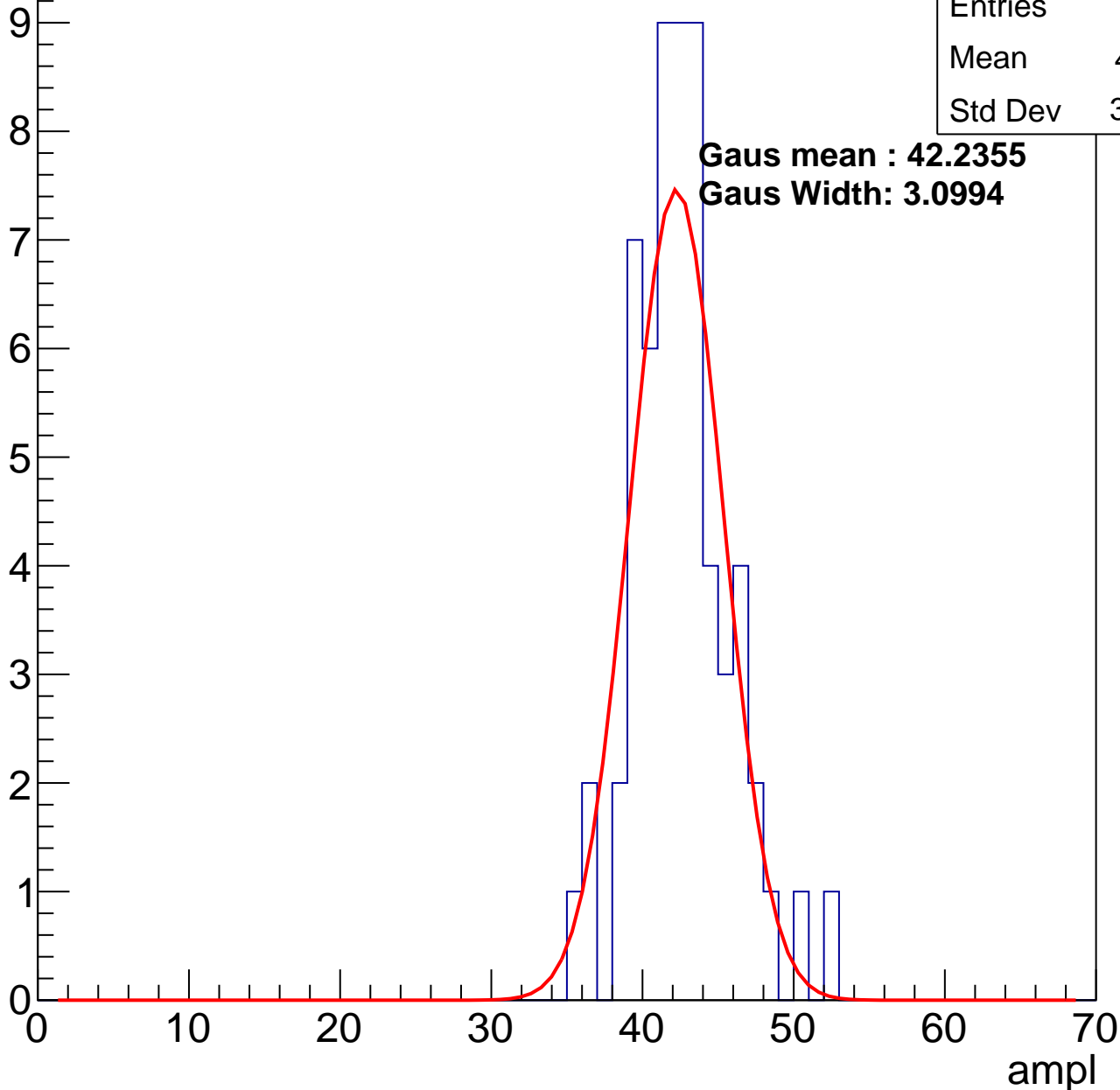
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.11
Std Dev	3.173

Gaus mean : 42.2355

Gaus Width: 3.0994

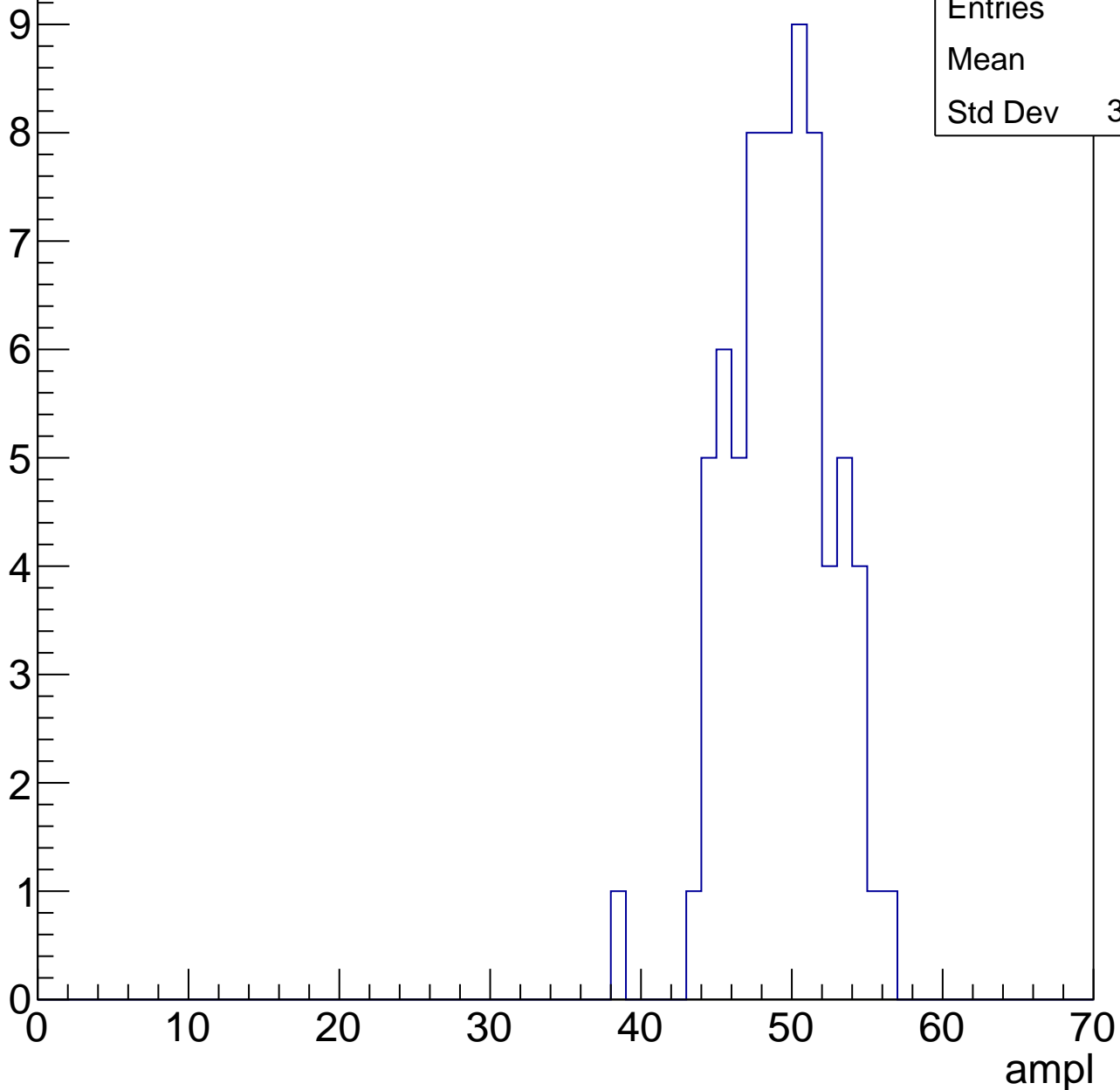


B1L102S, U26-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

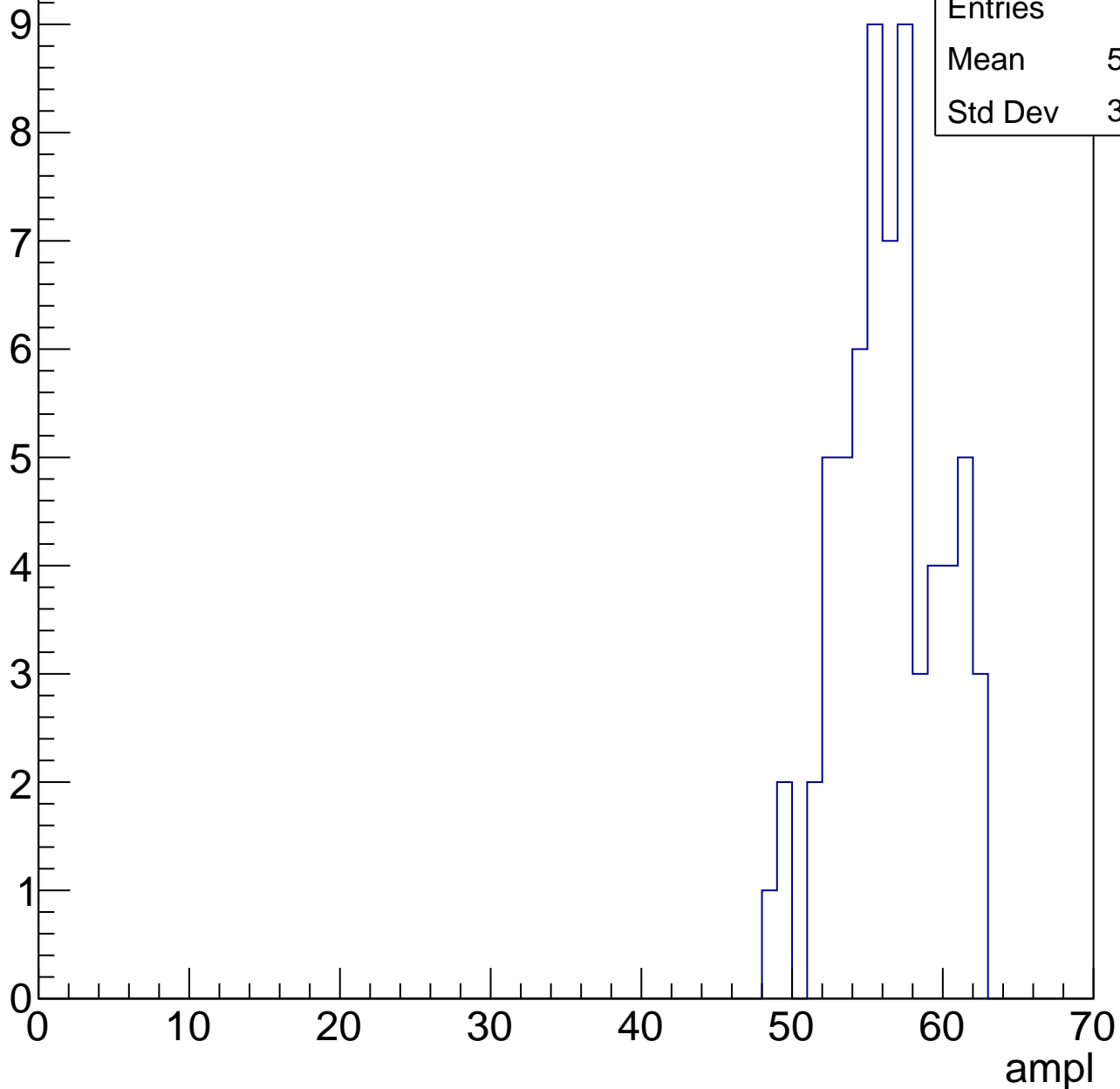
Entries	74
Mean	48.8
Std Dev	3.292



B1L102S, U26-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



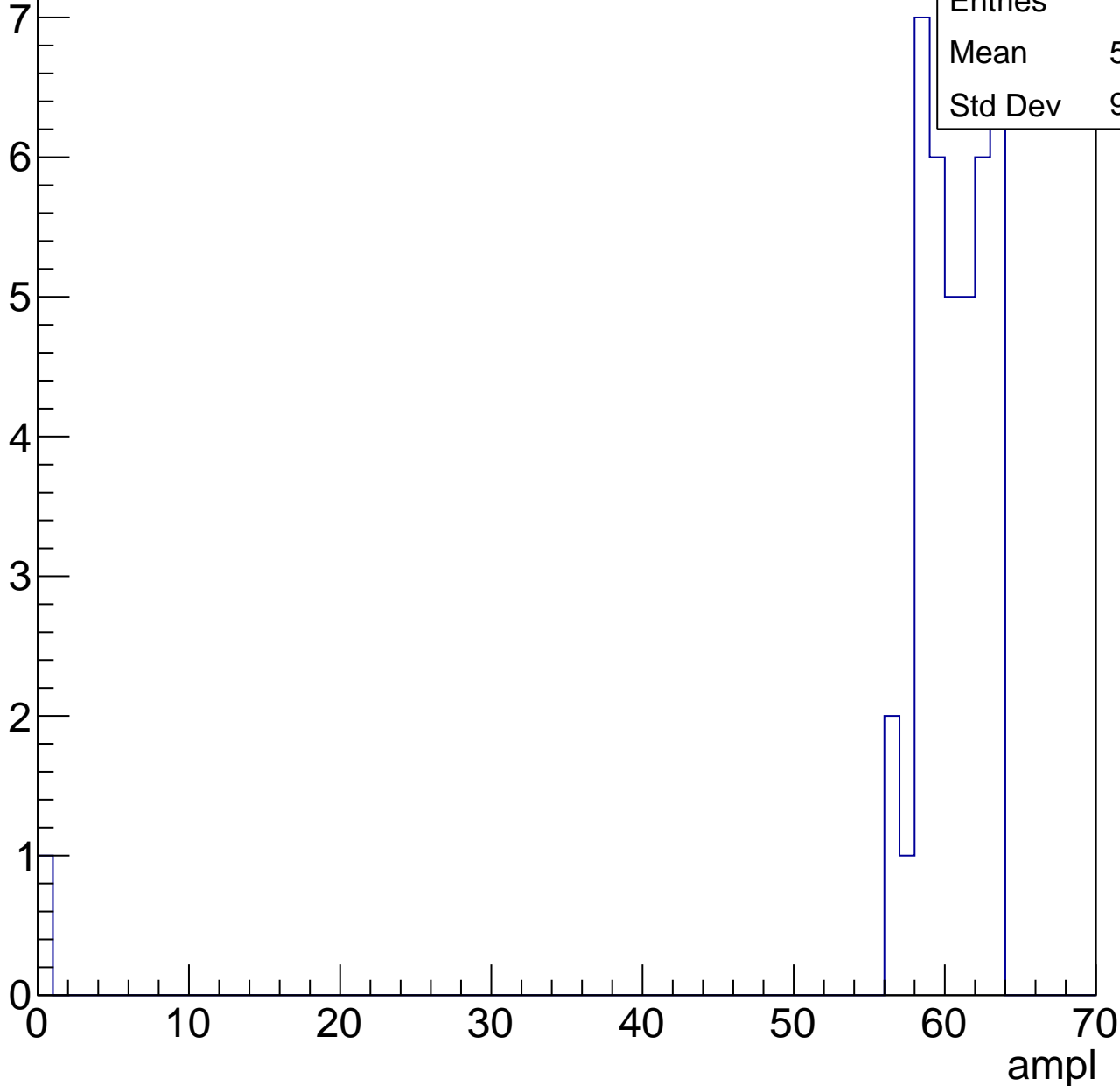
Entries	65
Mean	55.97
Std Dev	3.337

B1L102S, U26-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.67
Std Dev	9.613



B1L102S, U26-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

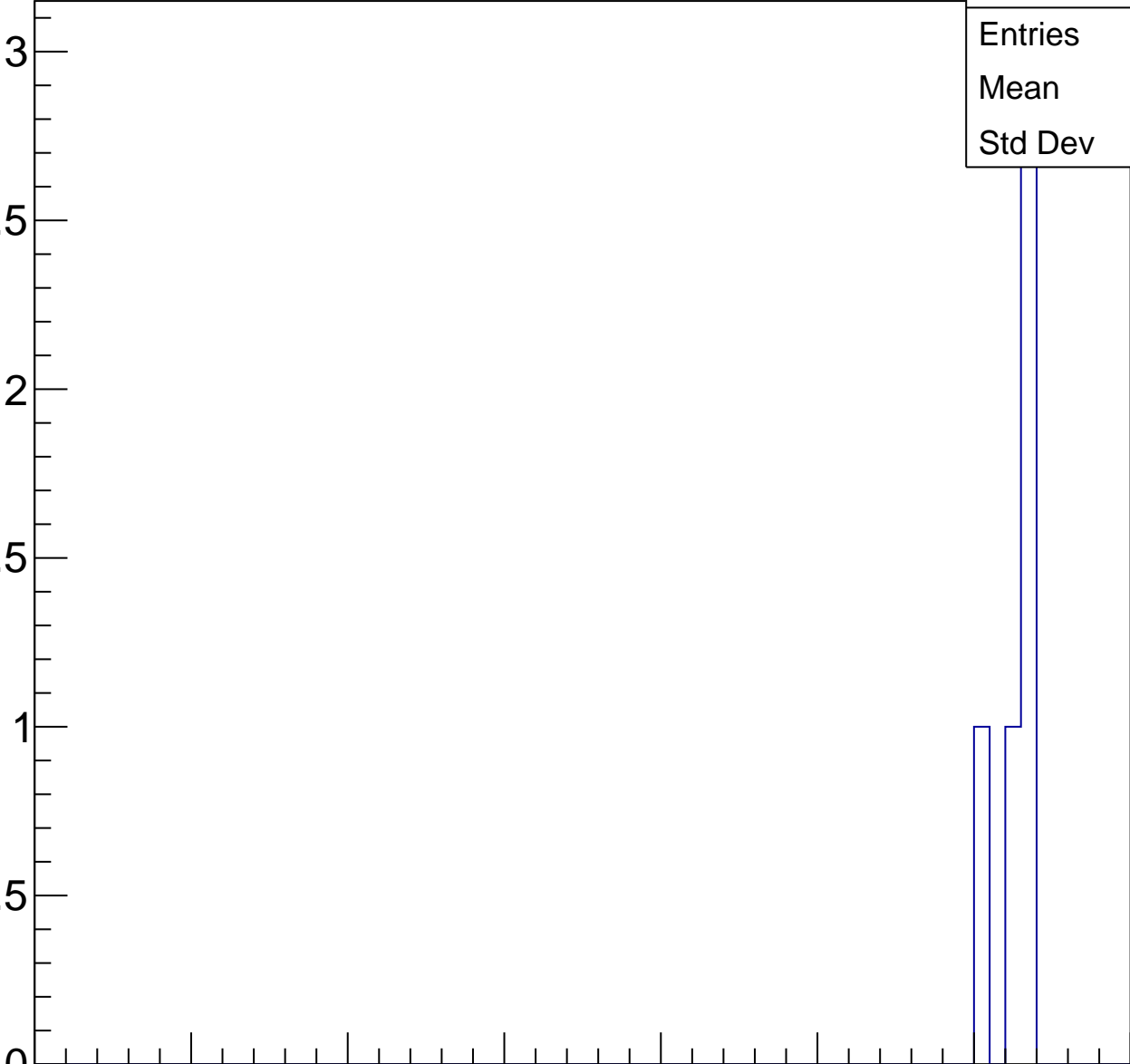
Entry

3
2.5
2
1.5
1
0.5
0

Entries	5
Mean	62.2
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch20, adc0

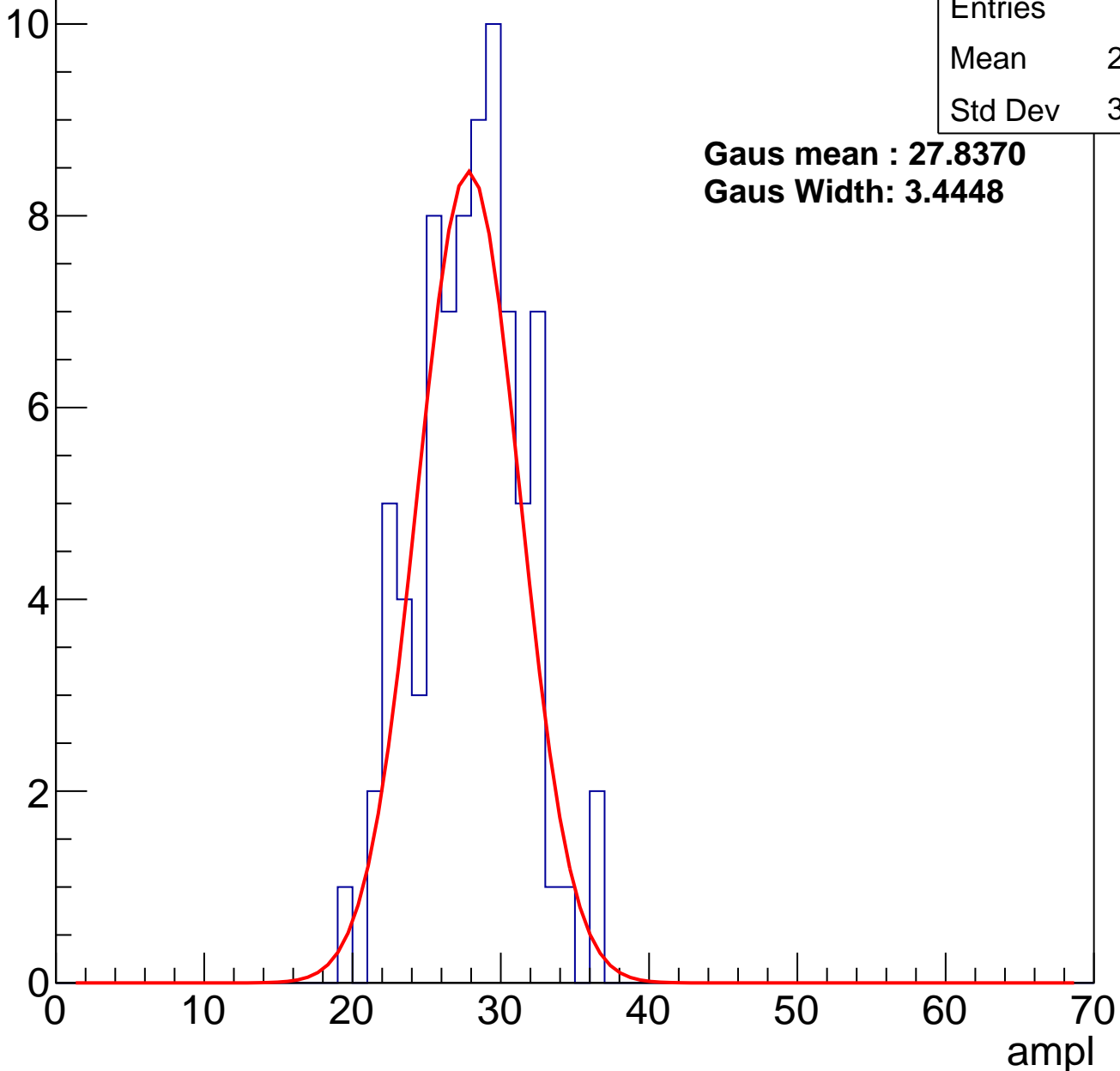
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	27.54
Std Dev	3.503

Gaus mean : 27.8370

Gaus Width: 3.4448

Entry



B1L102S, U26-ch20, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	35.29
Std Dev	3.432

Gaus mean : 35.0575

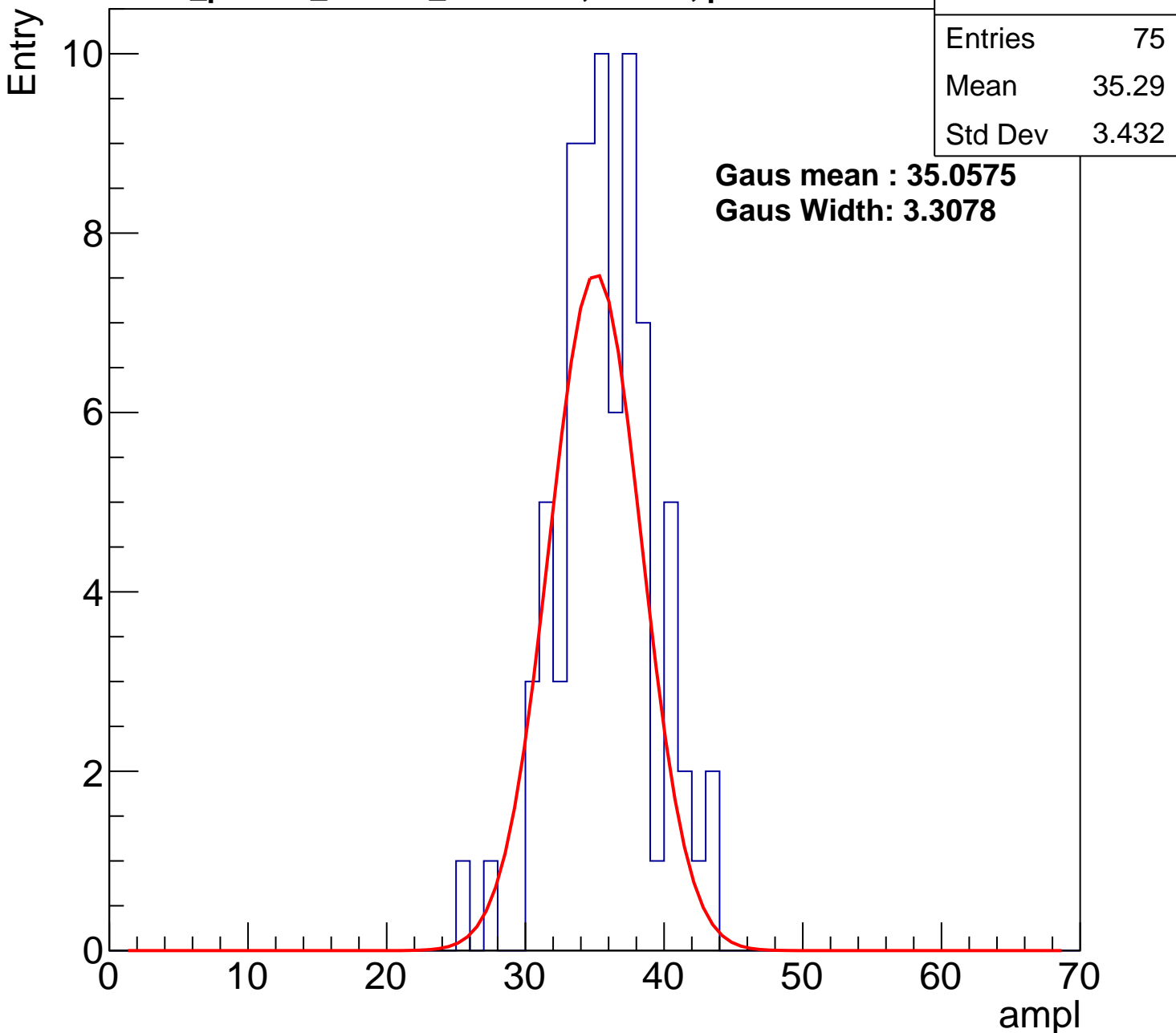
Gaus Width: 3.3078

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch20, adc2

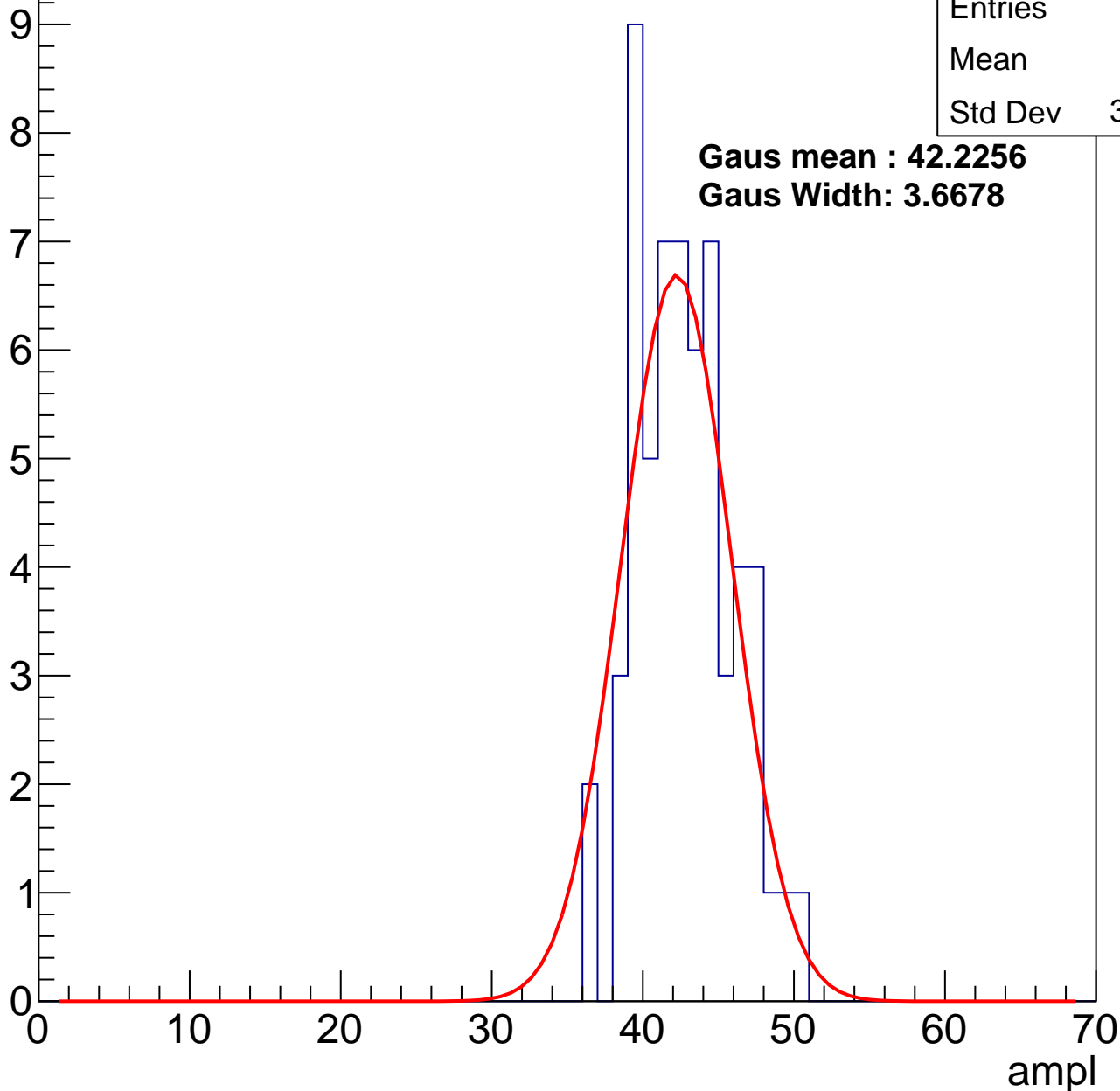
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.3
Std Dev	3.153

Gaus mean : 42.2256

Gaus Width: 3.6678

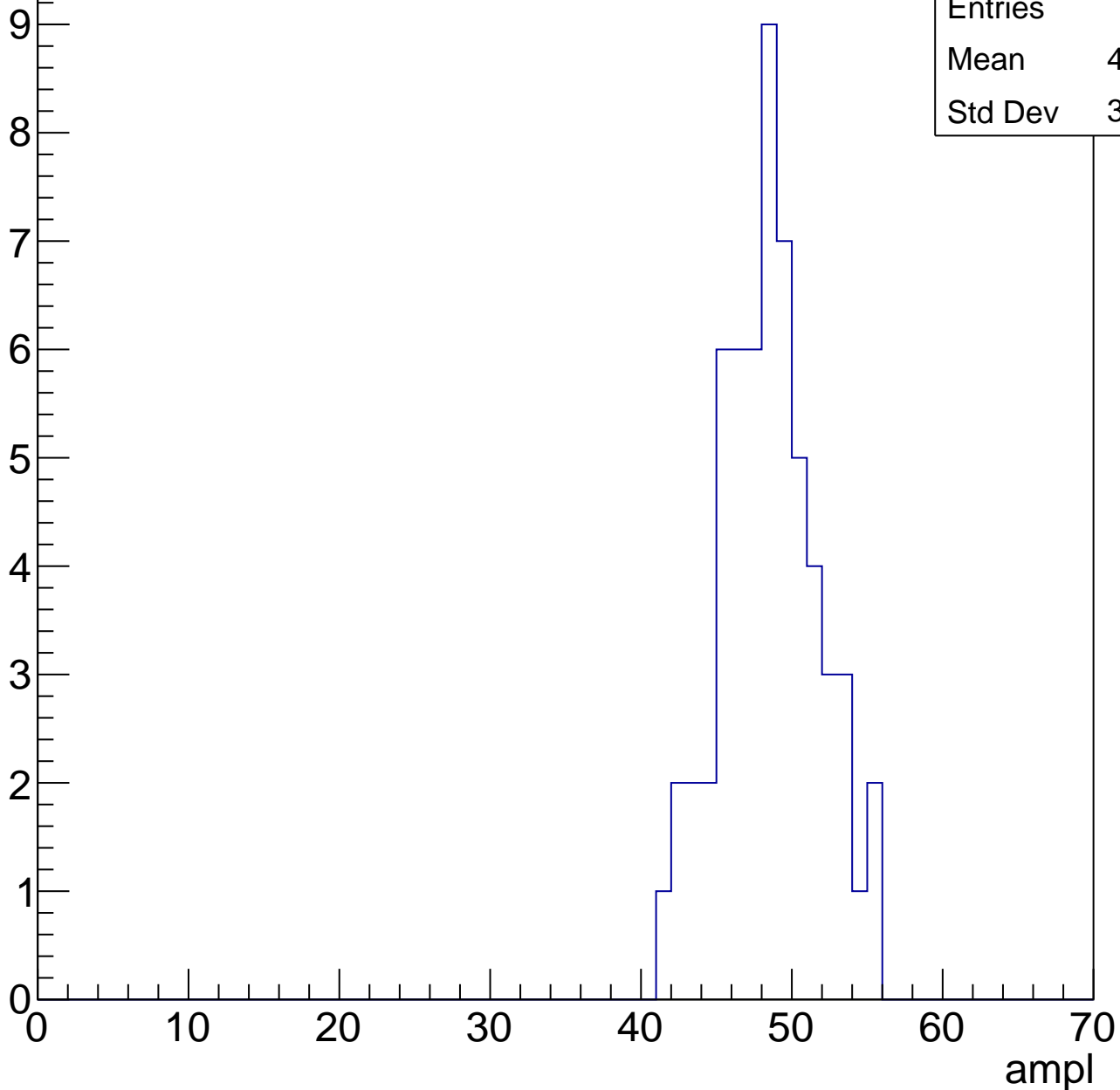


B1L102S, U26-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.05
Std Dev	3.207

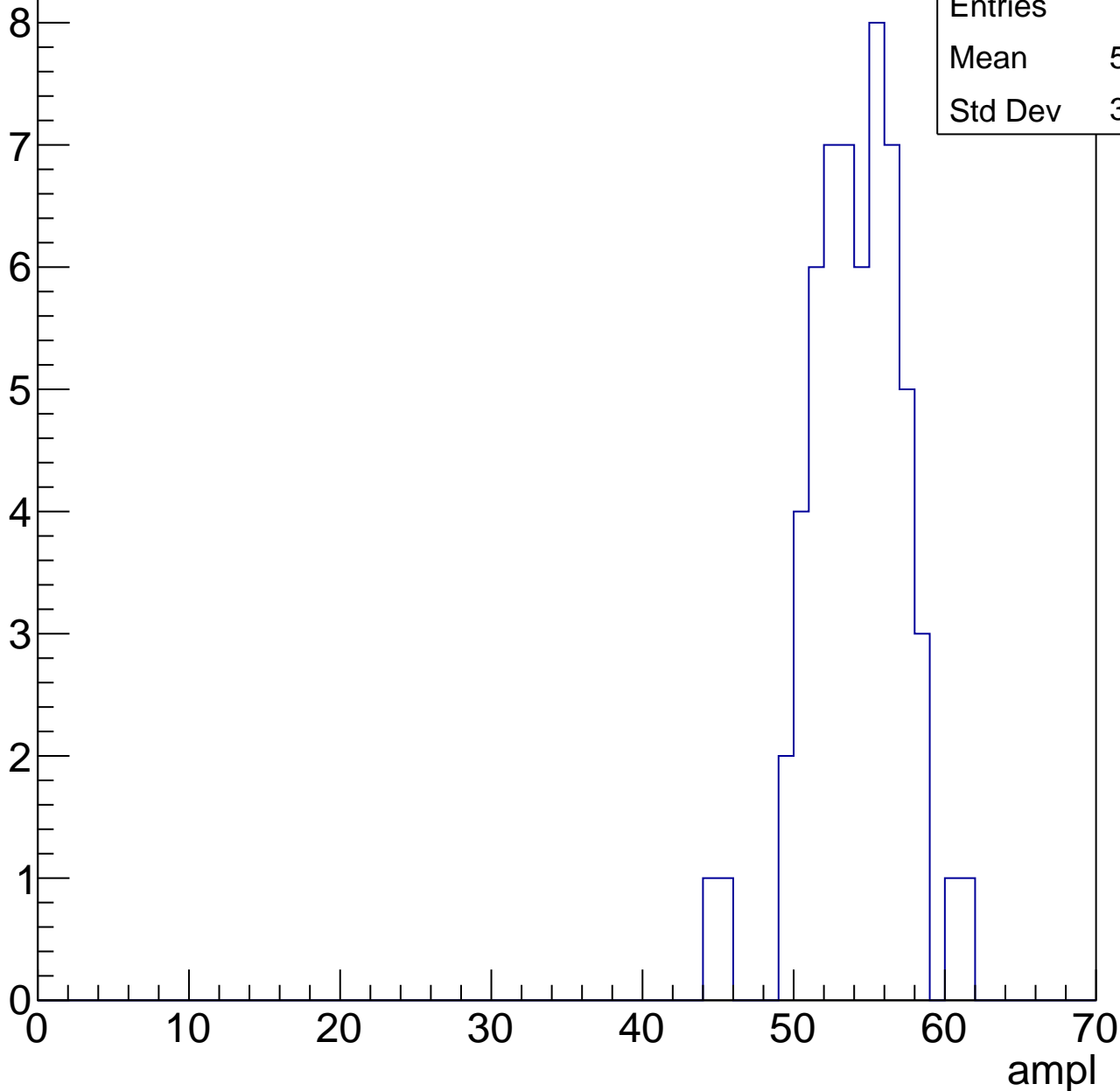


B1L102S, U26-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.63
Std Dev	3.162



B1L102S, U26-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

Entries 57

Mean 59.53

Std Dev 2.392

8

6

4

2

0

0

10

20

30

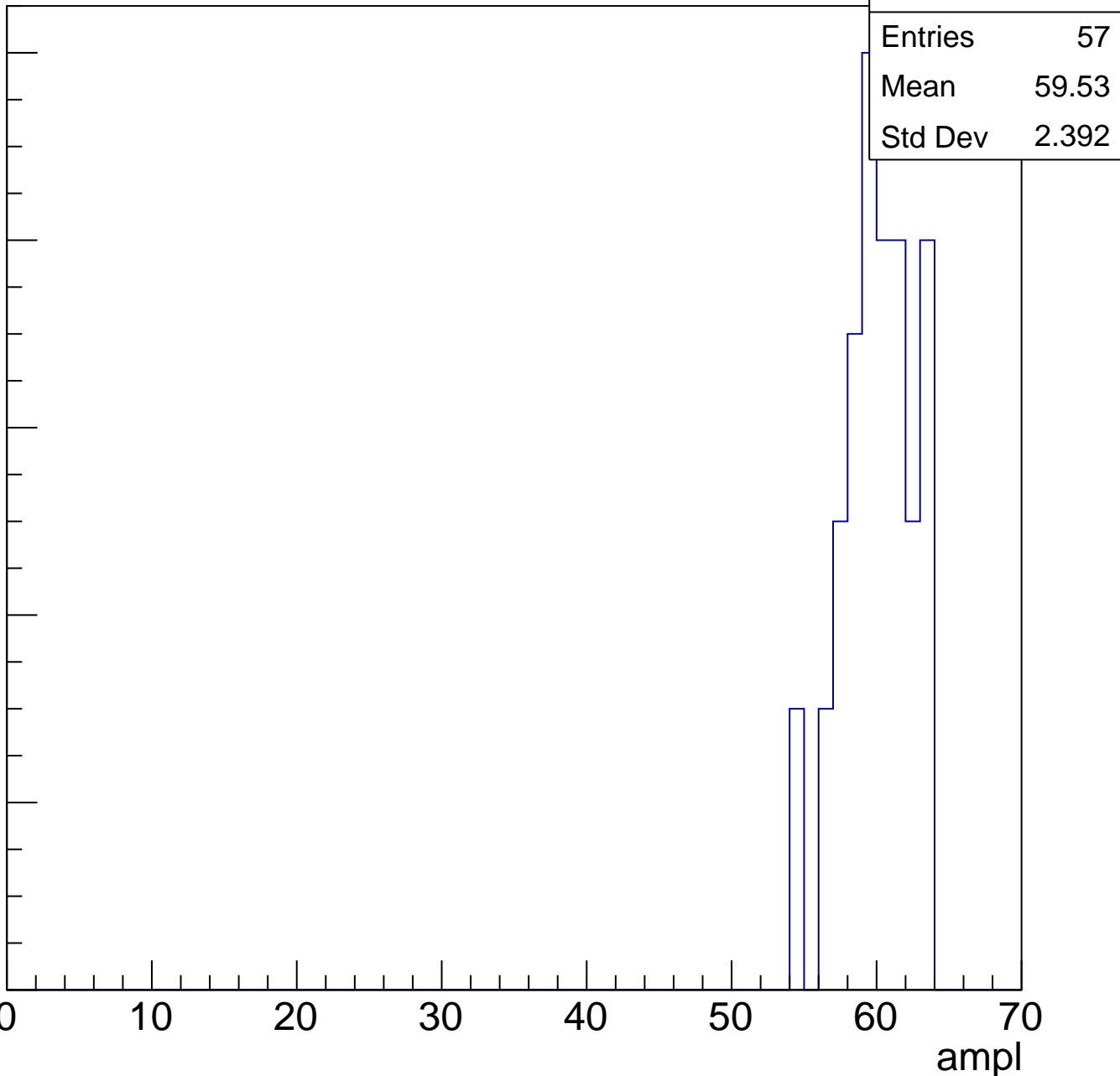
40

50

60

70

ampl

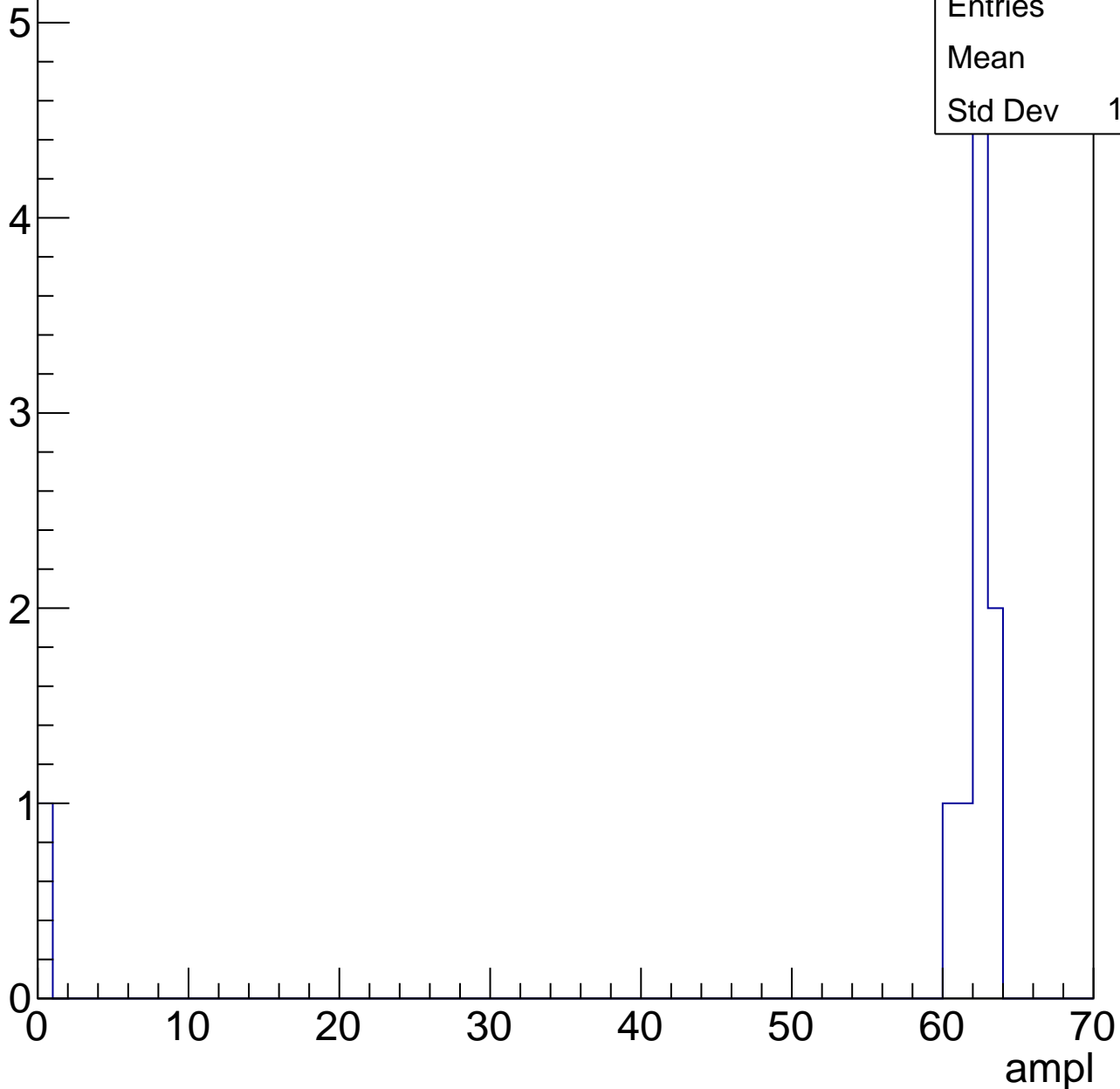


B1L102S, U26-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

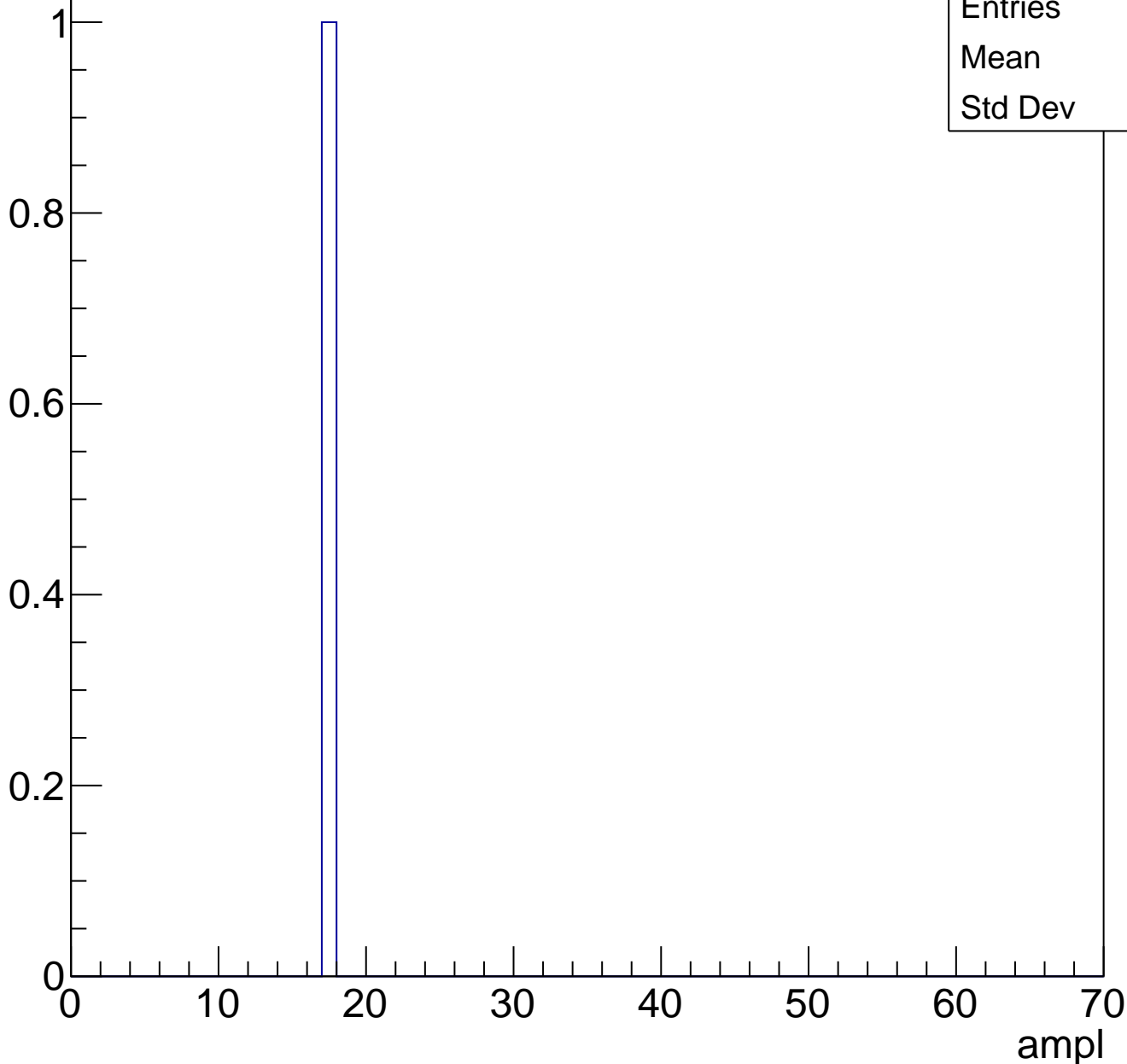
Entries	10
Mean	55.7
Std Dev	18.59



B1L102S, U26-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	17
Std Dev	0

B1L102S, U26-ch21, adc0

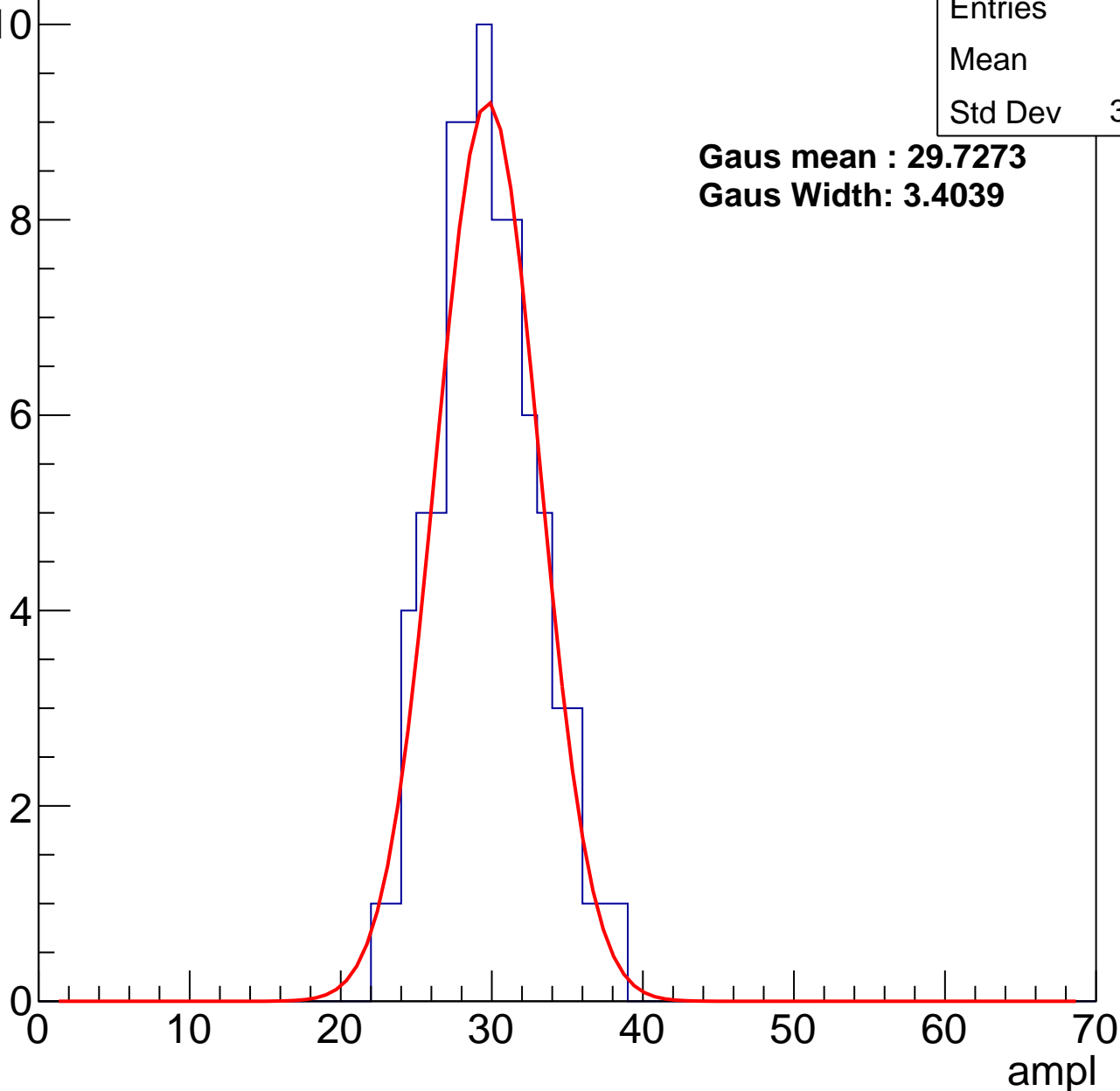
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.3
Std Dev	3.352

Gaus mean : 29.7273

Gaus Width: 3.4039



B1L102S, U26-ch21, adc1

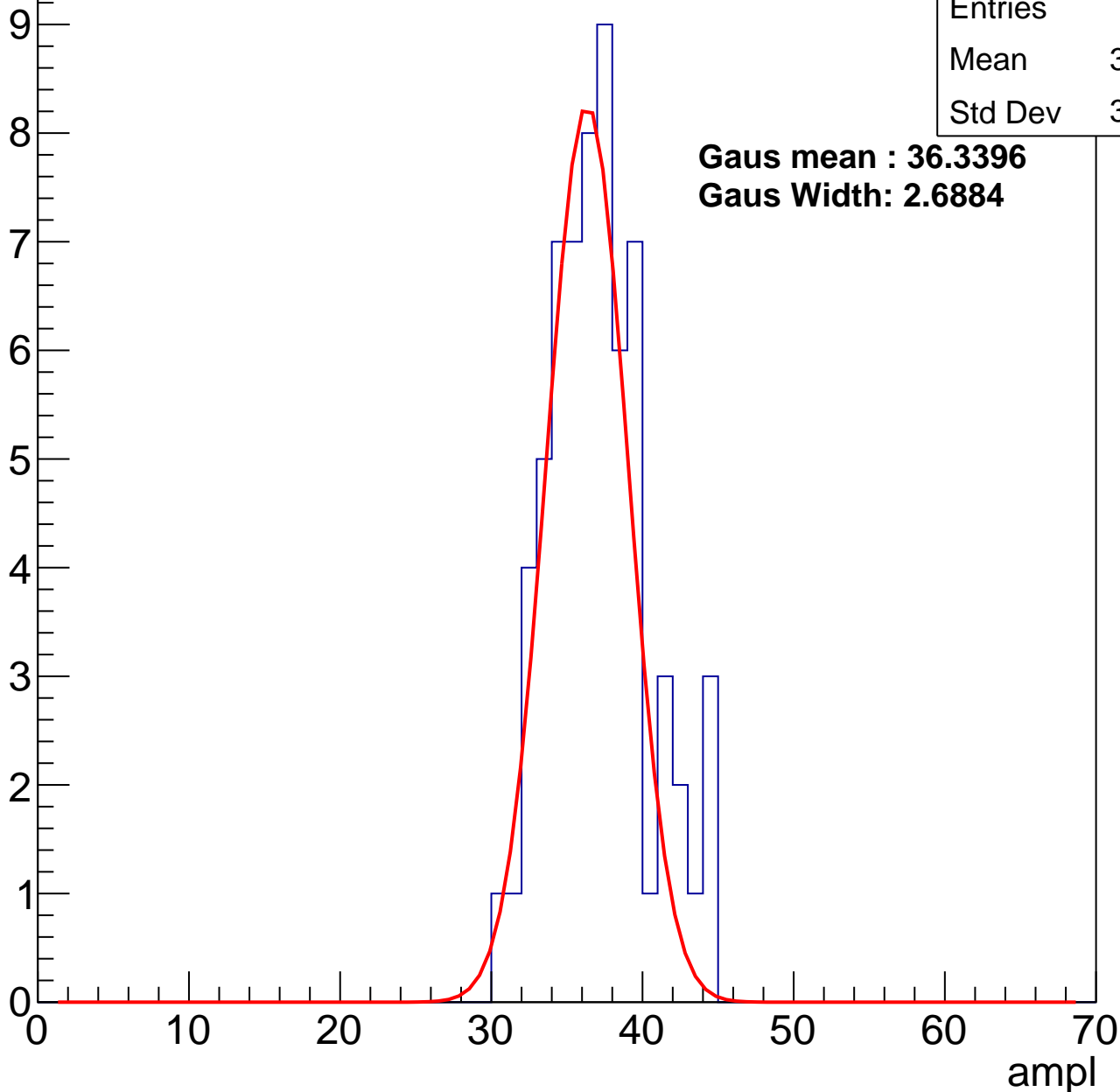
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.63
Std Dev	3.232

Gaus mean : 36.3396

Gaus Width: 2.6884



B1L102S, U26-ch21, adc2

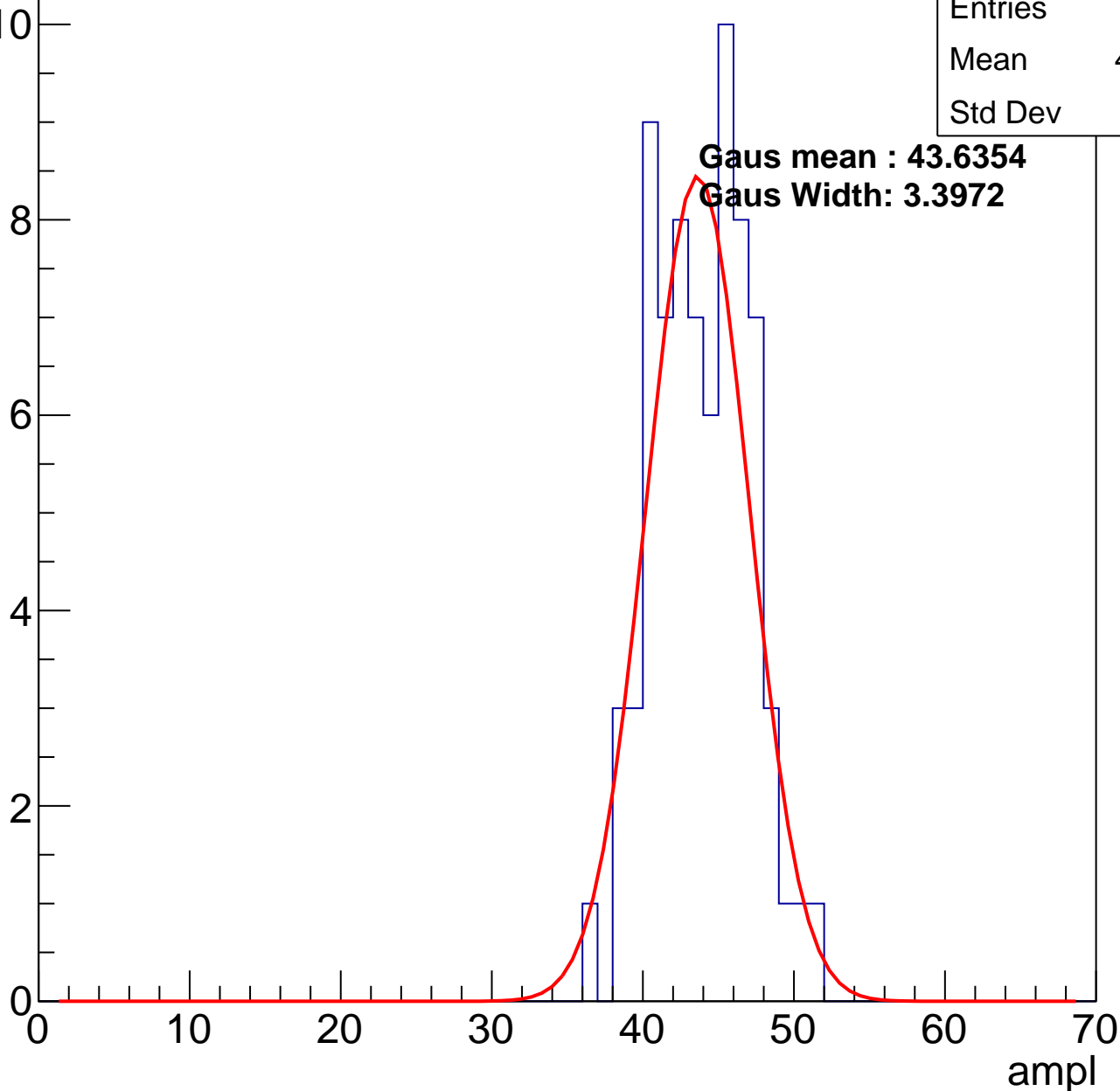
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.41
Std Dev	3.12

Gaus mean : 43.6354

Gaus Width: 3.3972

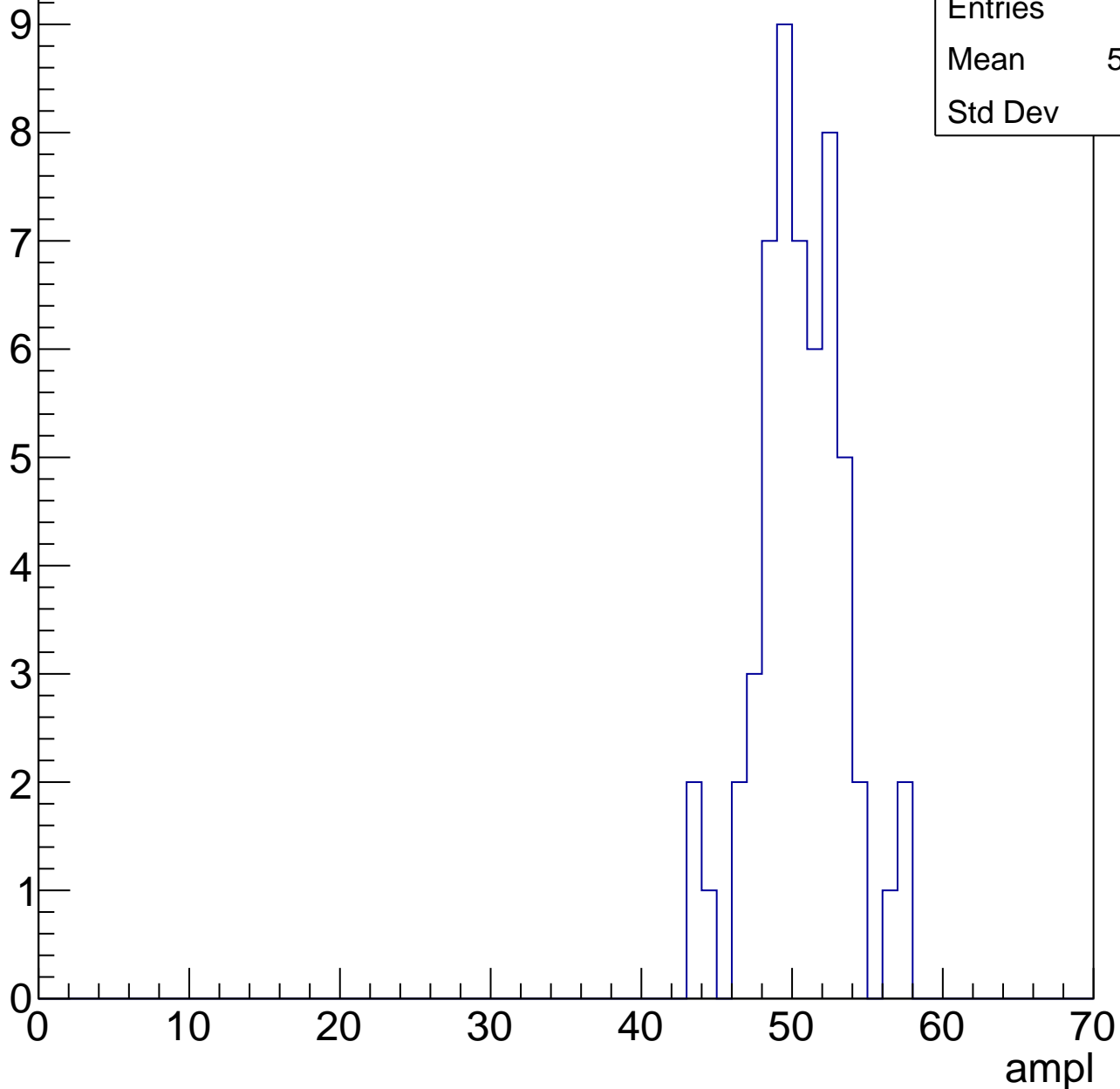


B1L102S, U26-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.09
Std Dev	2.95

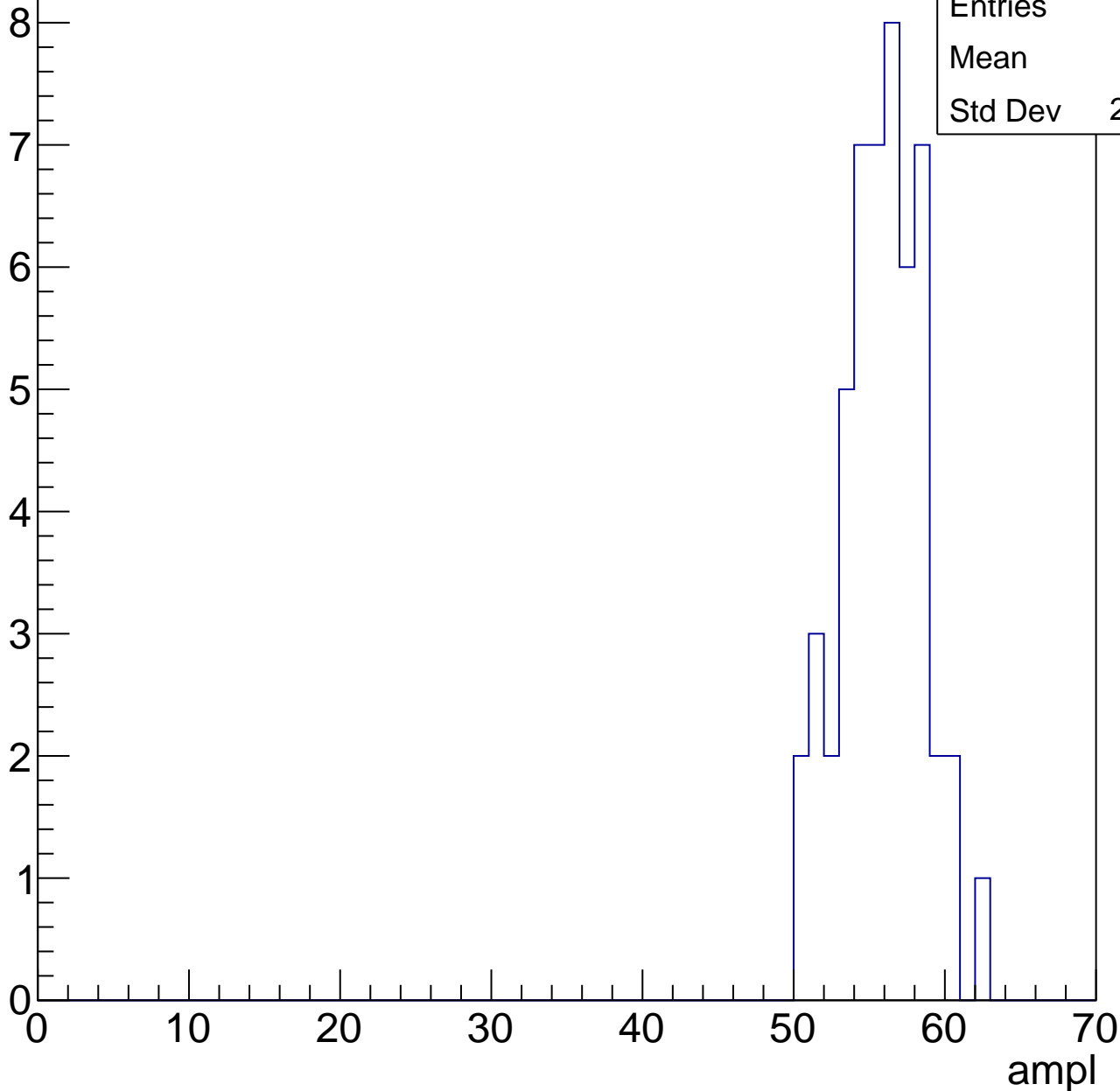


B1L102S, U26-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.4
Std Dev	2.633

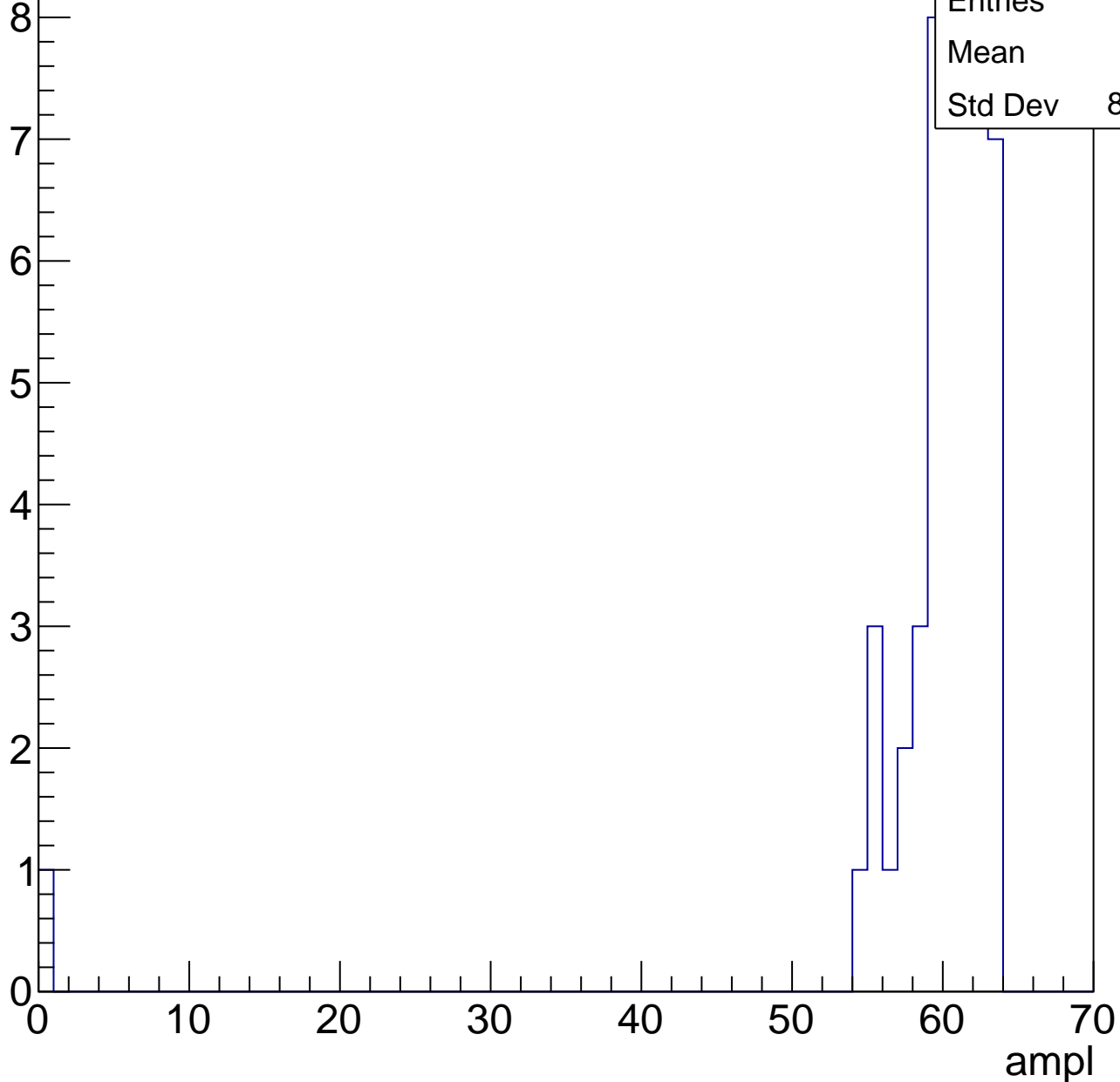


B1L102S, U26-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.8
Std Dev	8.713



B1L102S, U26-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch22, adc0

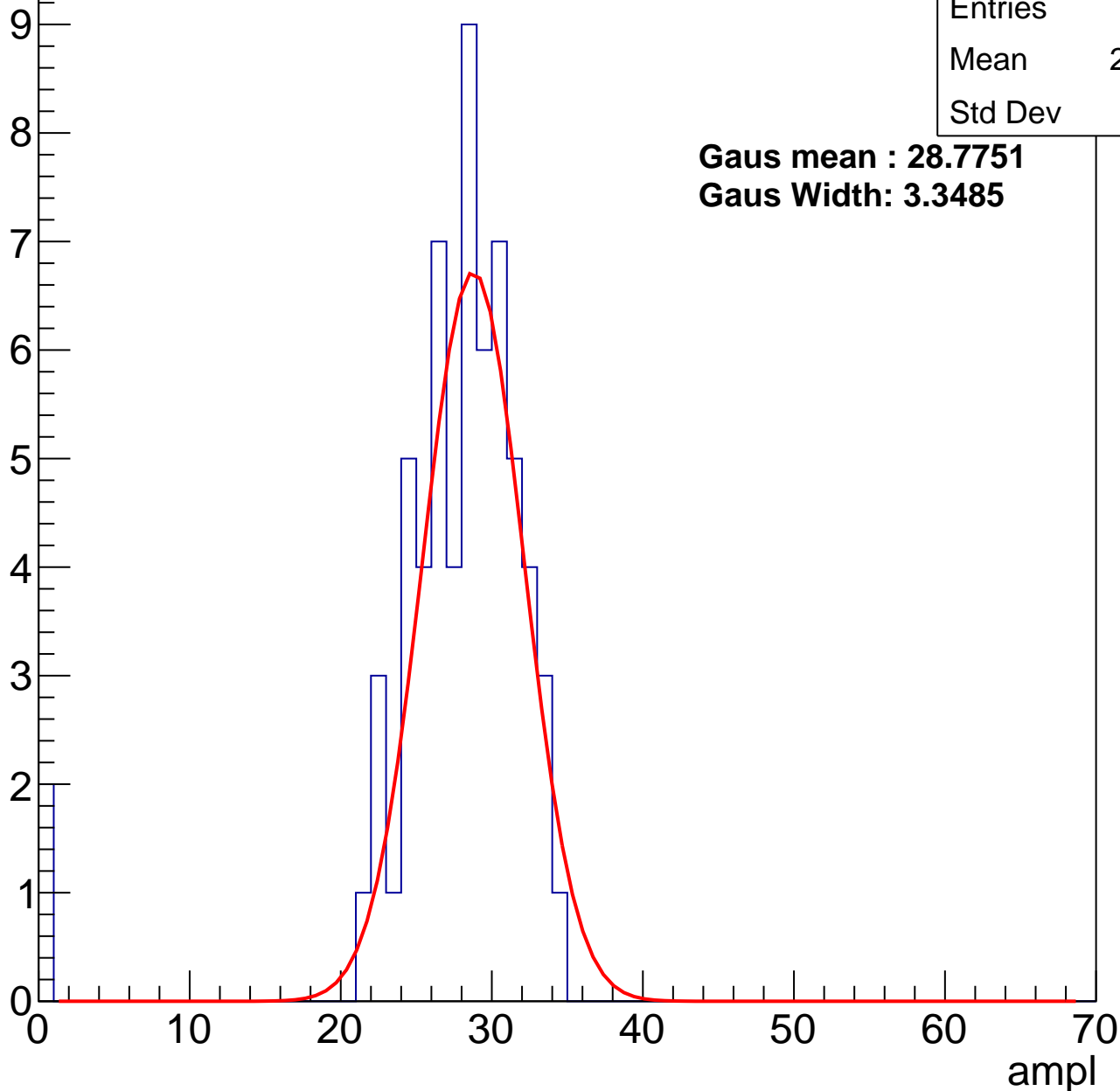
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	26.97
Std Dev	5.8

Gaus mean : 28.7751

Gaus Width: 3.3485



B1L102S, U26-ch22, adc1

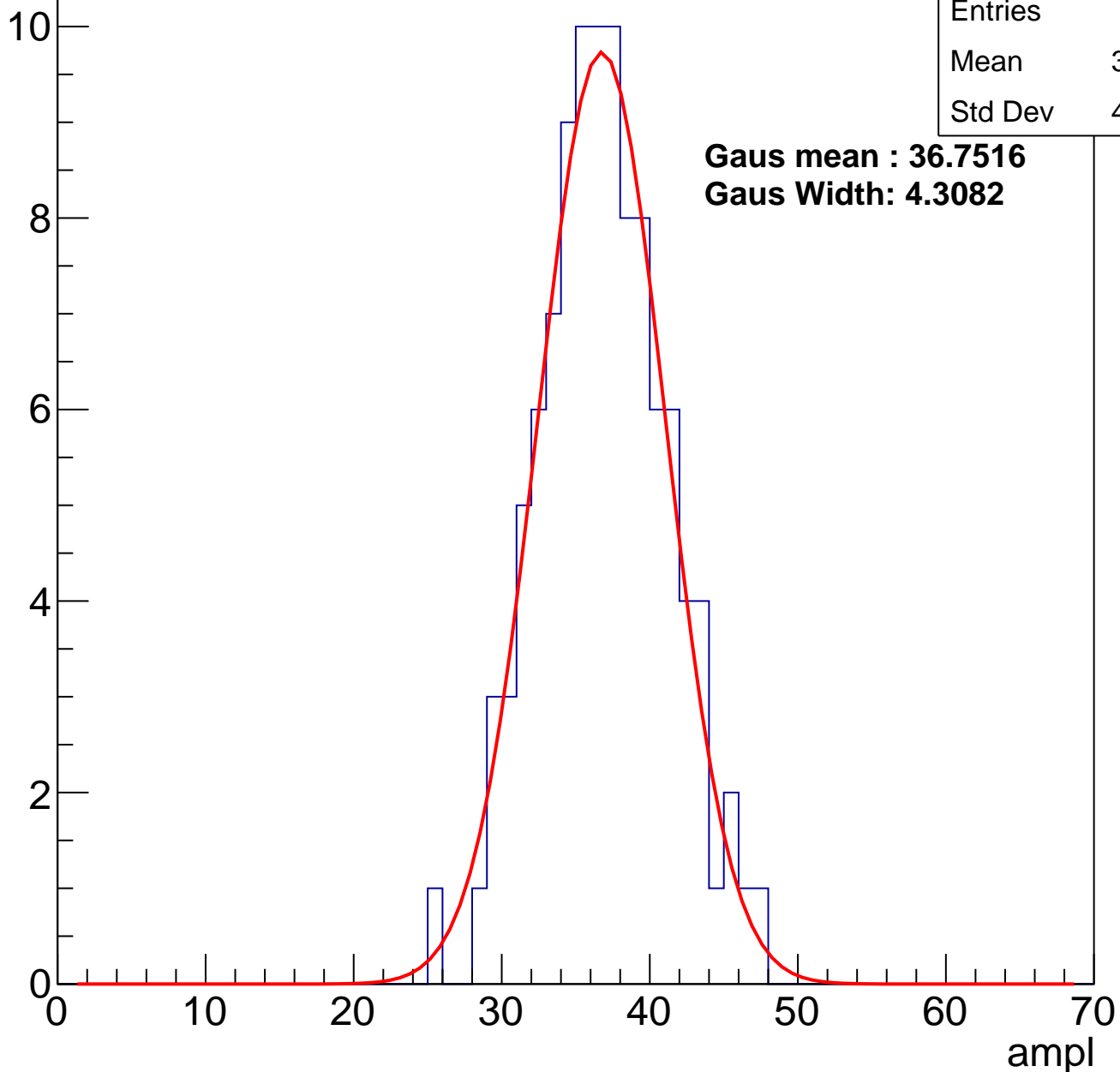
calib_packv5_042523_0143.root, FC#11, port A2

Entries	106
Mean	36.44
Std Dev	4.229

Gaus mean : 36.7516

Gaus Width: 4.3082

Entry



B1L102S, U26-ch22, adc2

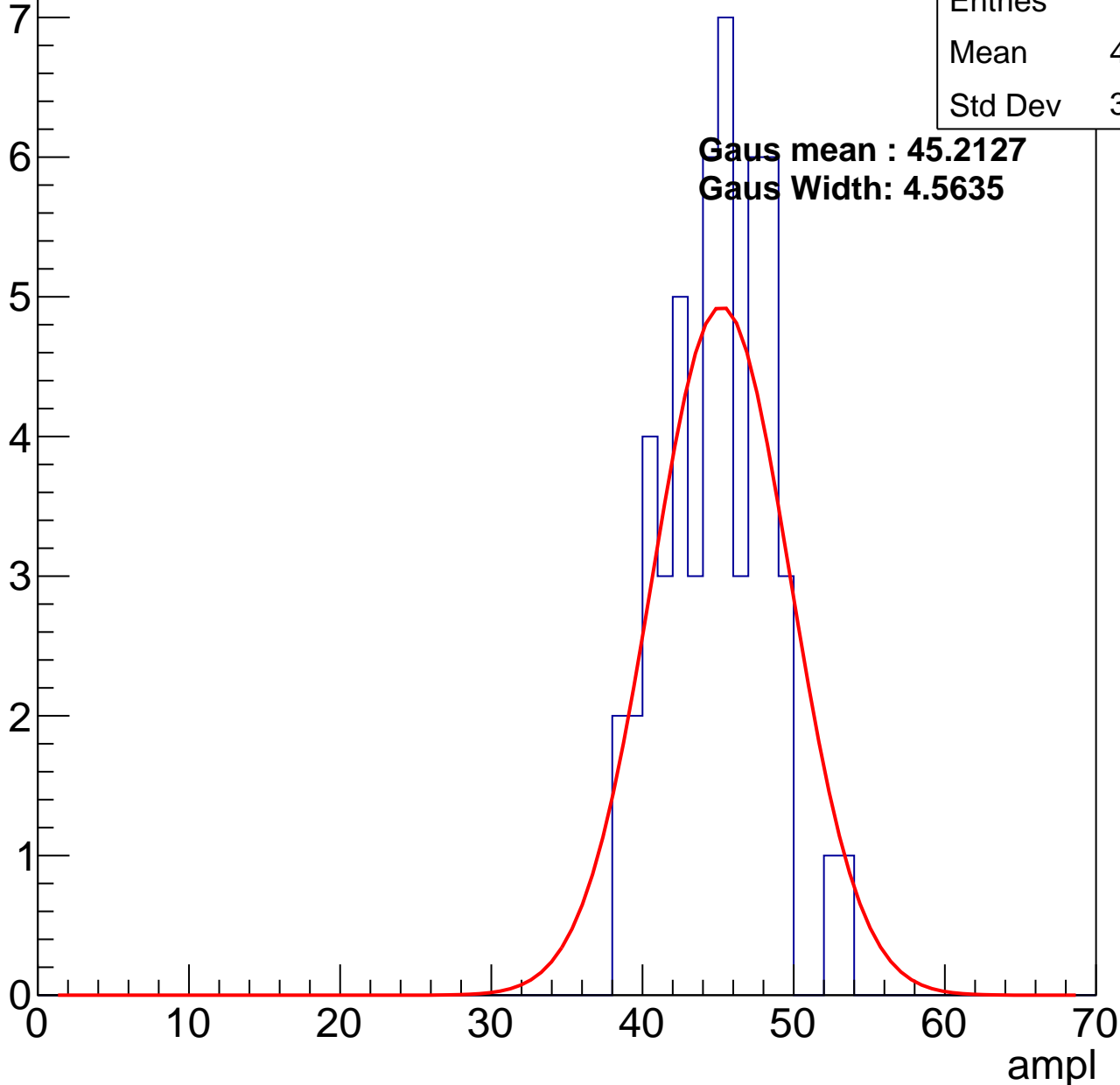
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	44.52
Std Dev	3.428

Gaus mean : 45.2127

Gaus Width: 4.5635



B1L102S, U26-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

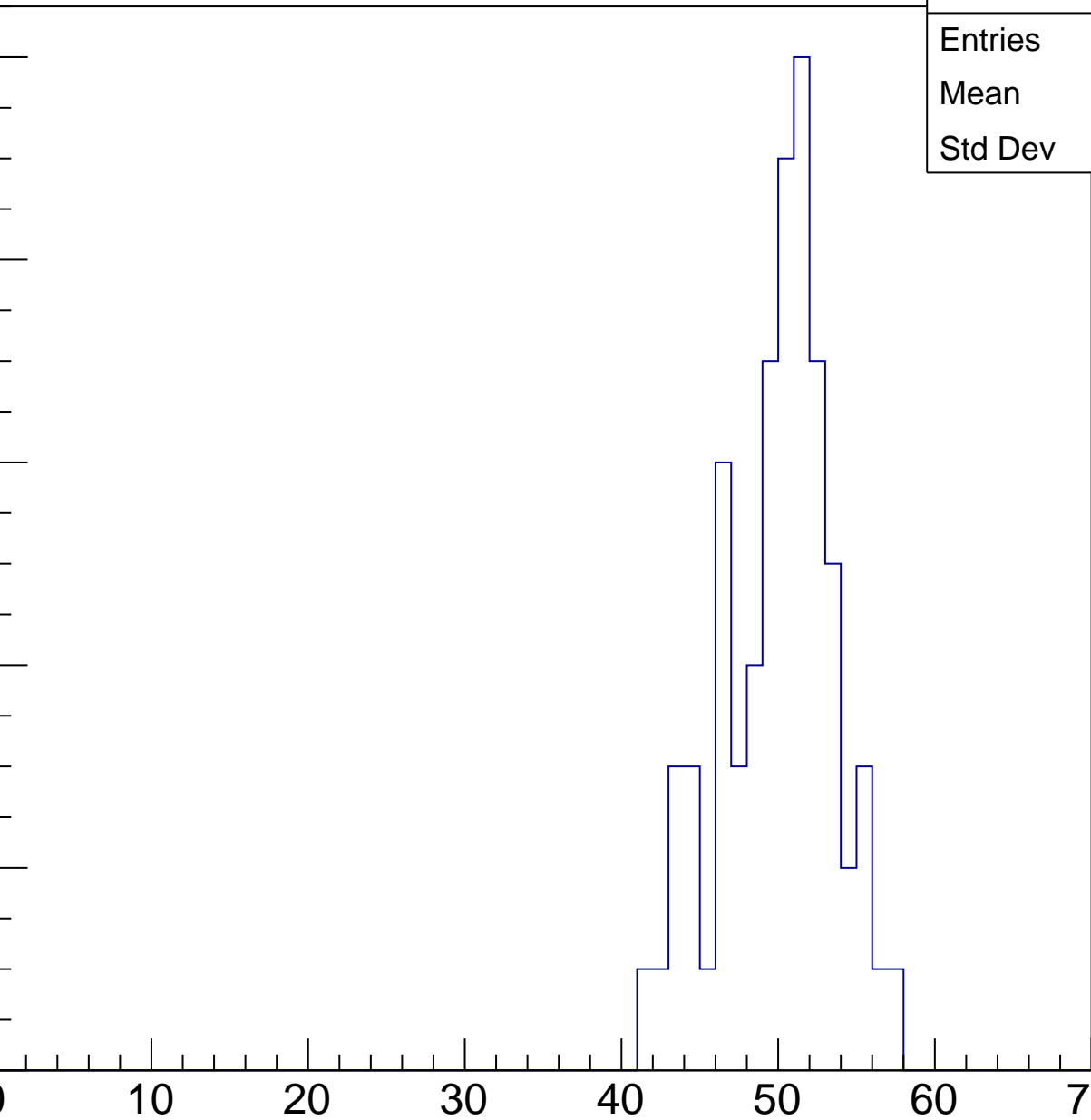
Entries	67
Mean	49.49
Std Dev	3.517

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

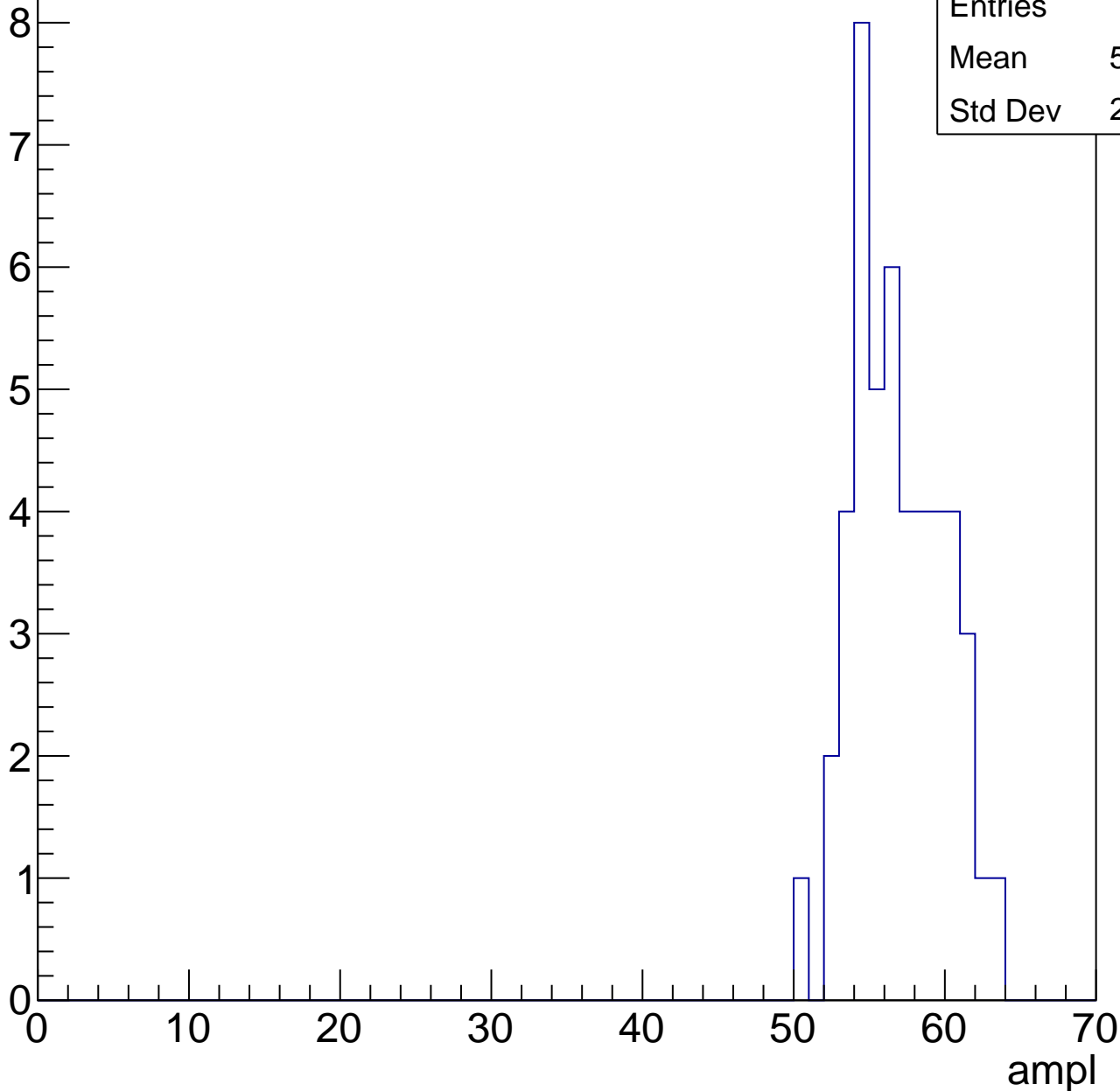


B1L102S, U26-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.45
Std Dev	2.959

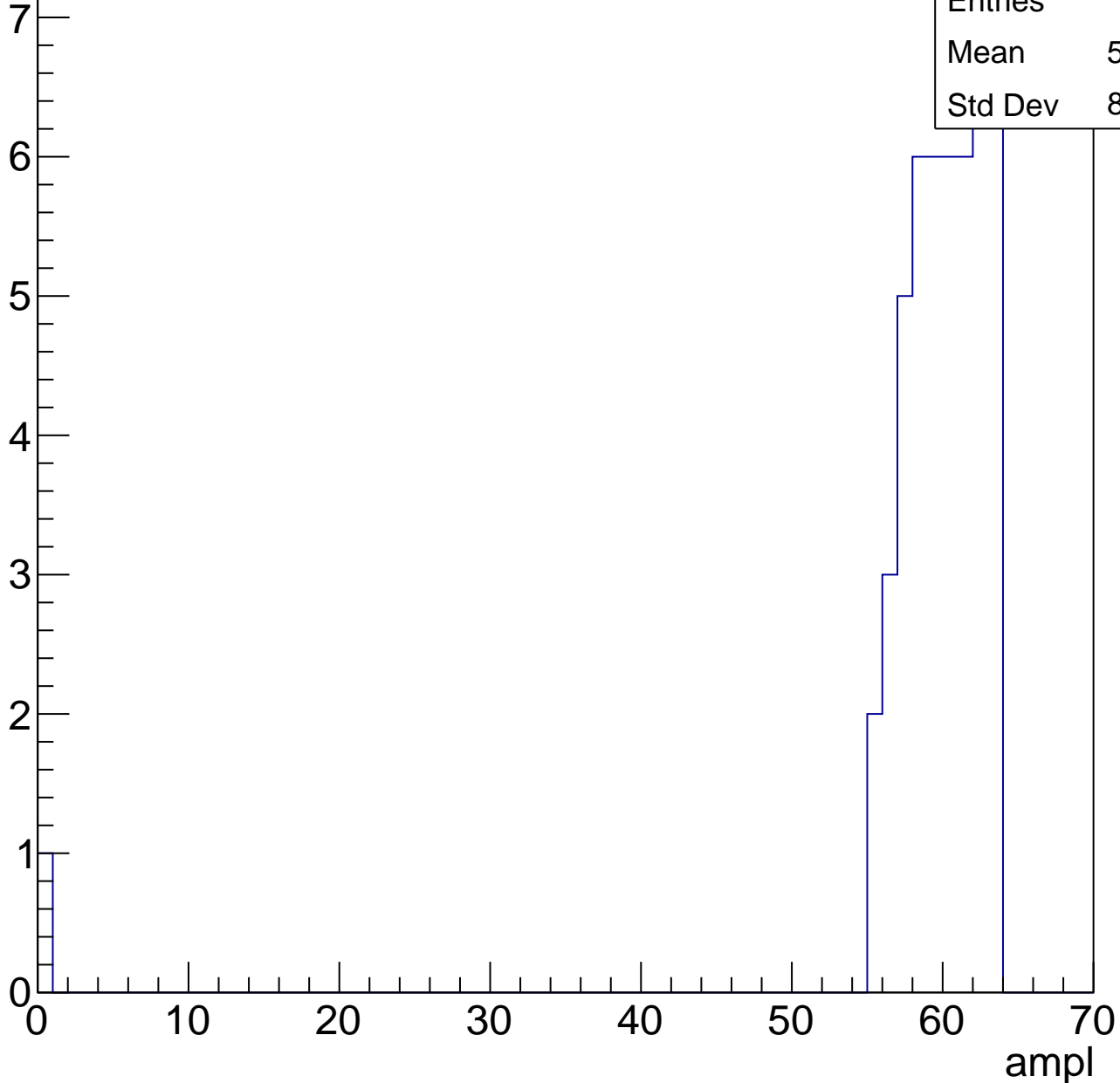


B1L102S, U26-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

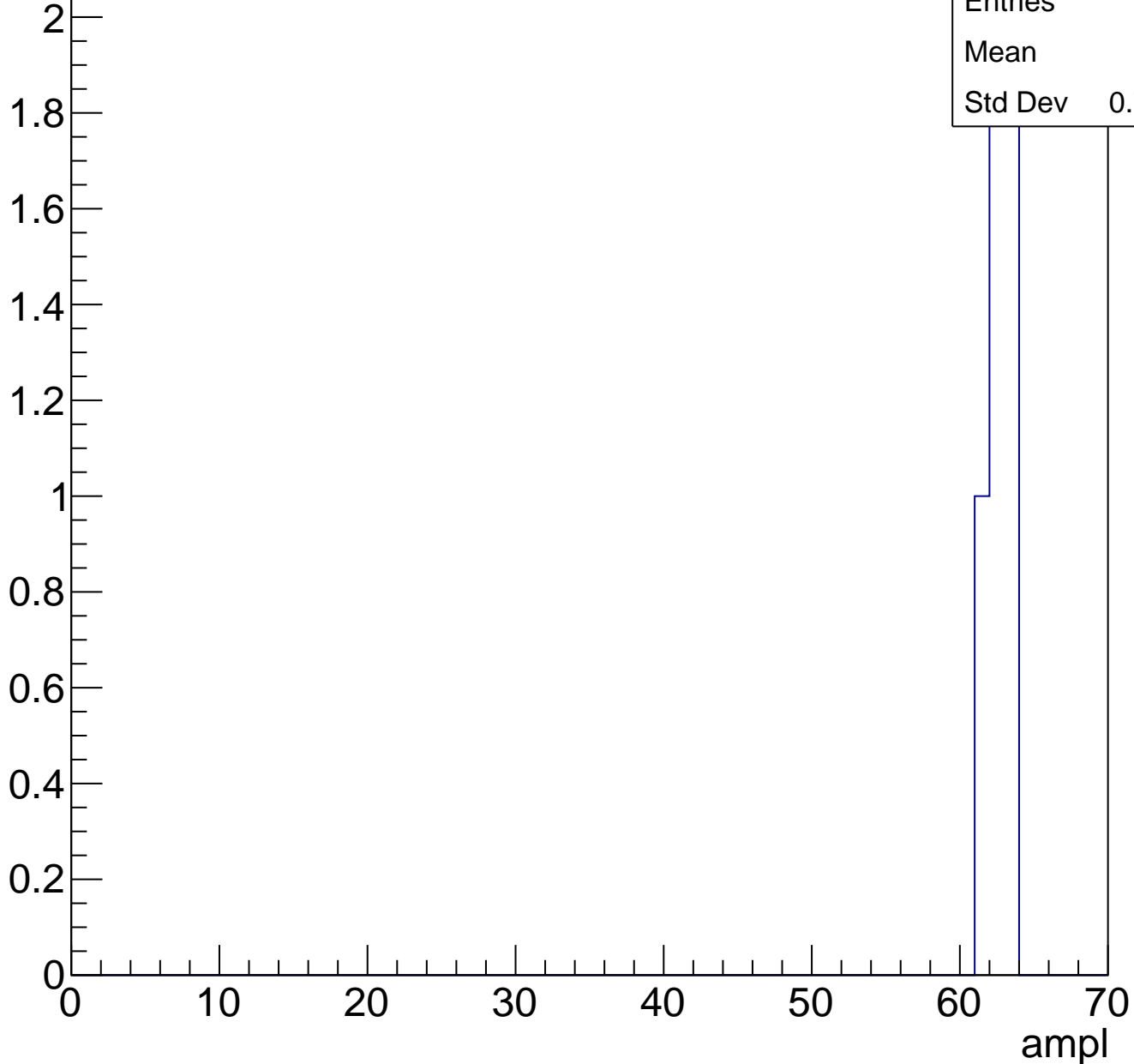
Entries	49
Mean	58.49
Std Dev	8.758



B1L102S, U26-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

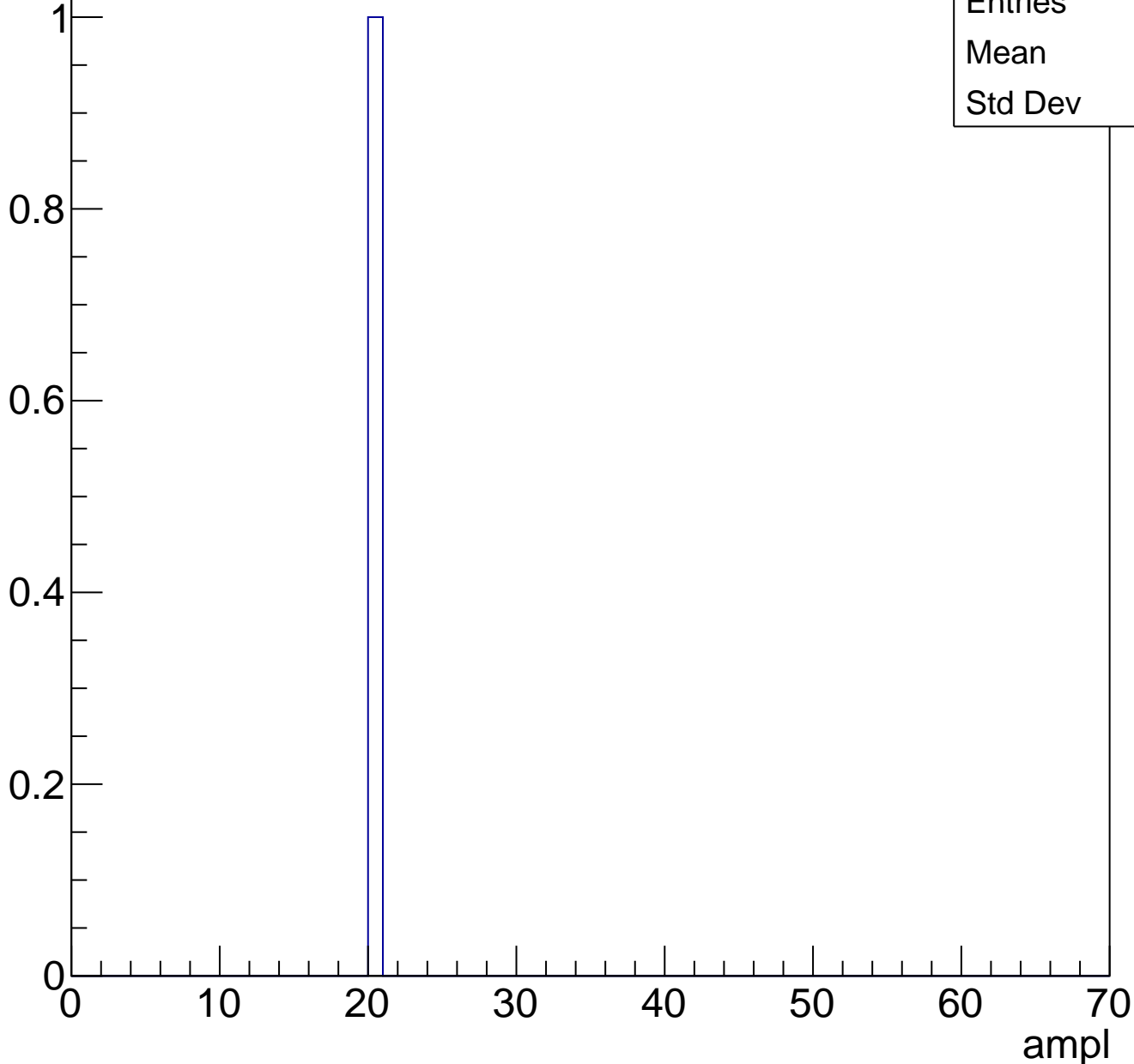
Entry



B1L102S, U26-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch23, adc0

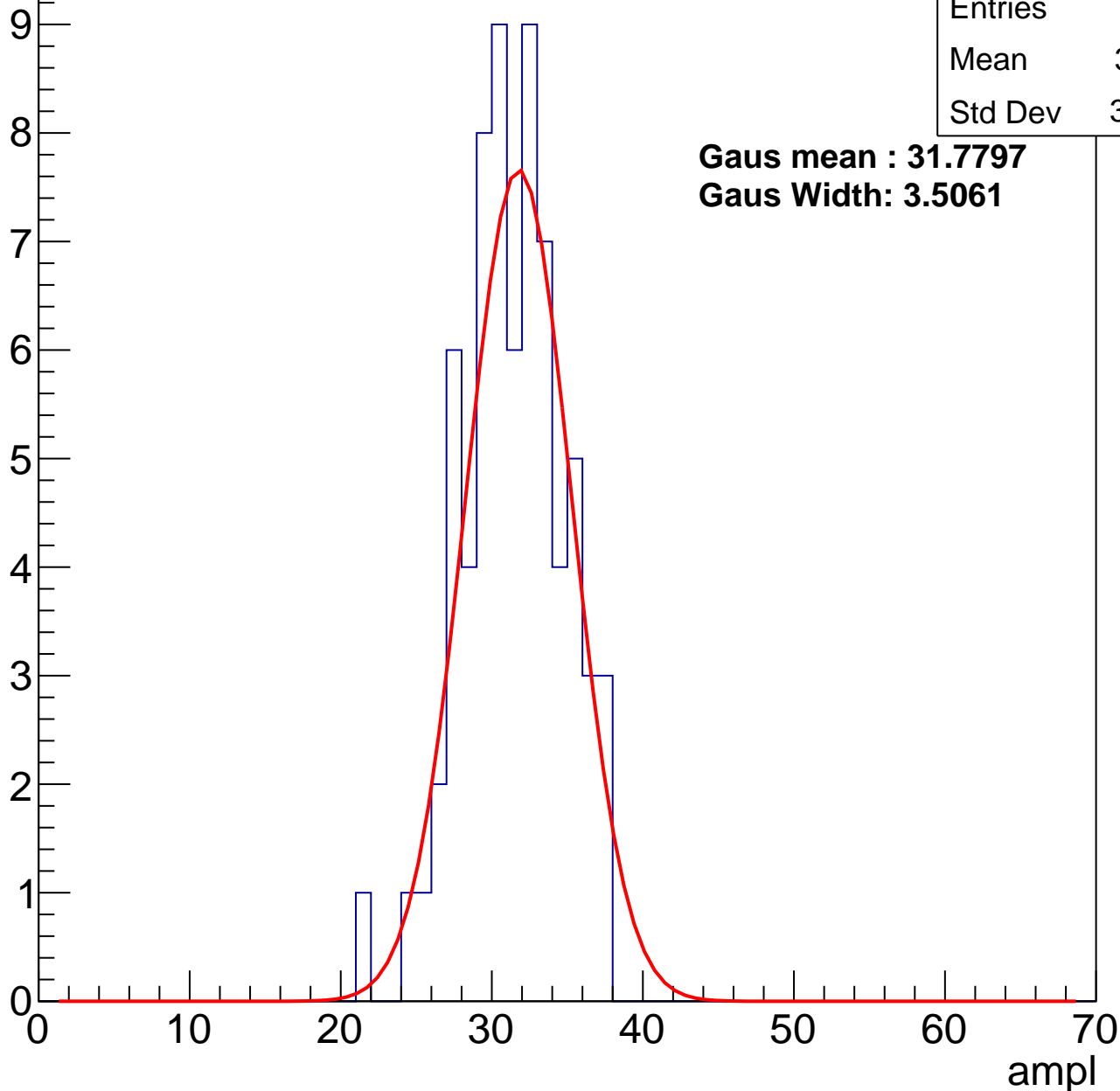
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.91
Std Dev	3.283

Gaus mean : 31.7797

Gaus Width: 3.5061



B1L102S, U26-ch23, adc1

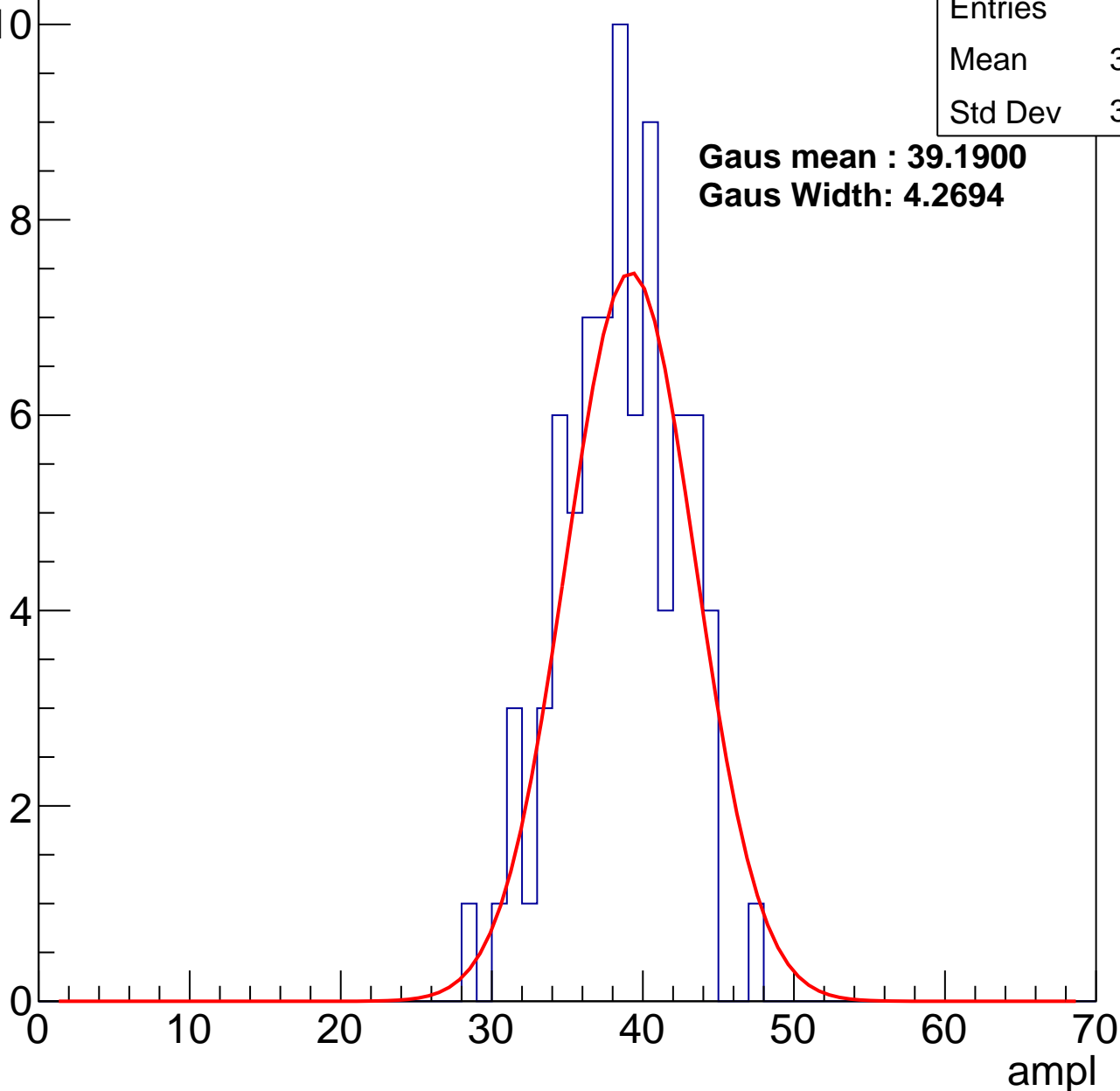
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	38.04
Std Dev	3.786

Gaus mean : 39.1900

Gaus Width: 4.2694



B1L102S, U26-ch23, adc2

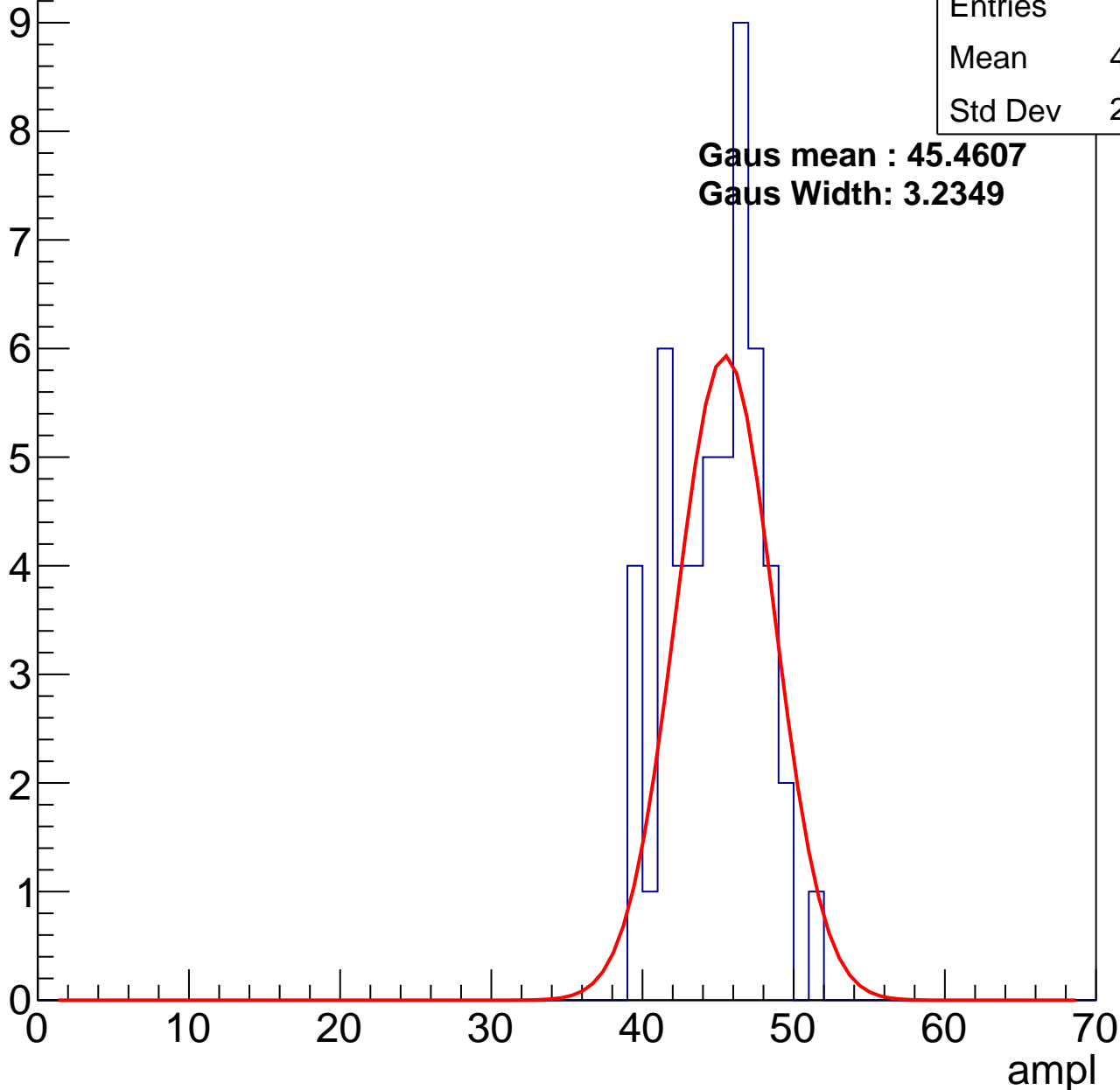
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	44.39
Std Dev	2.938

Gaus mean : 45.4607

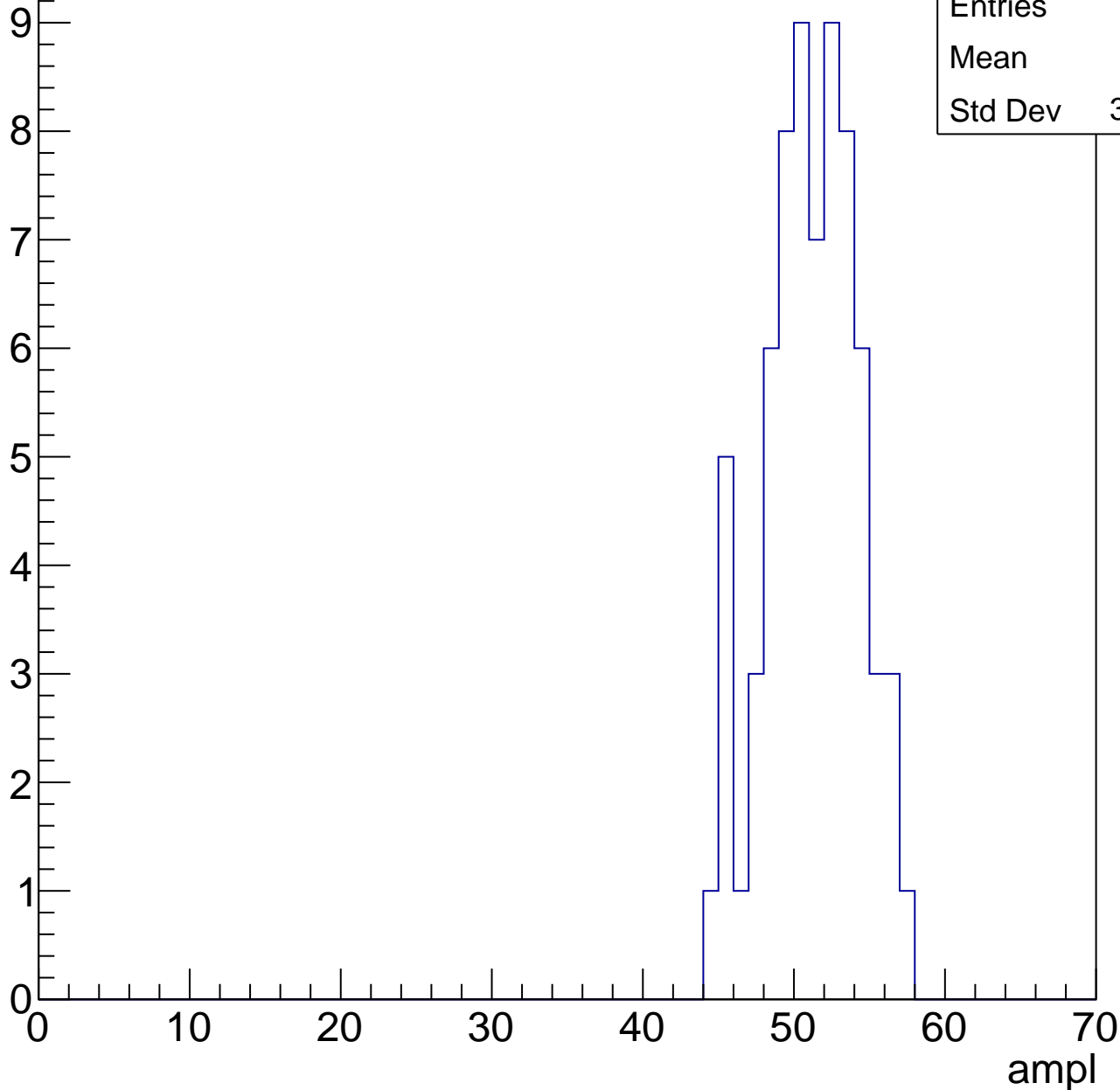
Gaus Width: 3.2349



B1L102S, U26-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

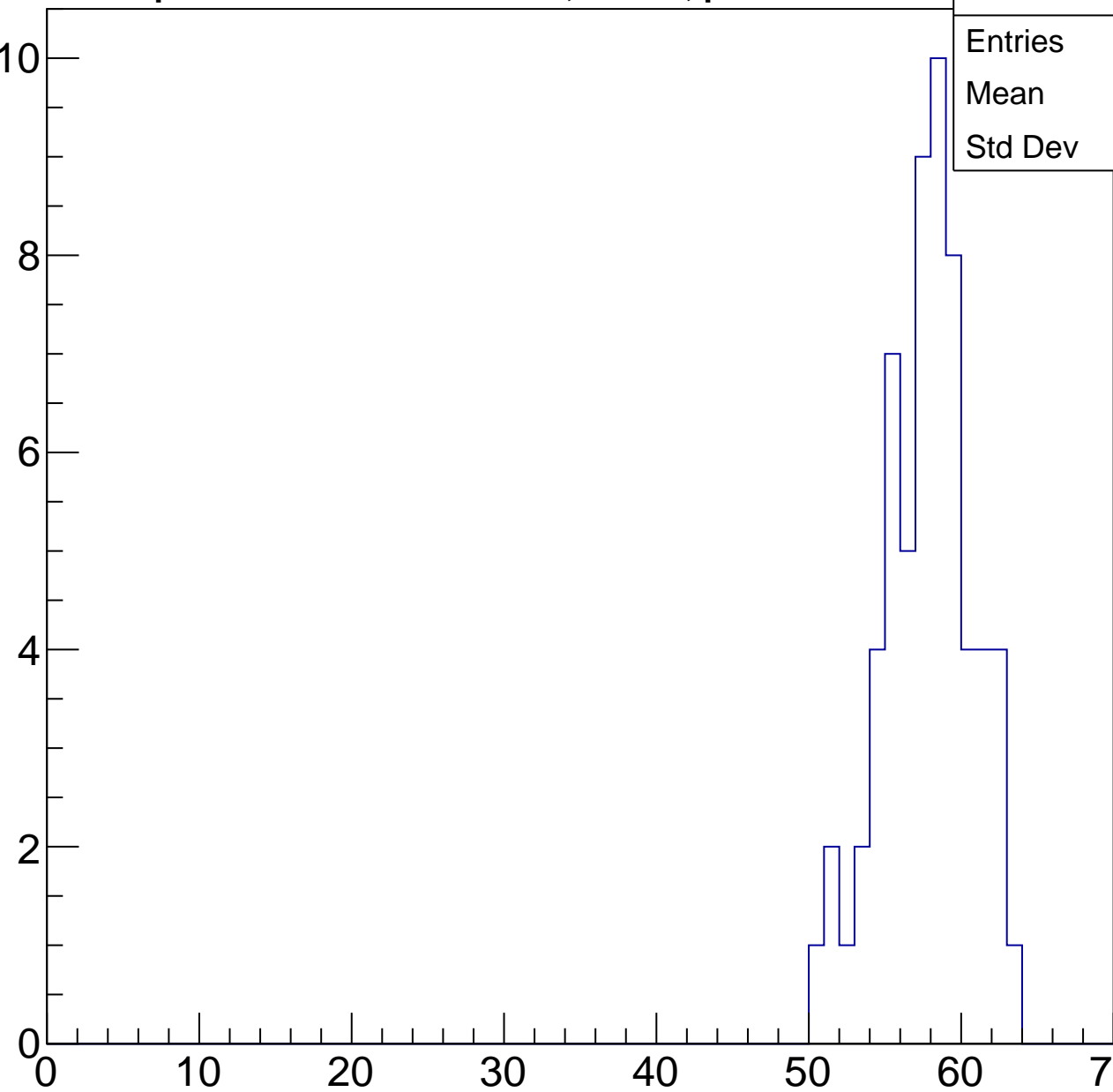
Entries	62
Mean	57.27
Std Dev	2.897

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

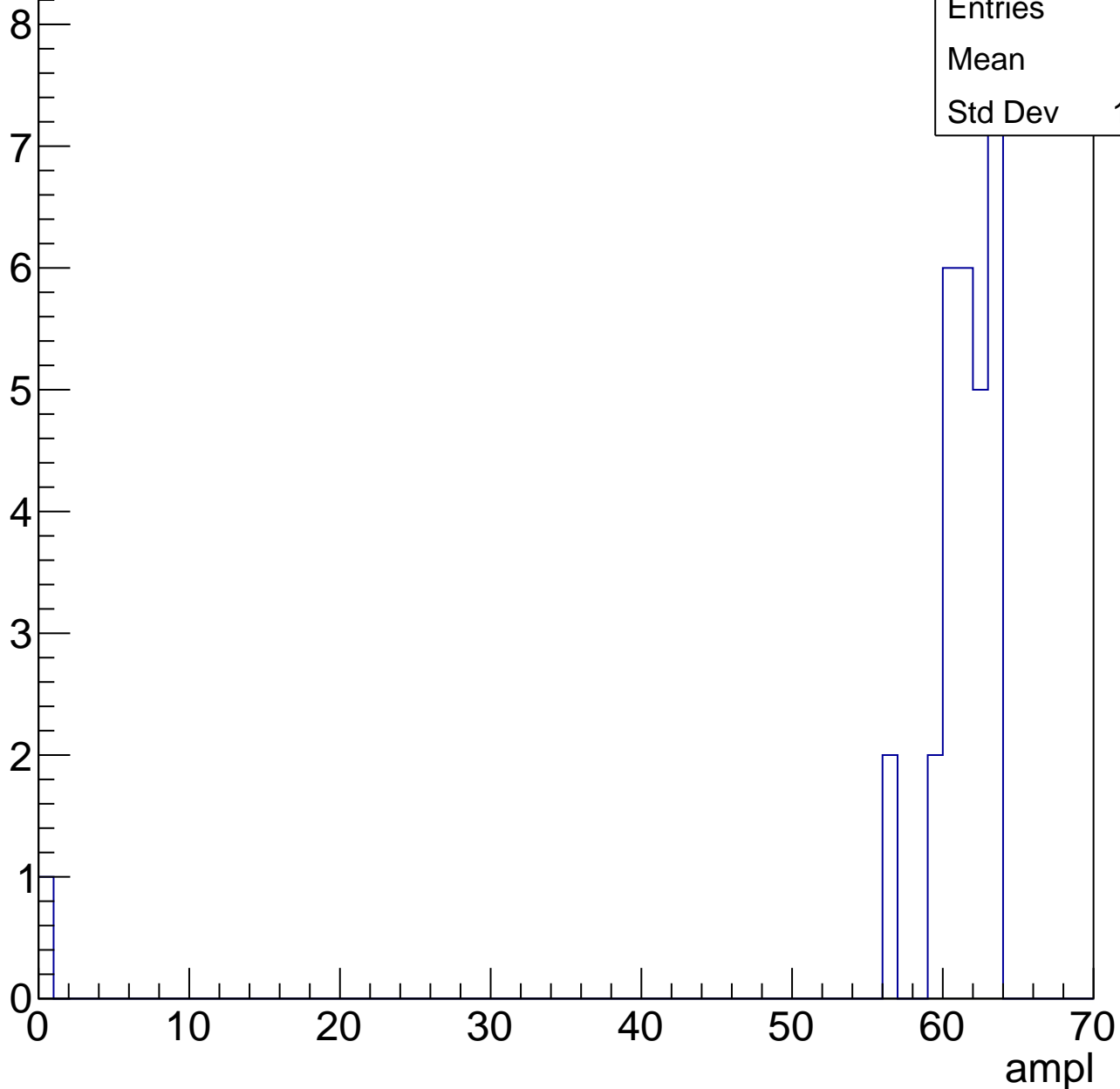


B1L102S, U26-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	59
Std Dev	11.11



B1L102S, U26-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch24, adc0

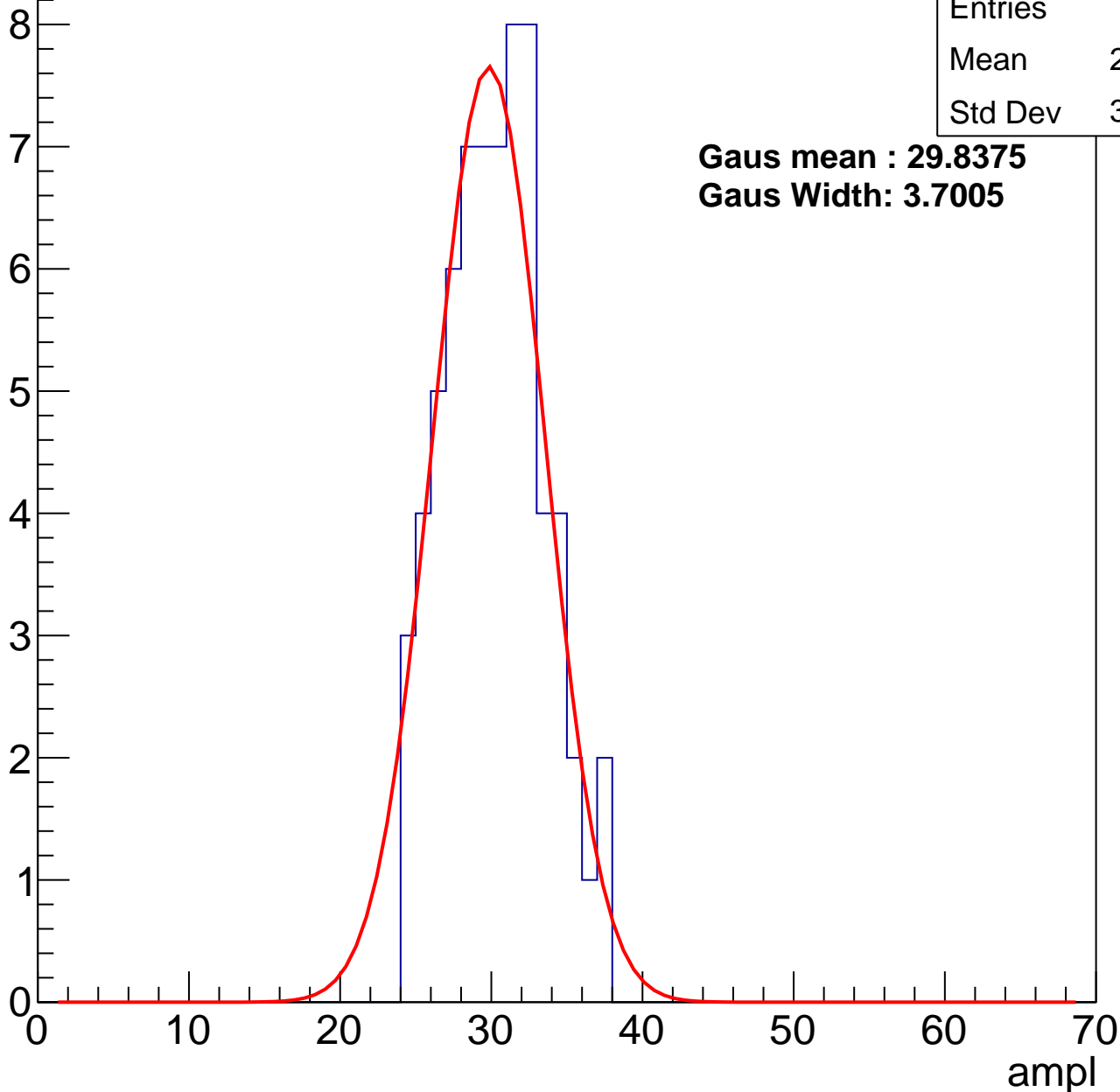
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.78
Std Dev	3.203

Gaus mean : 29.8375

Gaus Width: 3.7005



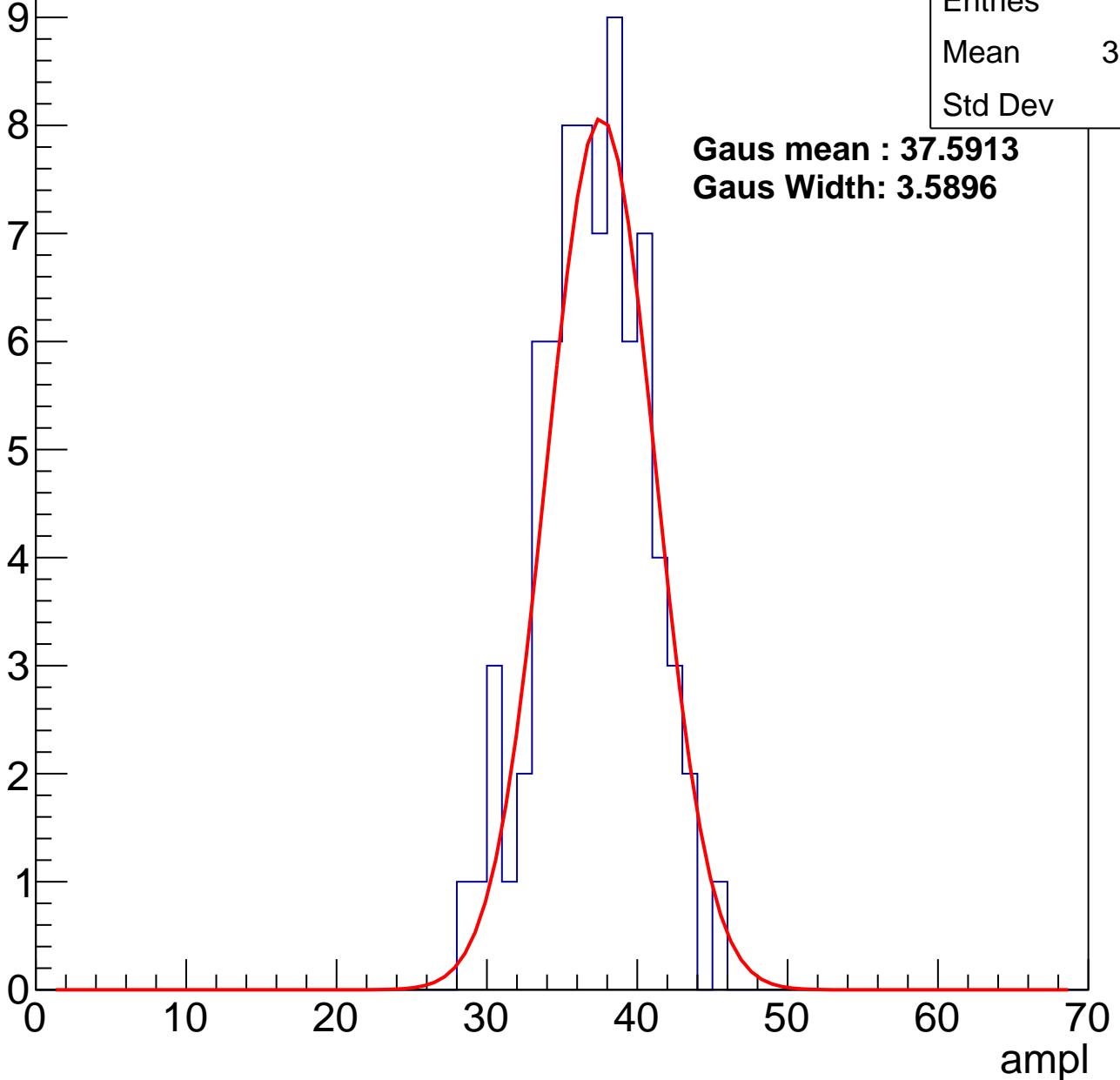
B1L102S, U26-ch24, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.64
Std Dev	3.52

Gaus mean : 37.5913
Gaus Width: 3.5896



B1L102S, U26-ch24, adc2

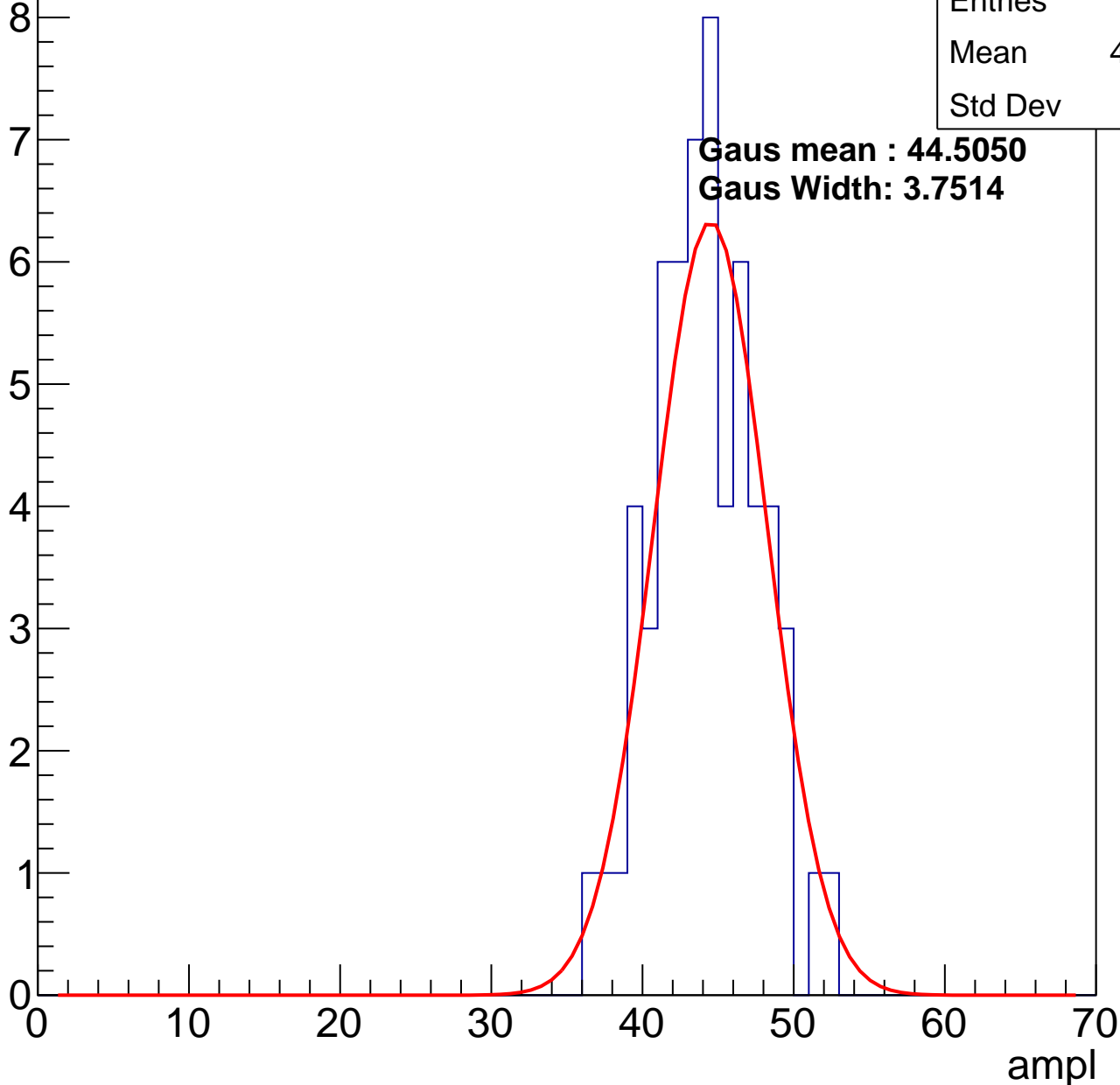
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.73
Std Dev	3.4

Gaus mean : 44.5050

Gaus Width: 3.7514



B1L102S, U26-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

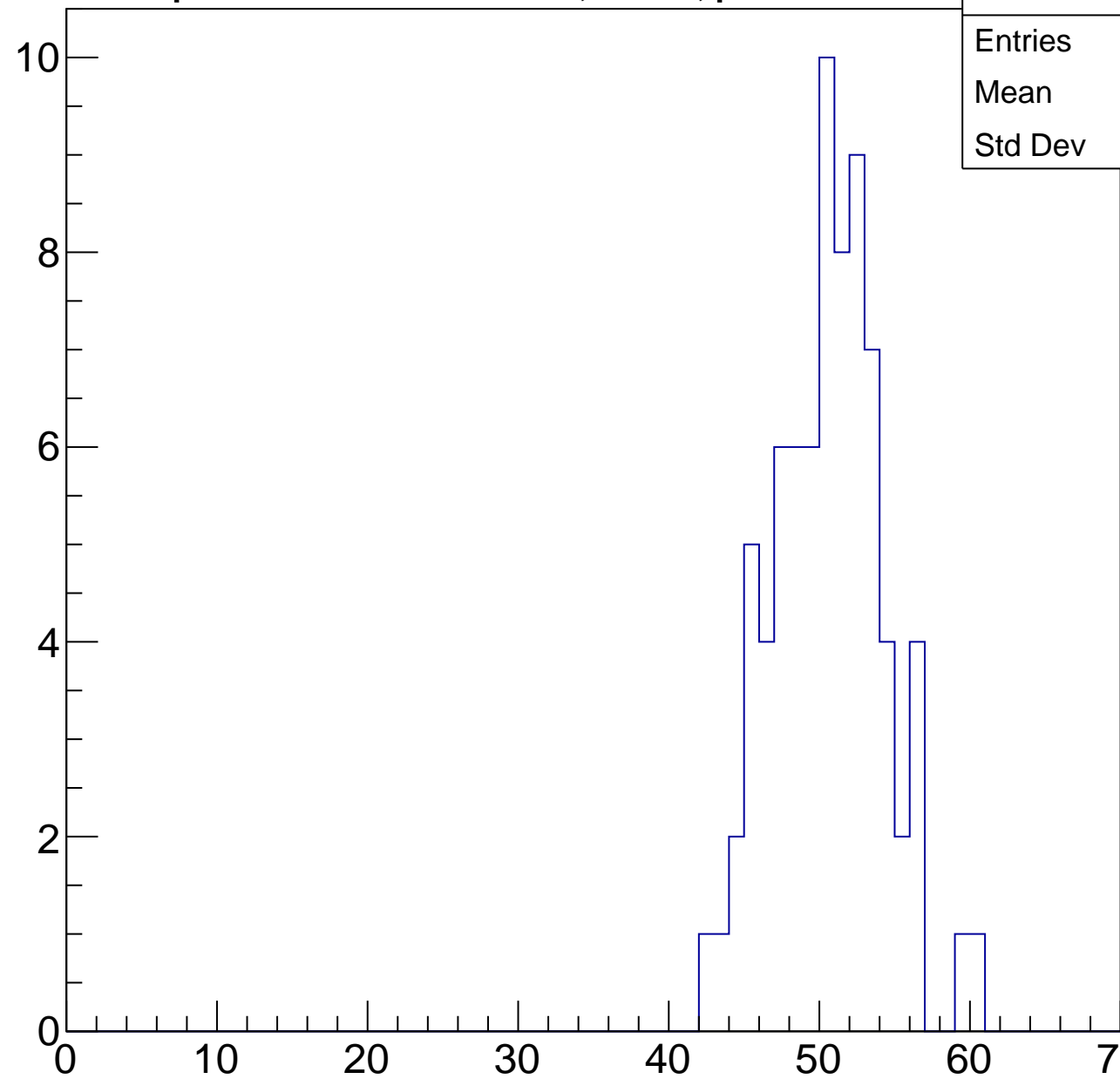
Entries	77
Mean	50.16
Std Dev	3.608

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

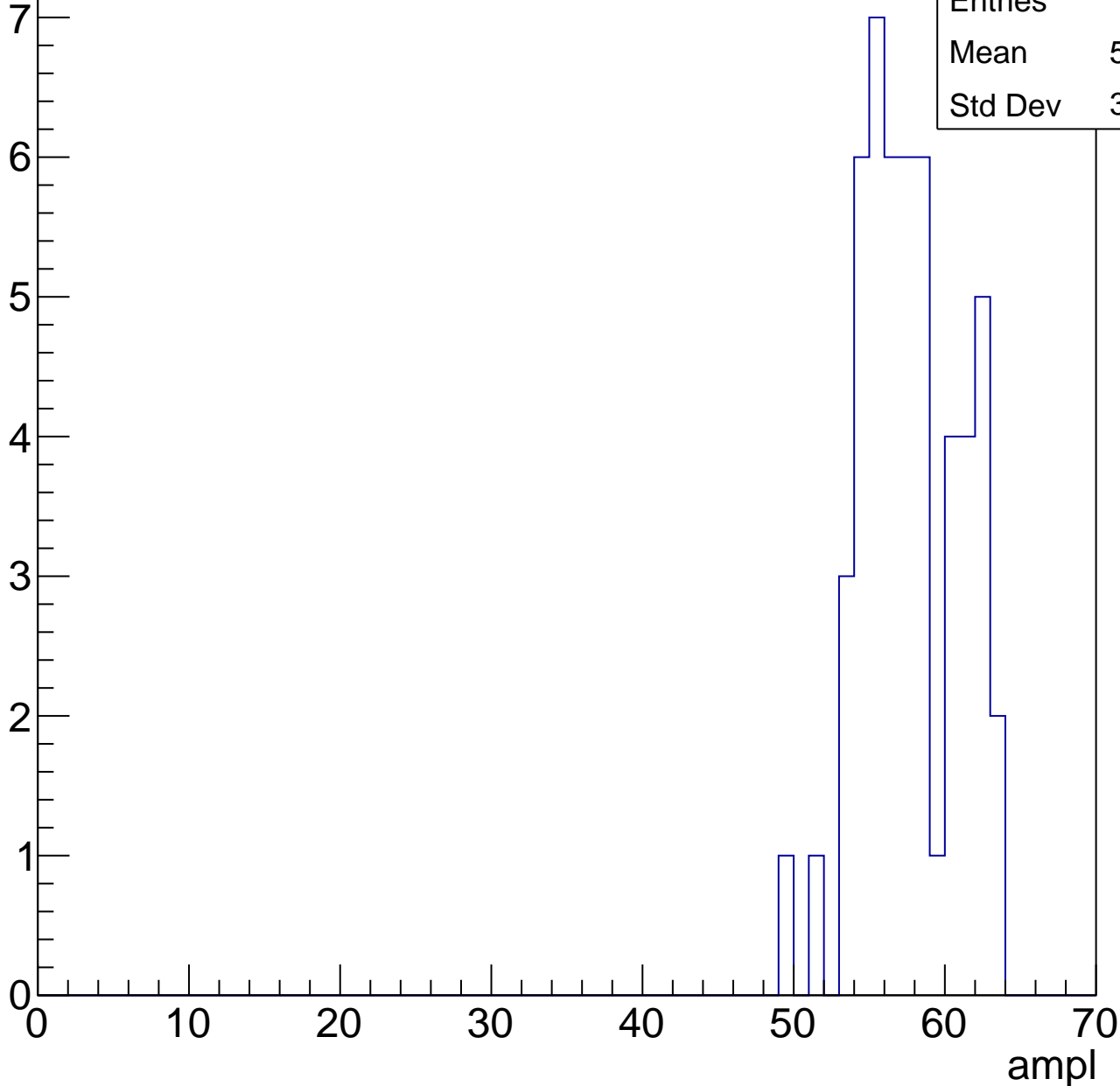


B1L102S, U26-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.17
Std Dev	3.227

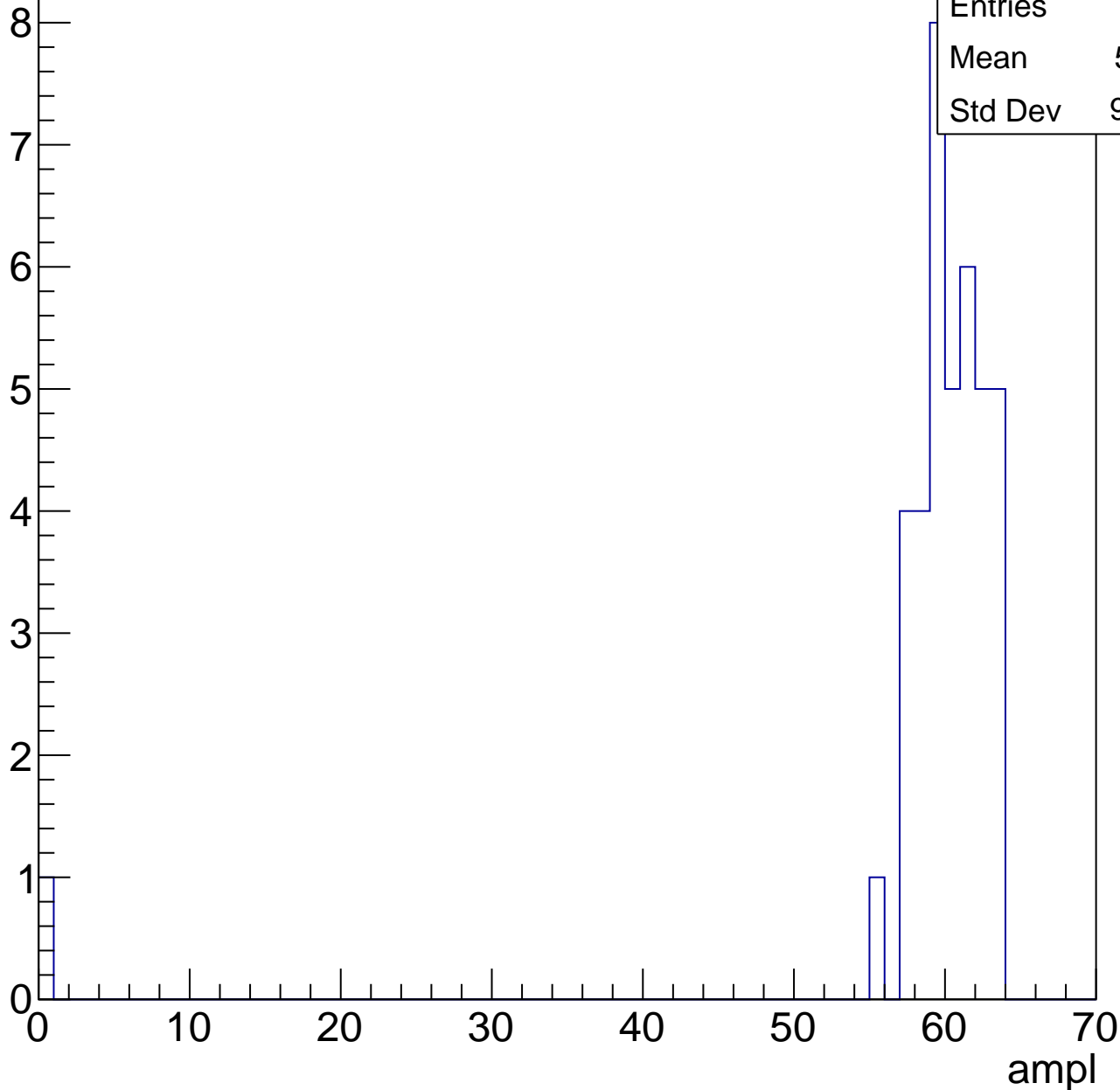


B1L102S, U26-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.41
Std Dev	9.684



B1L102S, U26-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch25, adc0

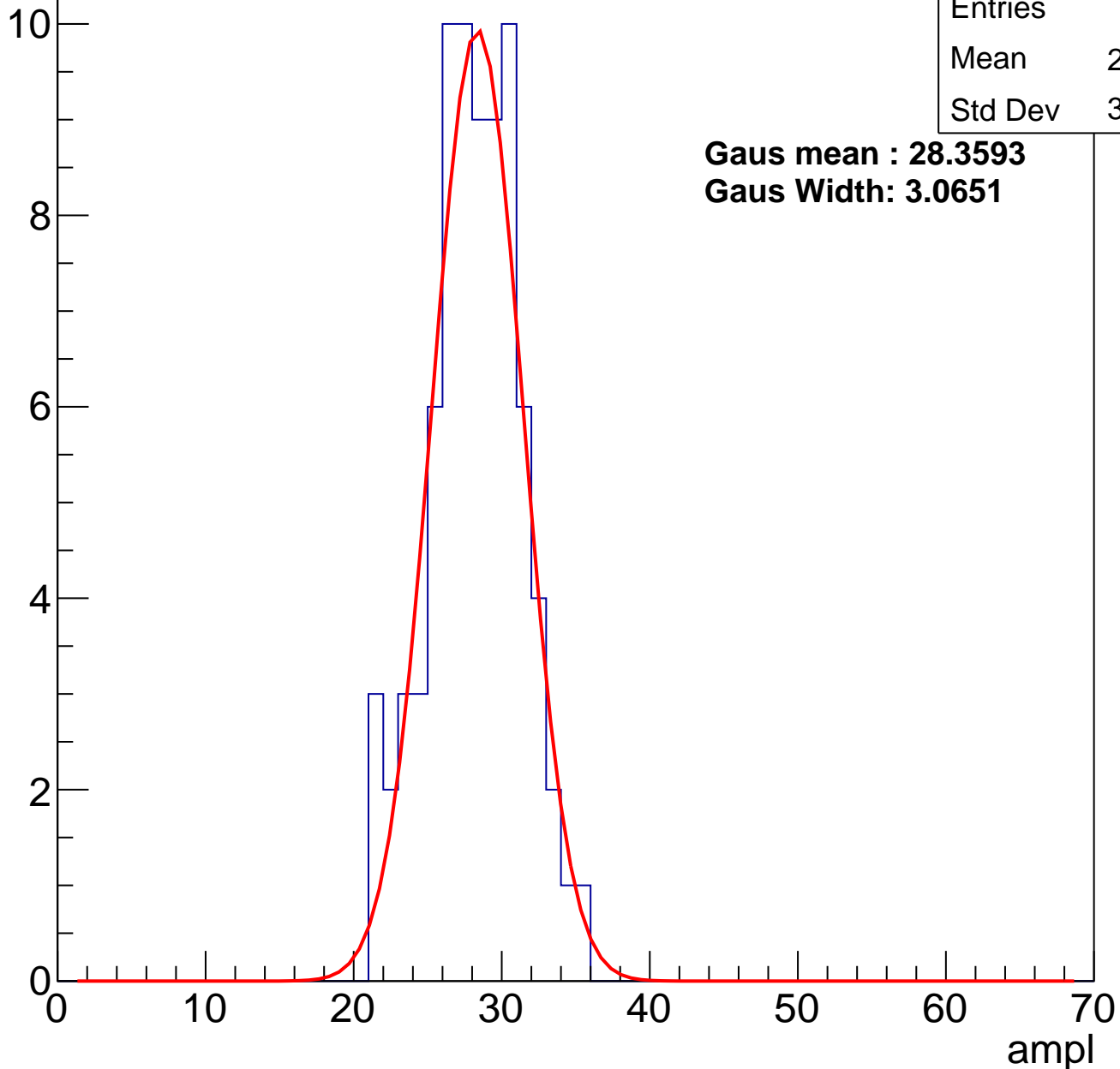
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	27.72
Std Dev	3.064

Gaus mean : 28.3593

Gaus Width: 3.0651

Entry



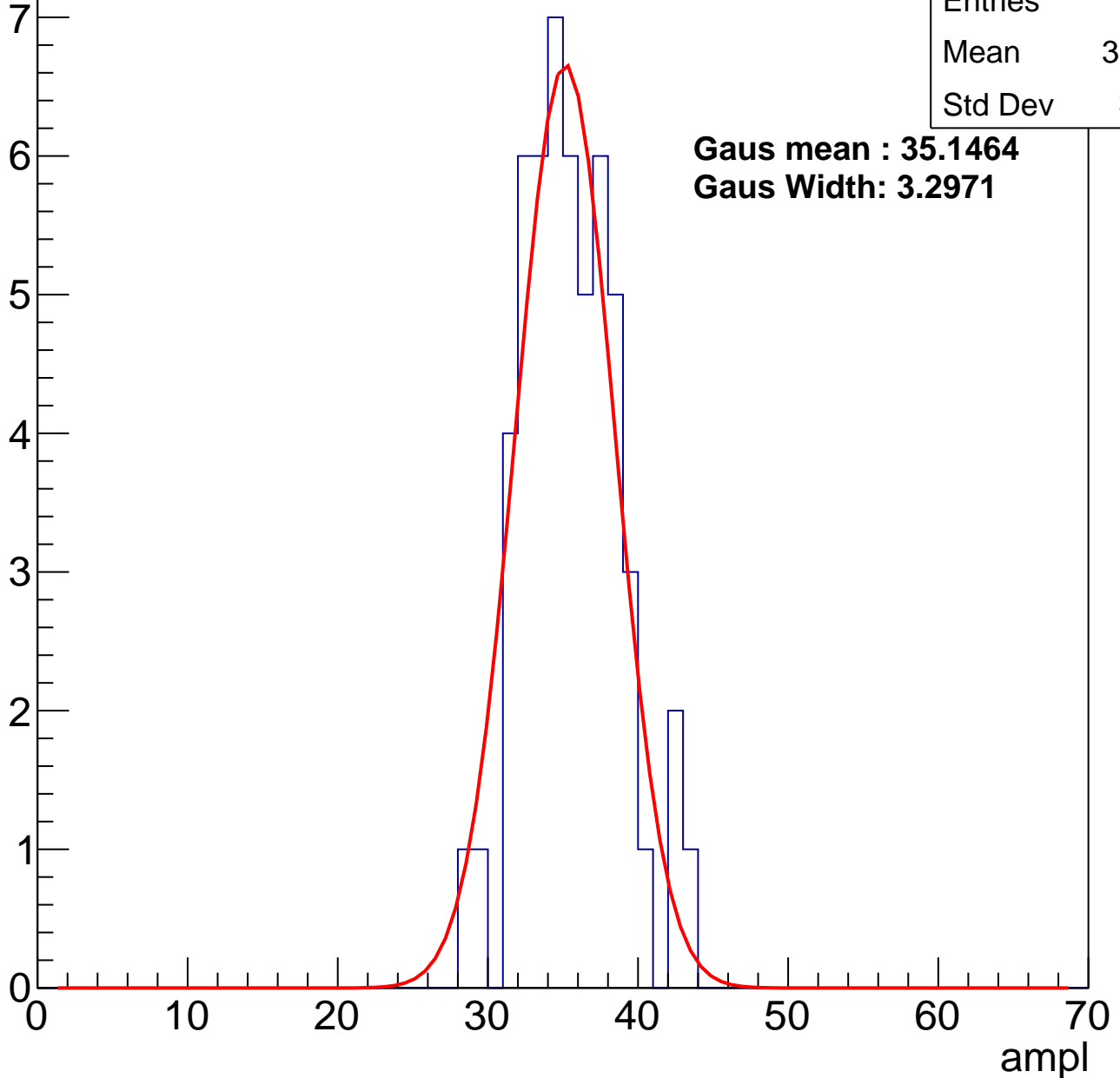
B1L102S, U26-ch25, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.09
Std Dev	3.17

Gaus mean : 35.1464
Gaus Width: 3.2971



B1L102S, U26-ch25, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	40.42
Std Dev	3.437

Gaus mean : 40.9096

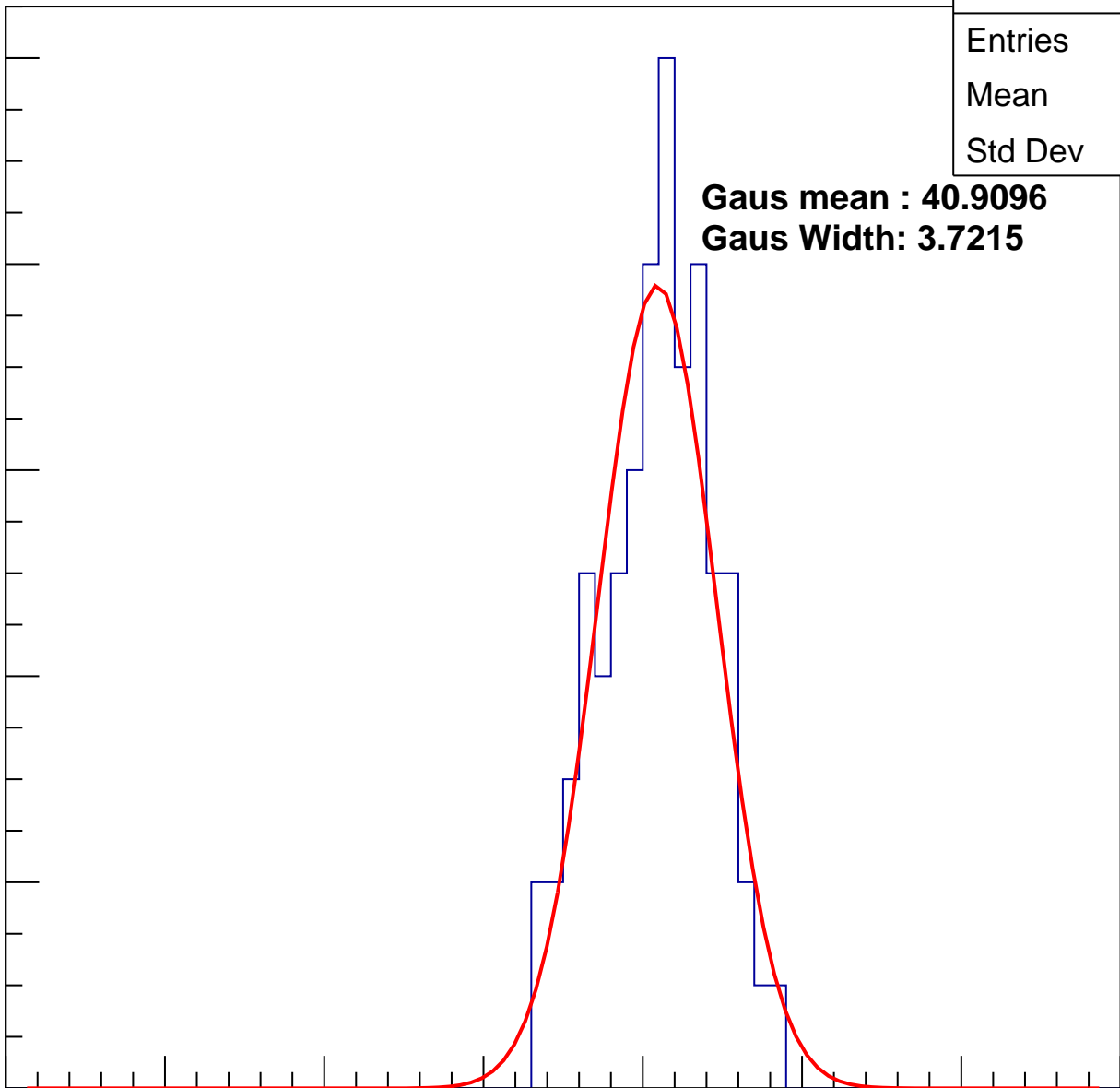
Gaus Width: 3.7215

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

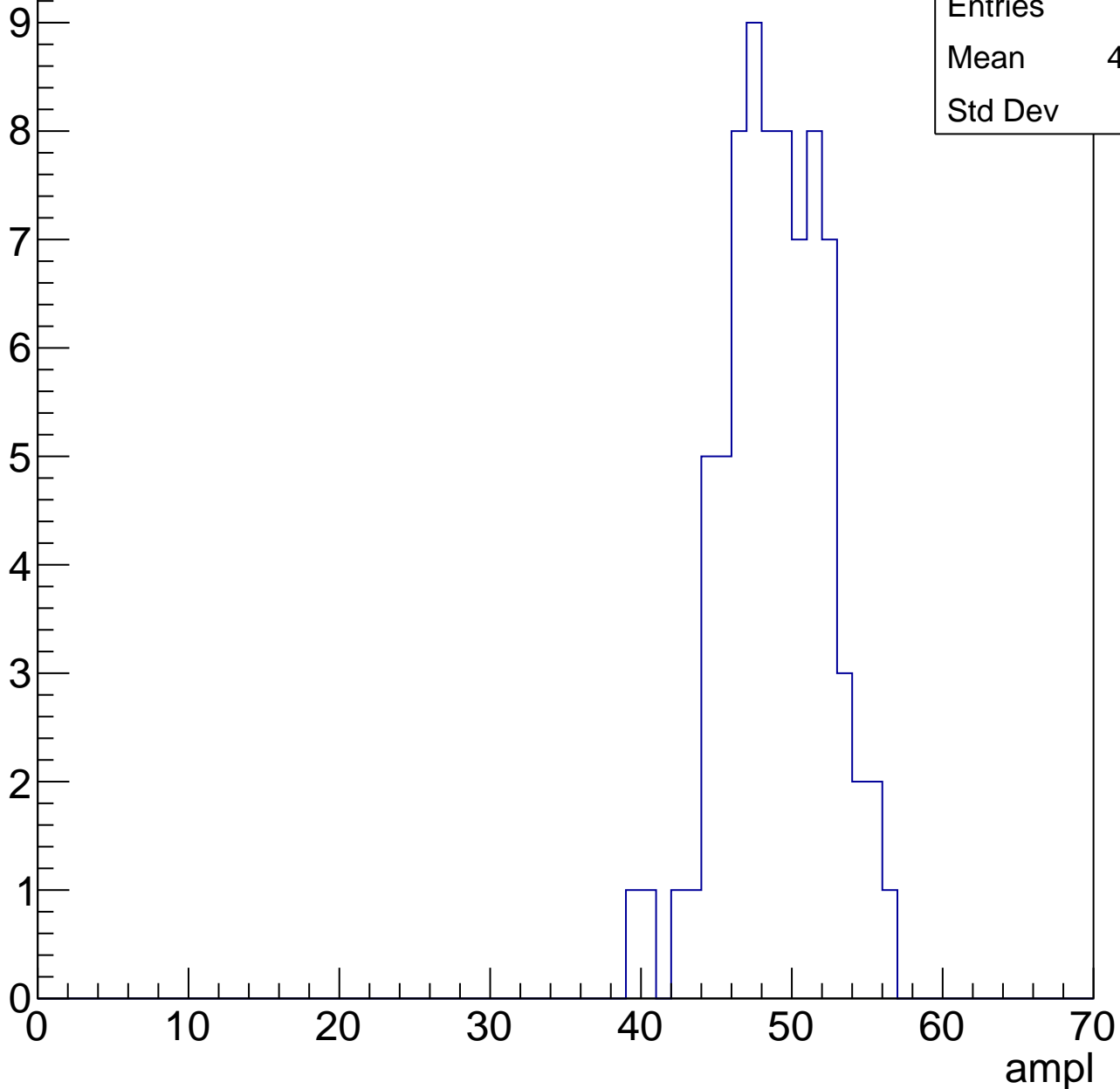


B1L102S, U26-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.45
Std Dev	3.39

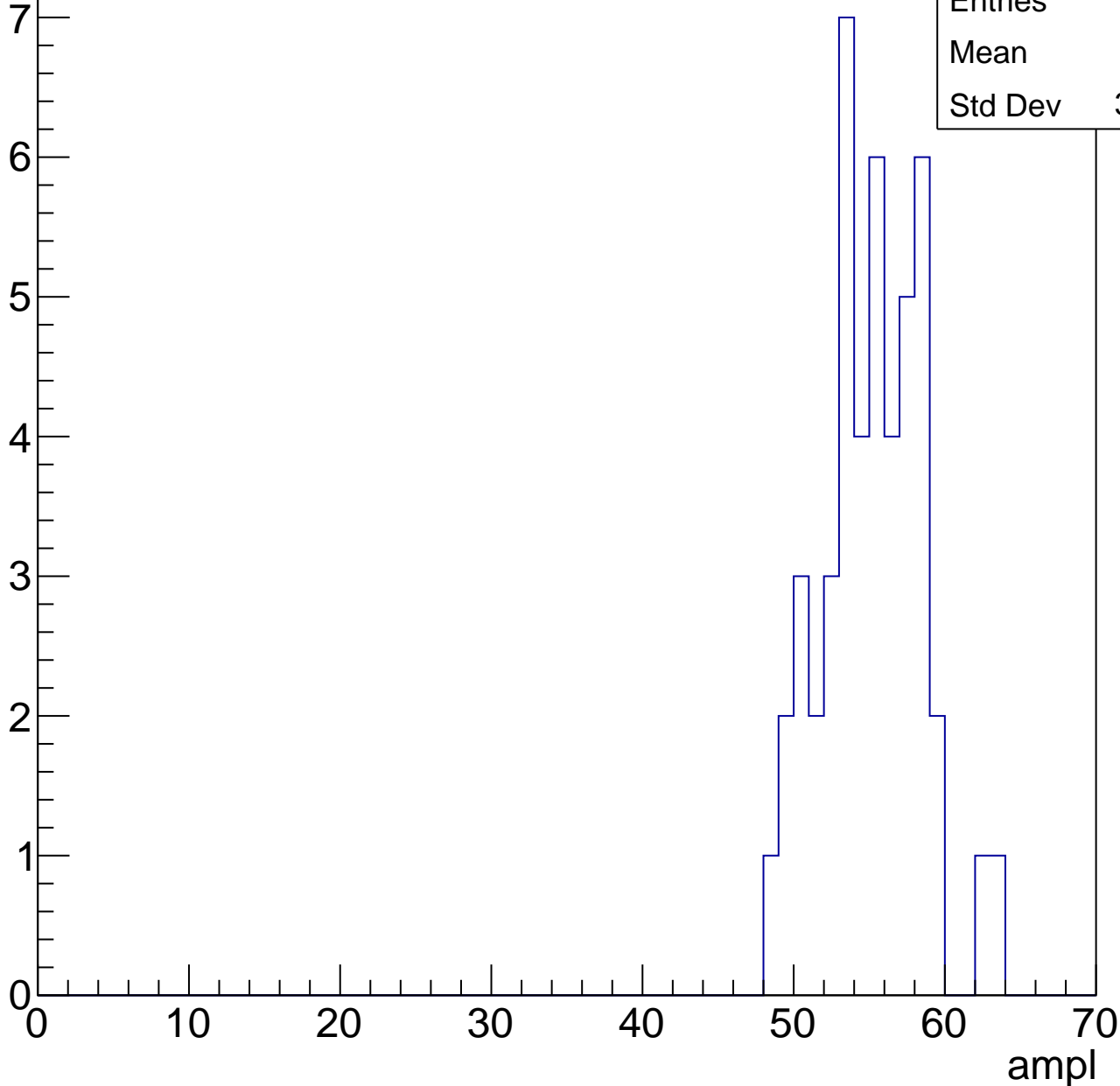


B1L102S, U26-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.7
Std Dev	3.281

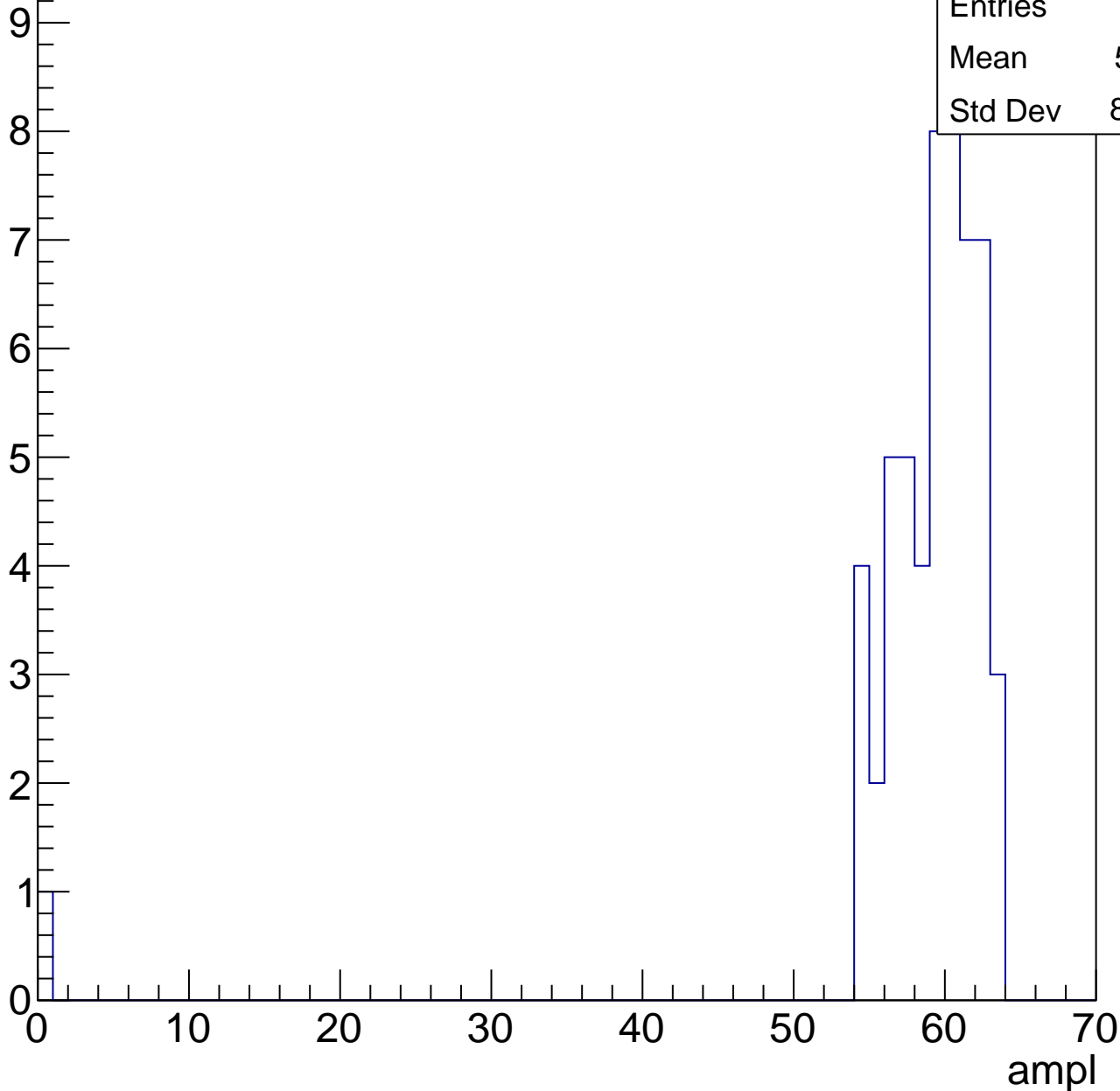


B1L102S, U26-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

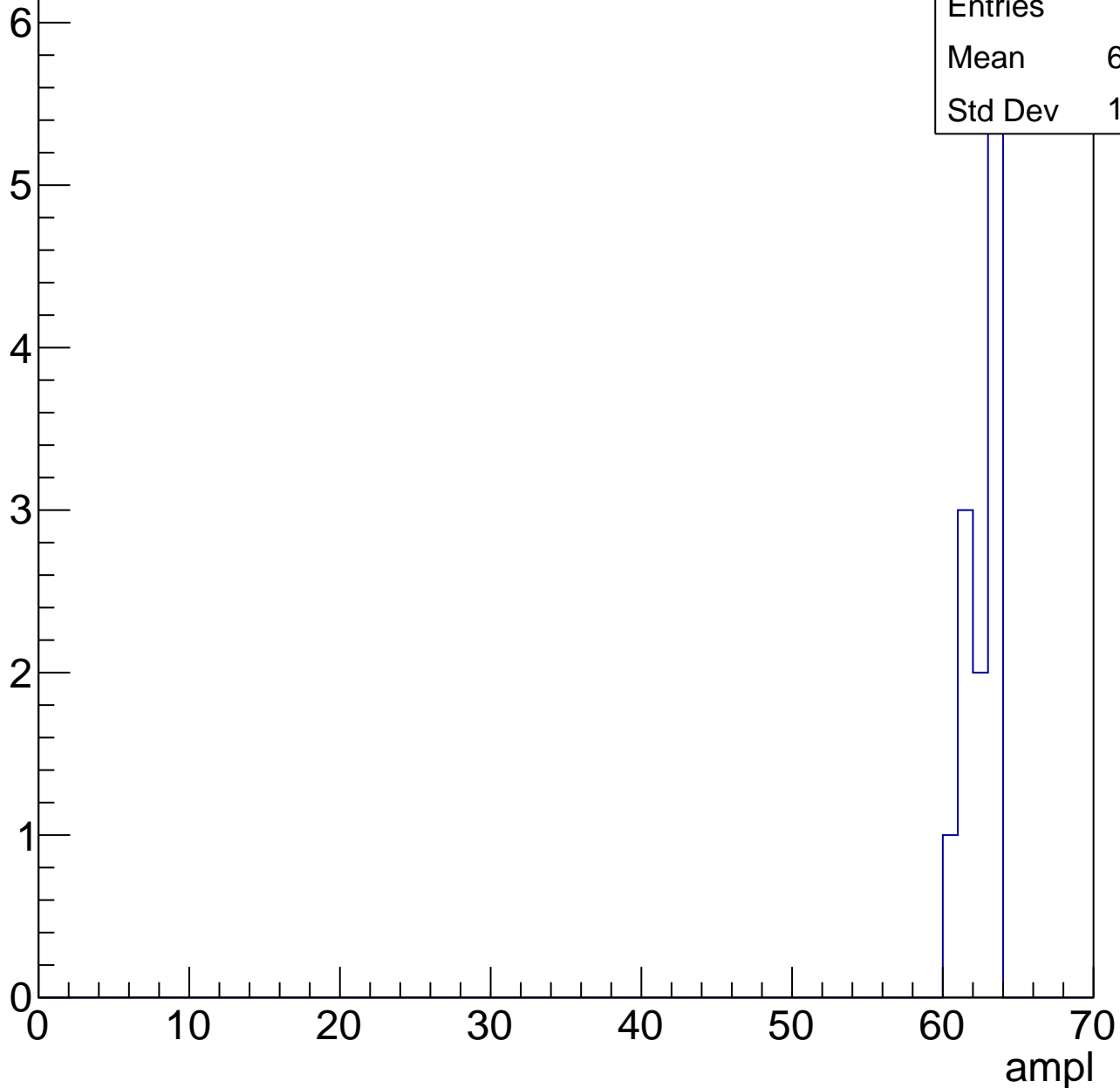
Entries	55
Mean	57.91
Std Dev	8.273



B1L102S, U26-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch26, adc0

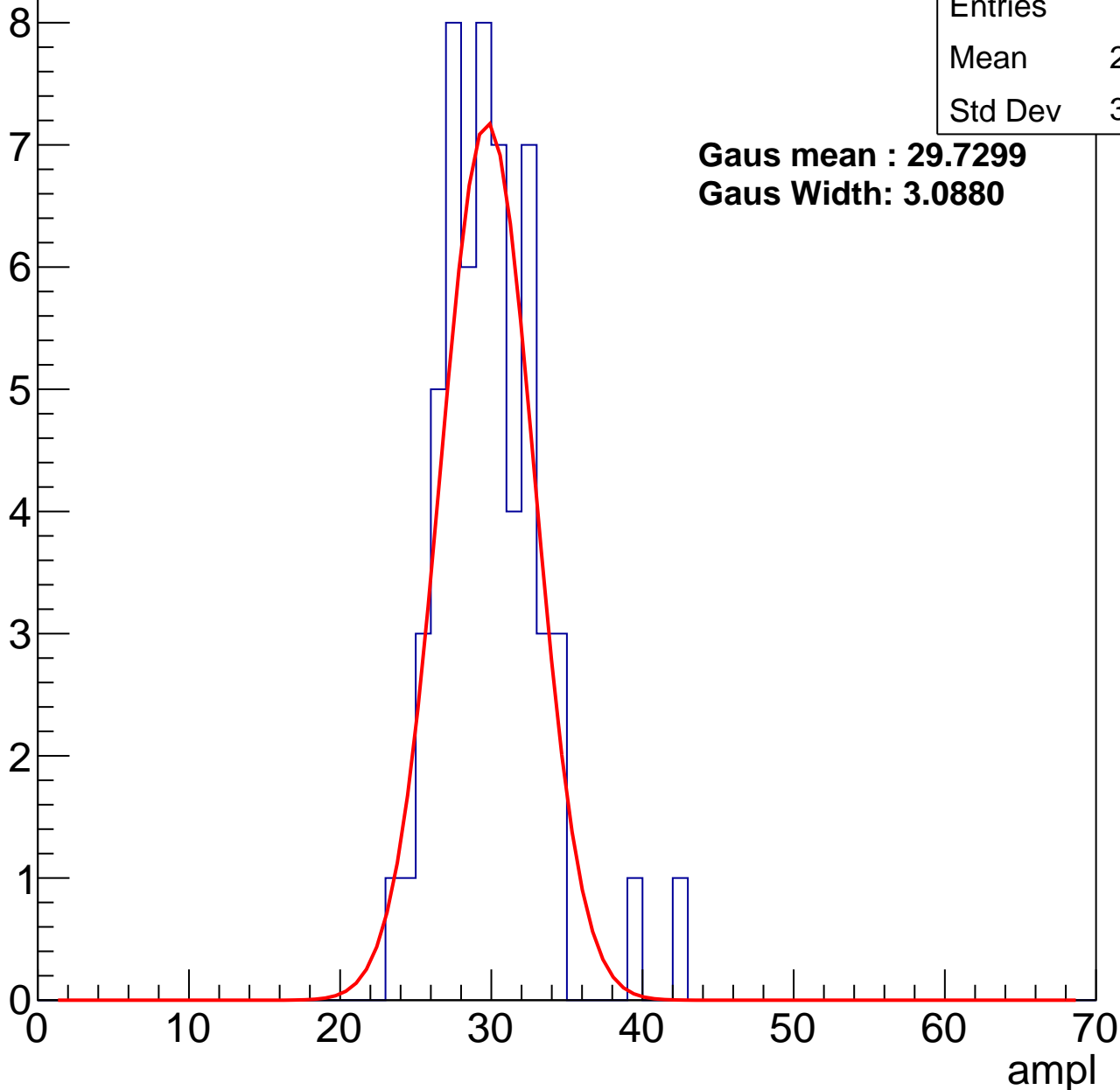
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.45
Std Dev	3.369

Gaus mean : 29.7299

Gaus Width: 3.0880



B1L102S, U26-ch26, adc1

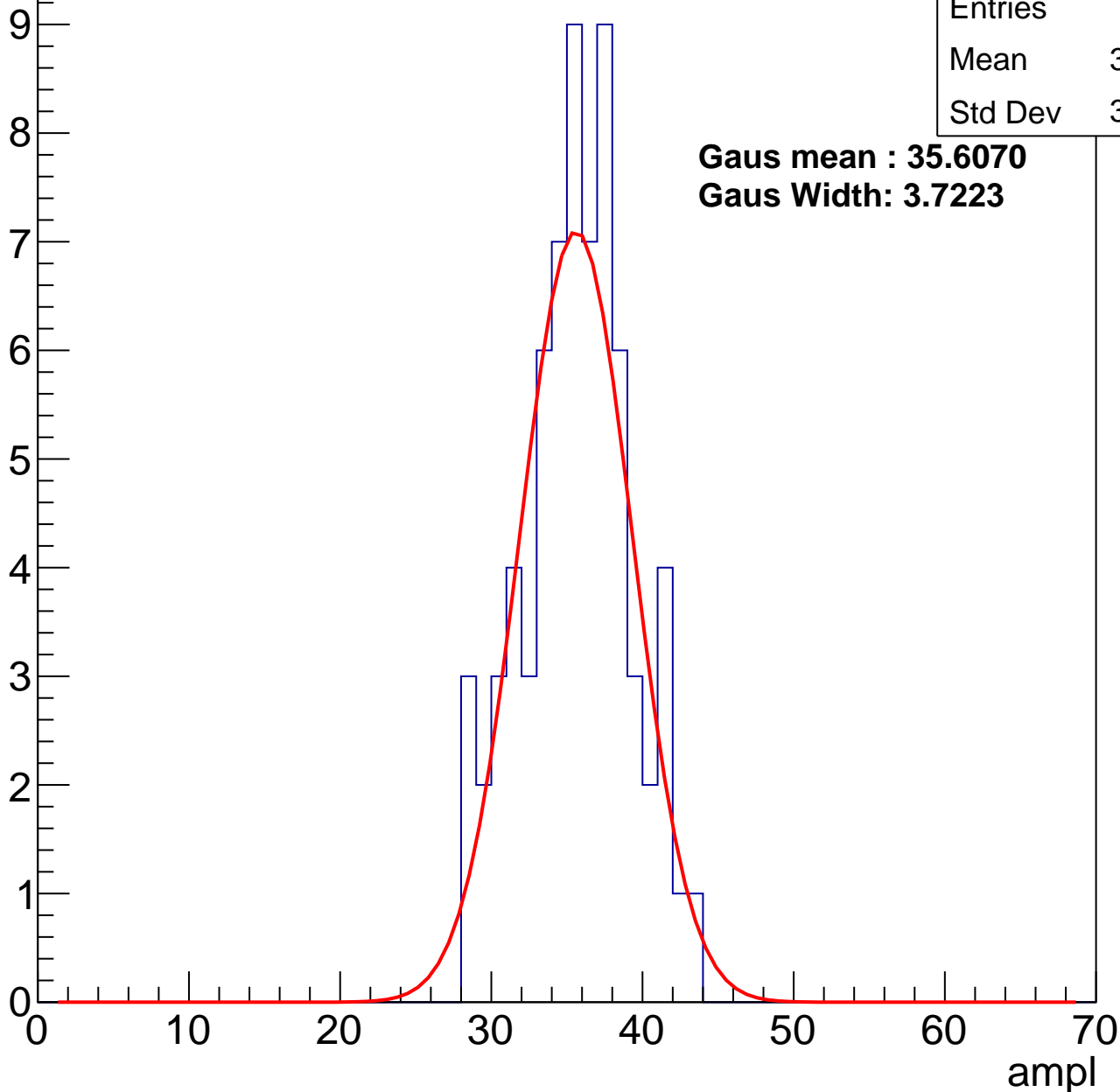
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.17
Std Dev	3.517

Gaus mean : 35.6070

Gaus Width: 3.7223



B1L102S, U26-ch26, adc2

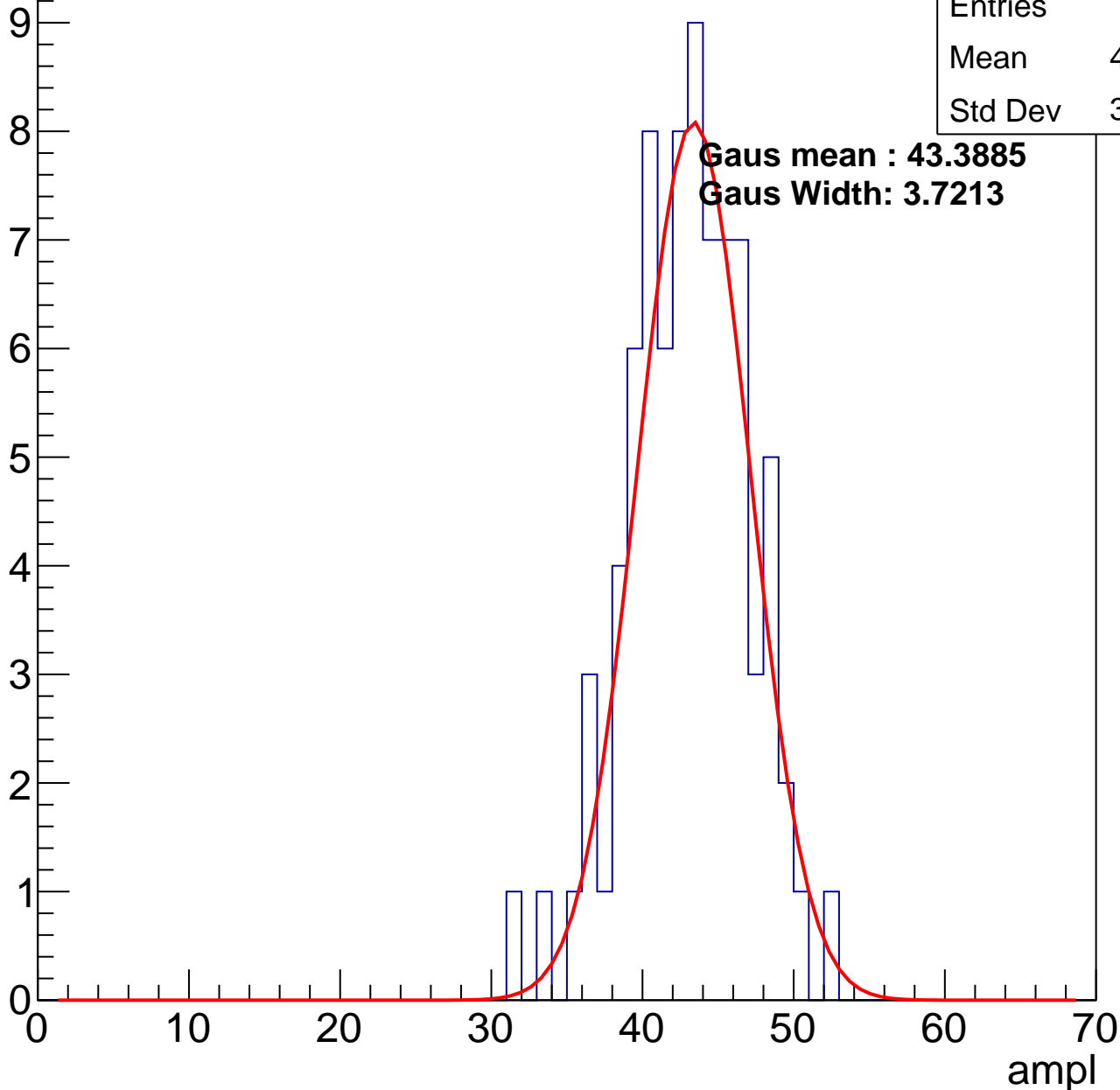
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.53
Std Dev	3.922

Gaus mean : 43.3885

Gaus Width: 3.7213

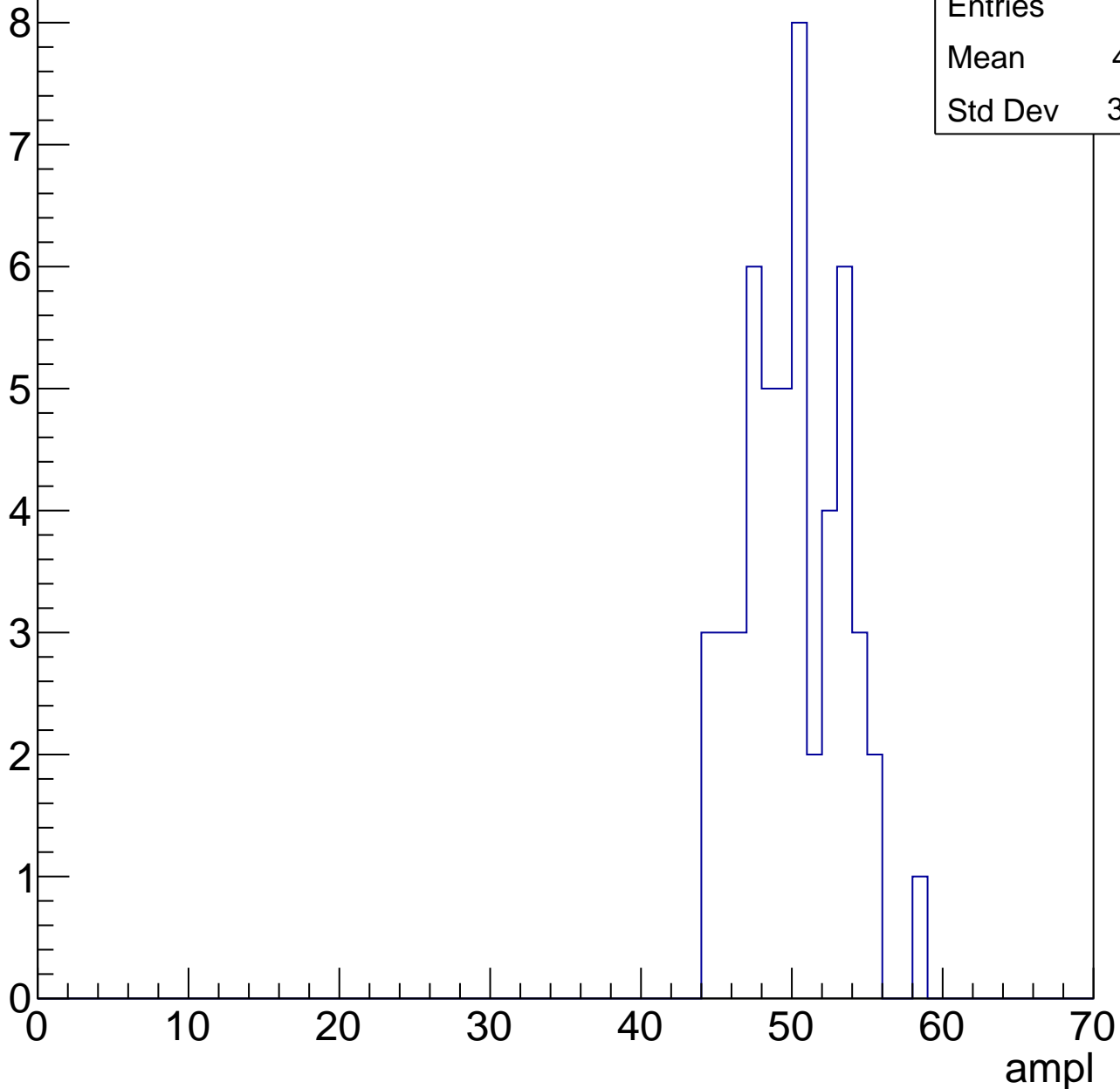


B1L102S, U26-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.61
Std Dev	3.242

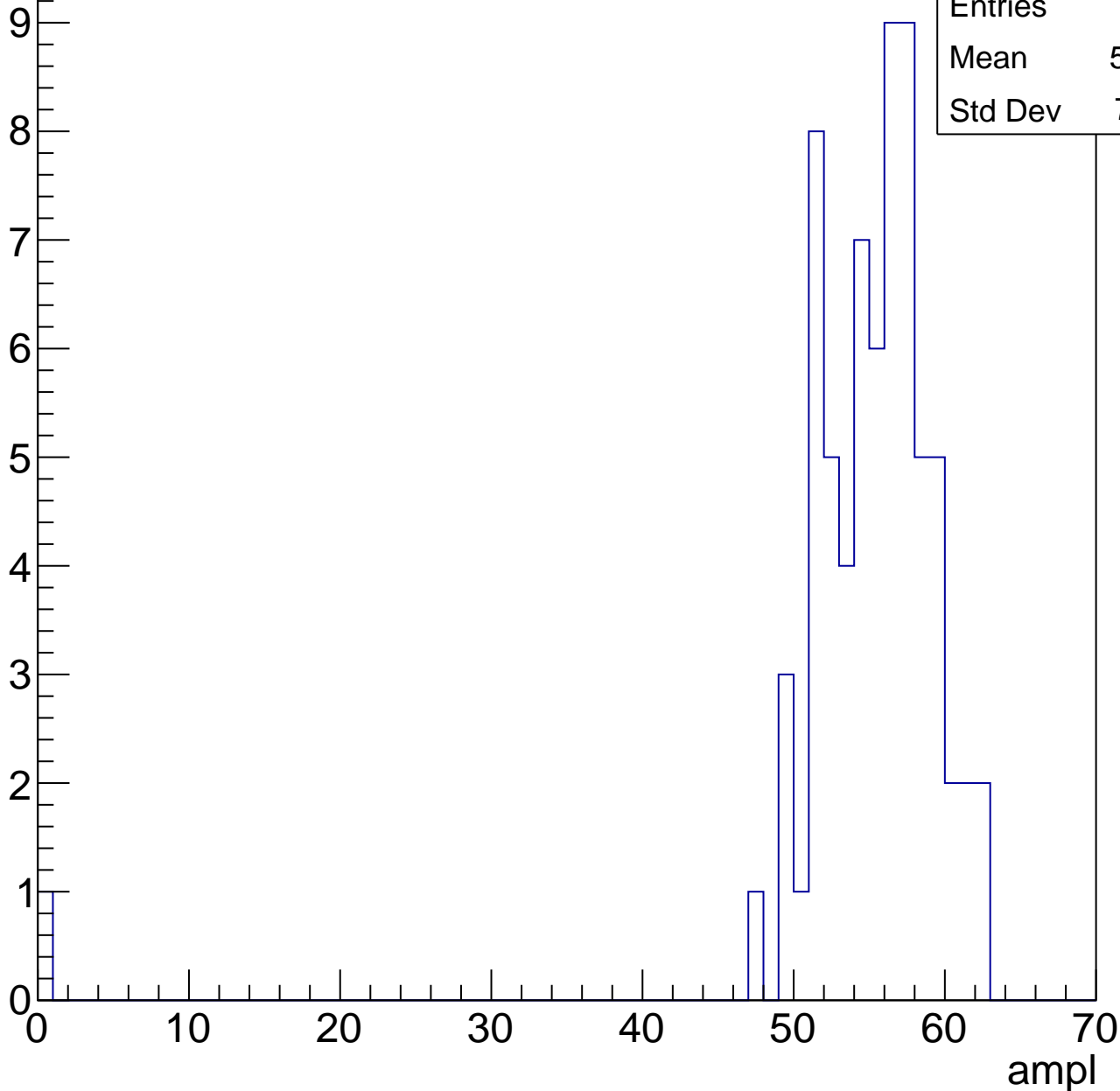


B1L102S, U26-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.29
Std Dev	7.341

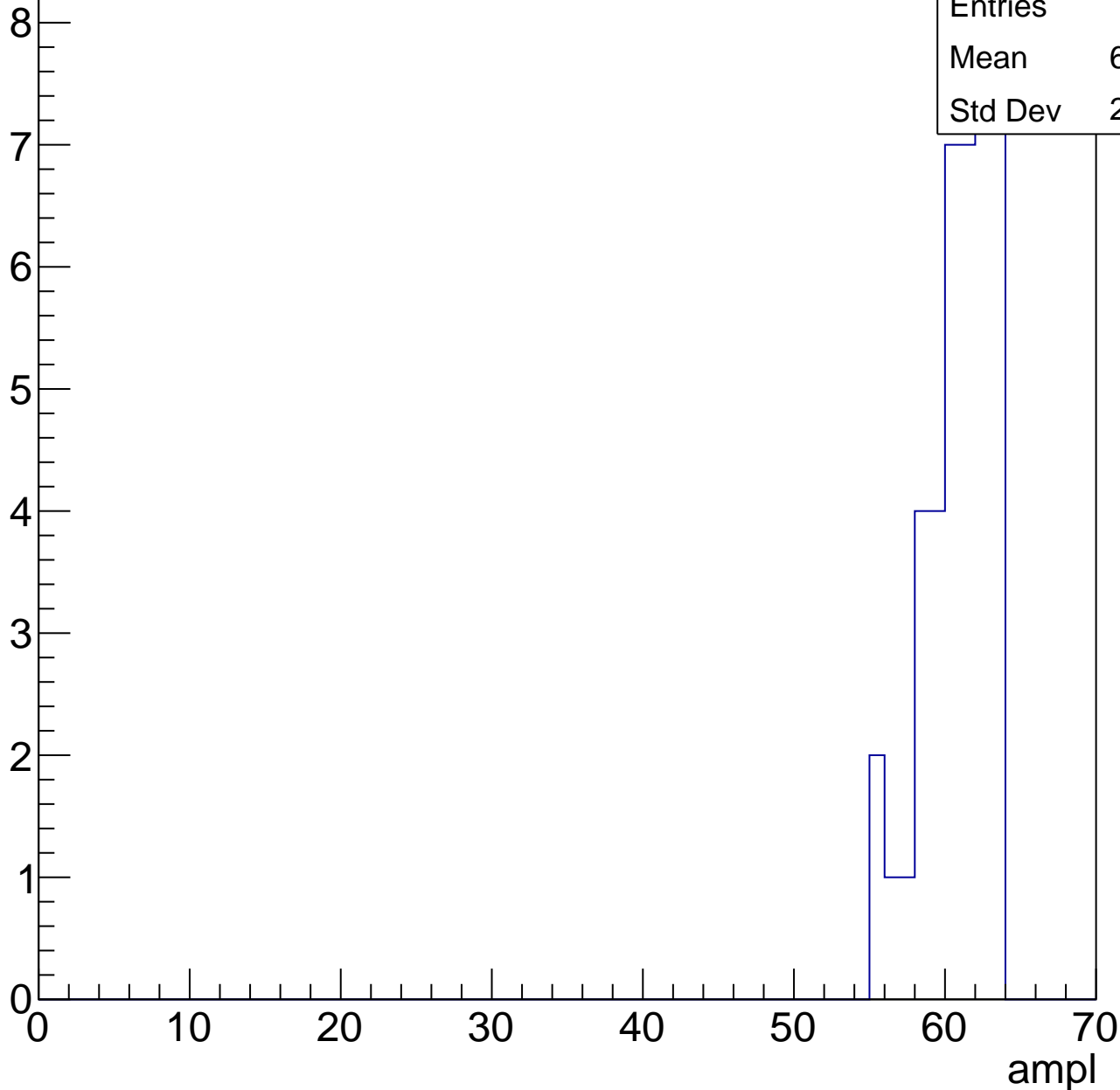


B1L102S, U26-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

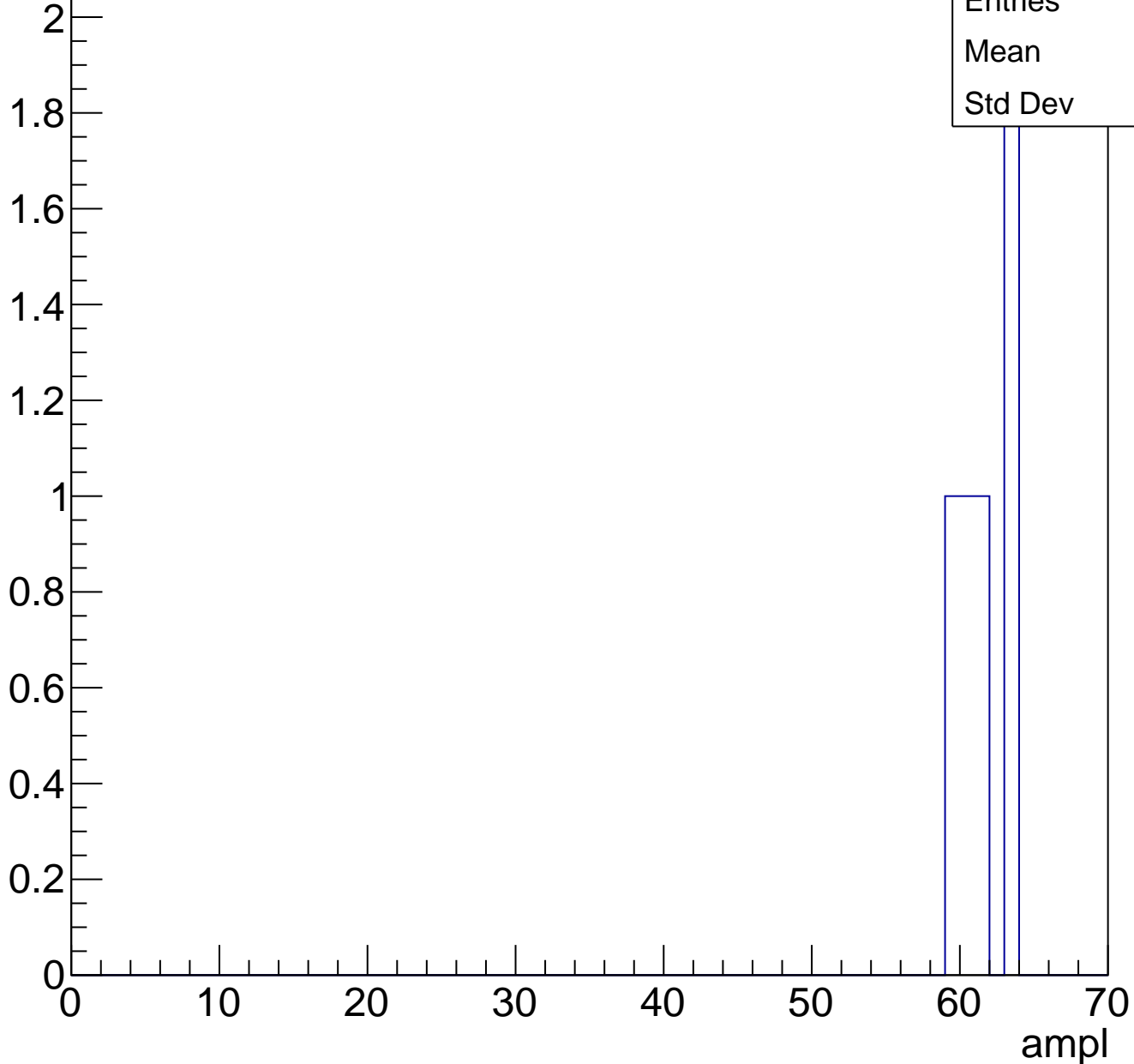
Entries	42
Mean	60.43
Std Dev	2.173



B1L102S, U26-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch27, adc0

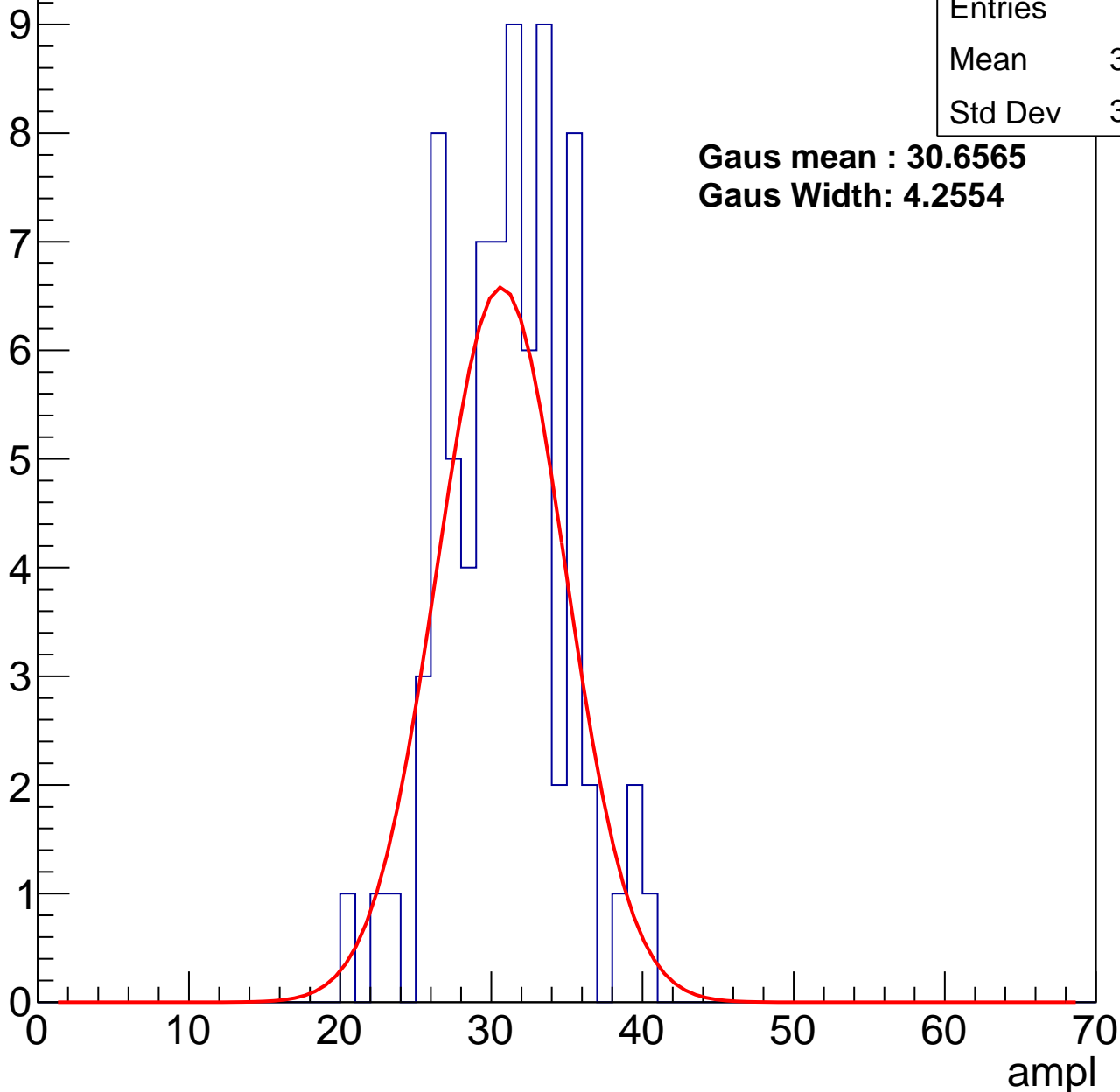
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	30.55
Std Dev	3.956

Gaus mean : 30.6565

Gaus Width: 4.2554



B1L102S, U26-ch27, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	57
Mean	36.28
Std Dev	3.329

Gaus mean : 37.7248

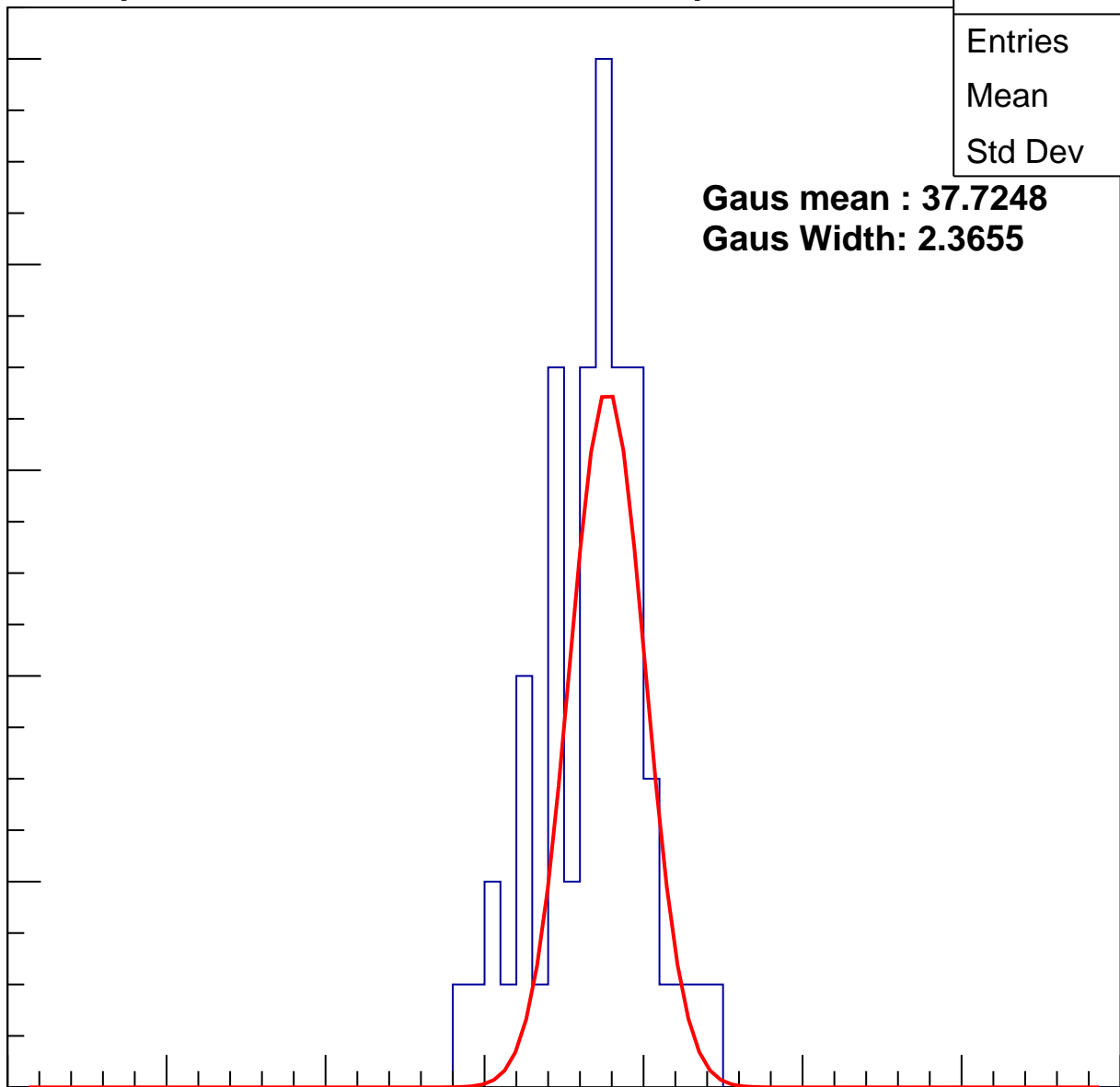
Gaus Width: 2.3655

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch27, adc2

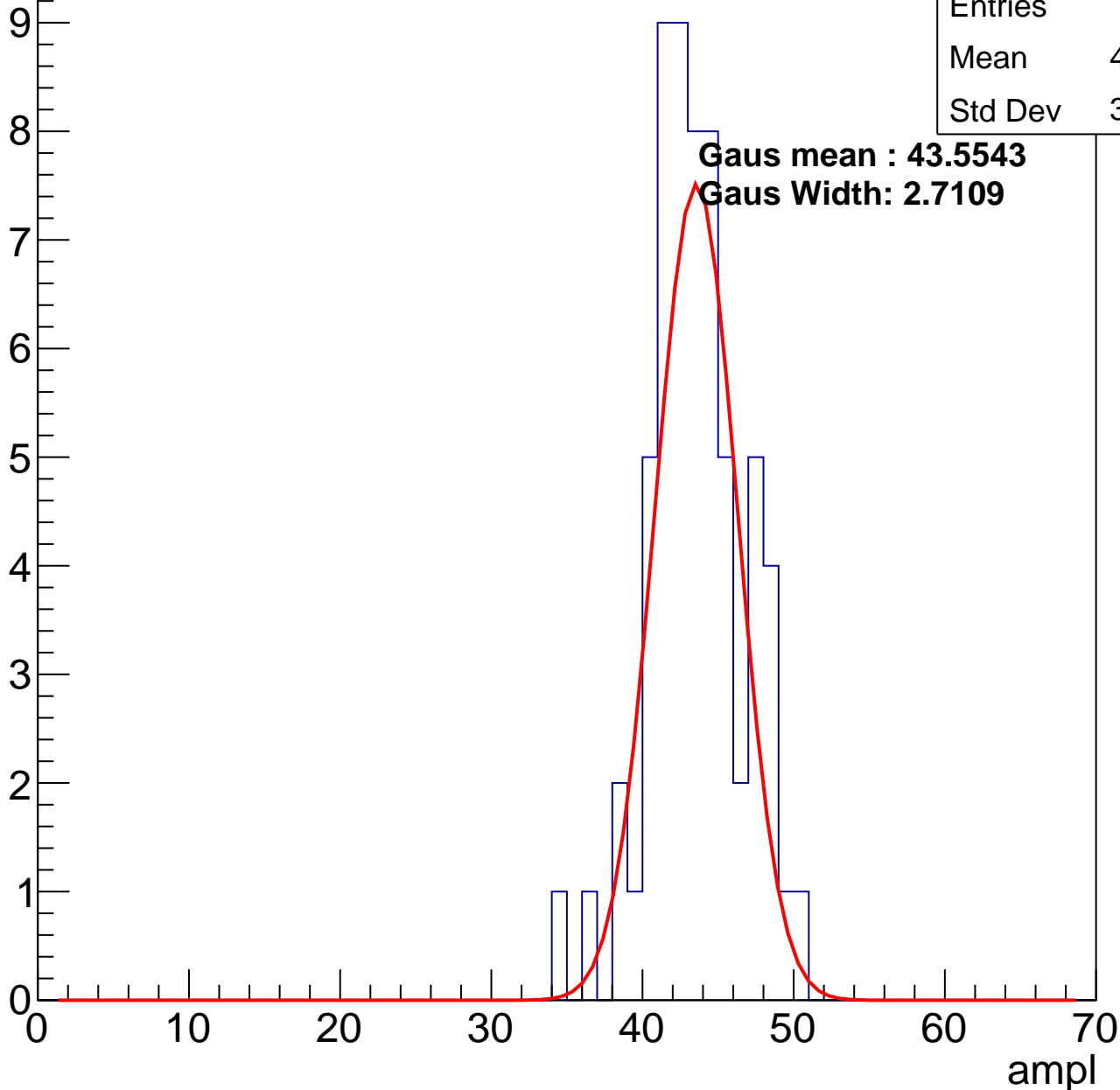
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.08
Std Dev	3.102

Gaus mean : 43.5543

Gaus Width: 2.7109

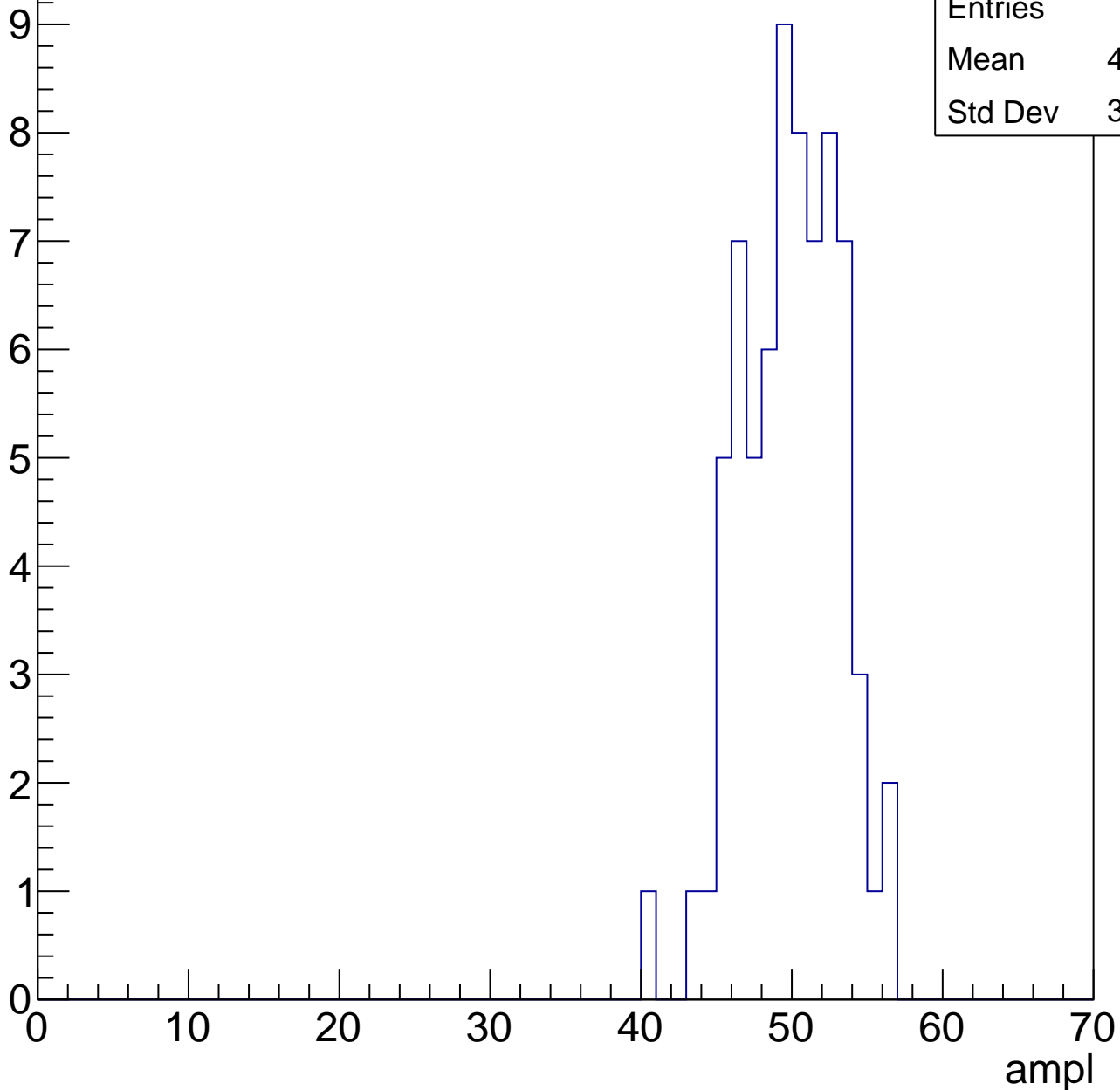


B1L102S, U26-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.45
Std Dev	3.197

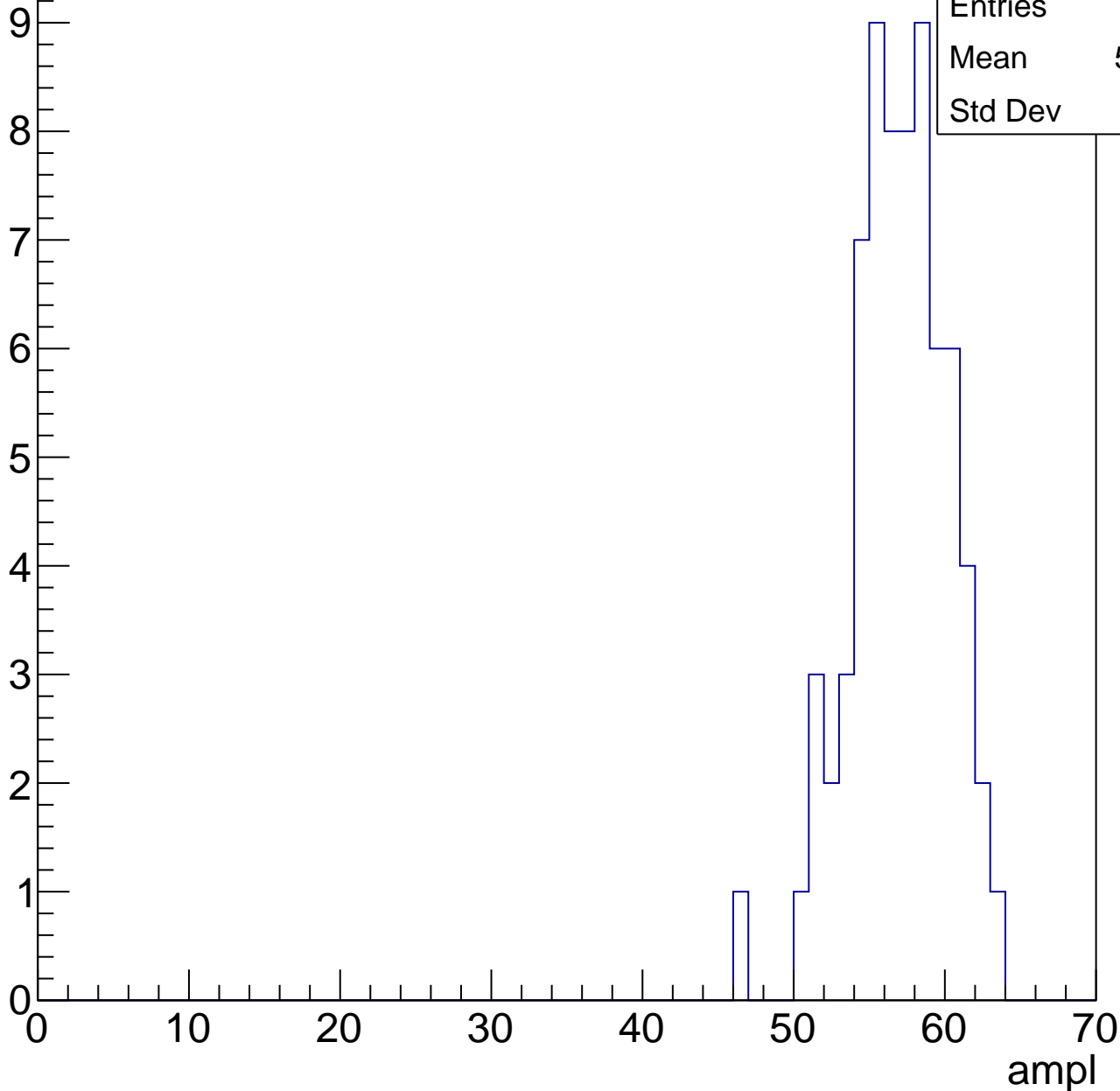


B1L102S, U26-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	56.51
Std Dev	3.17

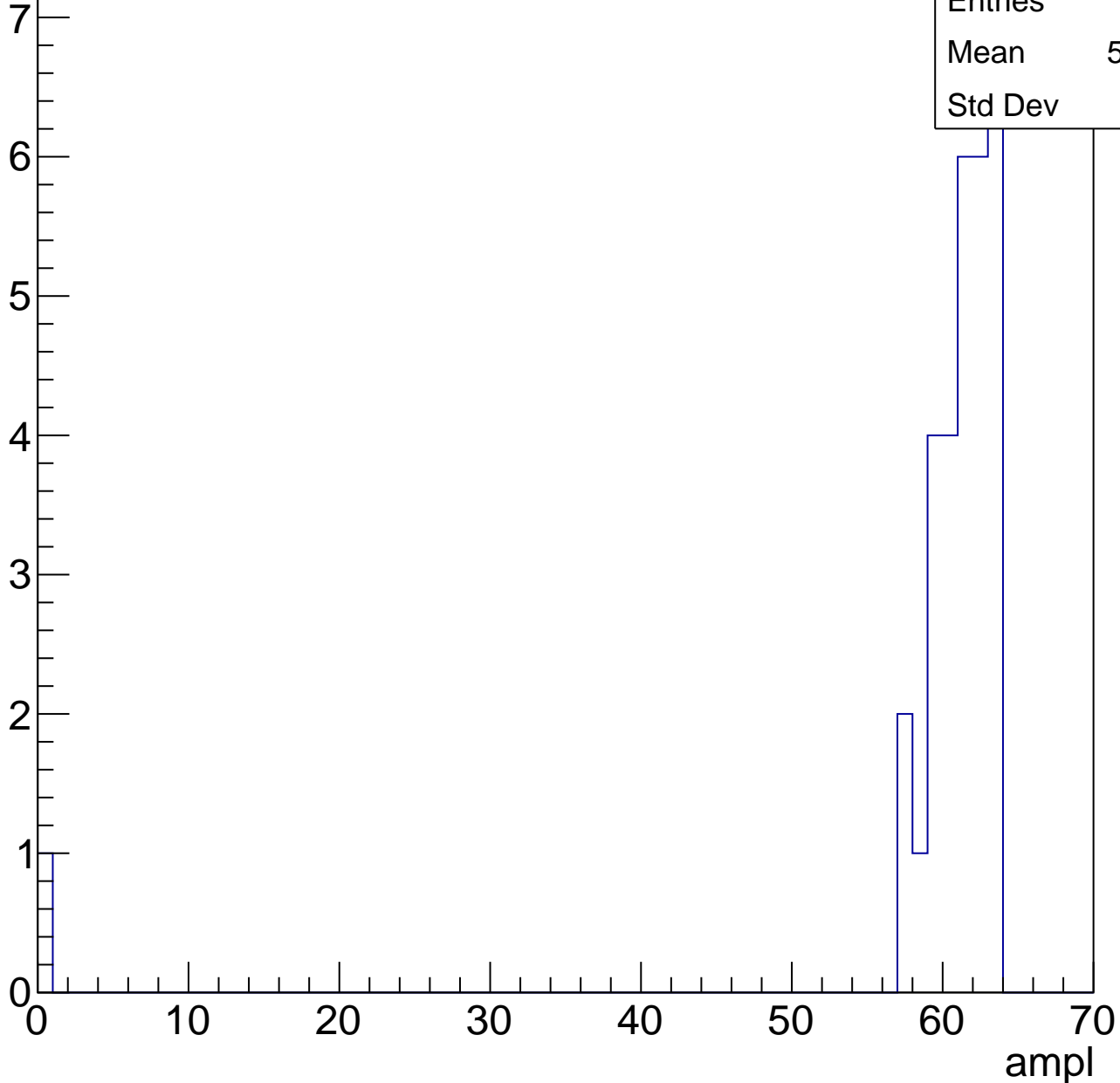


B1L102S, U26-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	58.94
Std Dev	10.9



B1L102S, U26-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

ampl

B1L102S, U26-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch28, adc0

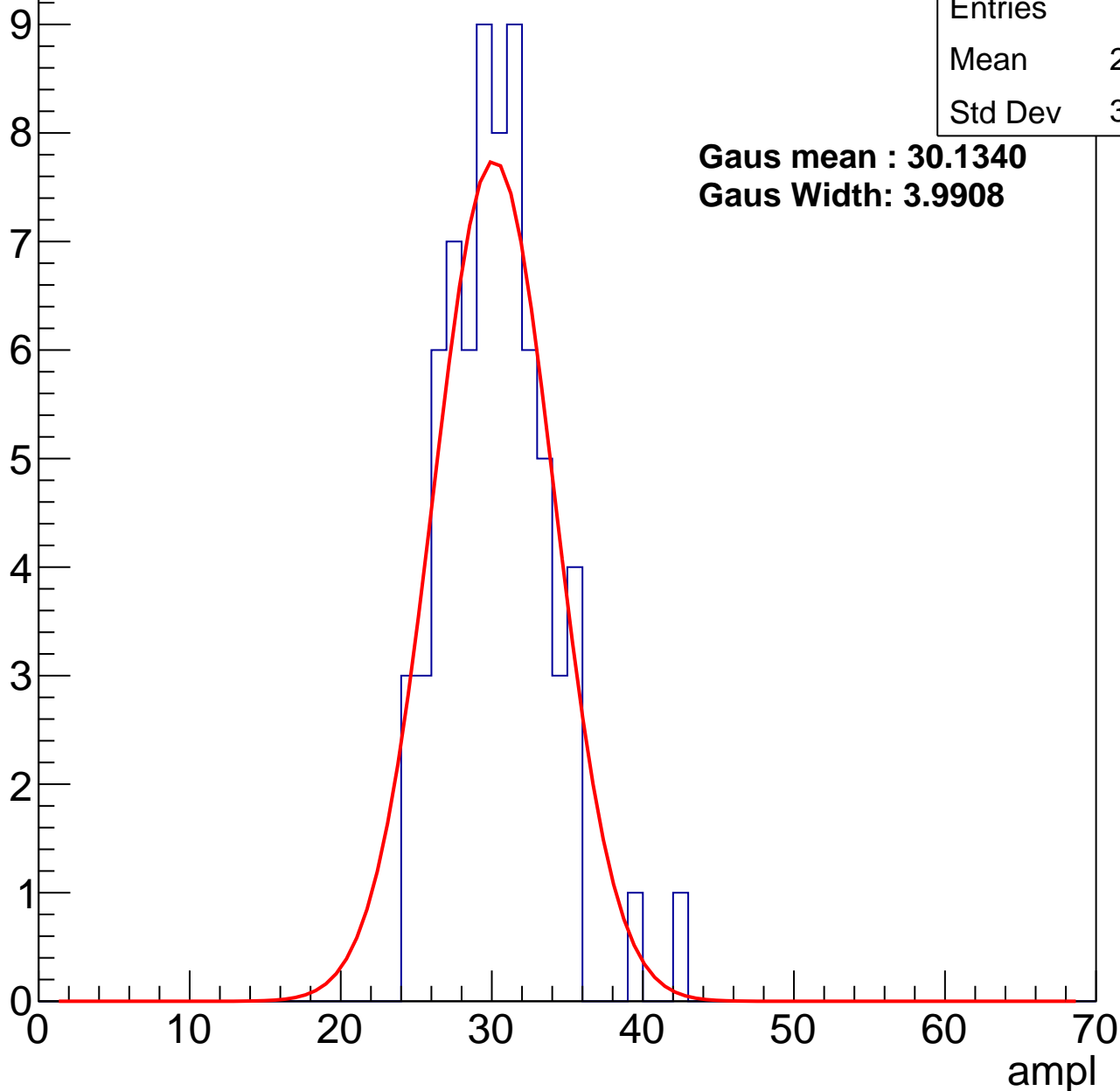
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.86
Std Dev	3.408

Gaus mean : 30.1340

Gaus Width: 3.9908



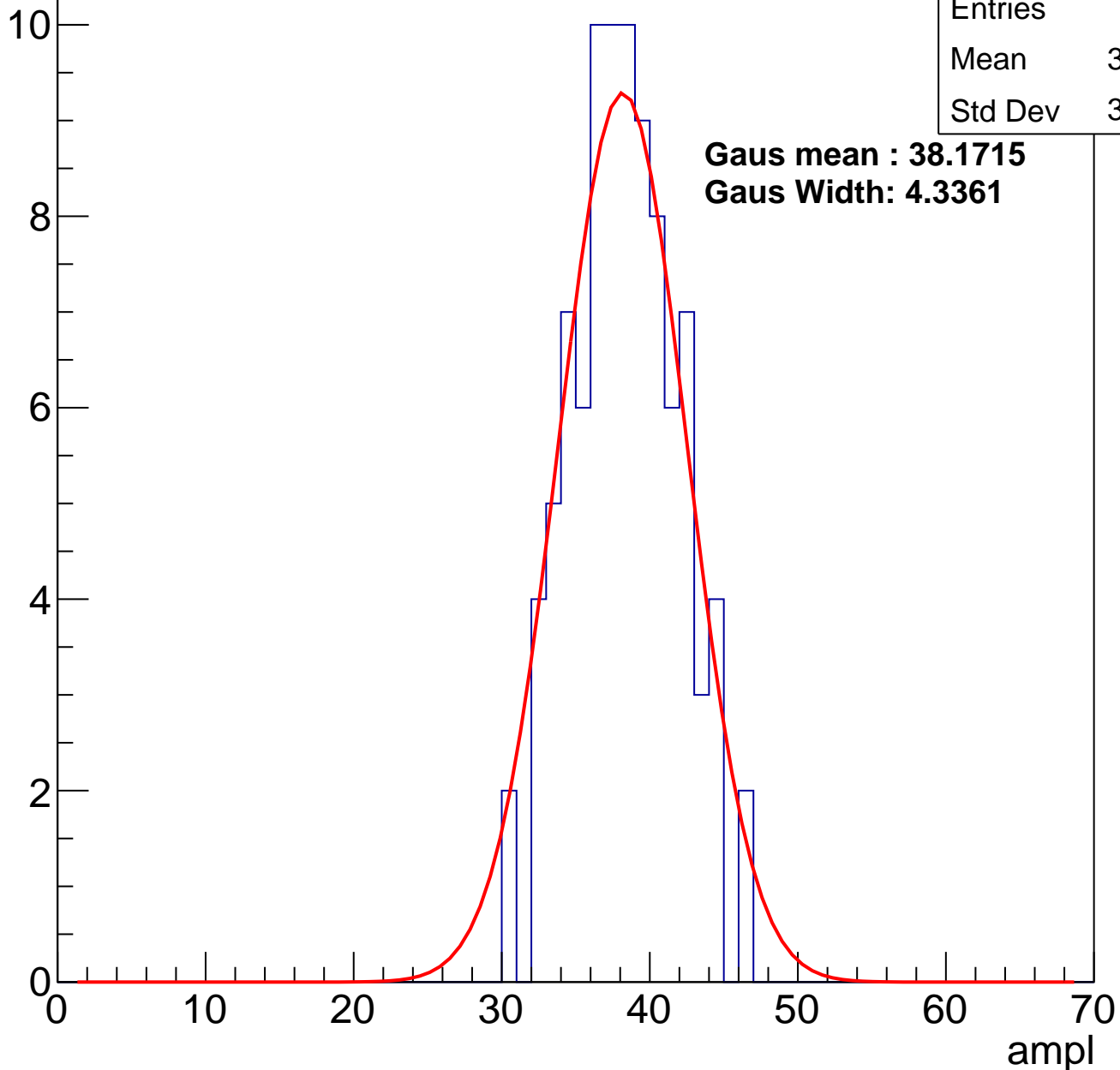
B1L102S, U26-ch28, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	37.84
Std Dev	3.539

Gaus mean : 38.1715
Gaus Width: 4.3361

Entry



B1L102S, U26-ch28, adc2

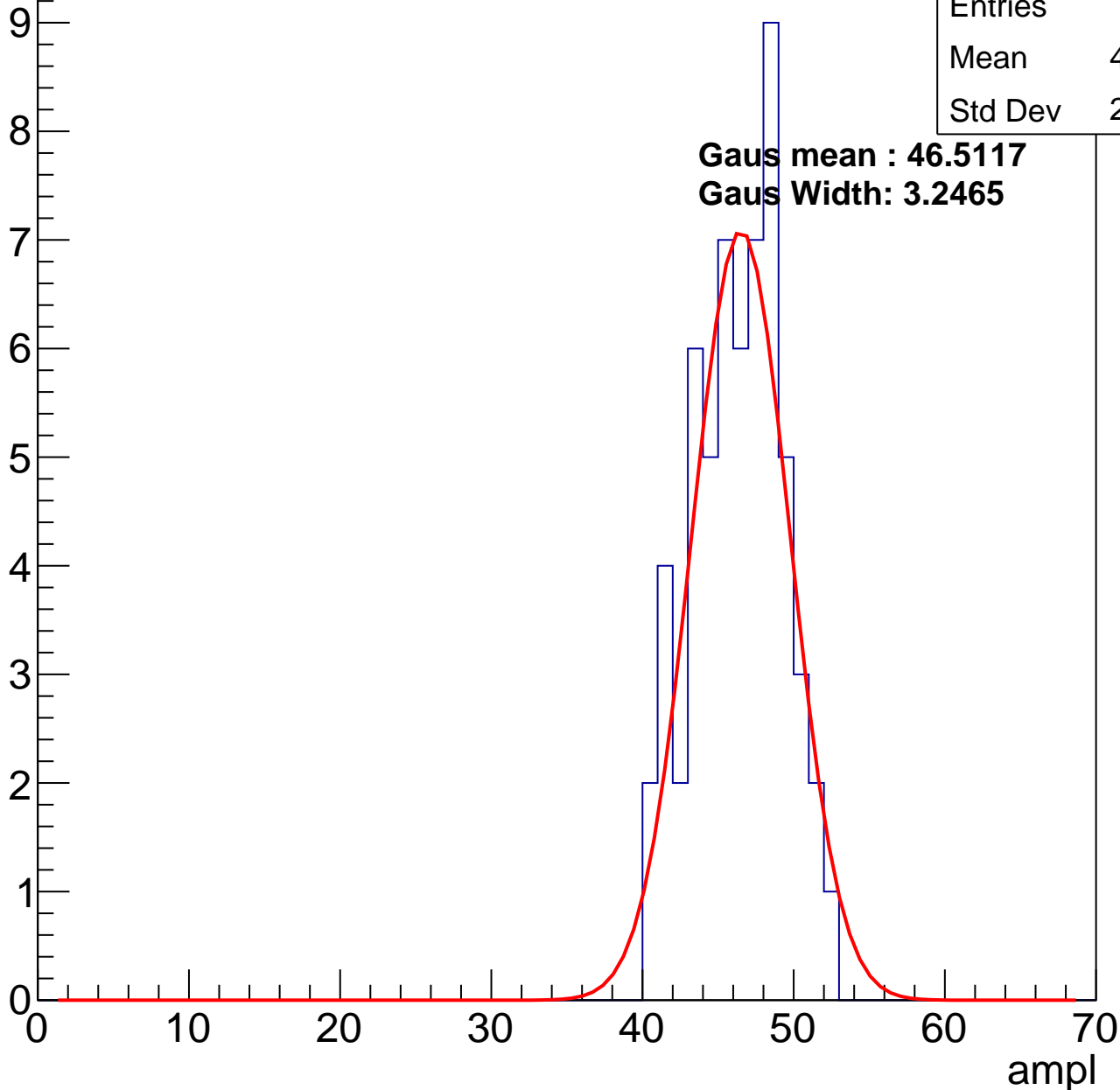
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	45.88
Std Dev	2.929

Gaus mean : 46.5117

Gaus Width: 3.2465



B1L102S, U26-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	51.45
Std Dev	3.509

Entry

10

8

6

4

2

0

0

10

20

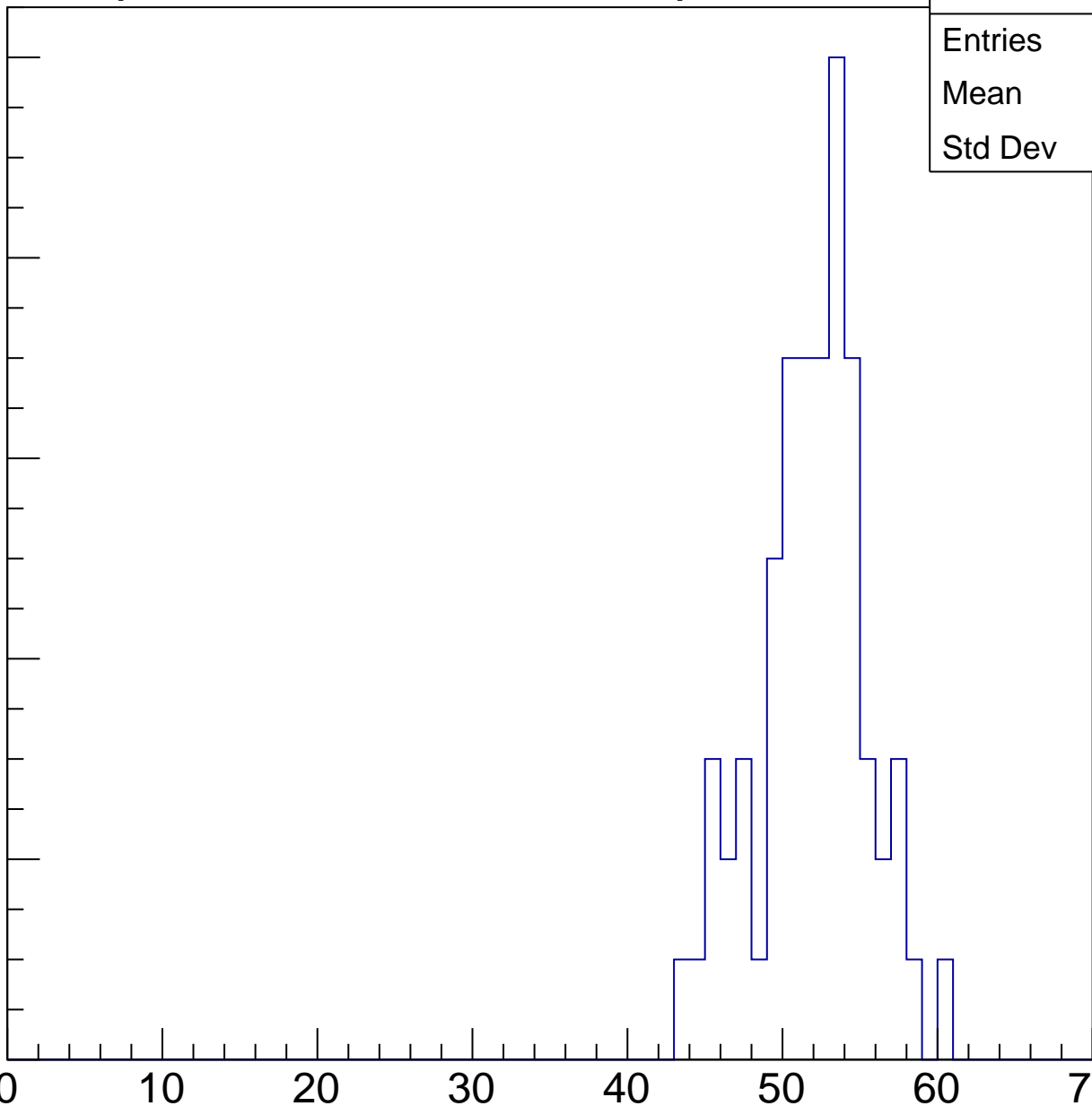
30

40

50

60

ampl

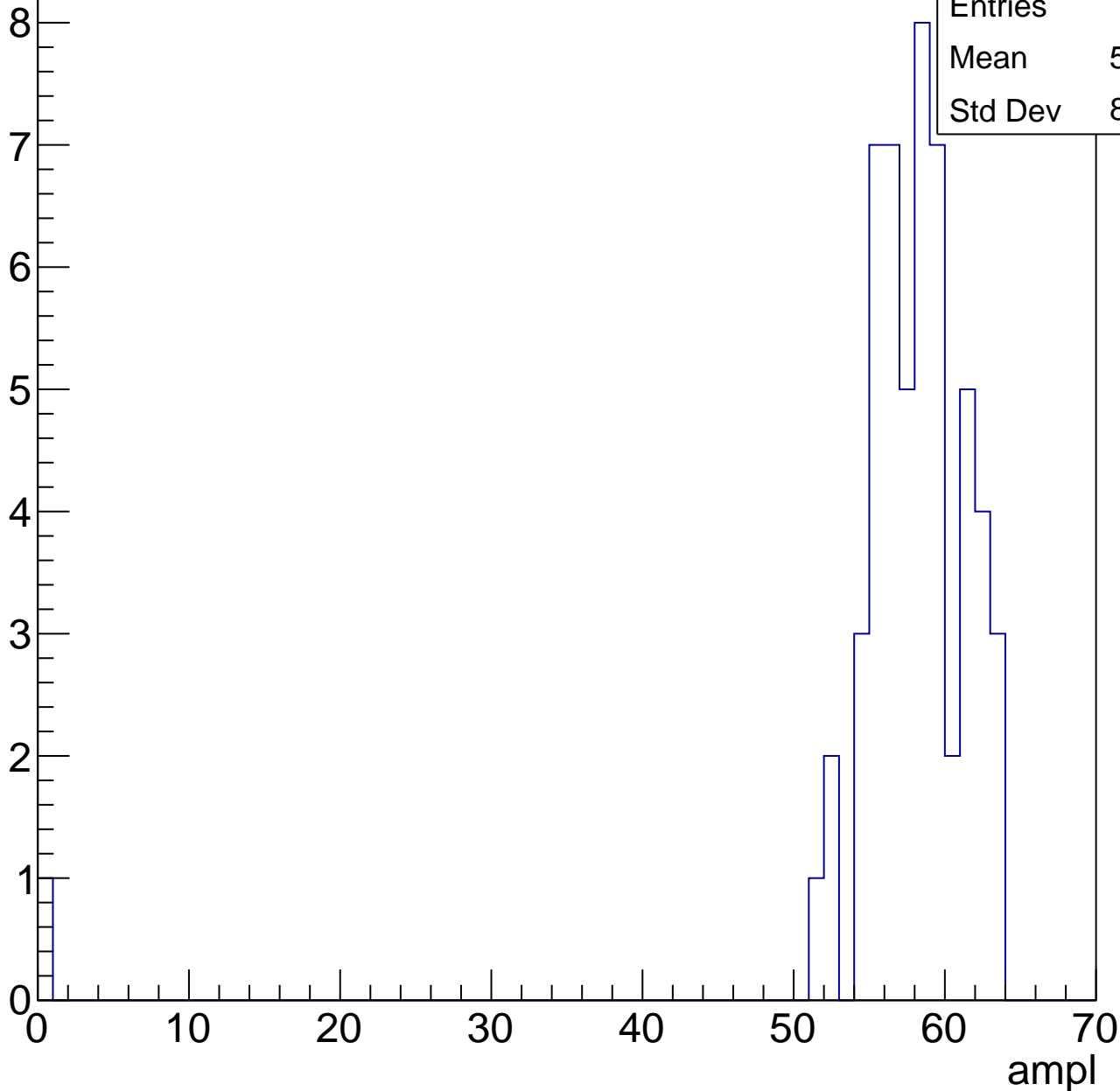


B1L102S, U26-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.69
Std Dev	8.239

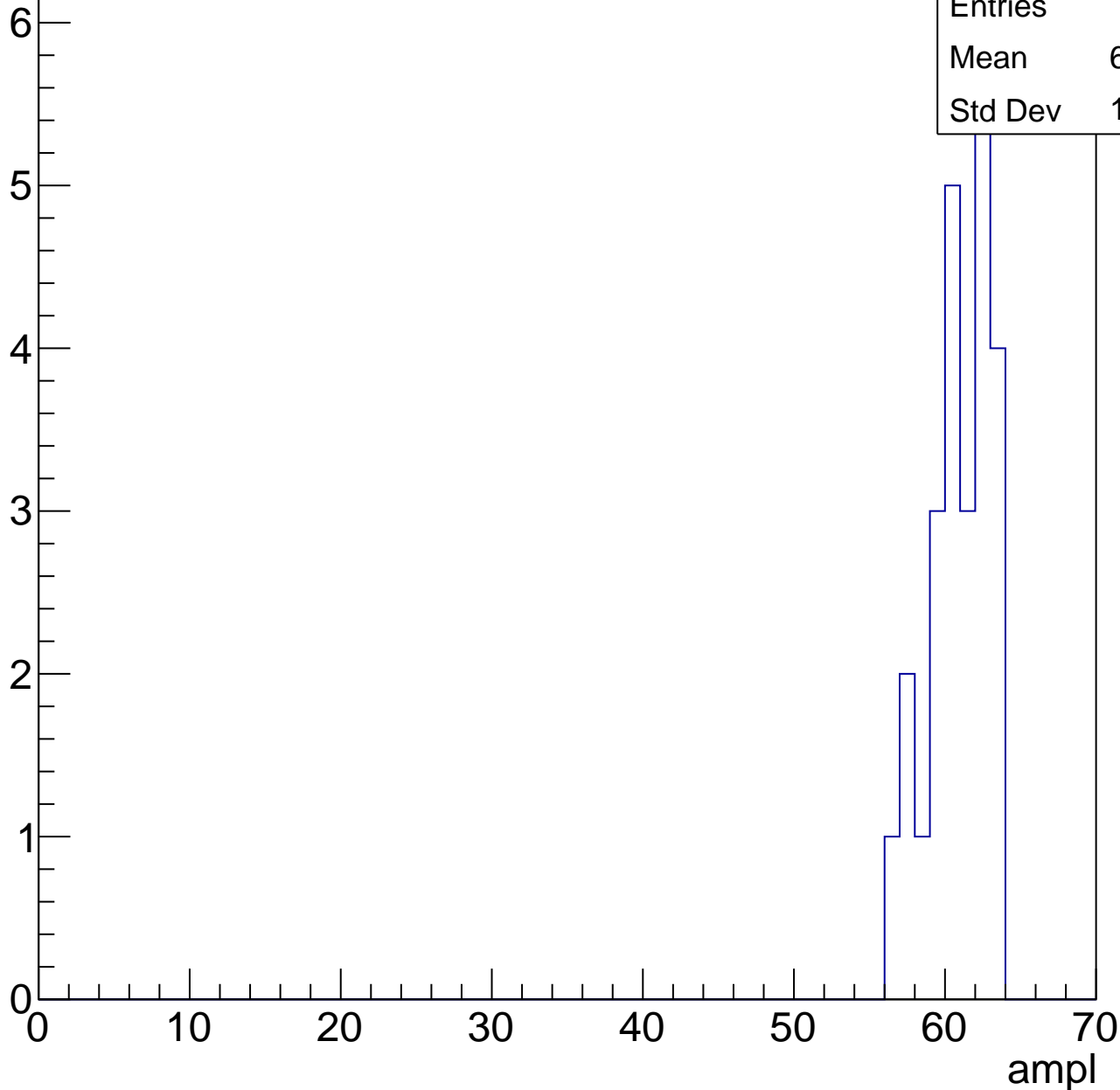


B1L102S, U26-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

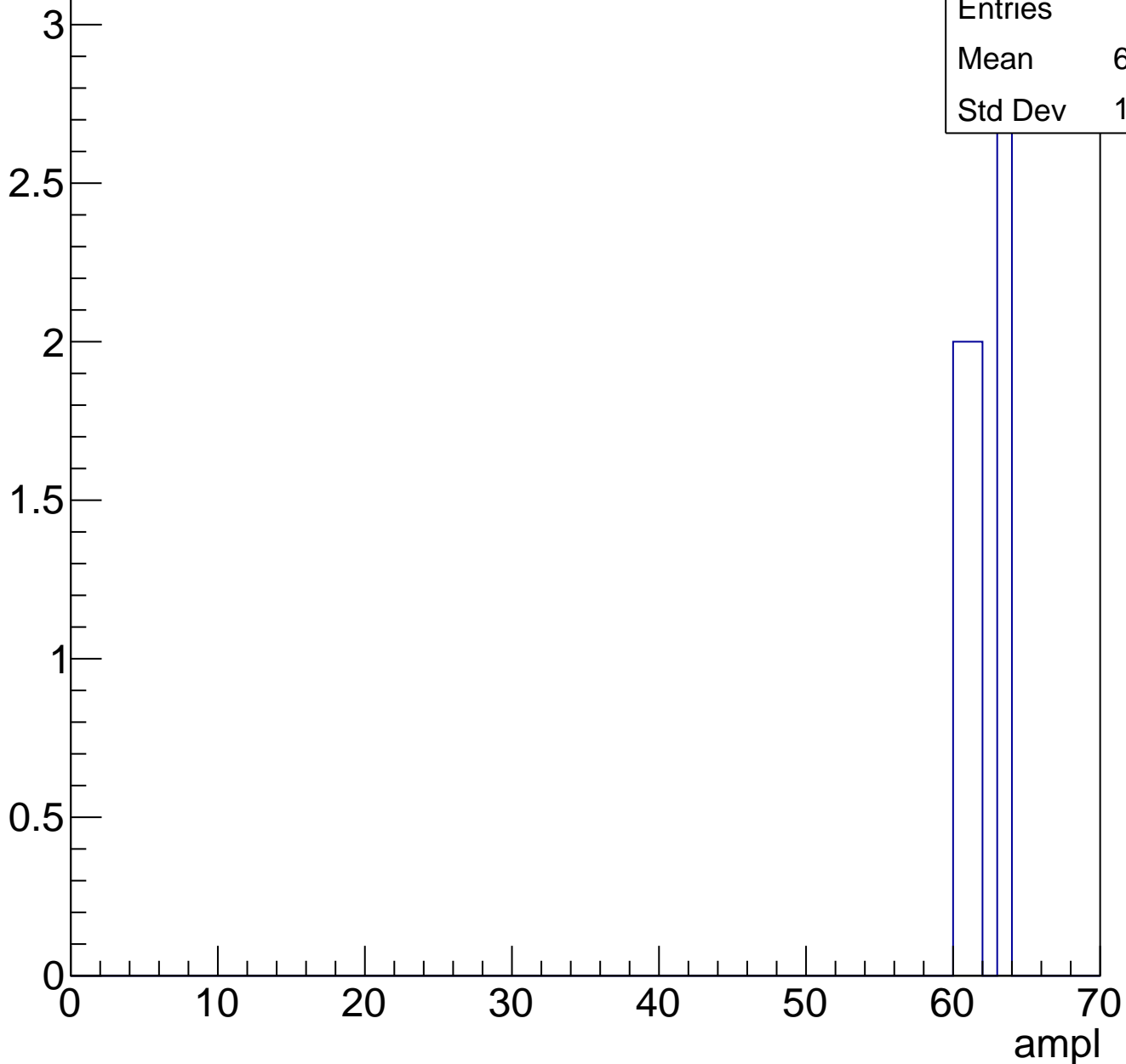
Entries	25
Mean	60.48
Std Dev	1.982



B1L102S, U26-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch29, adc0

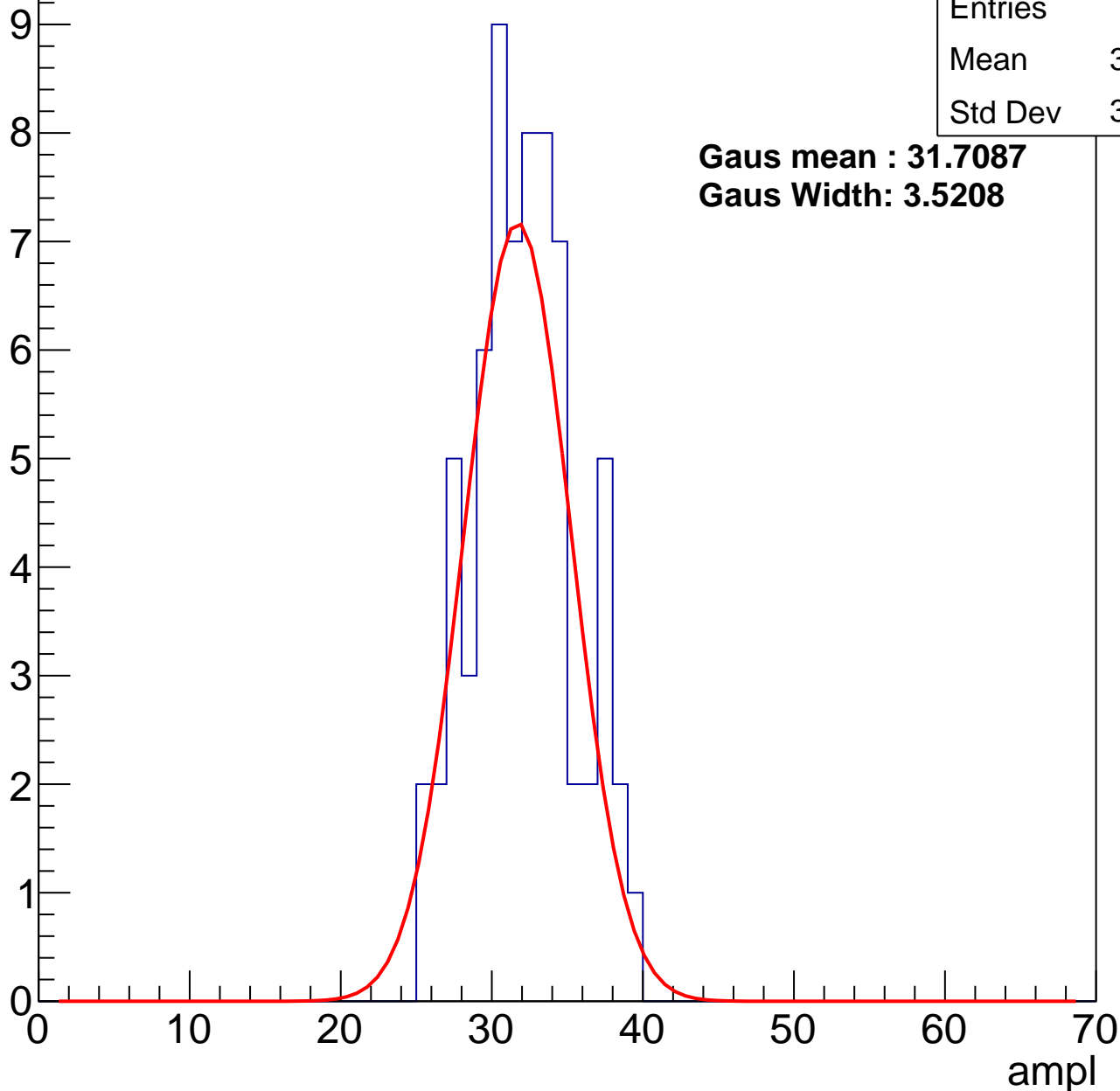
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	31.62
Std Dev	3.324

Gaus mean : 31.7087

Gaus Width: 3.5208



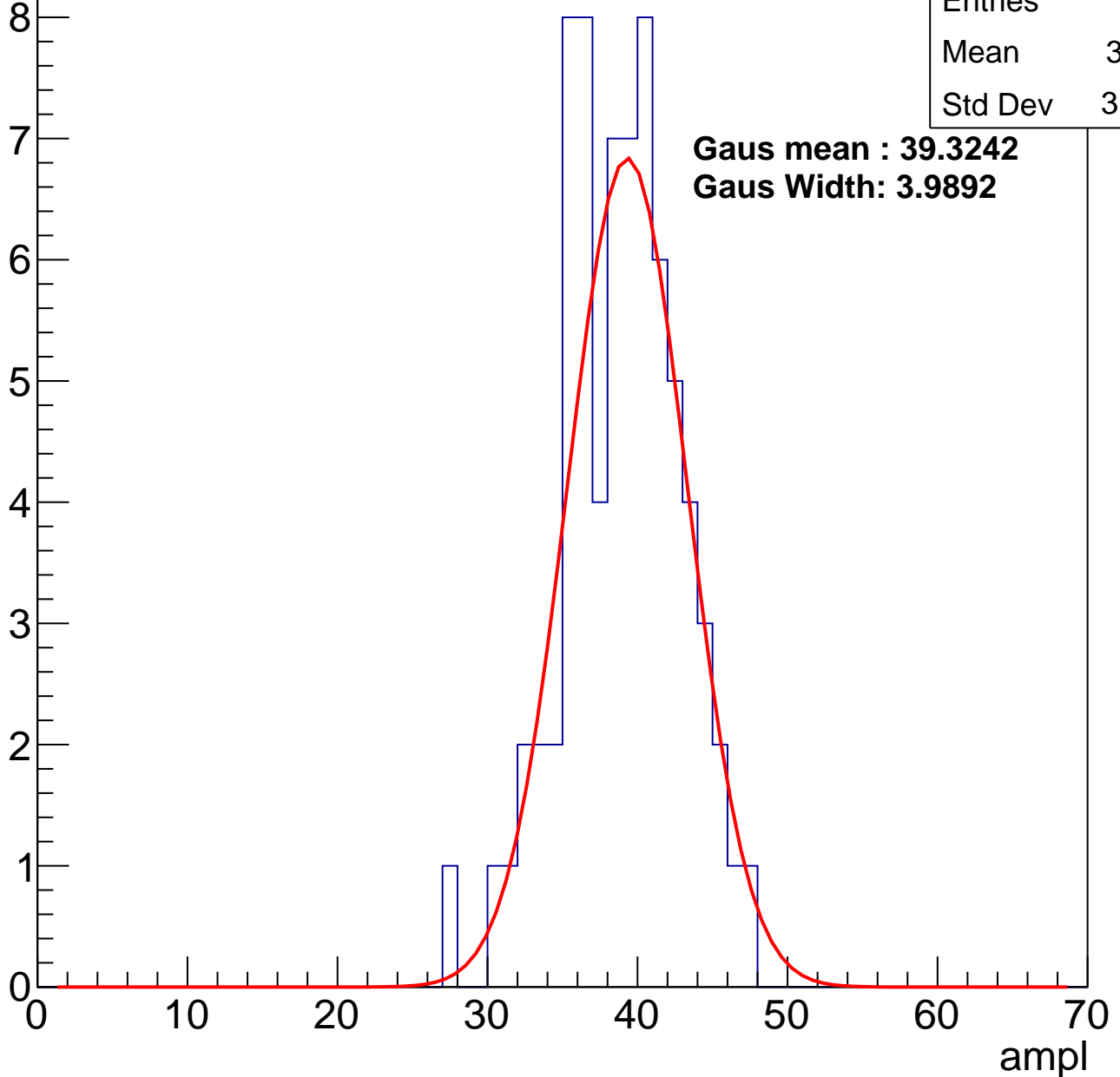
B1L102S, U26-ch29, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	38.41
Std Dev	3.895

Gaus mean : 39.3242
Gaus Width: 3.9892



B1L102S, U26-ch29, adc2

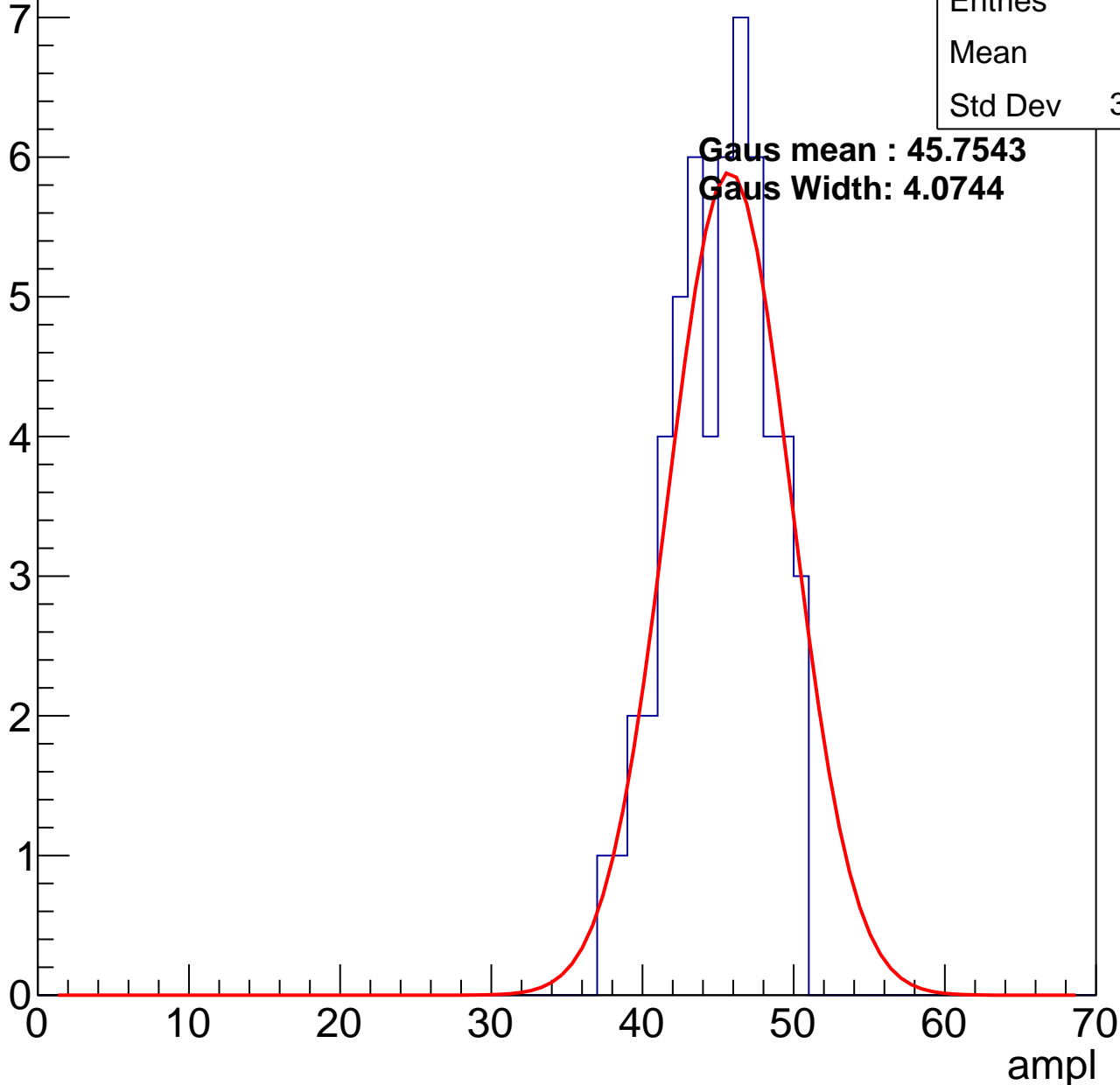
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	44.6
Std Dev	3.217

Gaus mean : 45.7543

Gaus Width: 4.0744

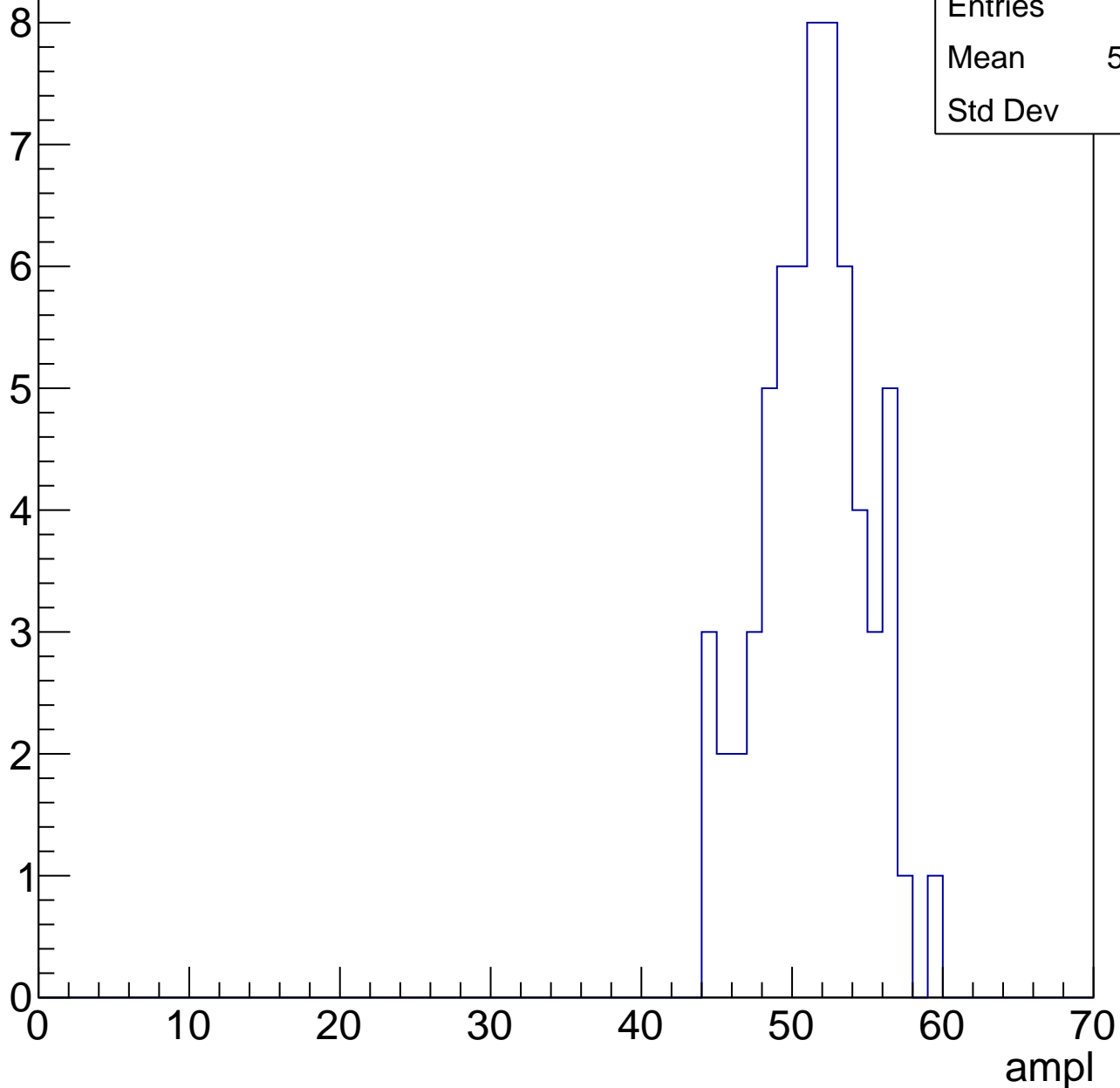


B1L102S, U26-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.92
Std Dev	3.41

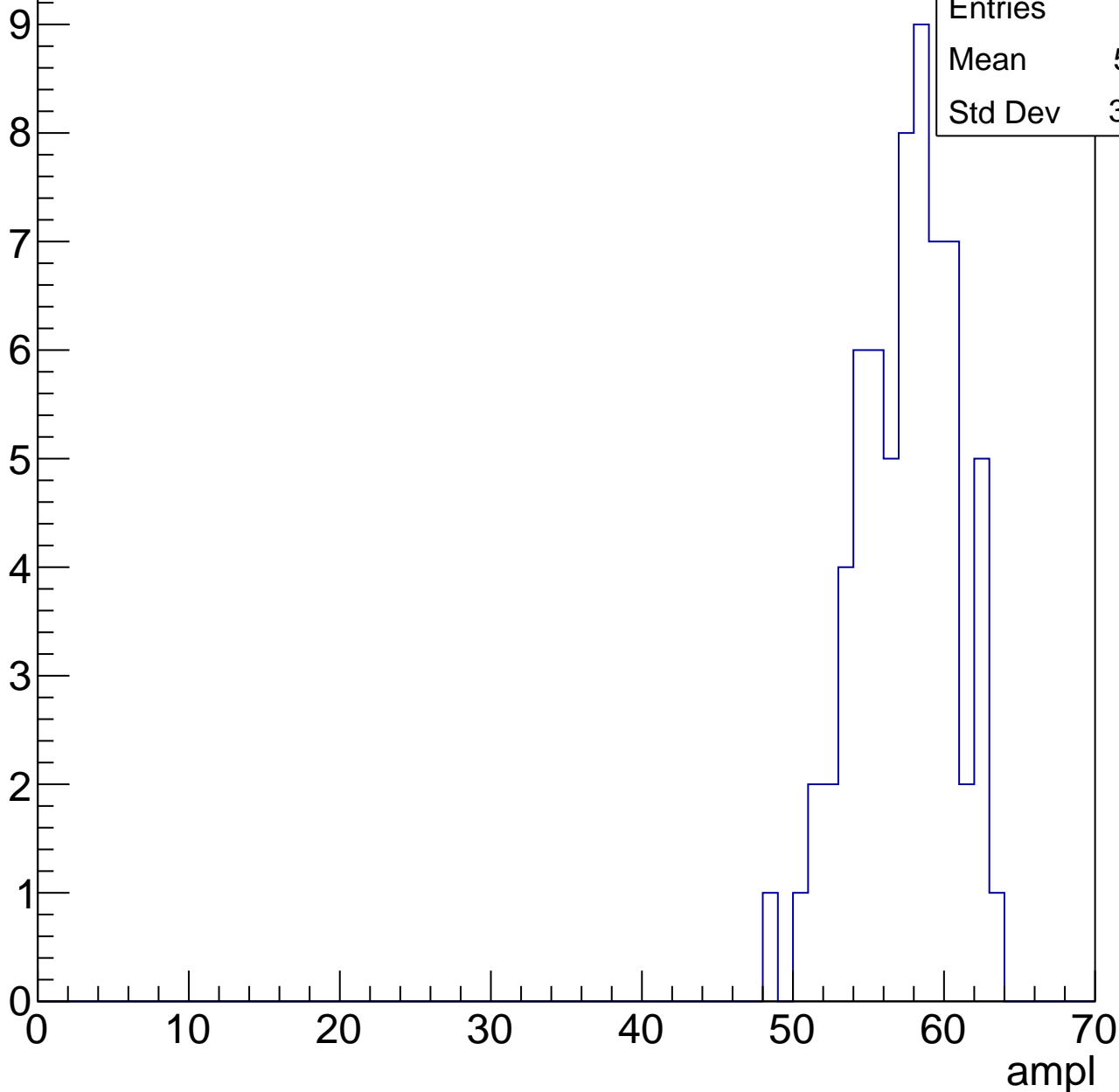


B1L102S, U26-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	56.91
Std Dev	3.237

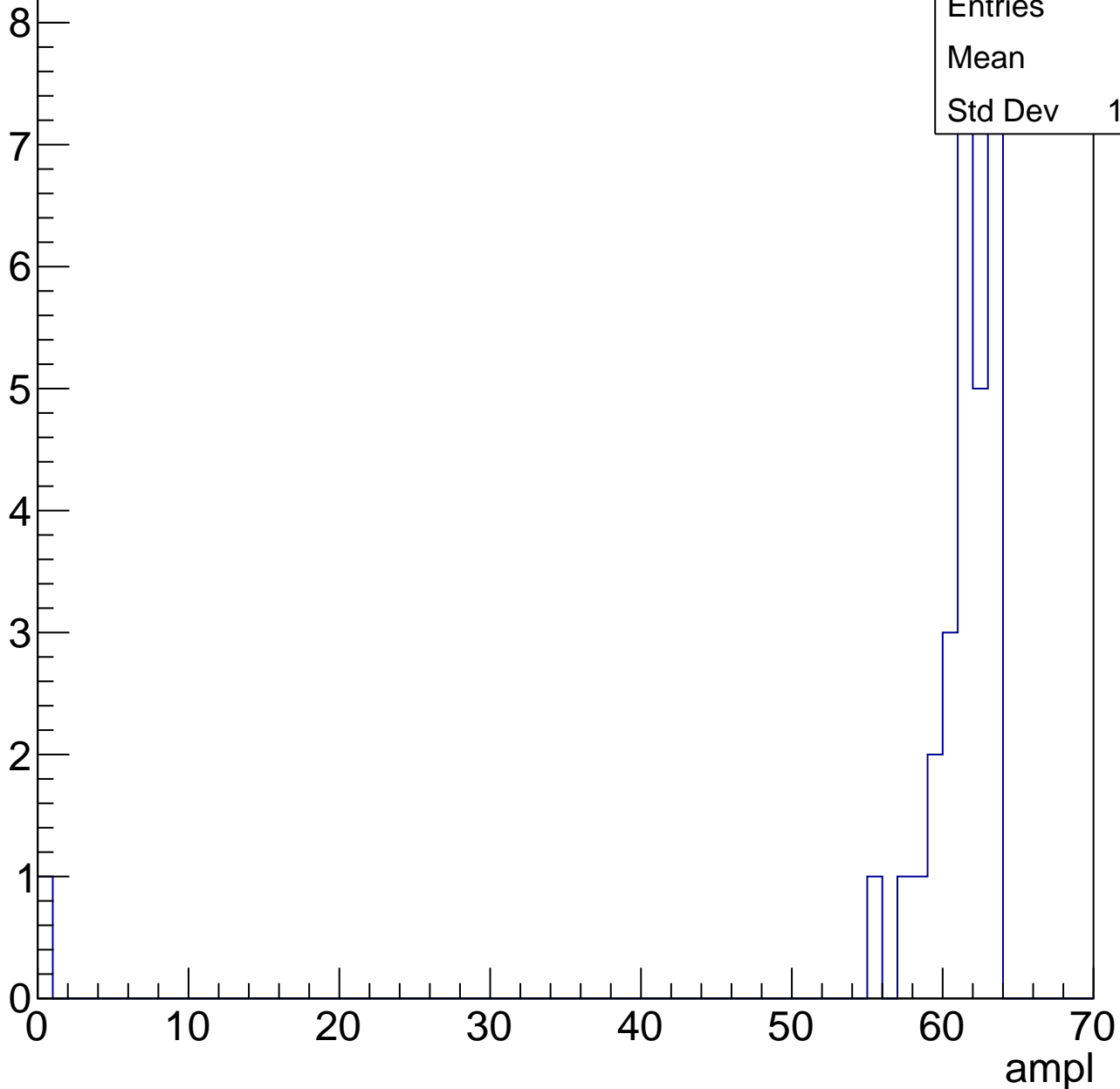


B1L102S, U26-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	59
Std Dev	11.12



B1L102S, U26-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch30, adc0

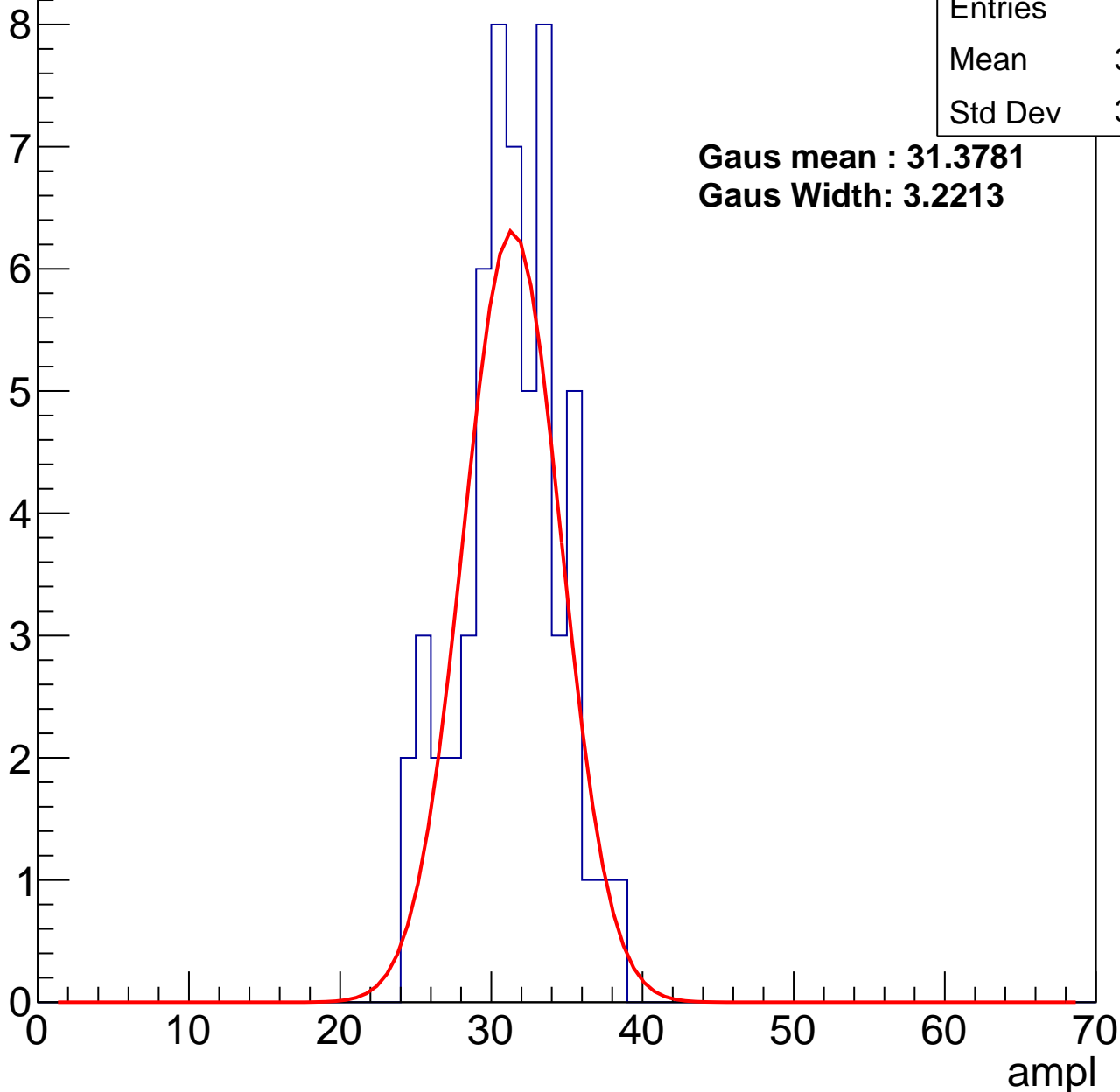
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	30.81
Std Dev	3.241

Gaus mean : 31.3781

Gaus Width: 3.2213



B1L102S, U26-ch30, adc1

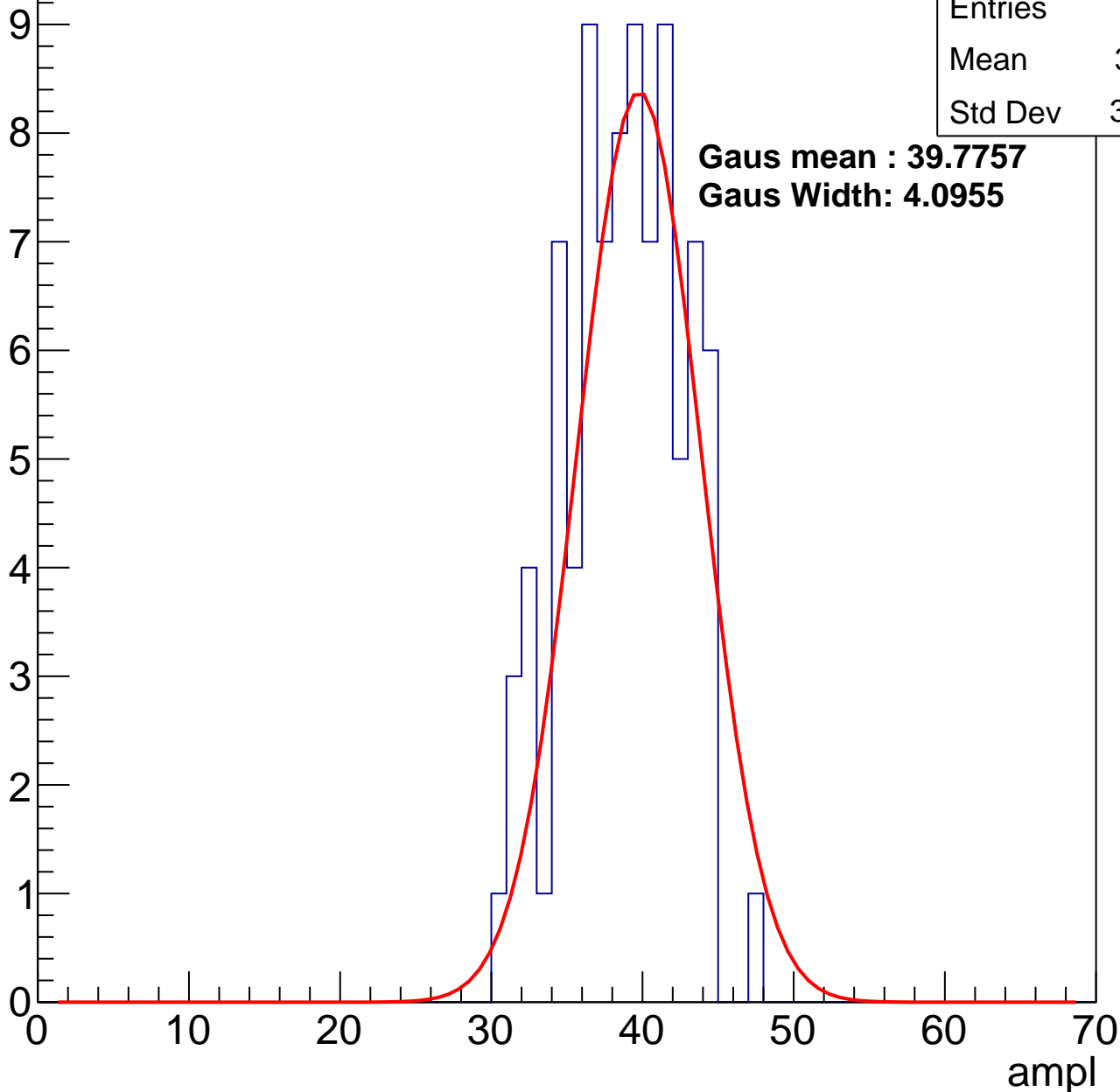
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	38.31
Std Dev	3.743

Gaus mean : 39.7757

Gaus Width: 4.0955



B1L102S, U26-ch30, adc2

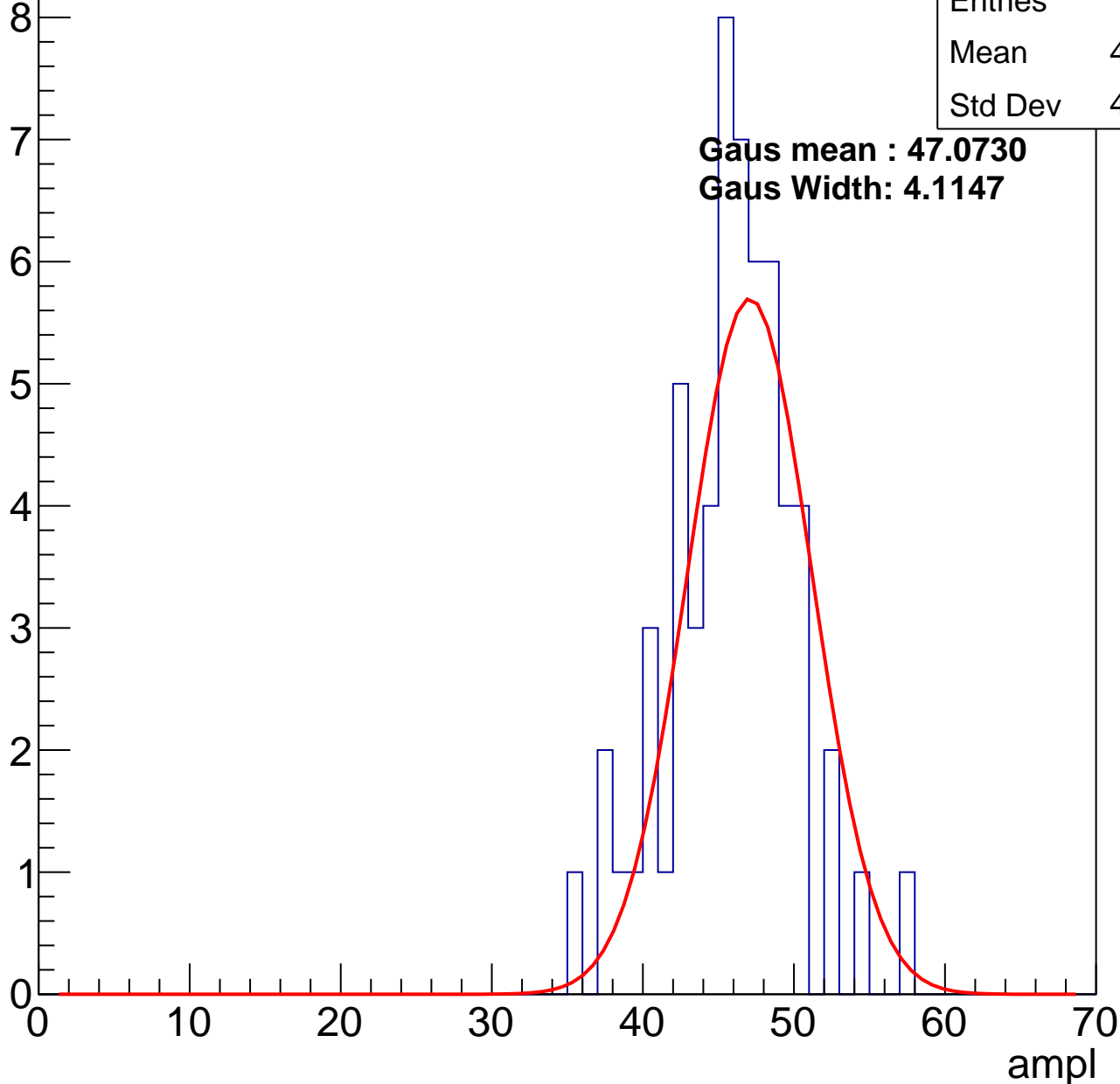
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	45.42
Std Dev	4.136

Gaus mean : 47.0730

Gaus Width: 4.1147

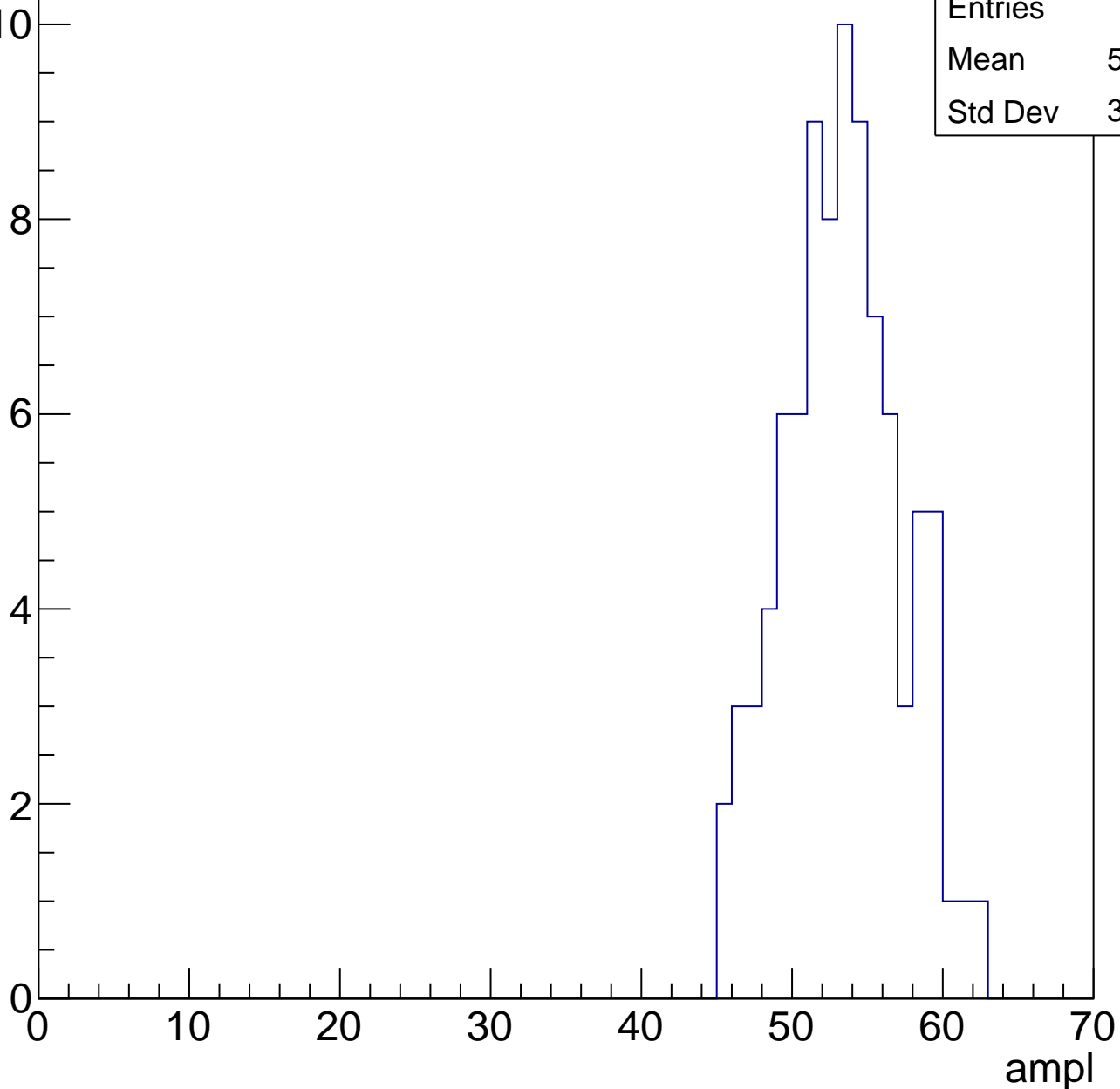


B1L102S, U26-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	52.88
Std Dev	3.842

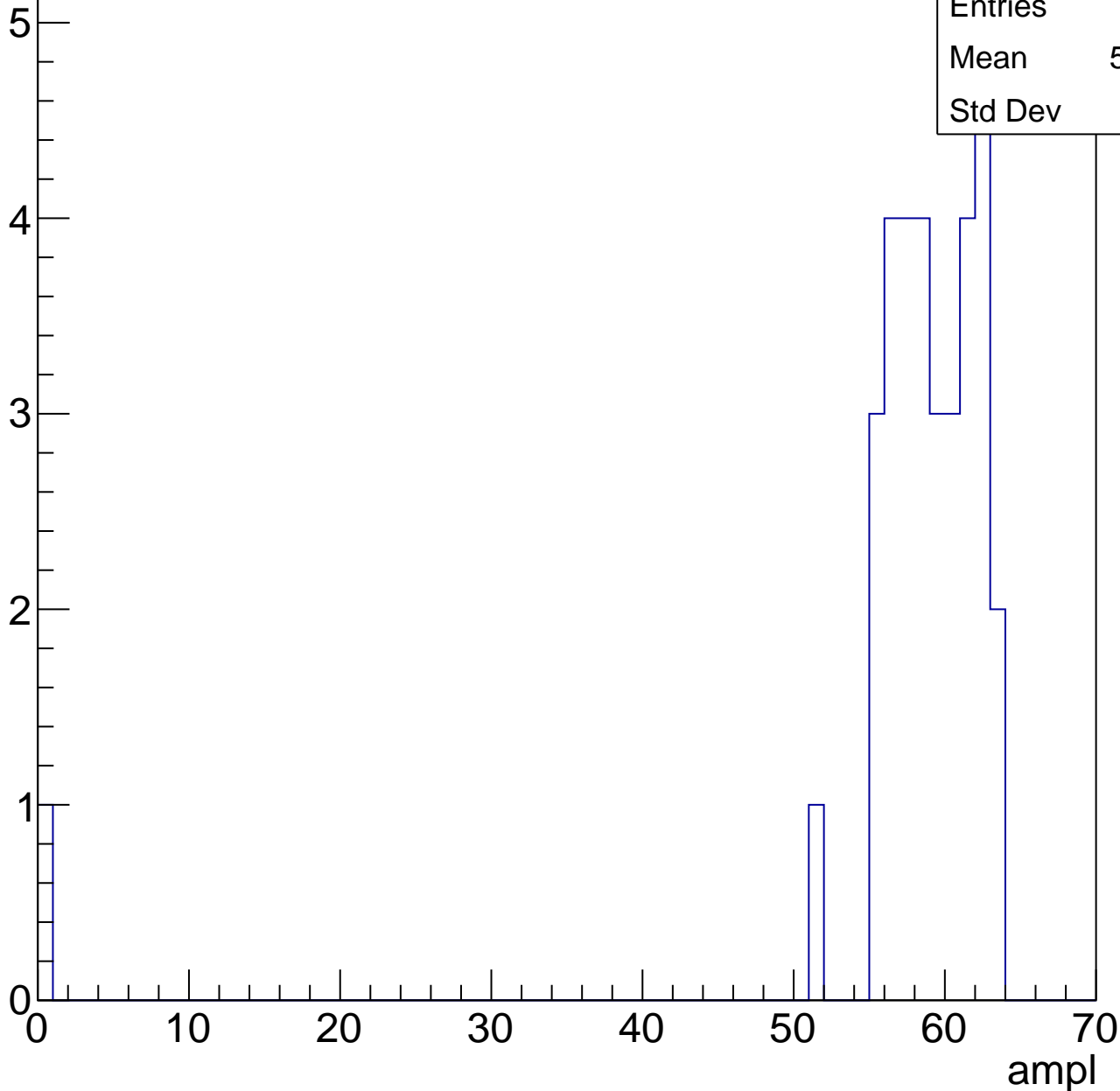


B1L102S, U26-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	56.97
Std Dev	10.3

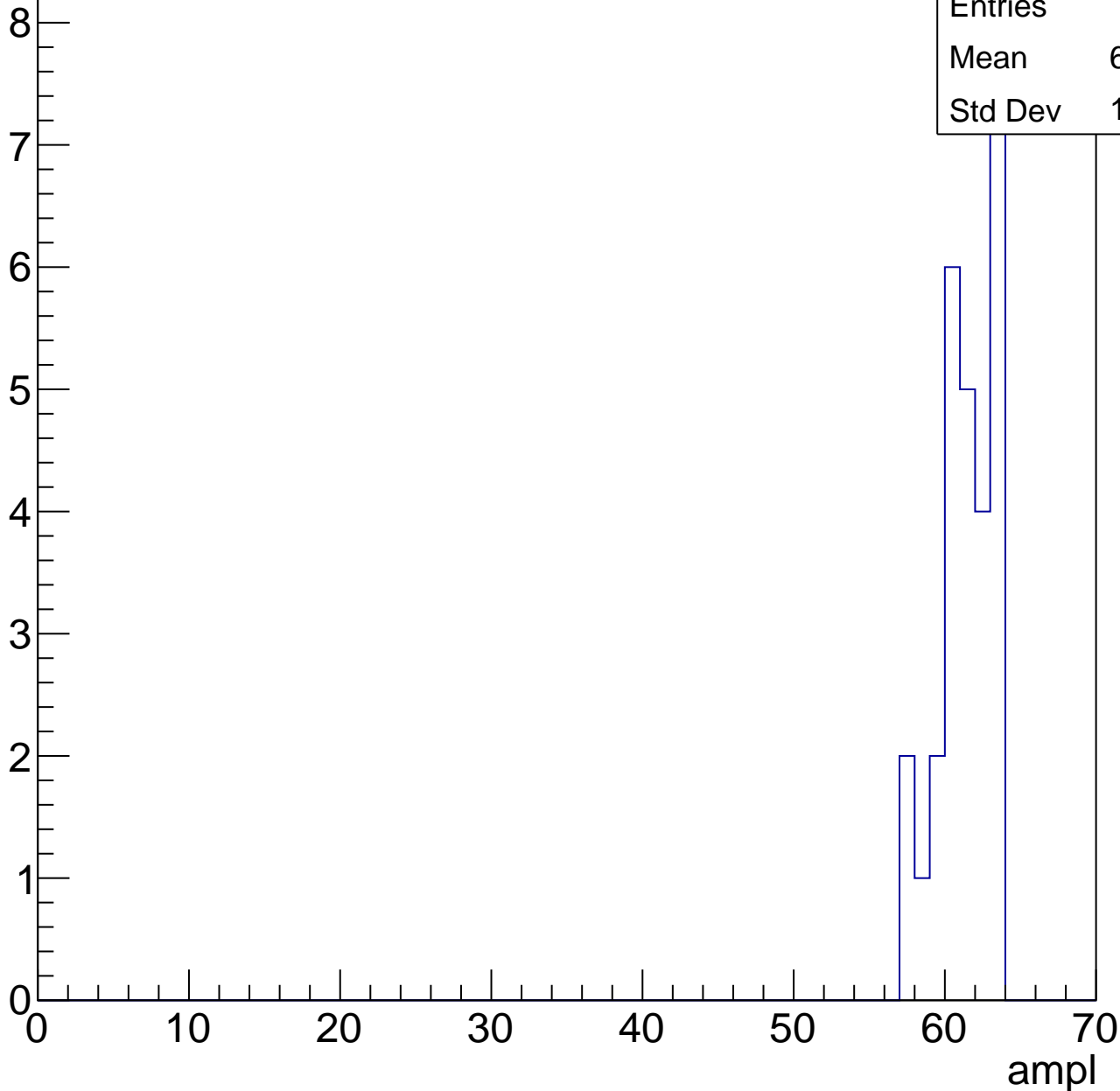


B1L102S, U26-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	60.96
Std Dev	1.802



B1L102S, U26-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U26-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch31, adc0

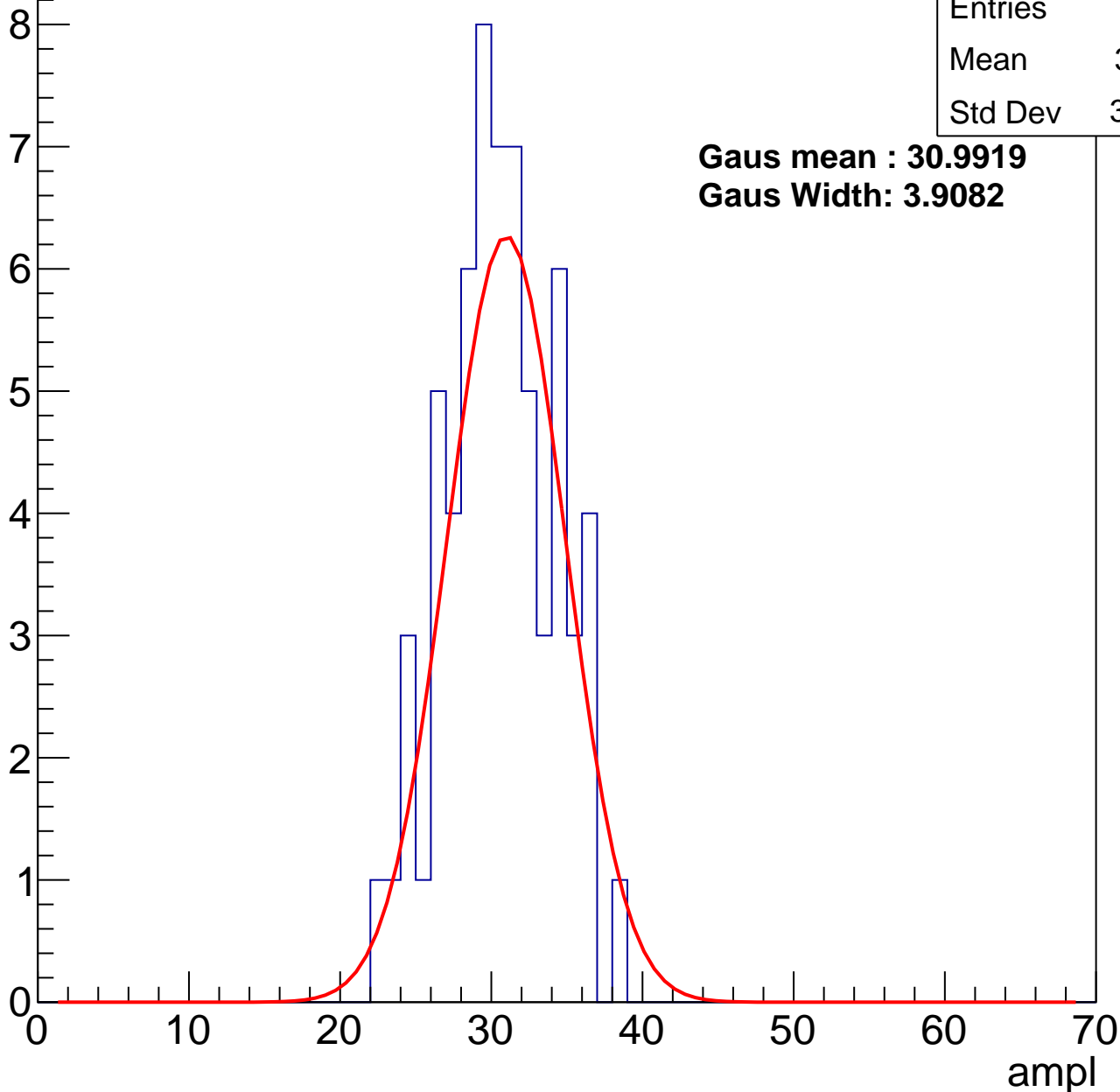
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.11
Std Dev	3.565

Gaus mean : 30.9919

Gaus Width: 3.9082



B1L102S, U26-ch31, adc1

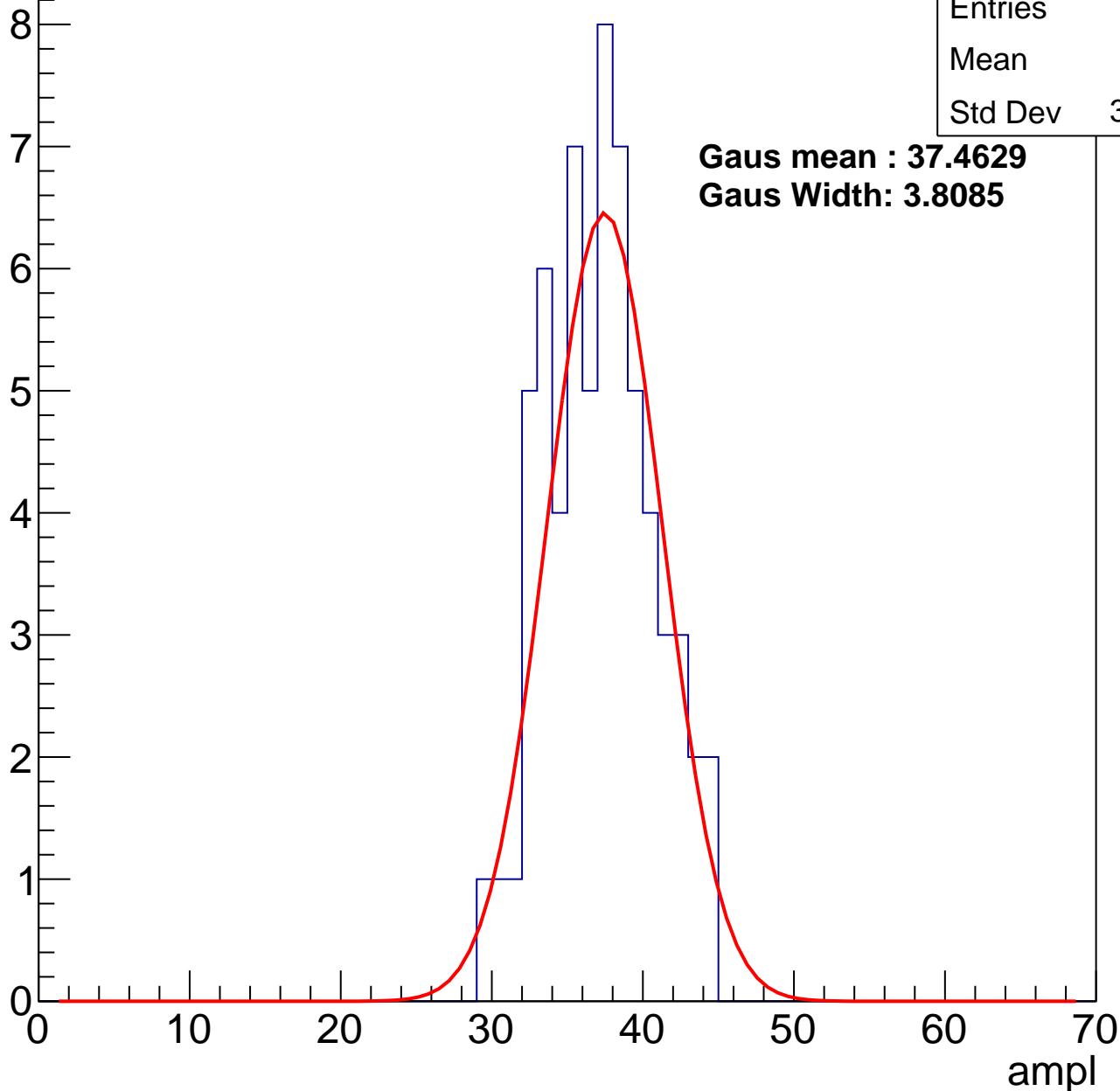
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.7
Std Dev	3.512

Gaus mean : 37.4629

Gaus Width: 3.8085



B1L102S, U26-ch31, adc2

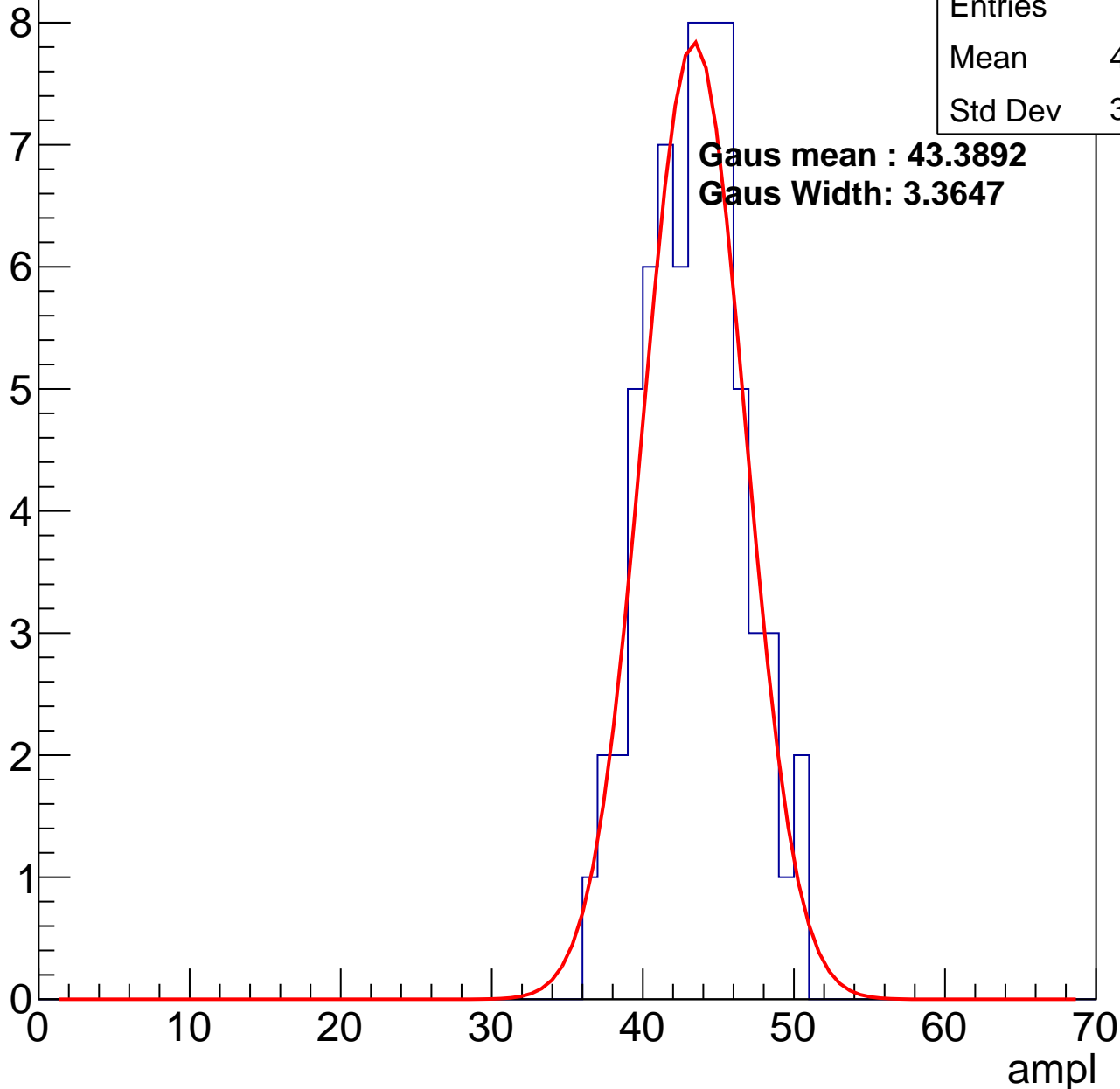
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.99
Std Dev	3.188

Gaus mean : 43.3892

Gaus Width: 3.3647

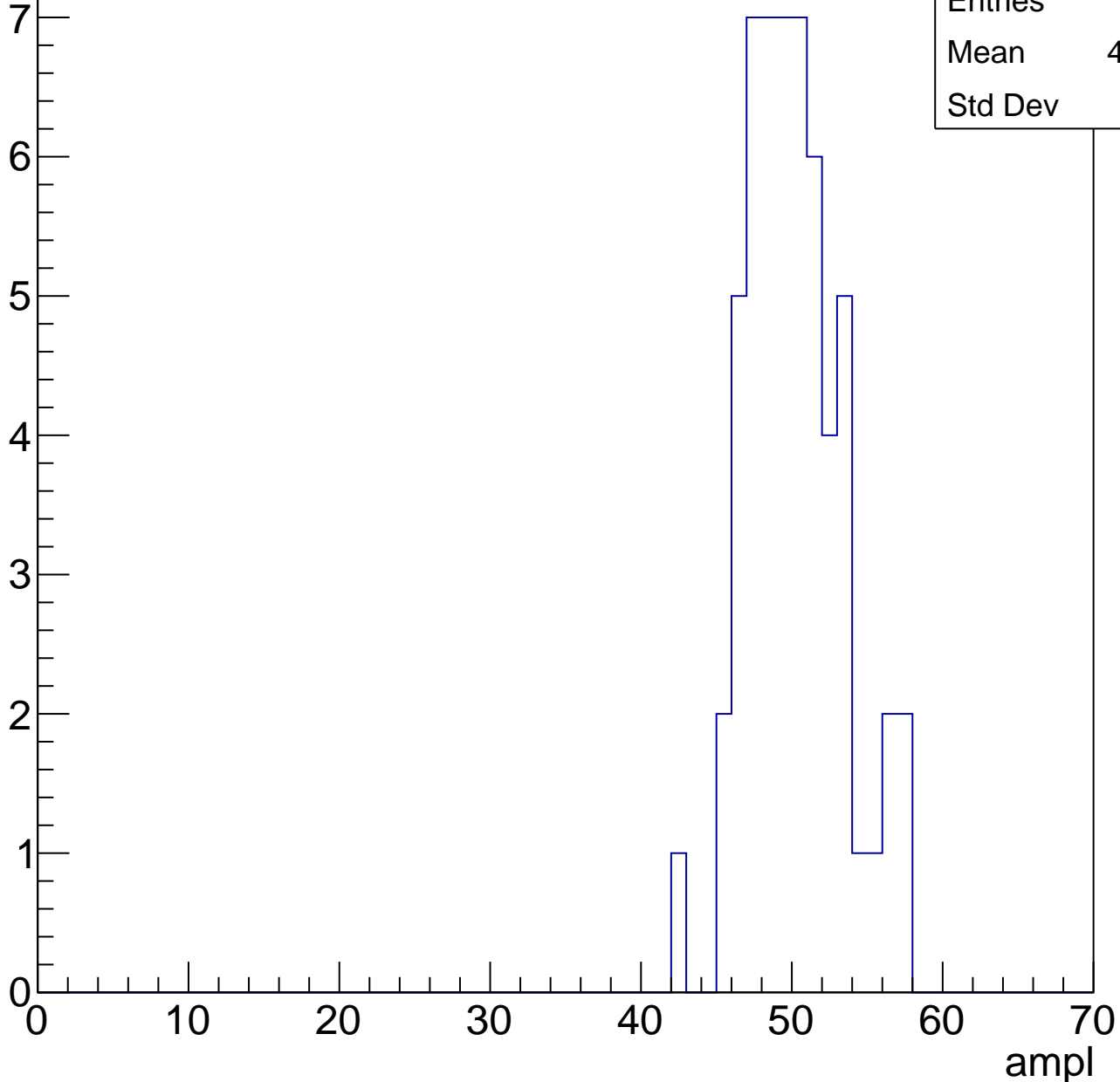


B1L102S, U26-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

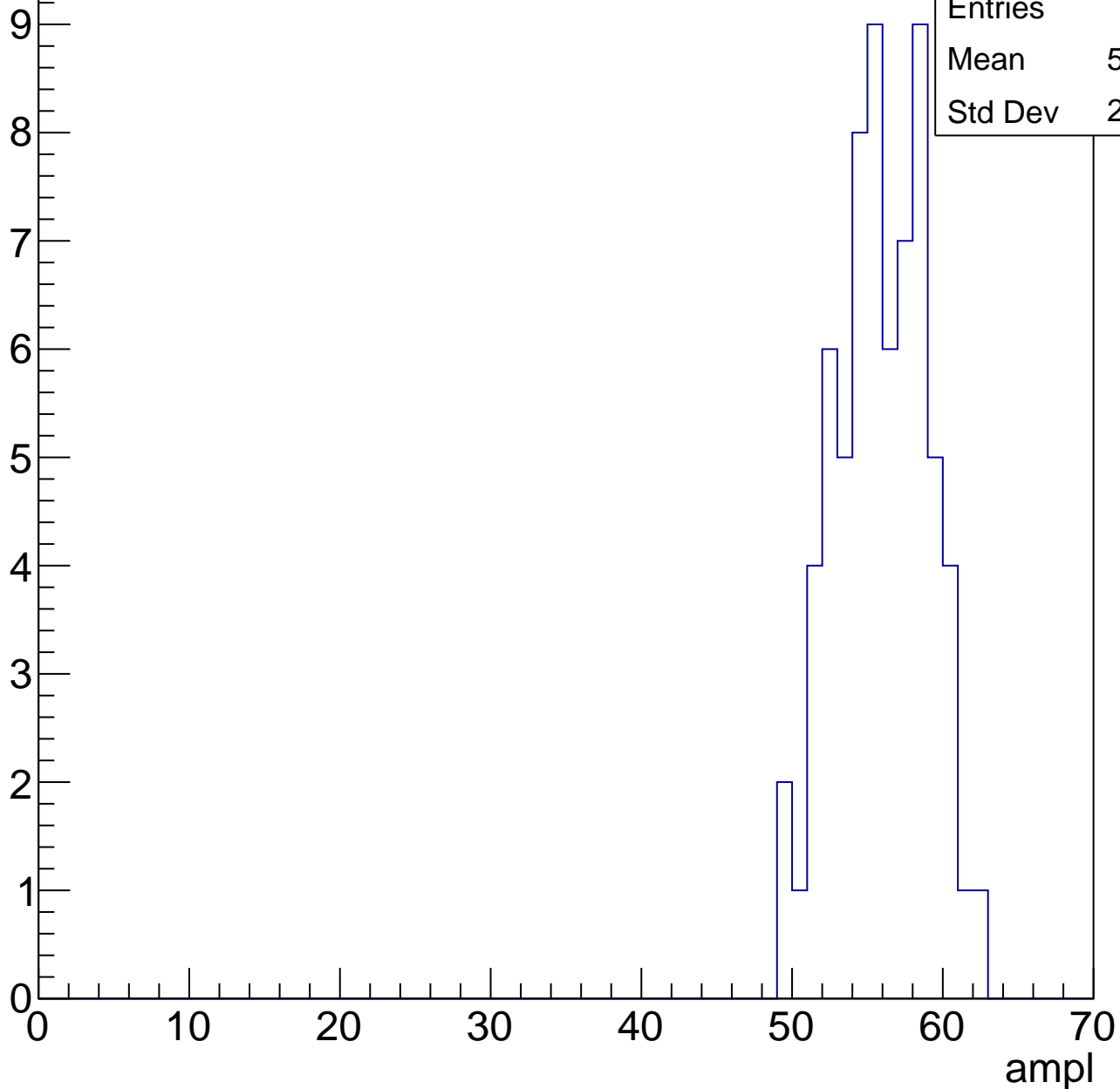
Entries	57
Mean	49.72
Std Dev	3.15



B1L102S, U26-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



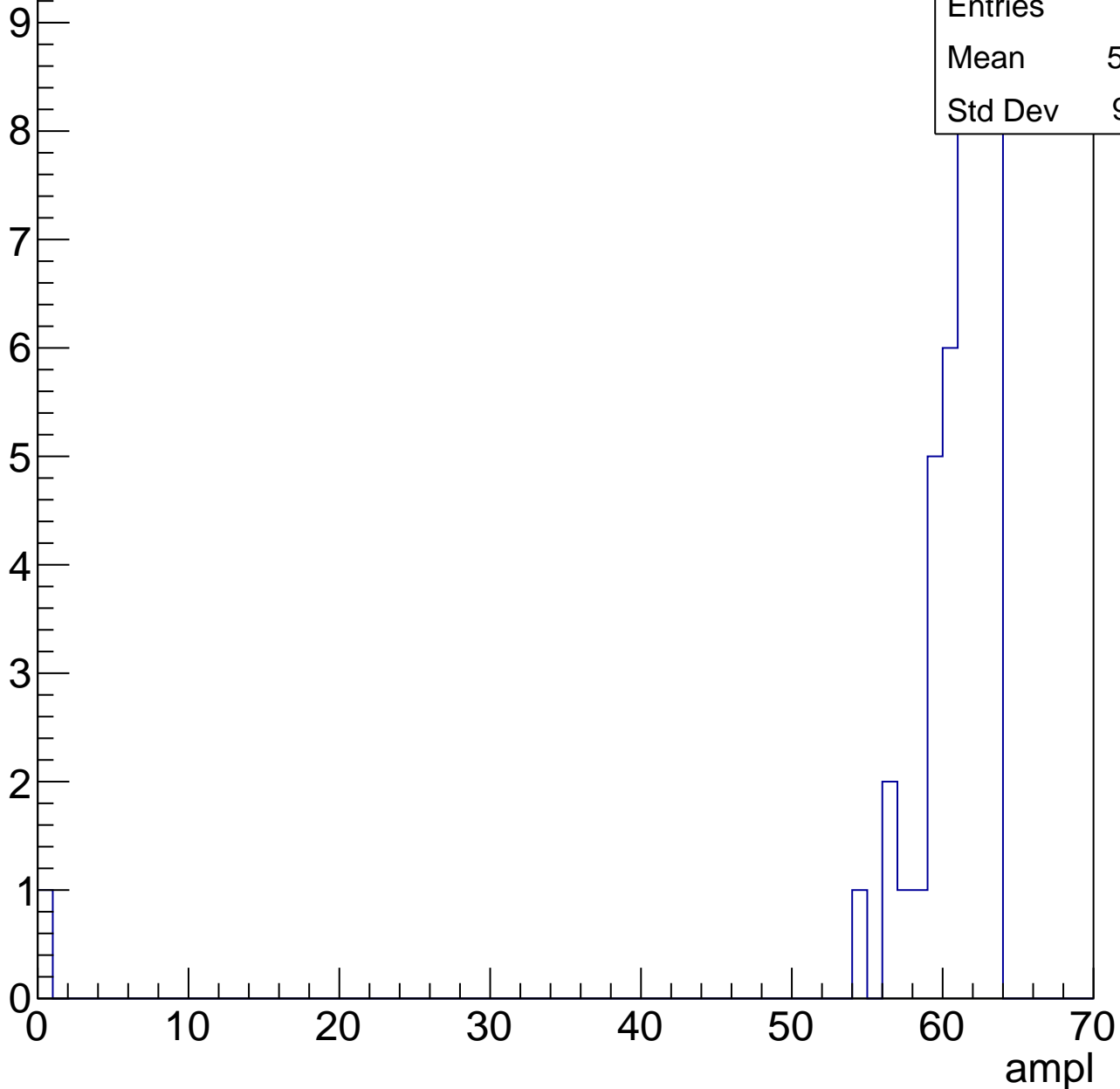
Entries	68
Mean	55.46
Std Dev	2.992

B1L102S, U26-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.26
Std Dev	9.381



B1L102S, U26-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch32, adc0

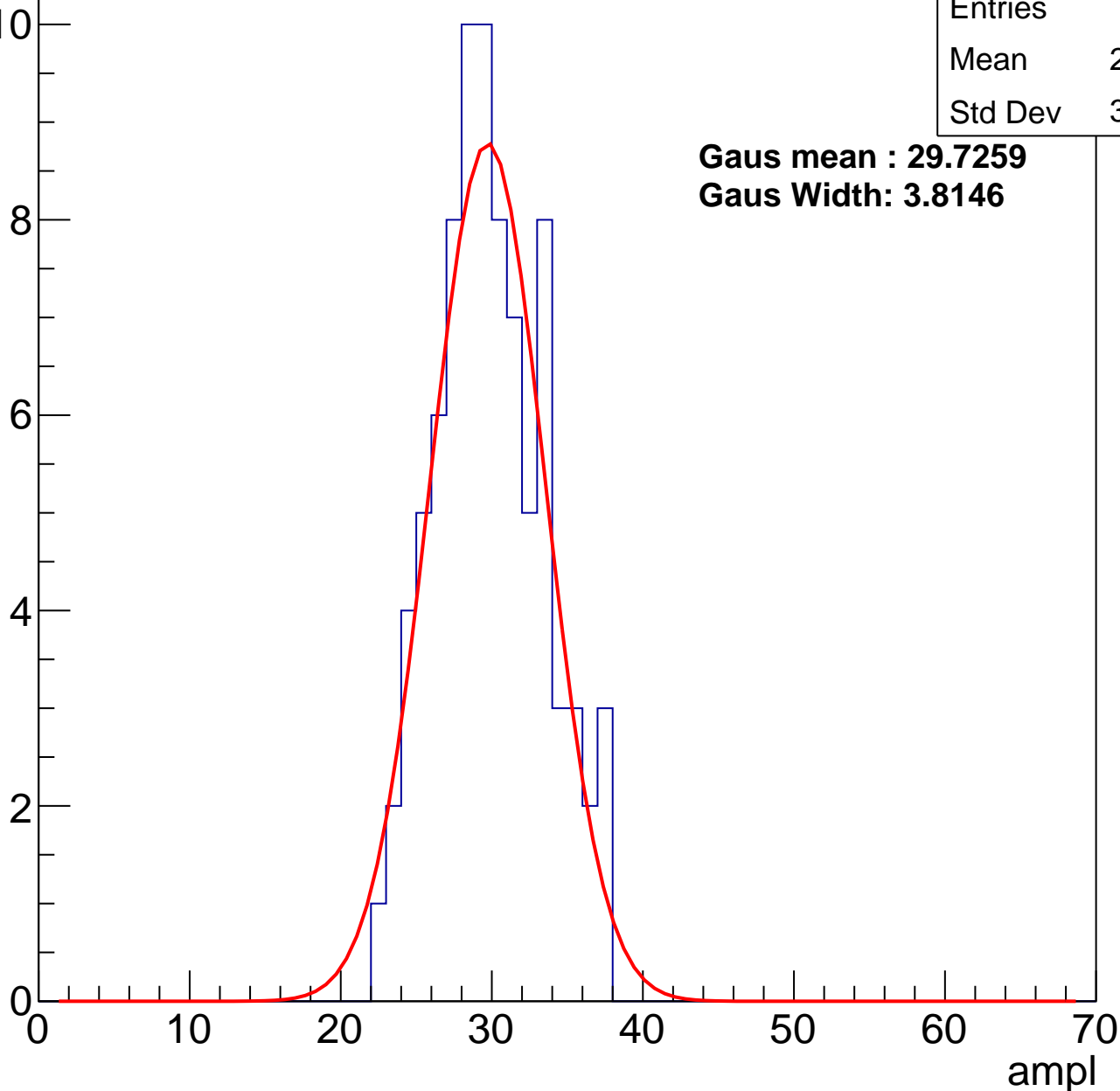
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.44
Std Dev	3.536

Gaus mean : 29.7259

Gaus Width: 3.8146



B1L102S, U26-ch32, adc1

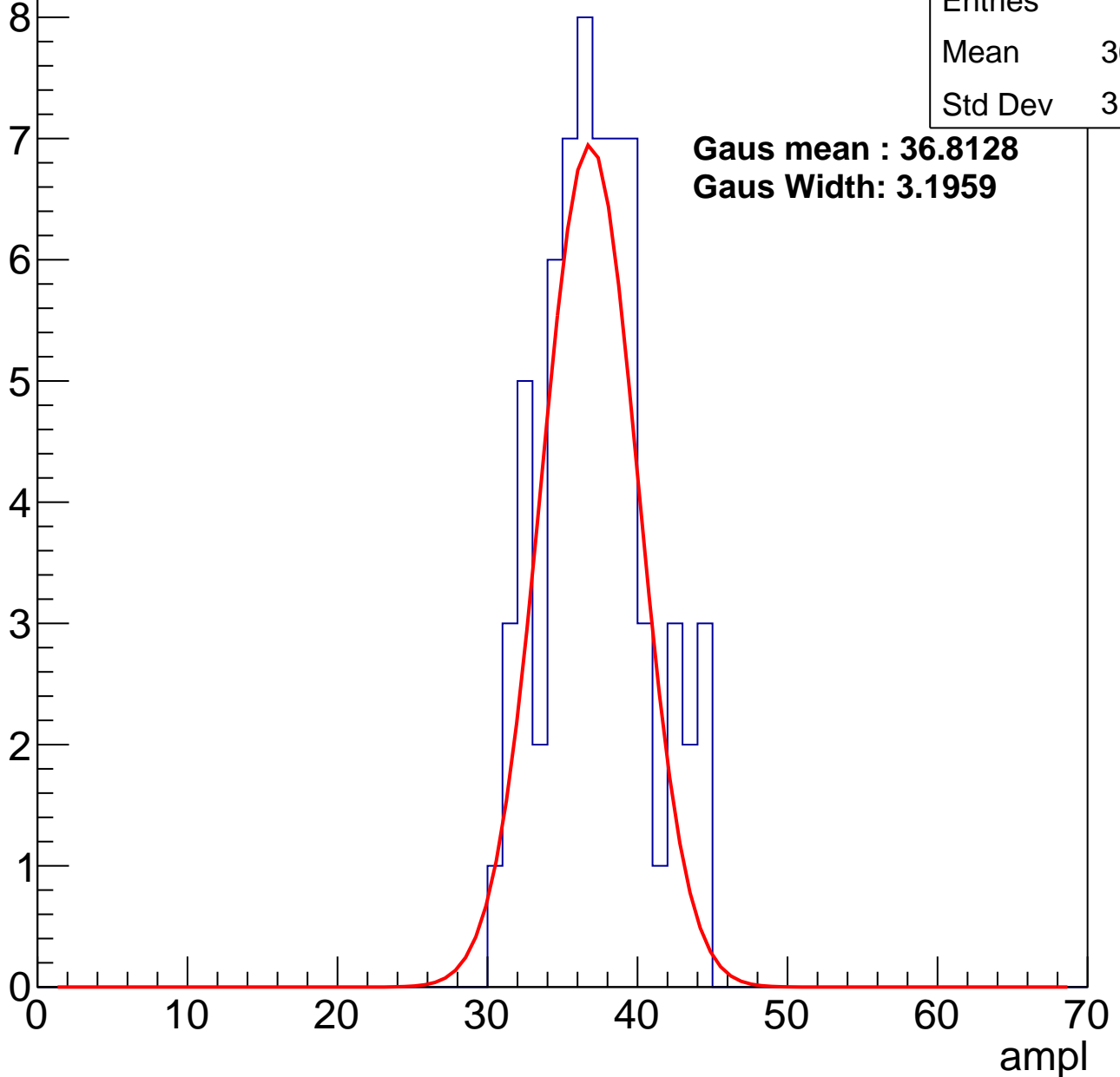
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.75
Std Dev	3.446

Gaus mean : 36.8128

Gaus Width: 3.1959



B1L102S, U26-ch32, adc2

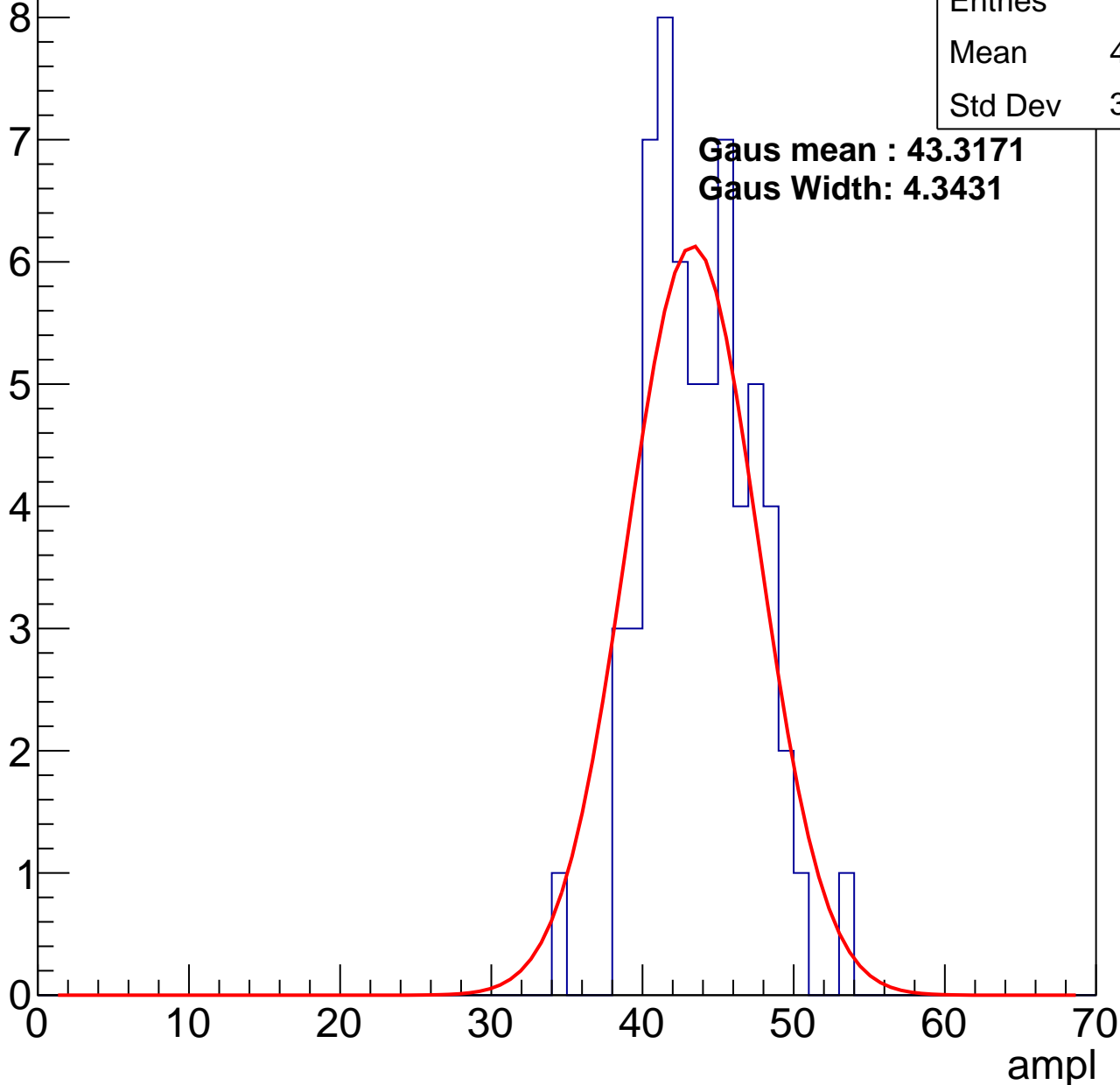
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.34
Std Dev	3.519

Gaus mean : 43.3171

Gaus Width: 4.3431

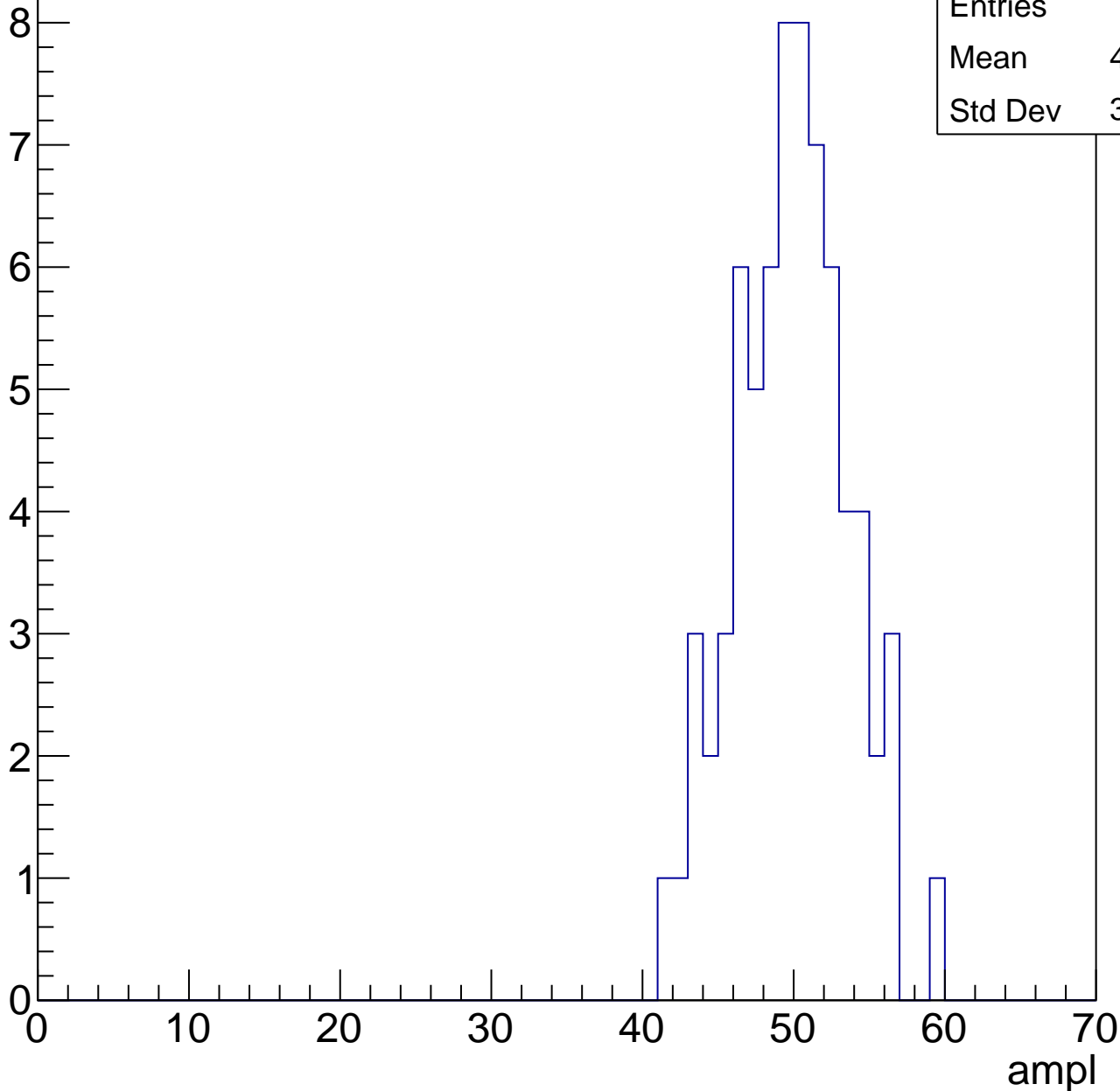


B1L102S, U26-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

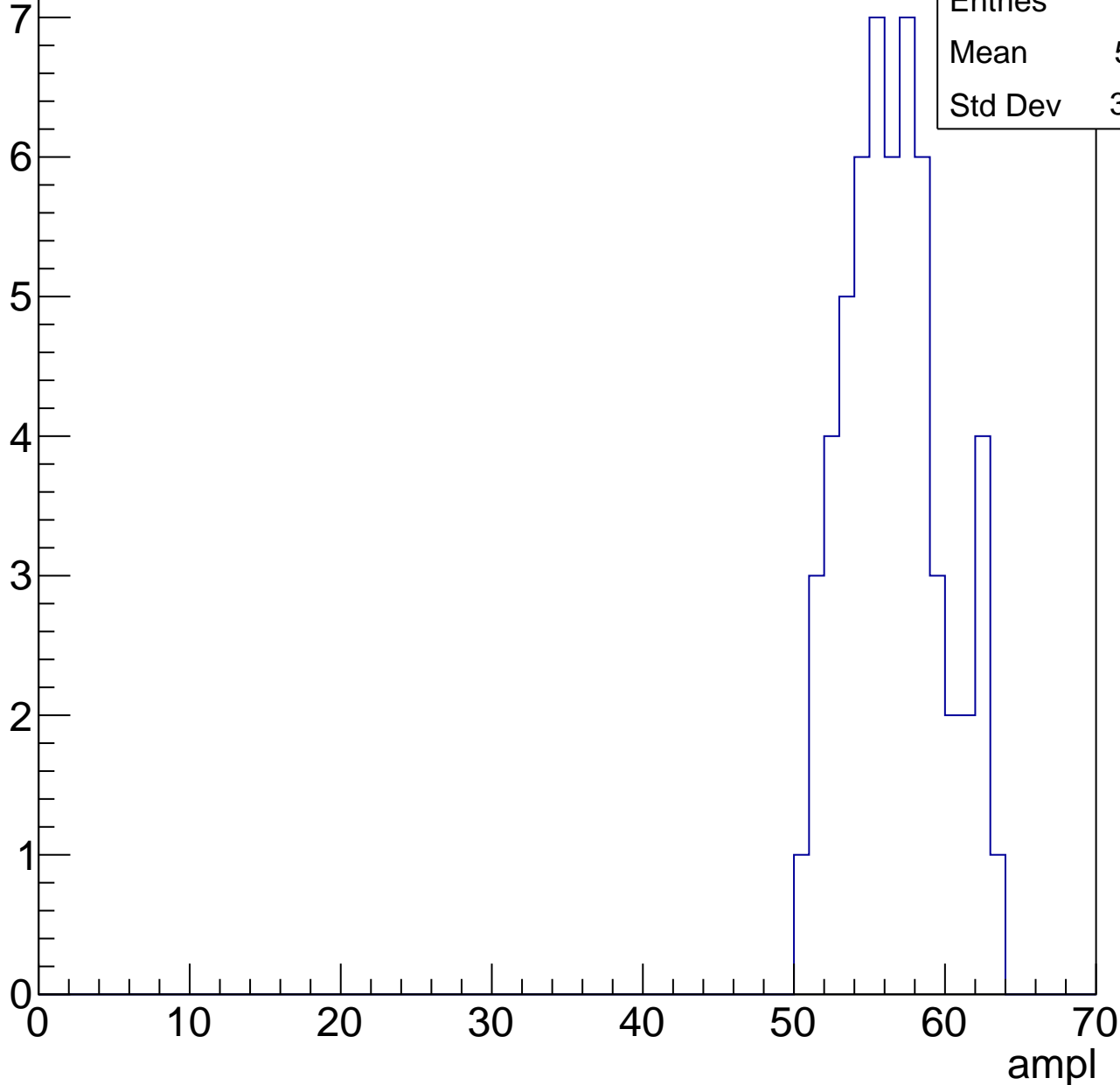
Entries	70
Mean	49.43
Std Dev	3.702



B1L102S, U26-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



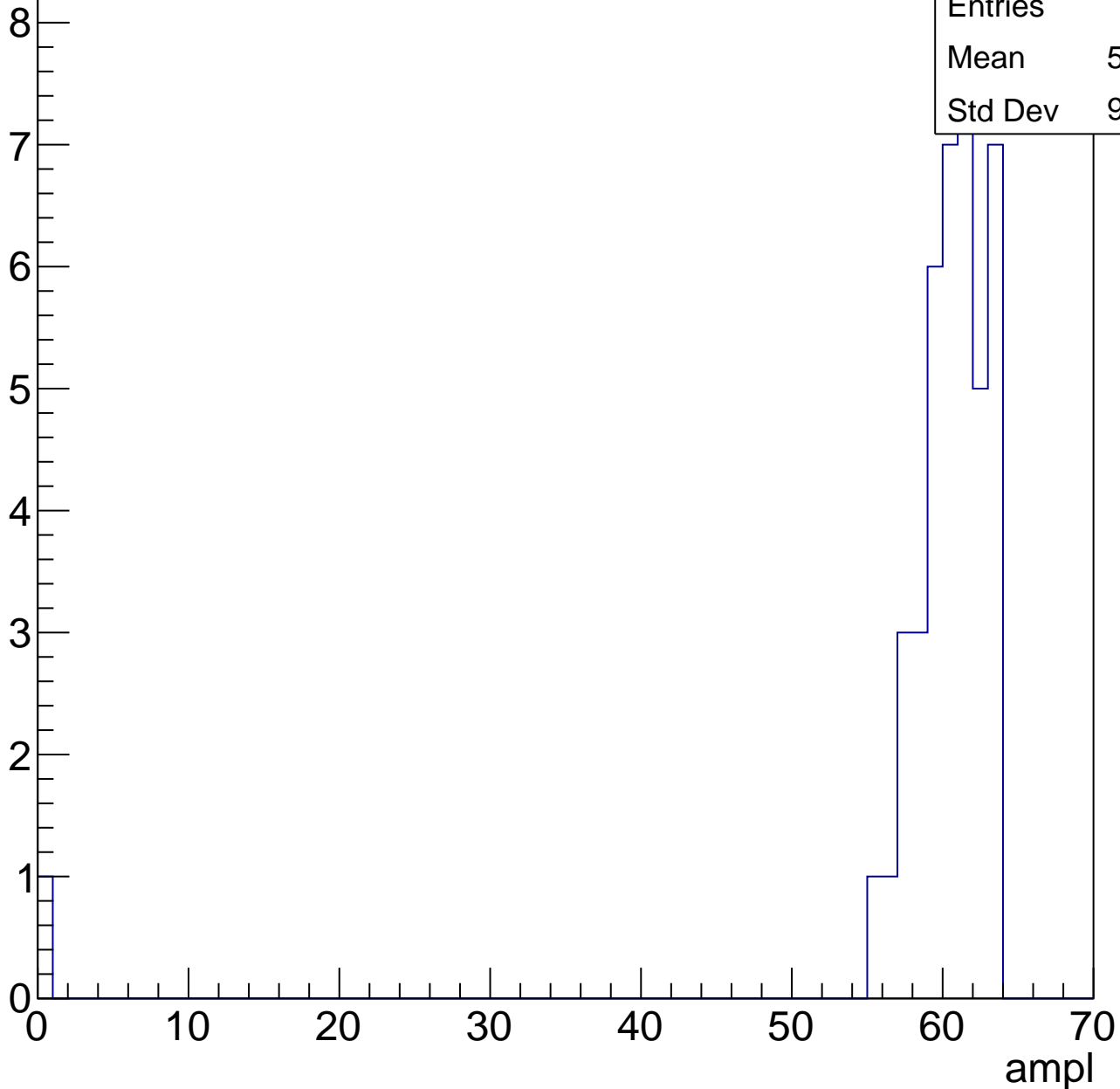
Entries	57
Mean	56.11
Std Dev	3.199

B1L102S, U26-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

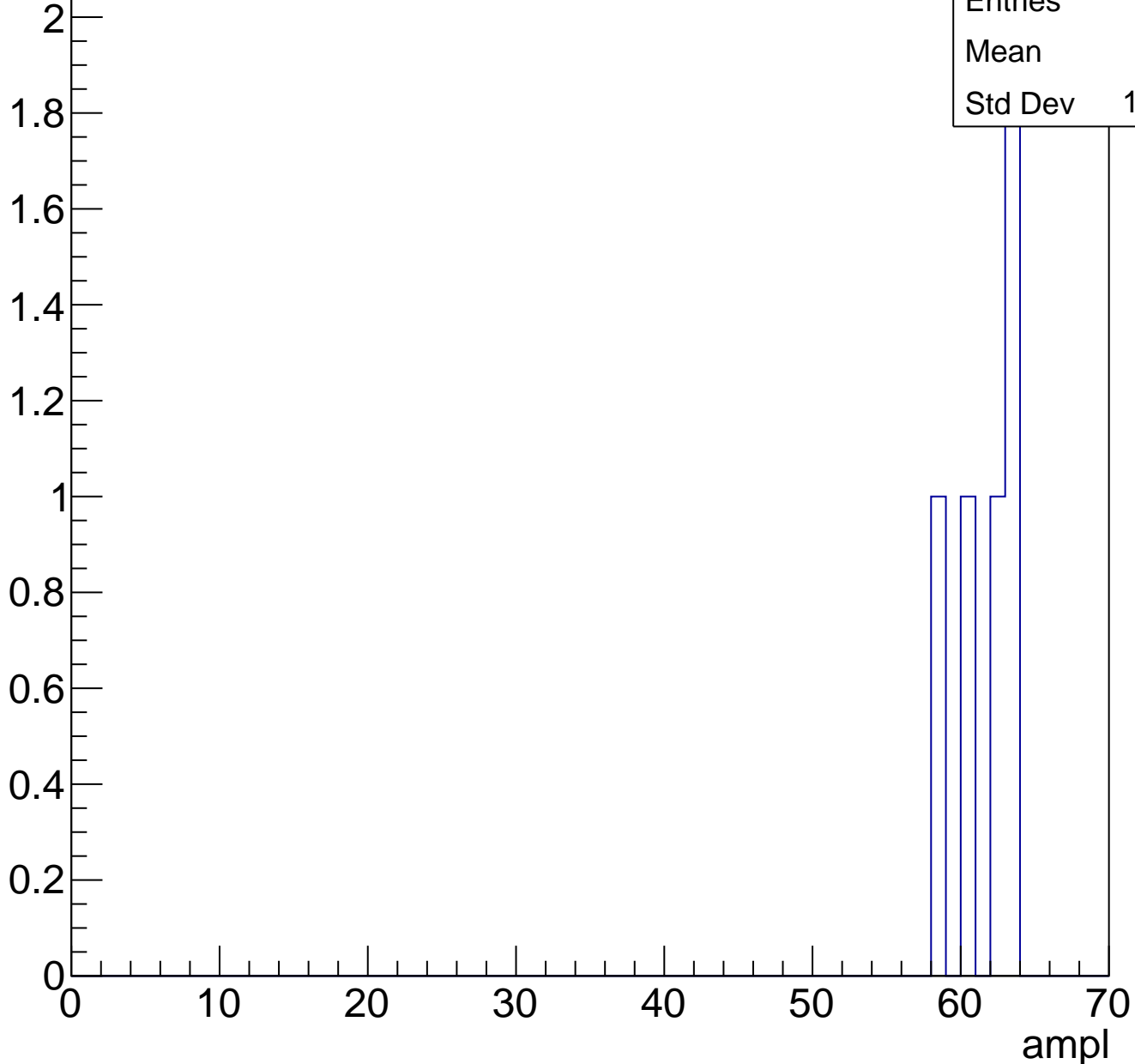
Entries	42
Mean	58.79
Std Dev	9.405



B1L102S, U26-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	61.2
Std Dev	1.939

B1L102S, U26-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U26-ch33, adc0

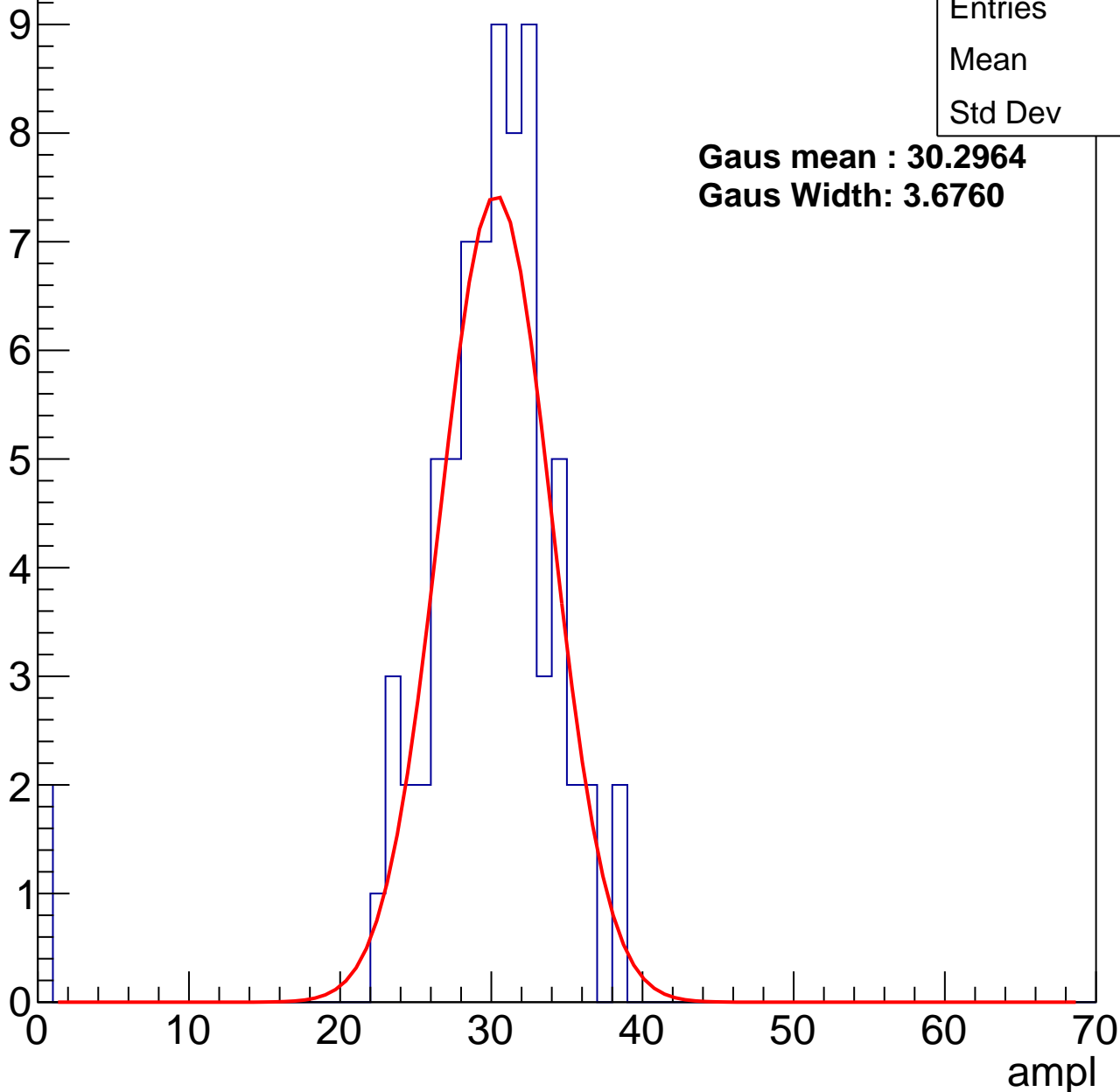
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29
Std Dev	5.95

Gaus mean : 30.2964

Gaus Width: 3.6760



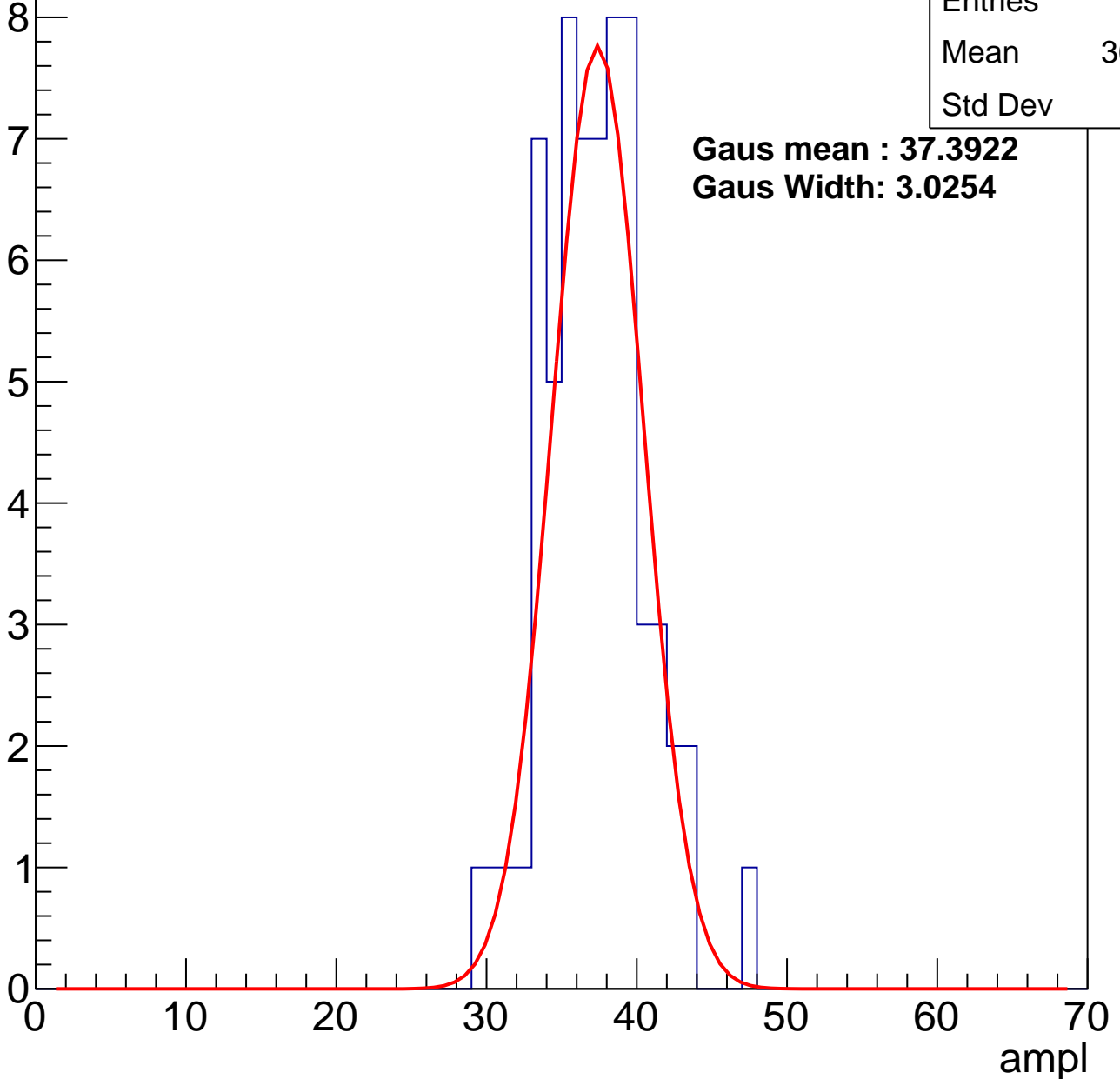
B1L102S, U26-ch33, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.77
Std Dev	3.29

Gaus mean : 37.3922
Gaus Width: 3.0254



B1L102S, U26-ch33, adc2

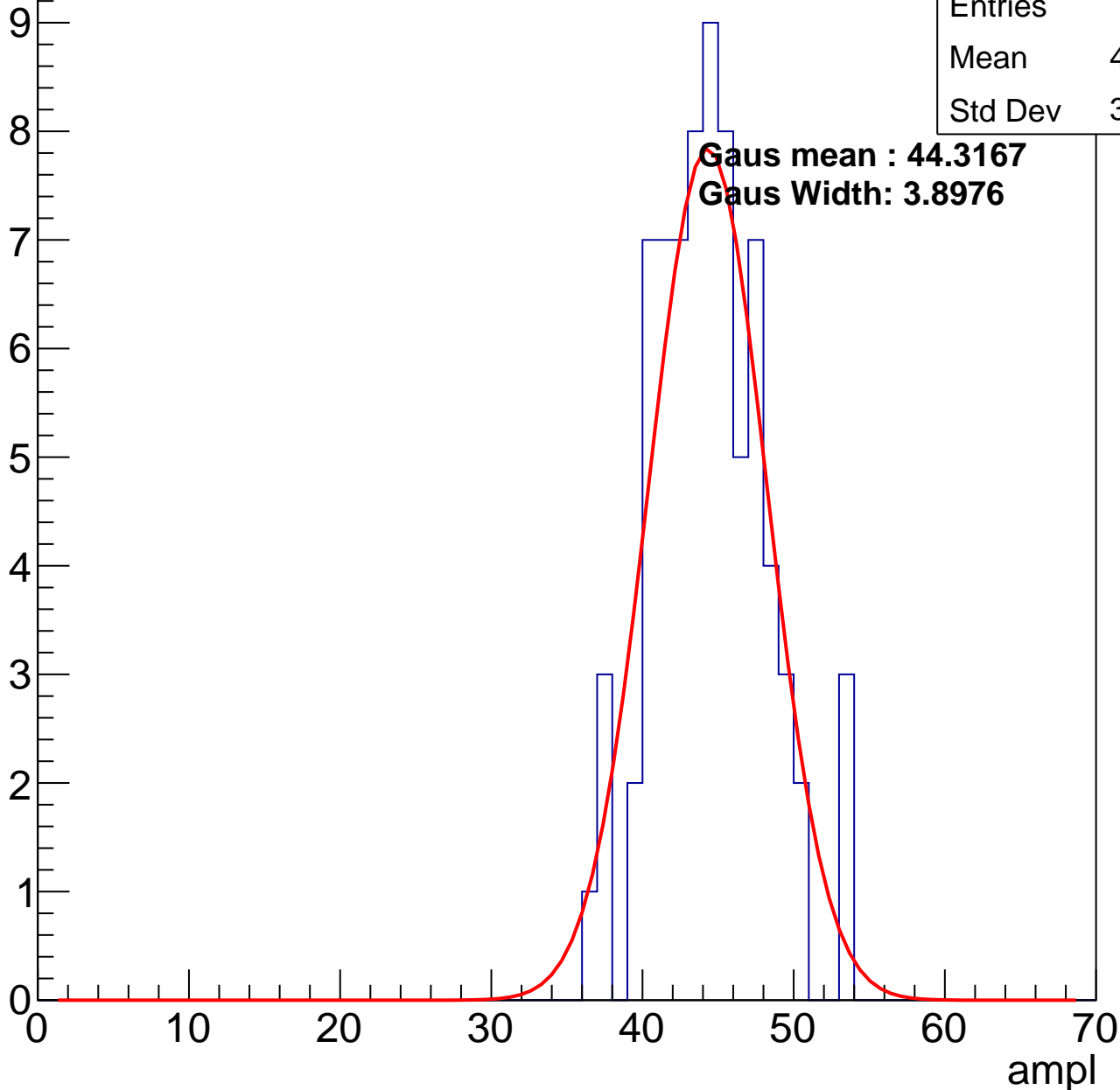
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.99
Std Dev	3.658

Gaus mean : 44.3167

Gaus Width: 3.8976

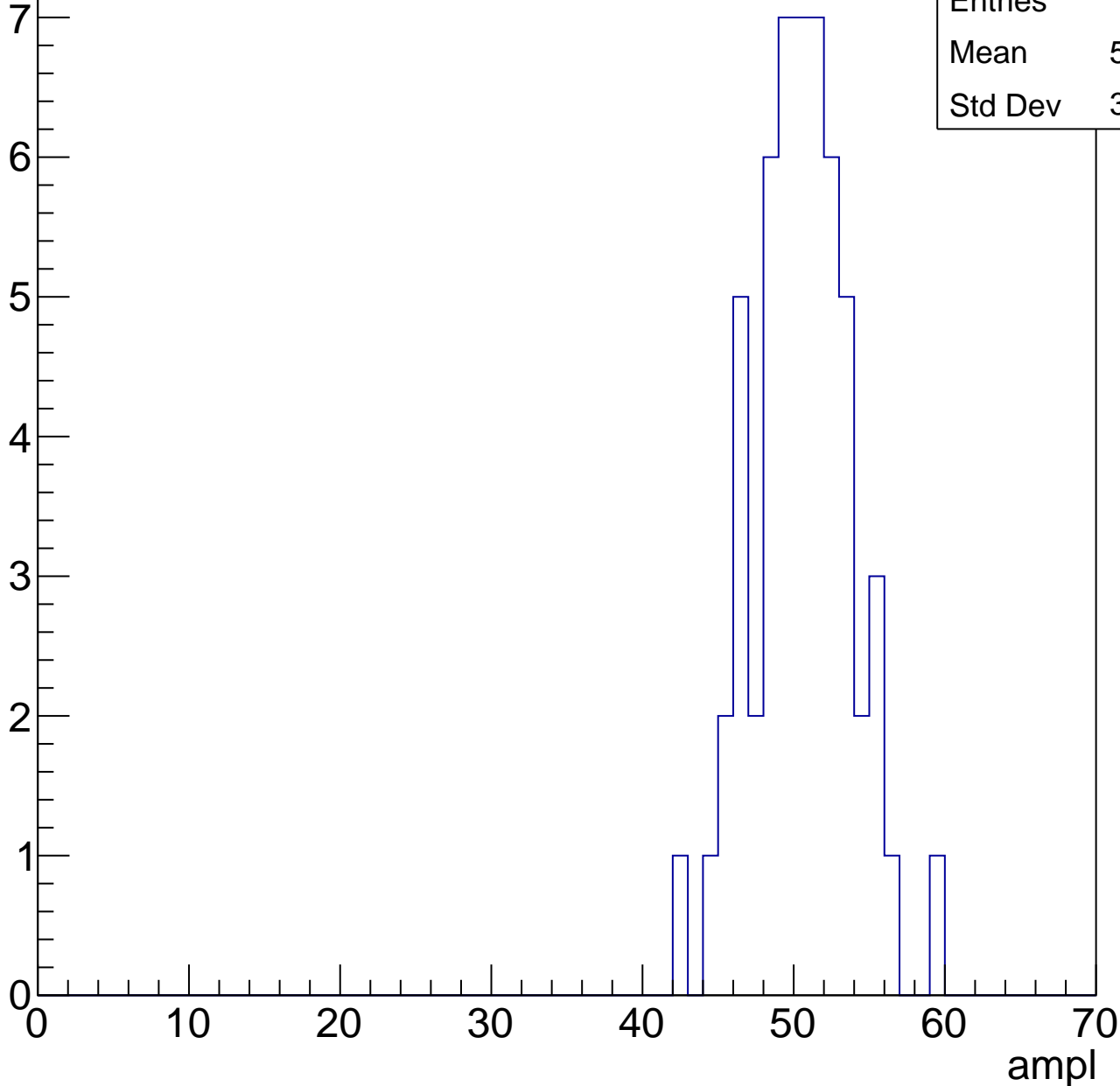


B1L102S, U26-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50.05
Std Dev	3.215

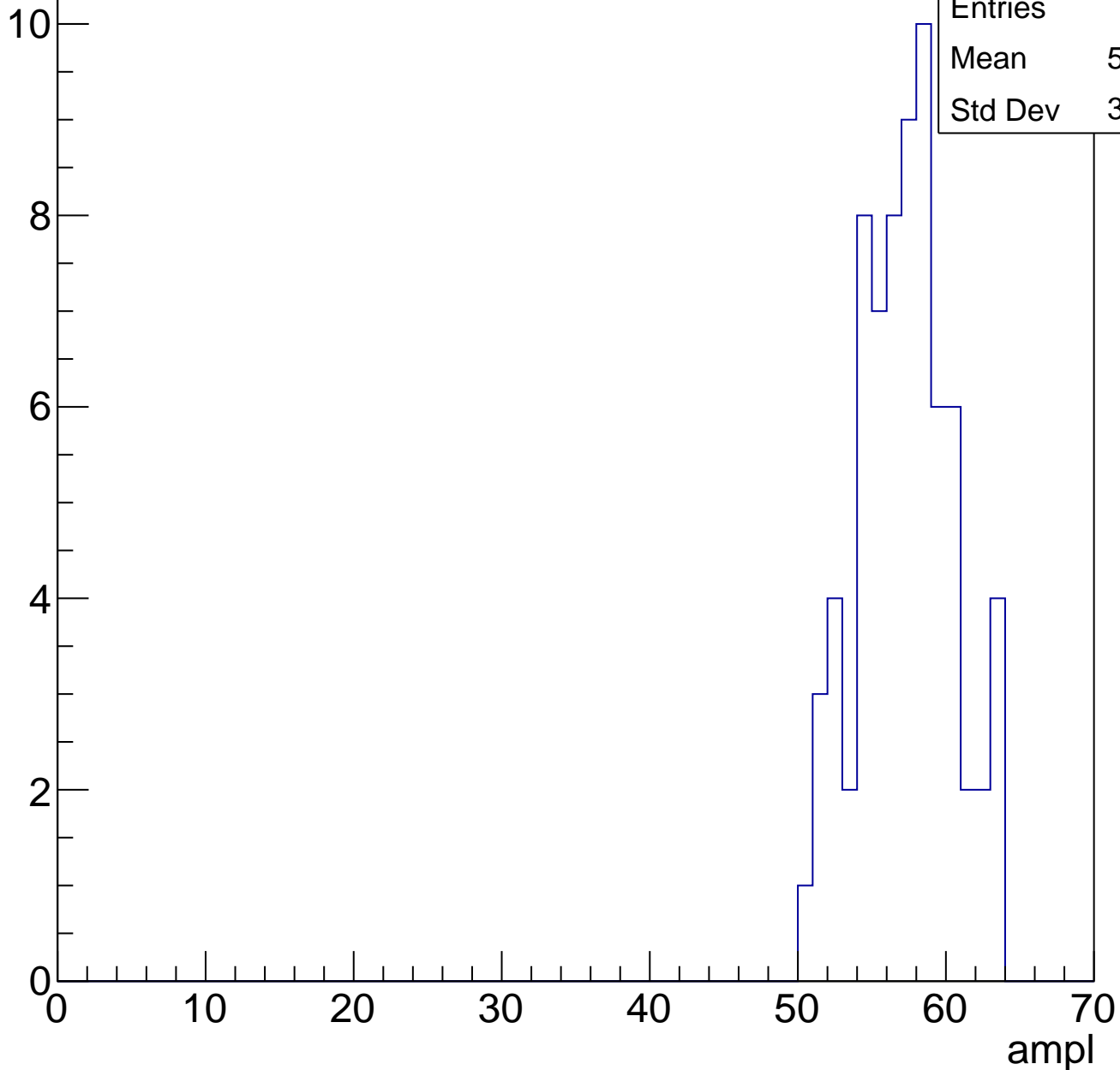


B1L102S, U26-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	56.76
Std Dev	3.134

Entry

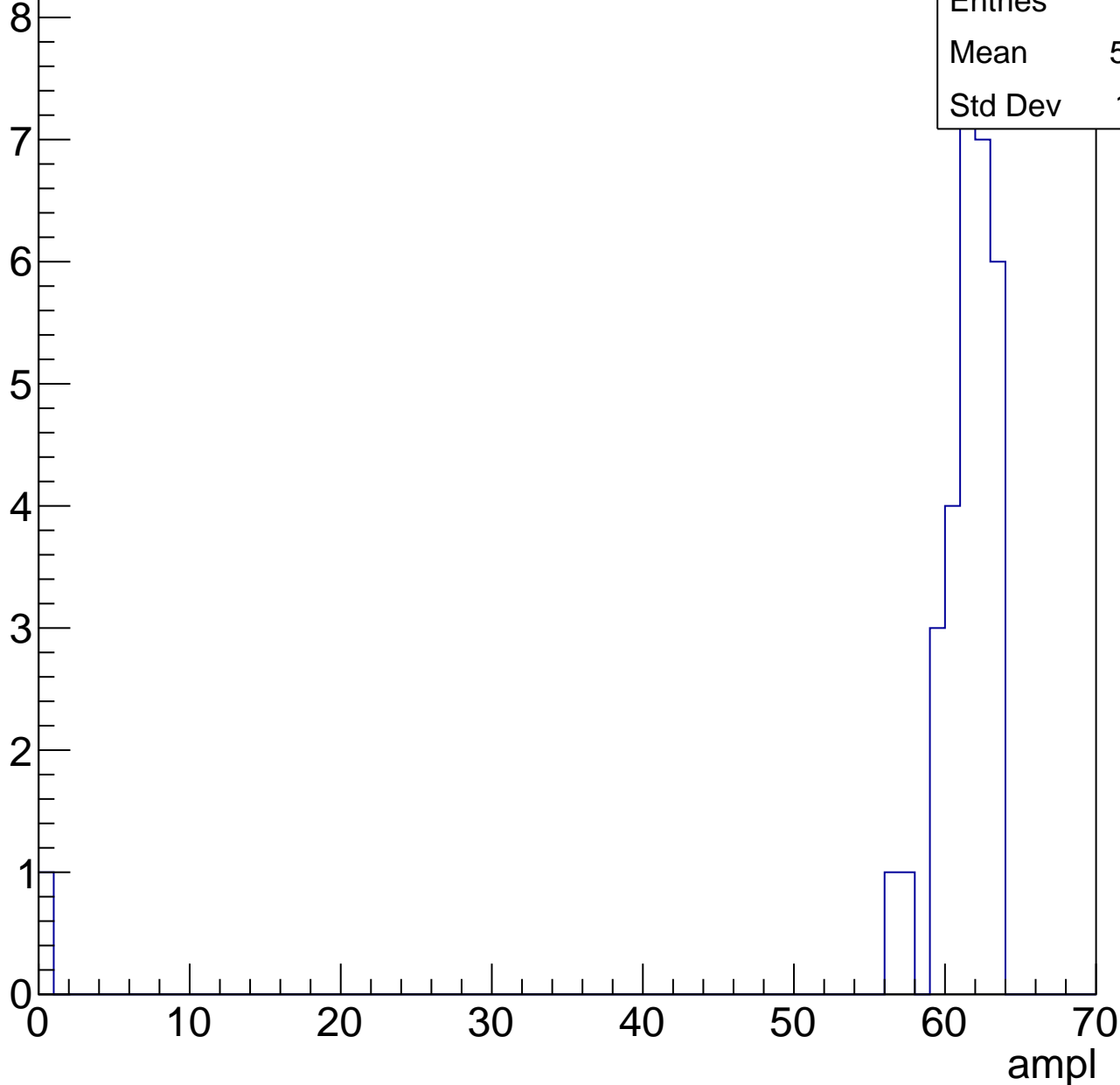


B1L102S, U26-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	59.03
Std Dev	10.91



B1L102S, U26-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch34, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	29.81
Std Dev	7.176

Gaus mean : 32.1440

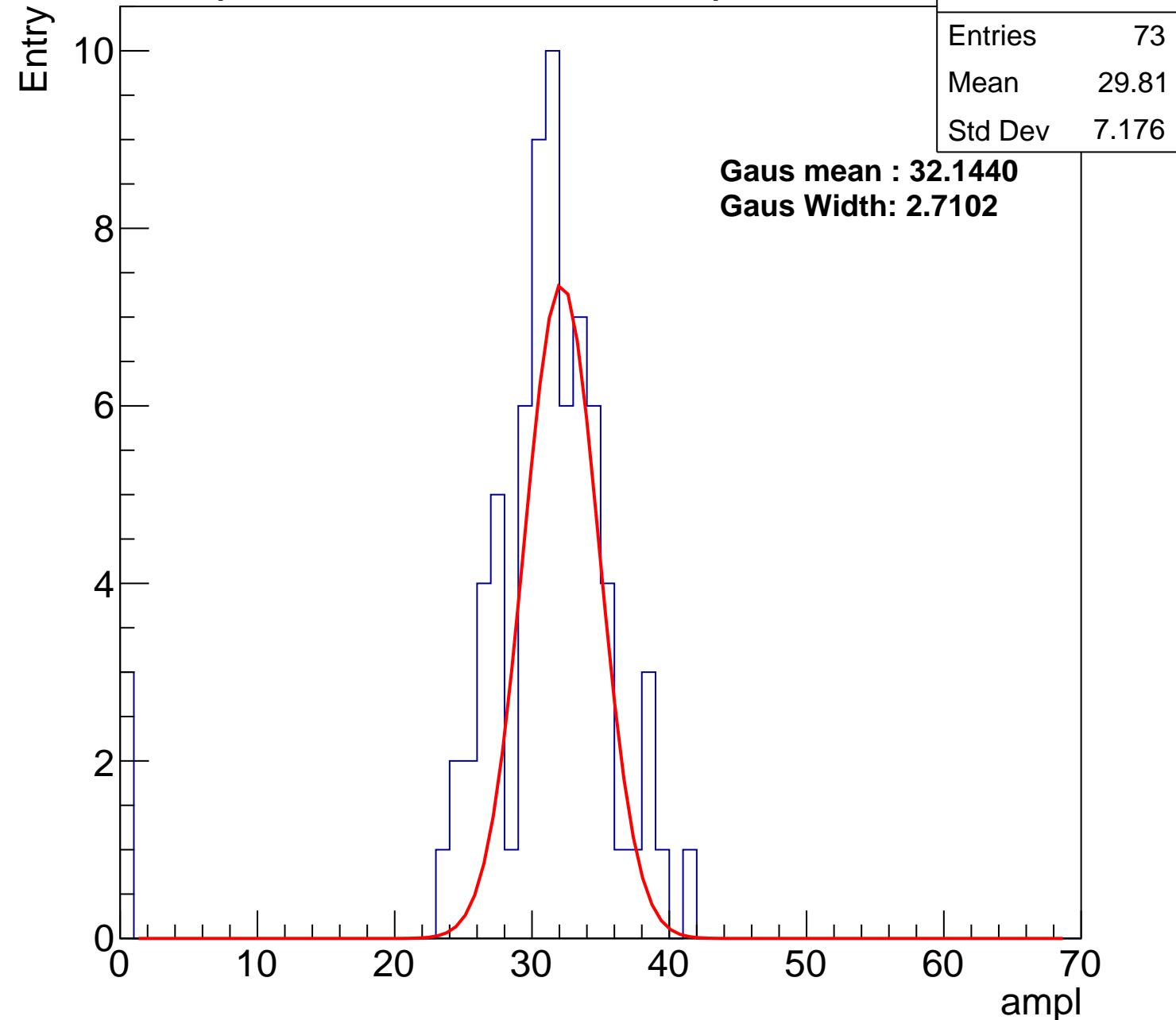
Gaus Width: 2.7102

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch34, adc1

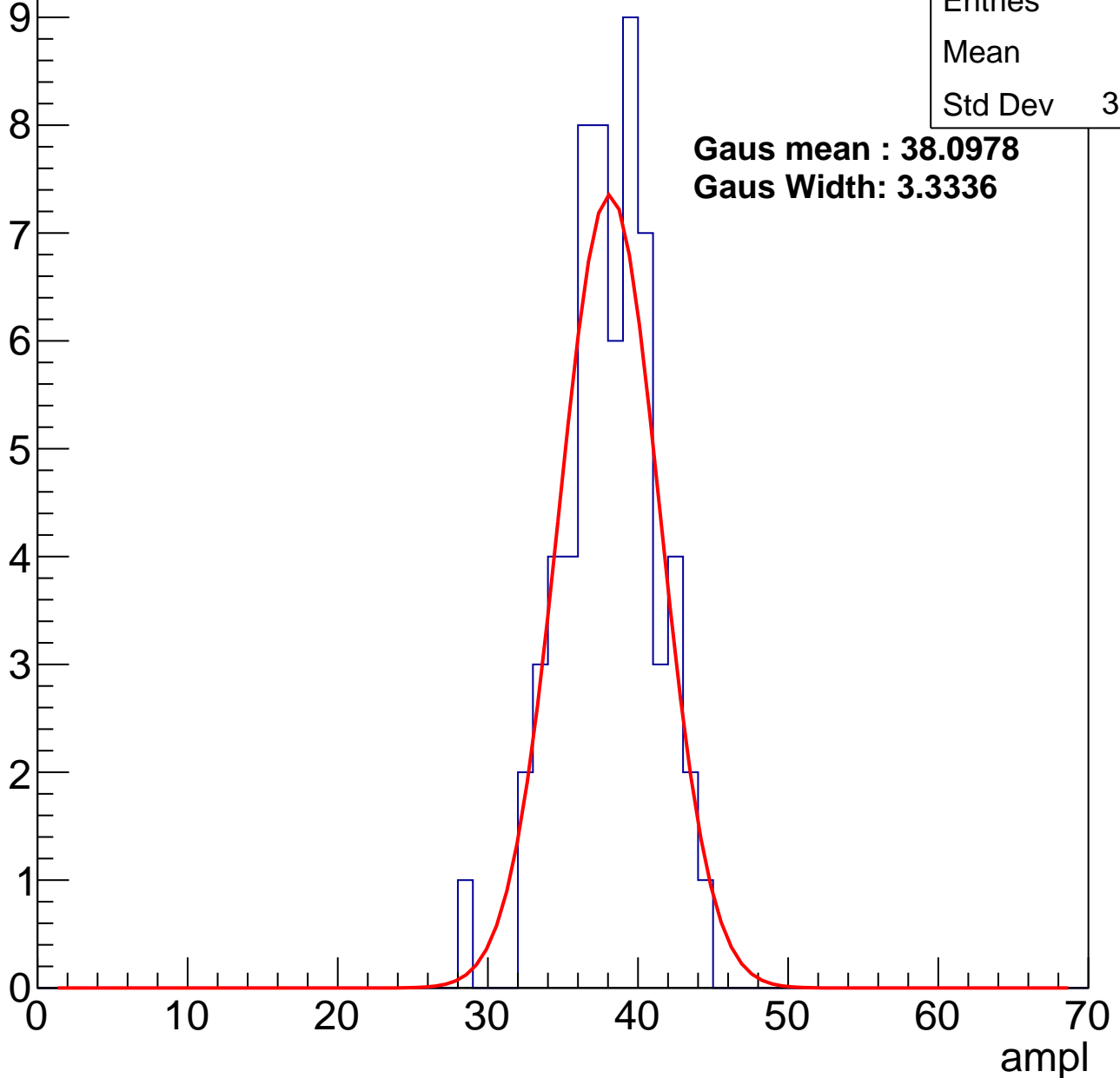
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.6
Std Dev	3.087

Gaus mean : 38.0978

Gaus Width: 3.3336



B1L102S, U26-ch34, adc2

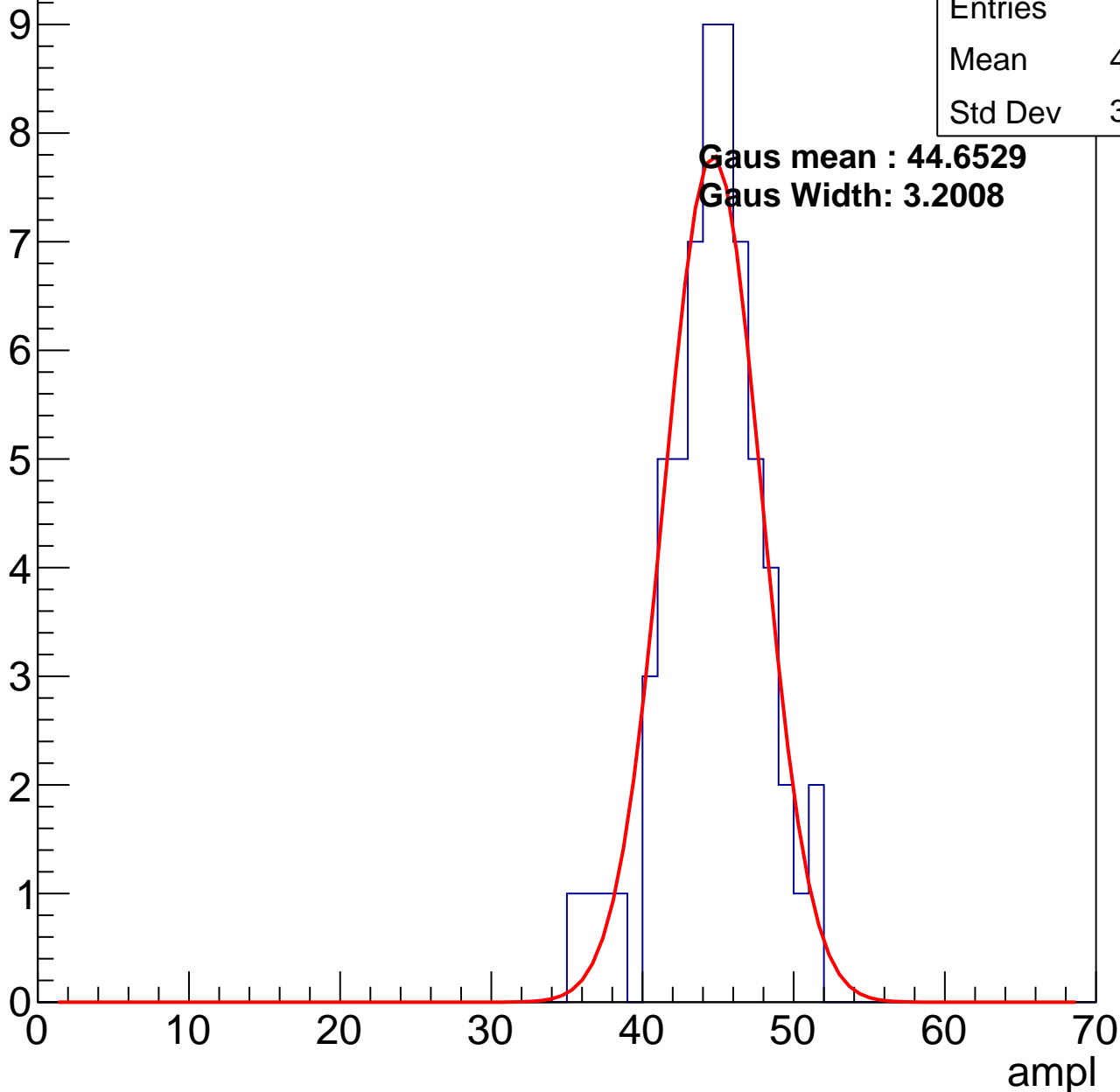
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	44.16
Std Dev	3.282

Gaus mean : 44.6529

Gaus Width: 3.2008

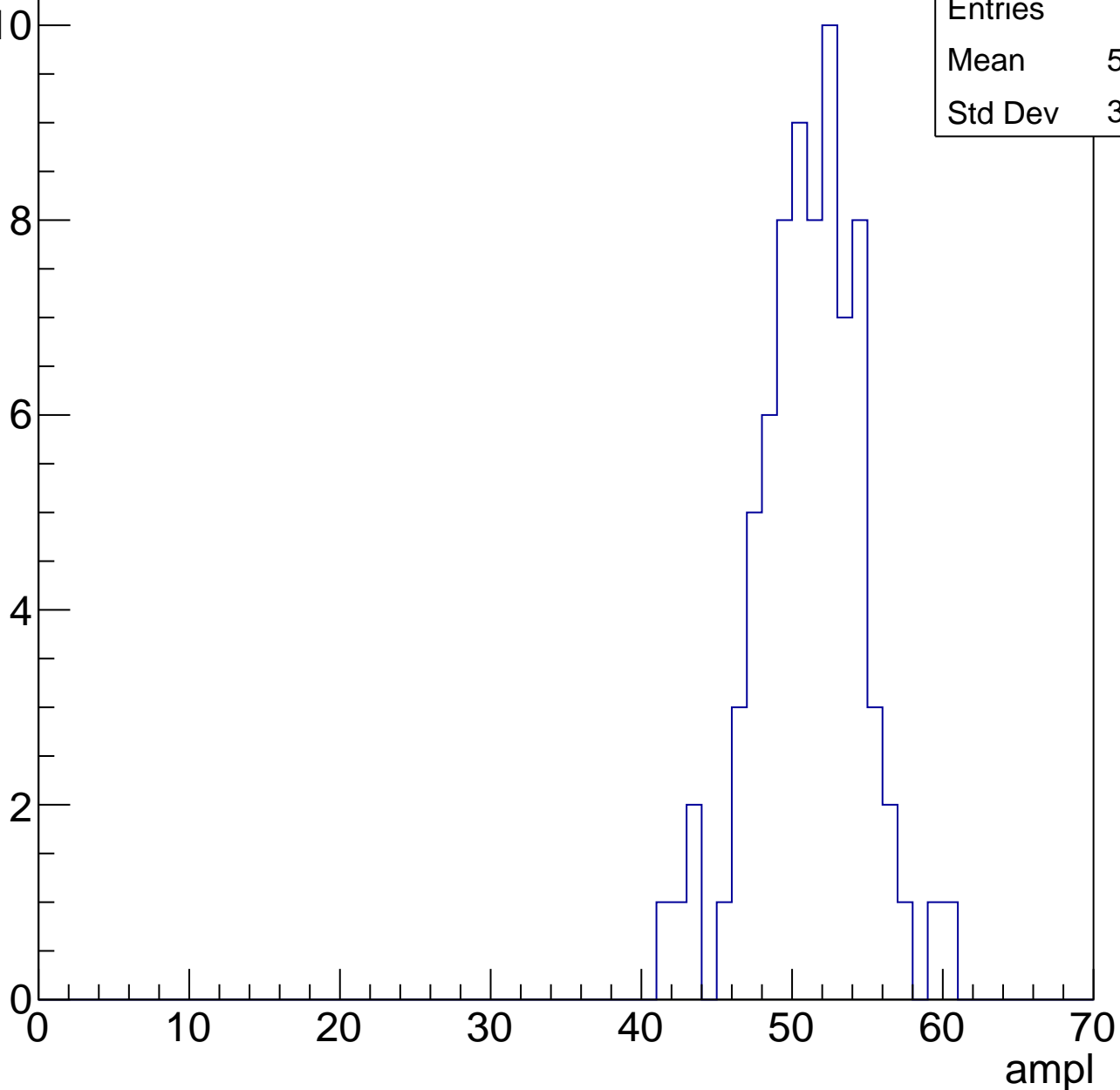


B1L102S, U26-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	50.66
Std Dev	3.559

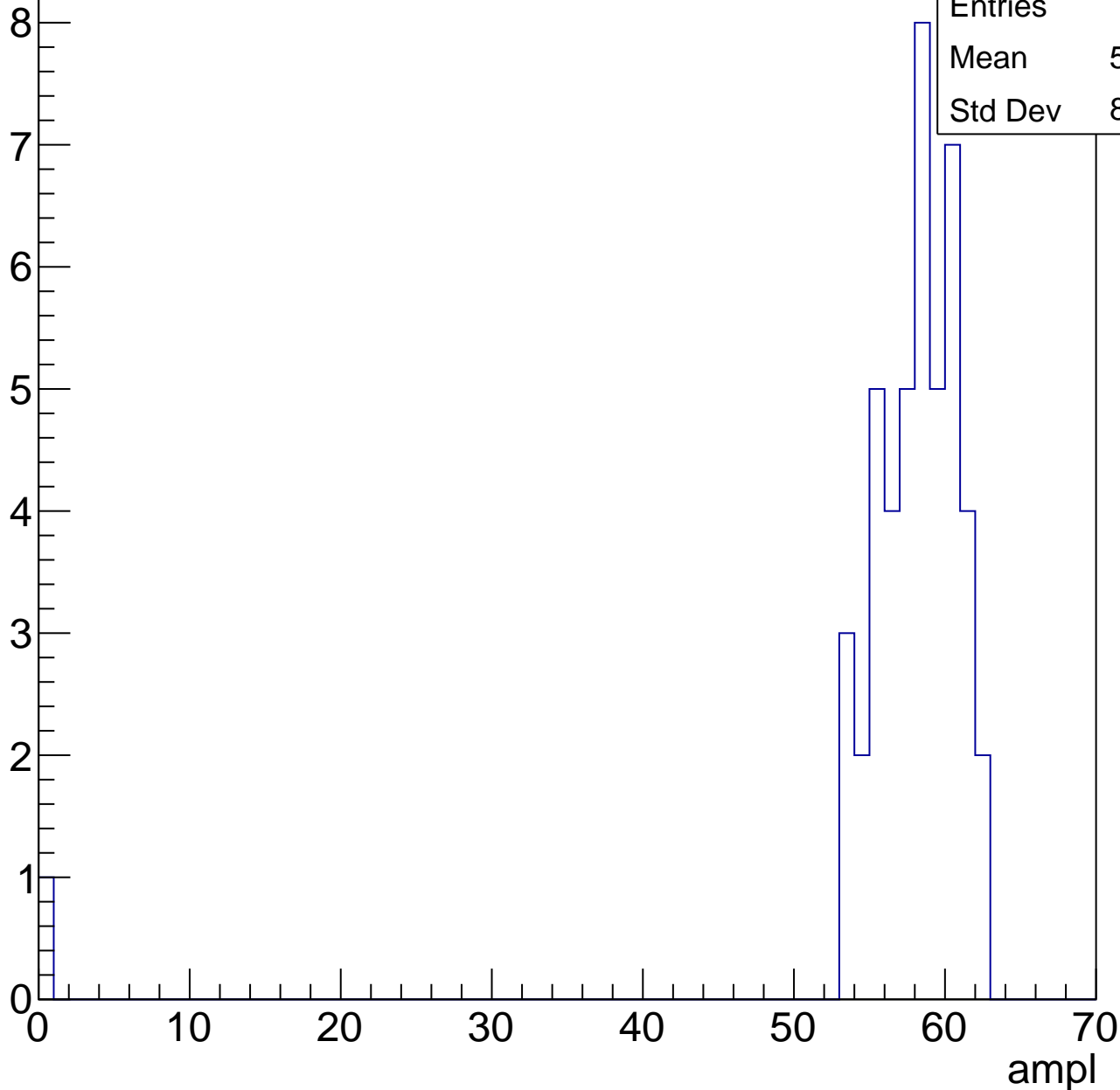


B1L102S, U26-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	56.48
Std Dev	8.762

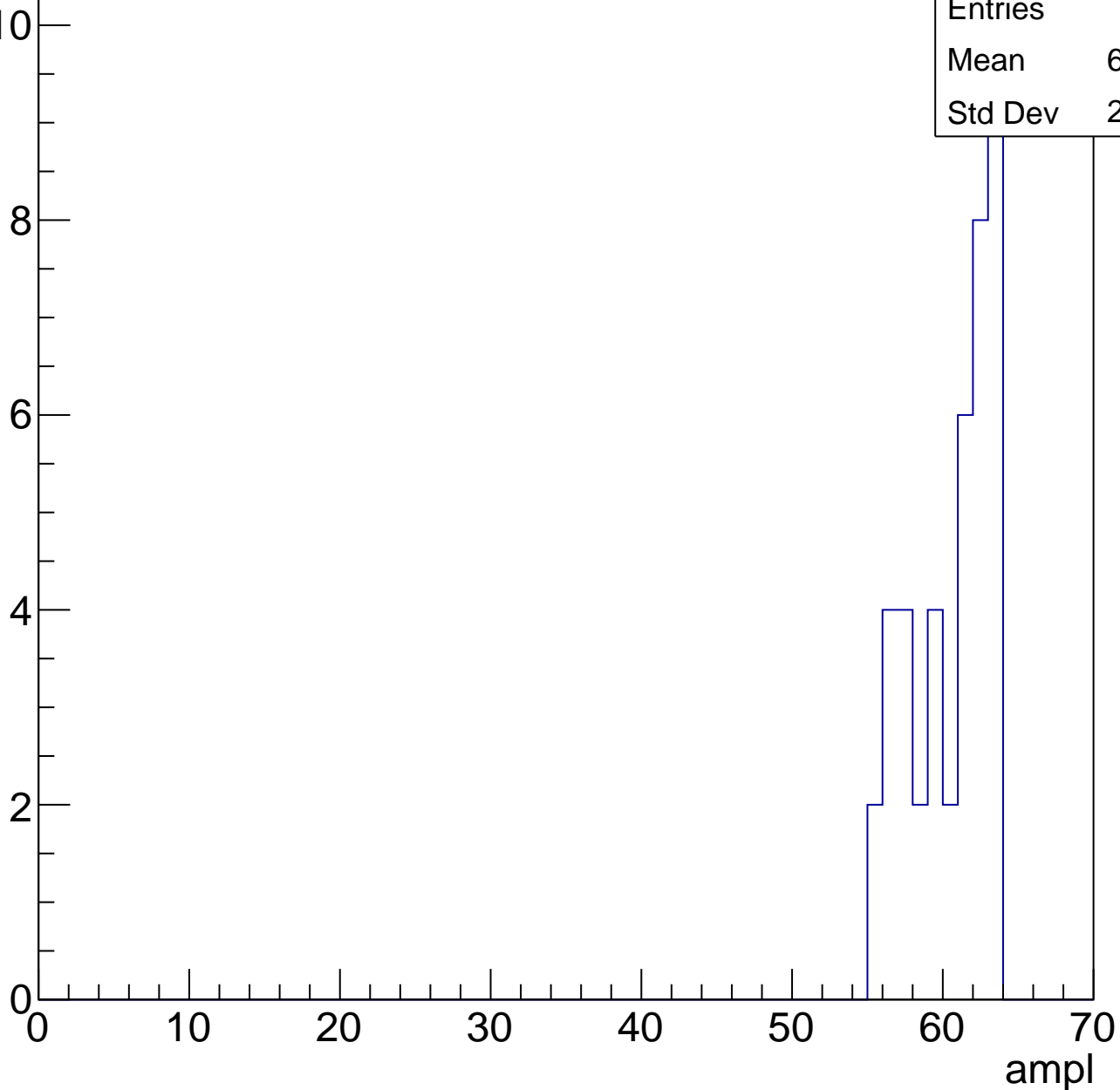


B1L102S, U26-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	60.14
Std Dev	2.624



B1L102S, U26-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U26-ch35, adc0

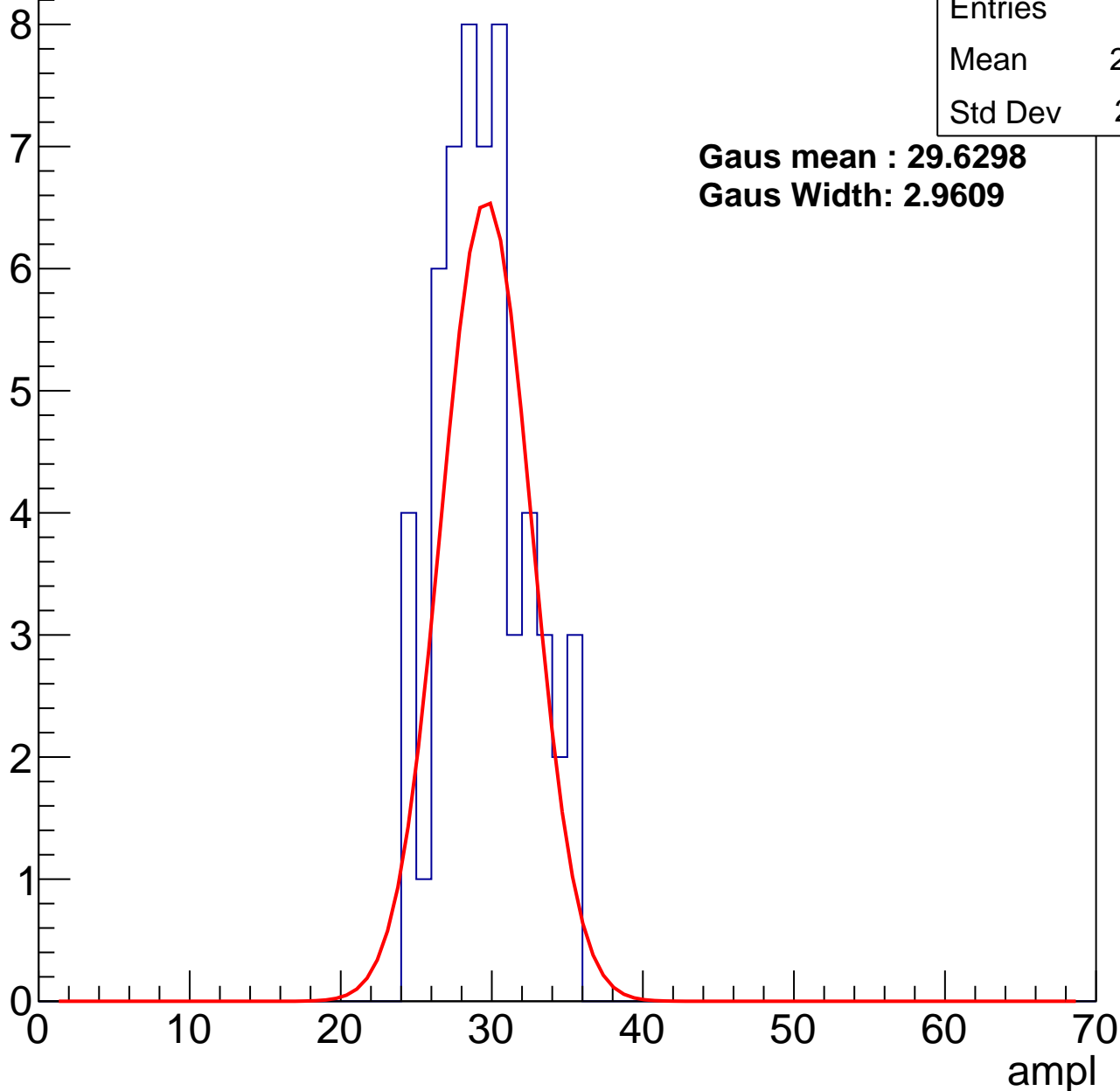
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	29.04
Std Dev	2.891

Gaus mean : 29.6298

Gaus Width: 2.9609

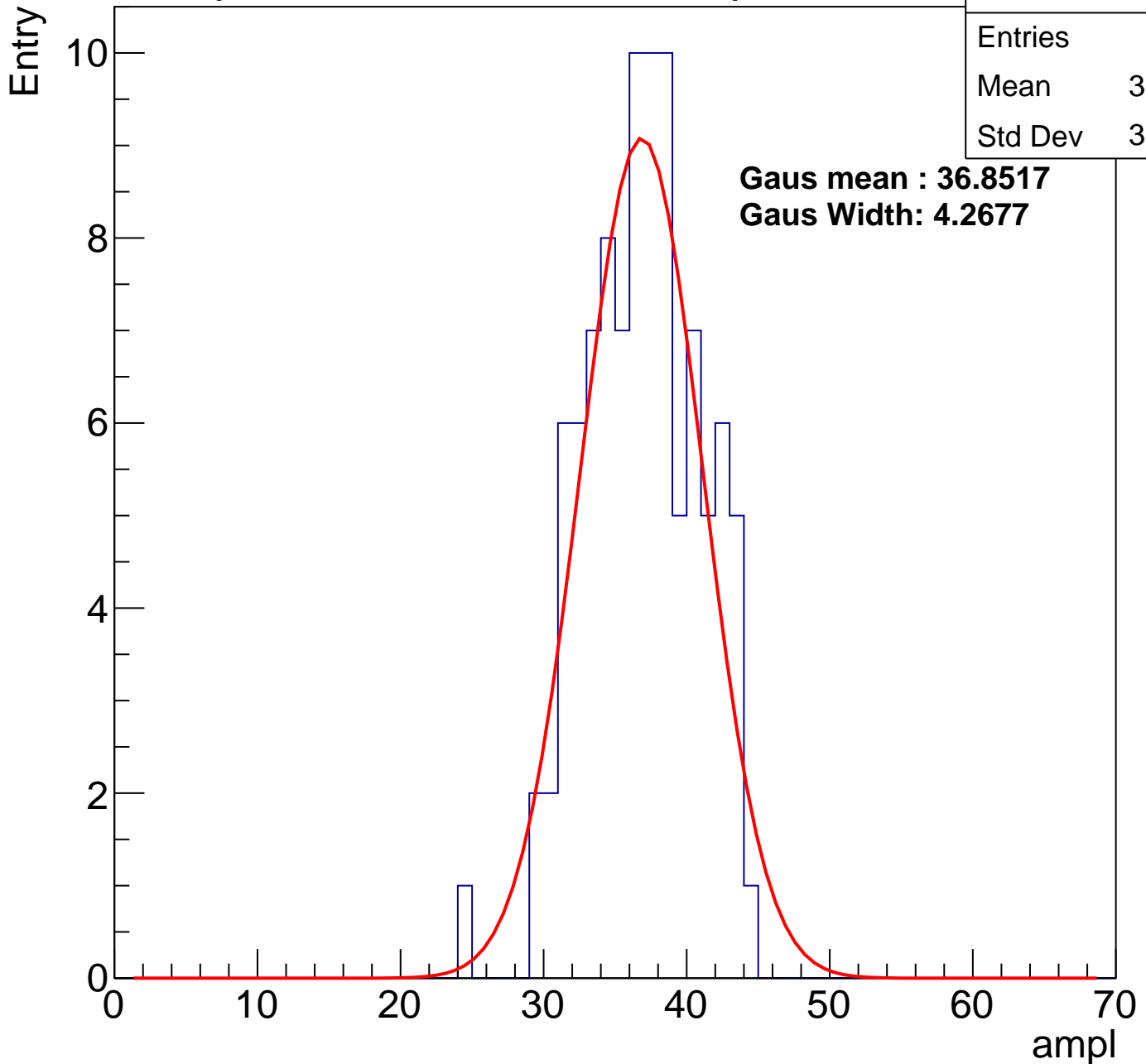


B1L102S, U26-ch35, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	98
Mean	36.42
Std Dev	3.915

Gaus mean : 36.8517
Gaus Width: 4.2677



B1L102S, U26-ch35, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 43.44

Std Dev 2.557

Gaus mean : 66.1525

Gaus Width: 17.6925

ampl

0

10

20

30

40

50

60

70

B1L102S, U26-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

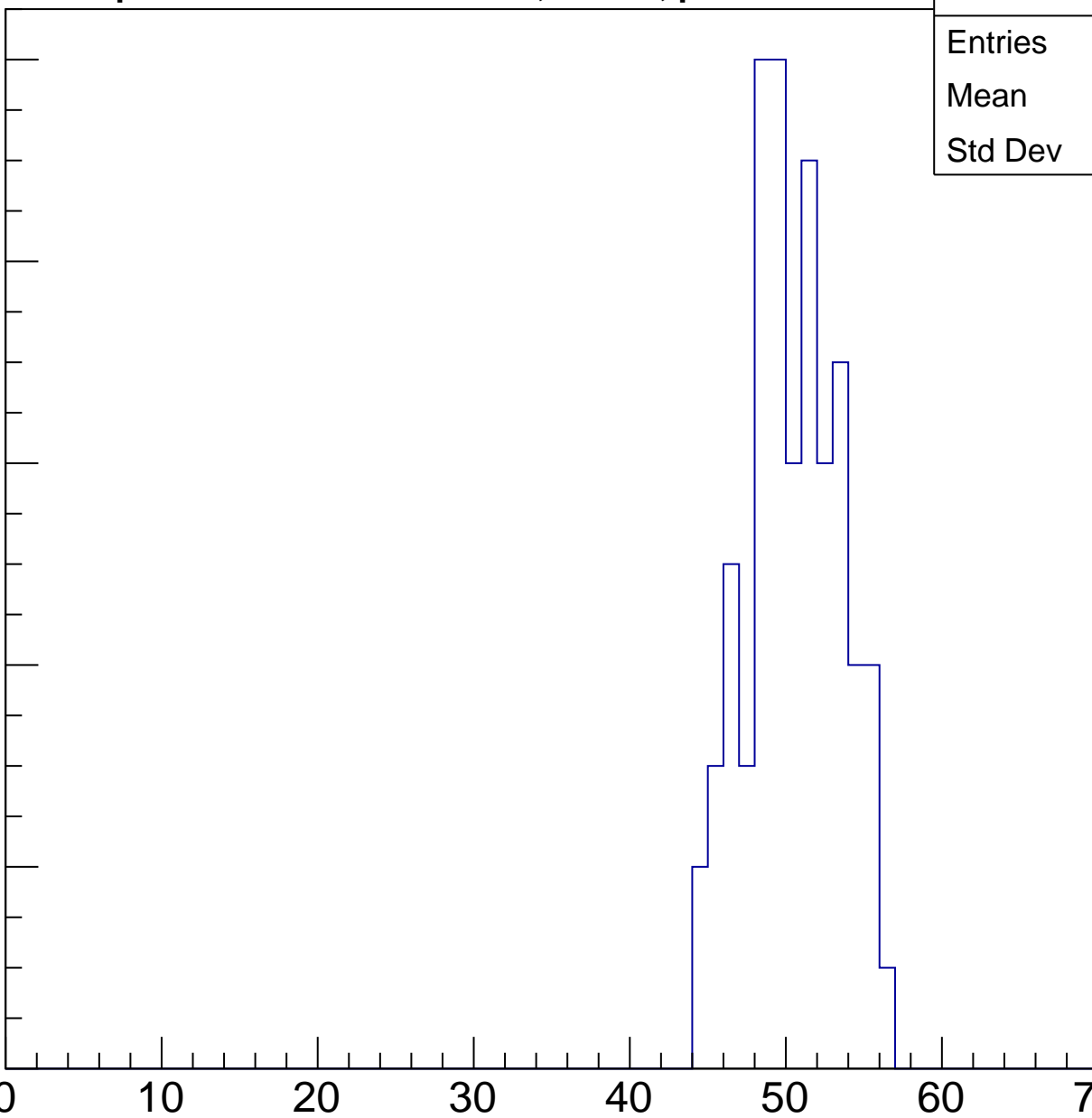
Entries	70
Mean	49.97
Std Dev	2.928

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

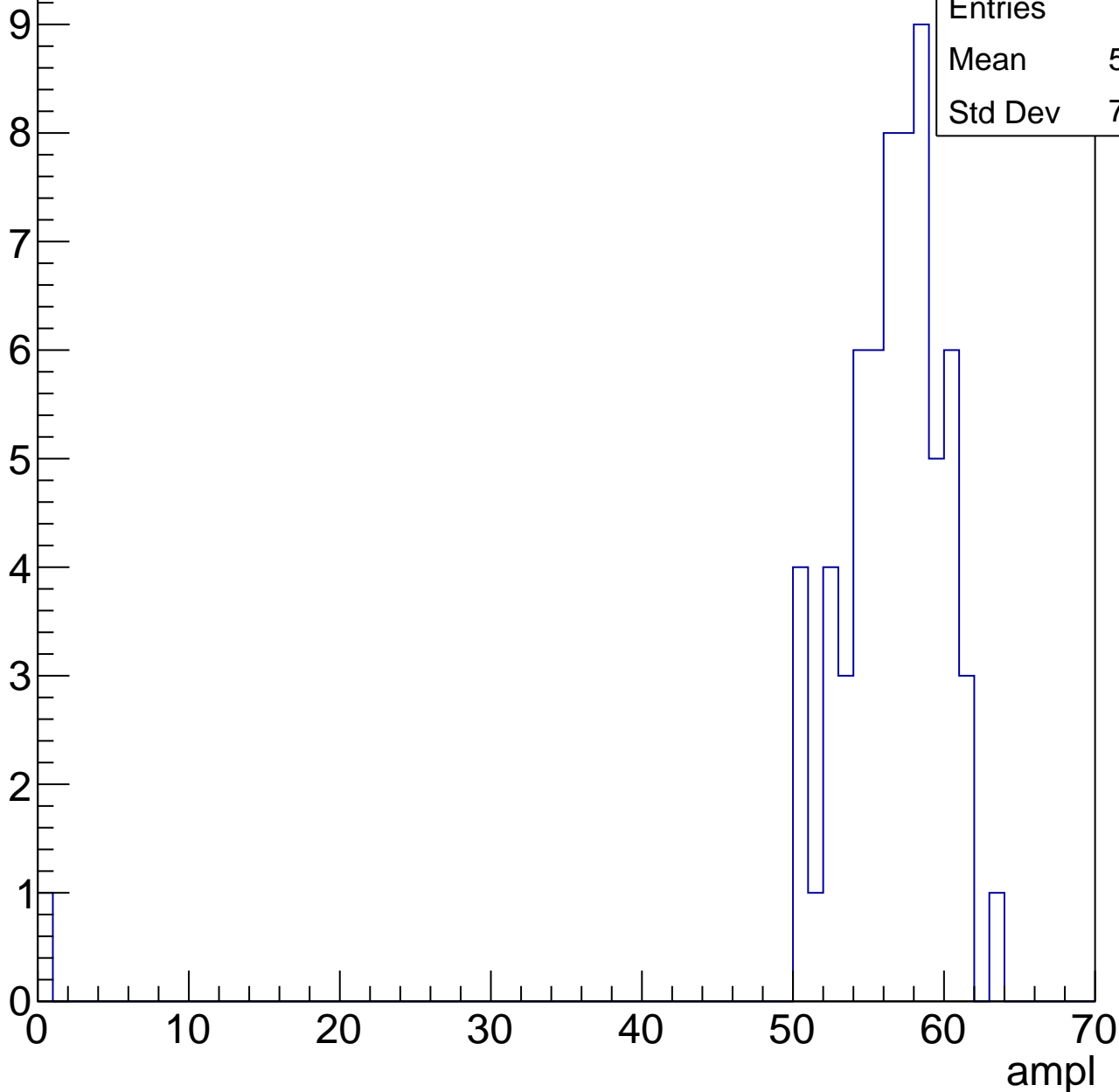


B1L102S, U26-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.37
Std Dev	7.554

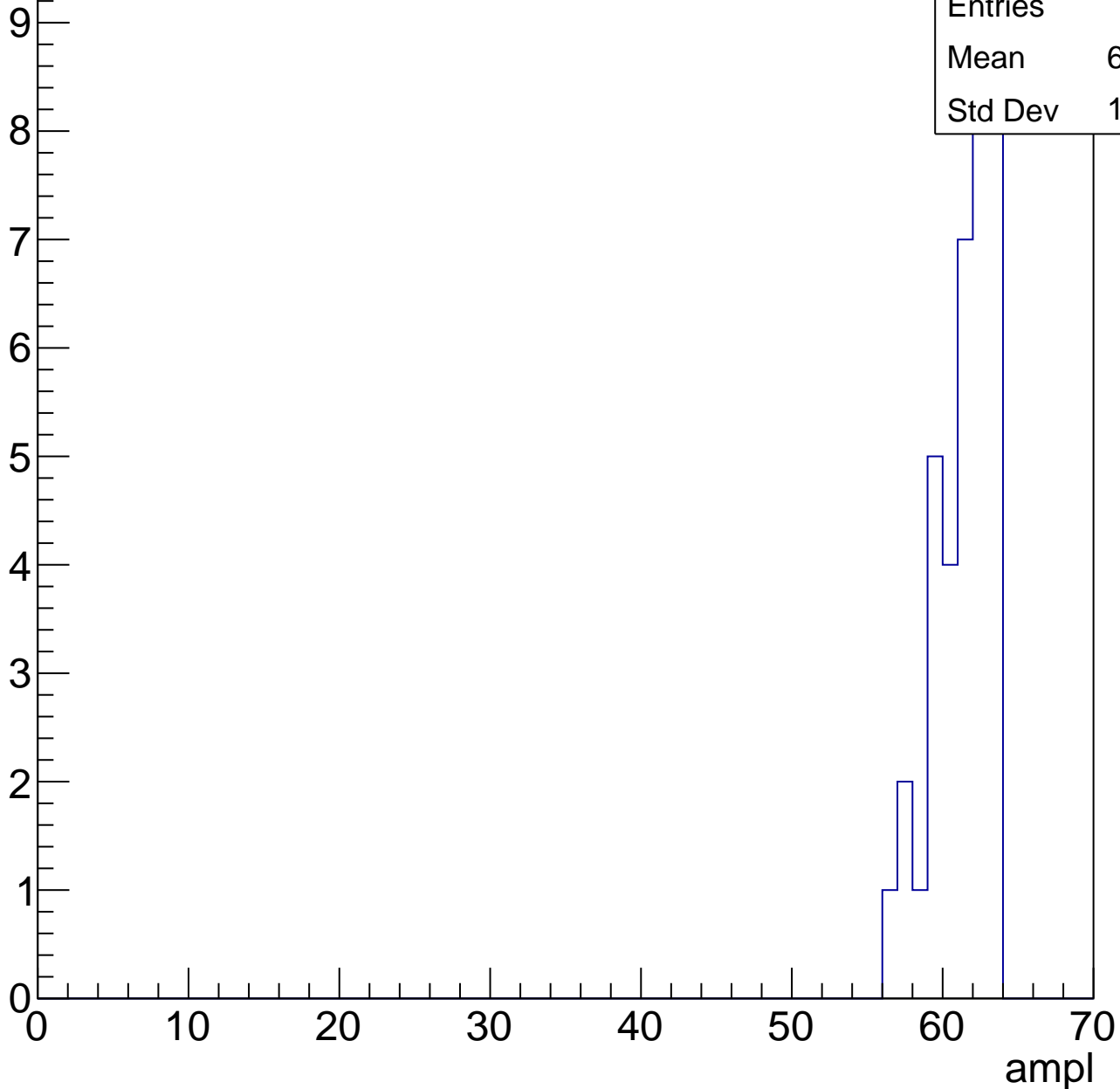


B1L102S, U26-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	60.92
Std Dev	1.883



B1L102S, U26-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U26-ch36, adc0

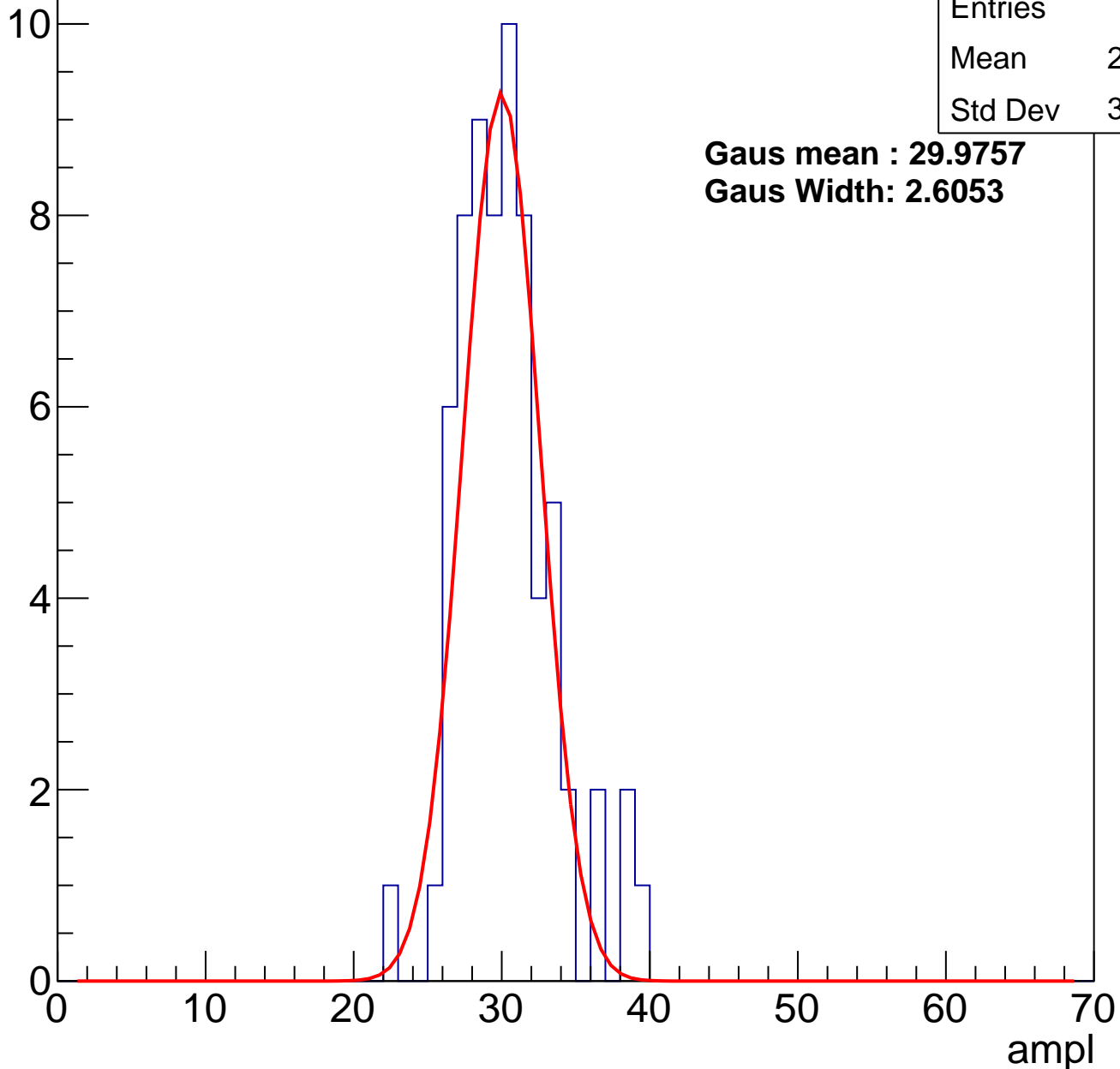
calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	29.84
Std Dev	3.189

Gaus mean : 29.9757

Gaus Width: 2.6053

Entry



B1L102S, U26-ch36, adc1

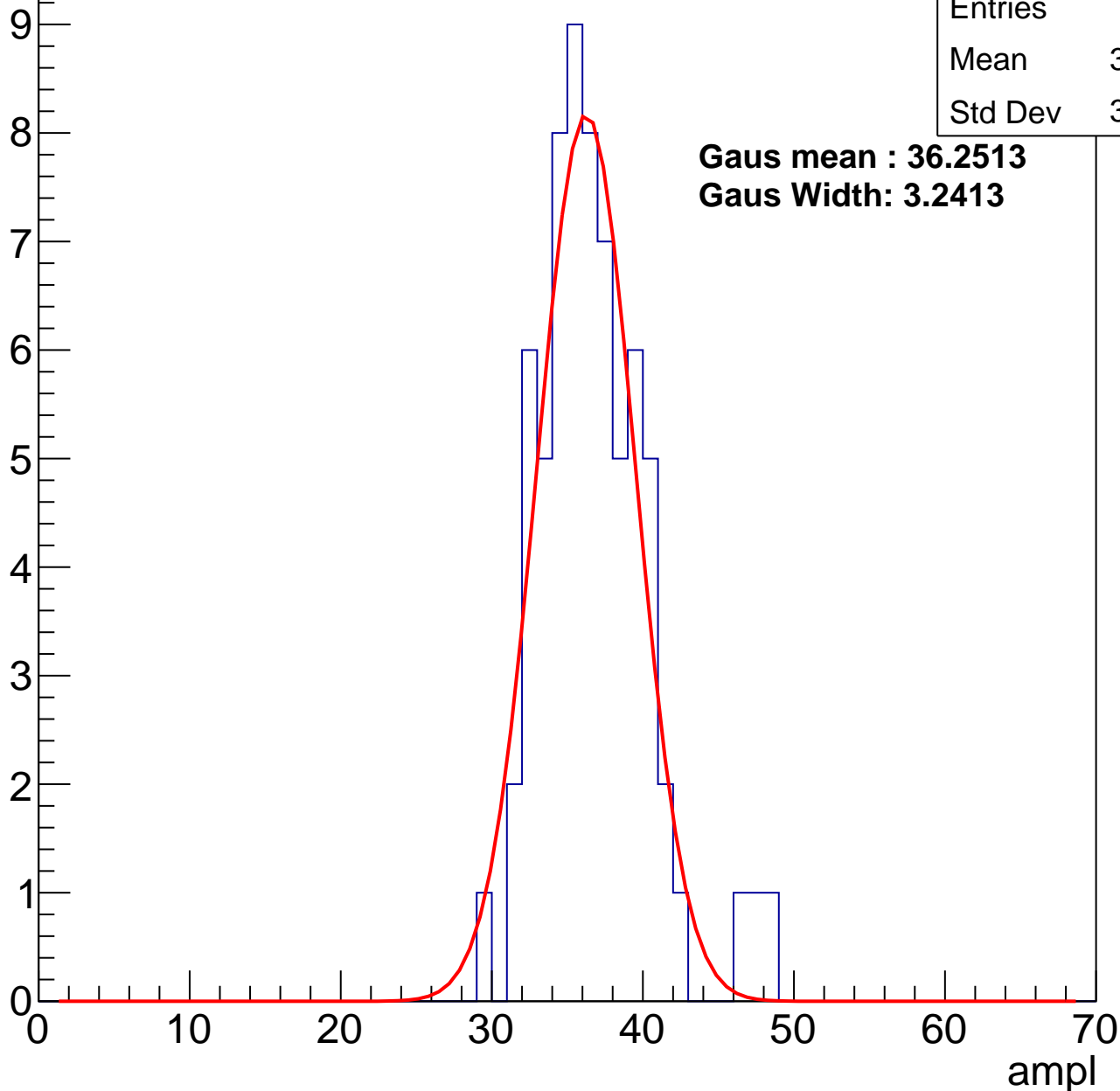
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.34
Std Dev	3.608

Gaus mean : 36.2513

Gaus Width: 3.2413



B1L102S, U26-ch36, adc2

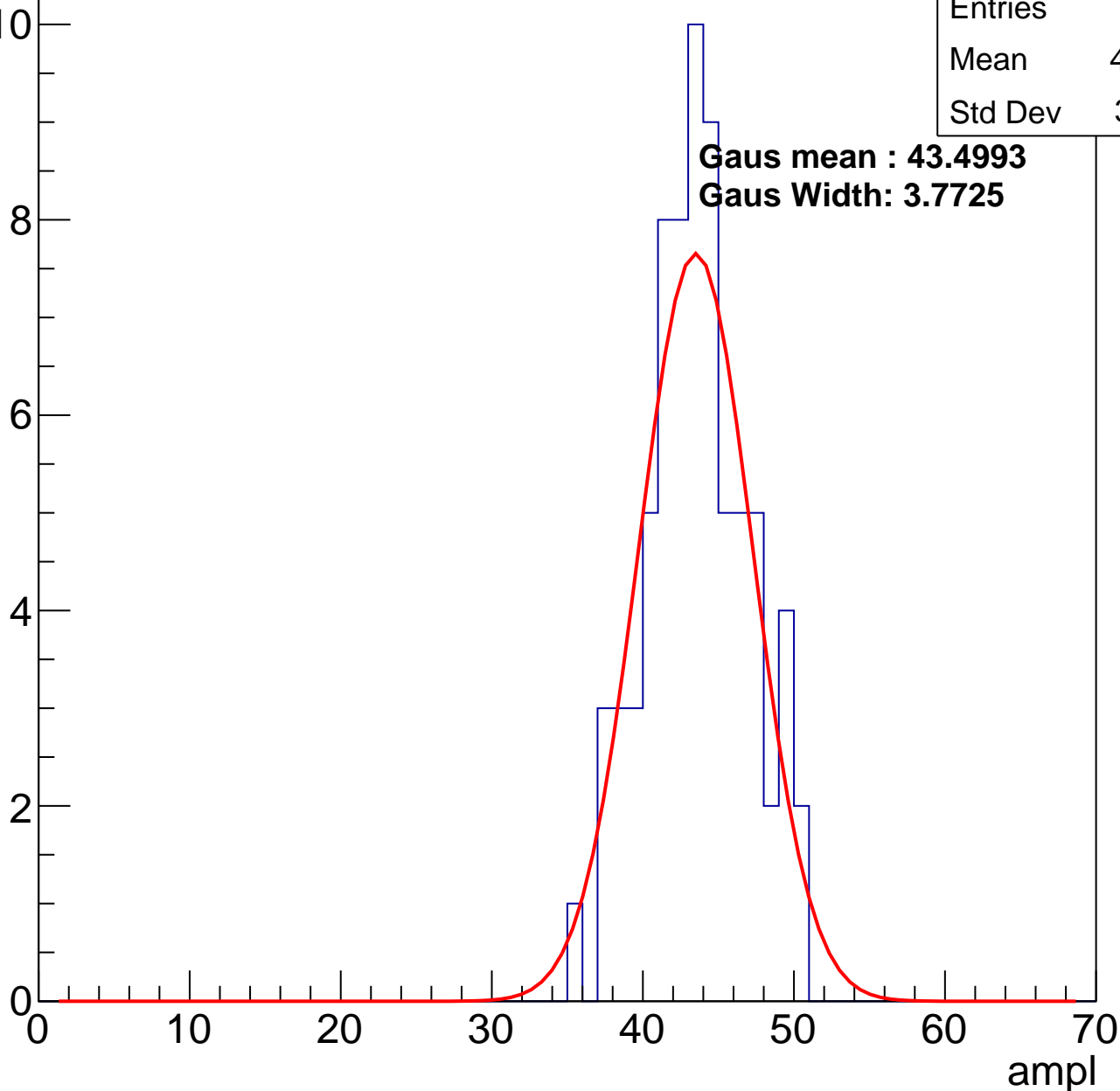
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.14
Std Dev	3.361

Gaus mean : 43.4993

Gaus Width: 3.7725

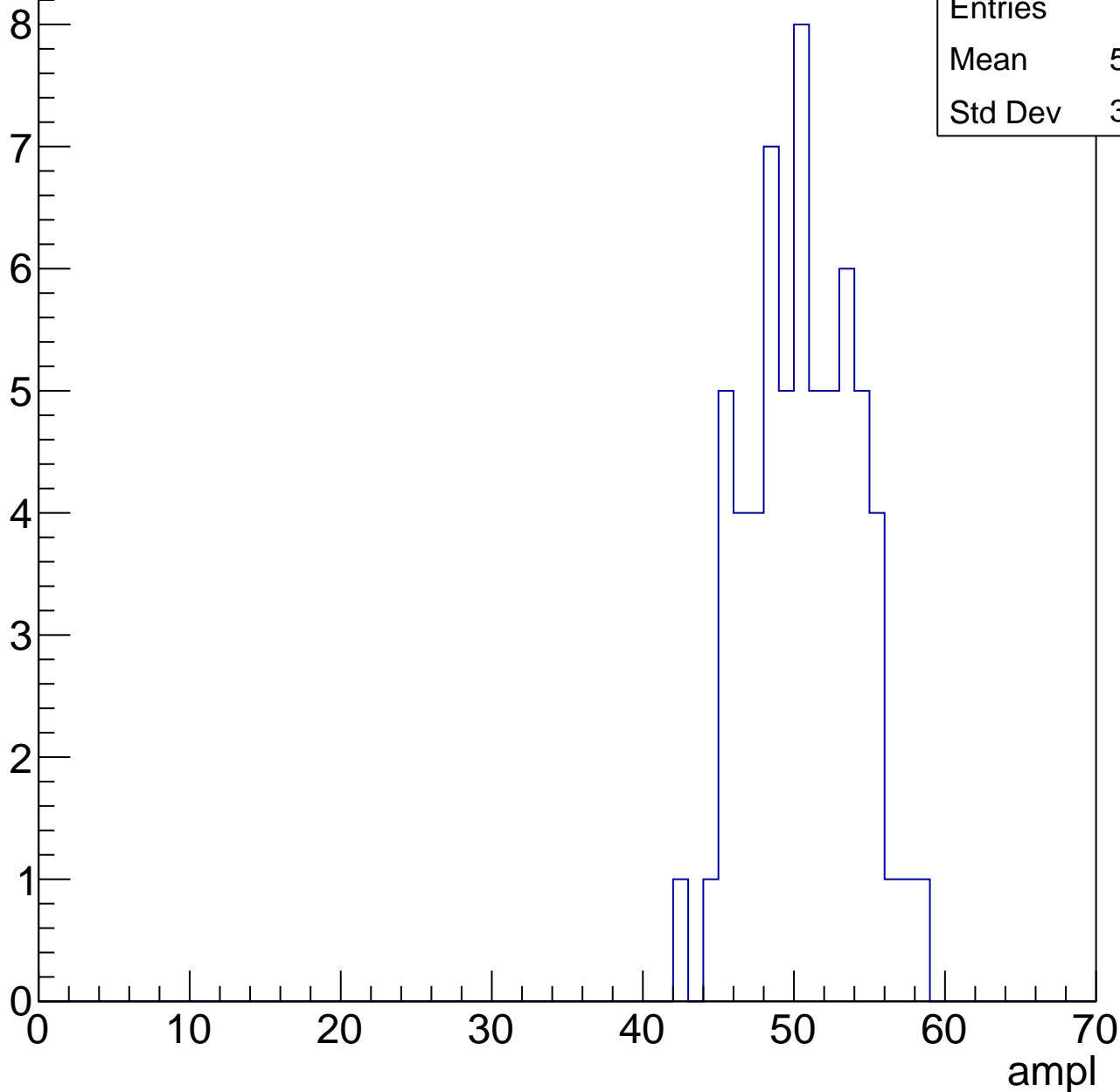


B1L102S, U26-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.13
Std Dev	3.485

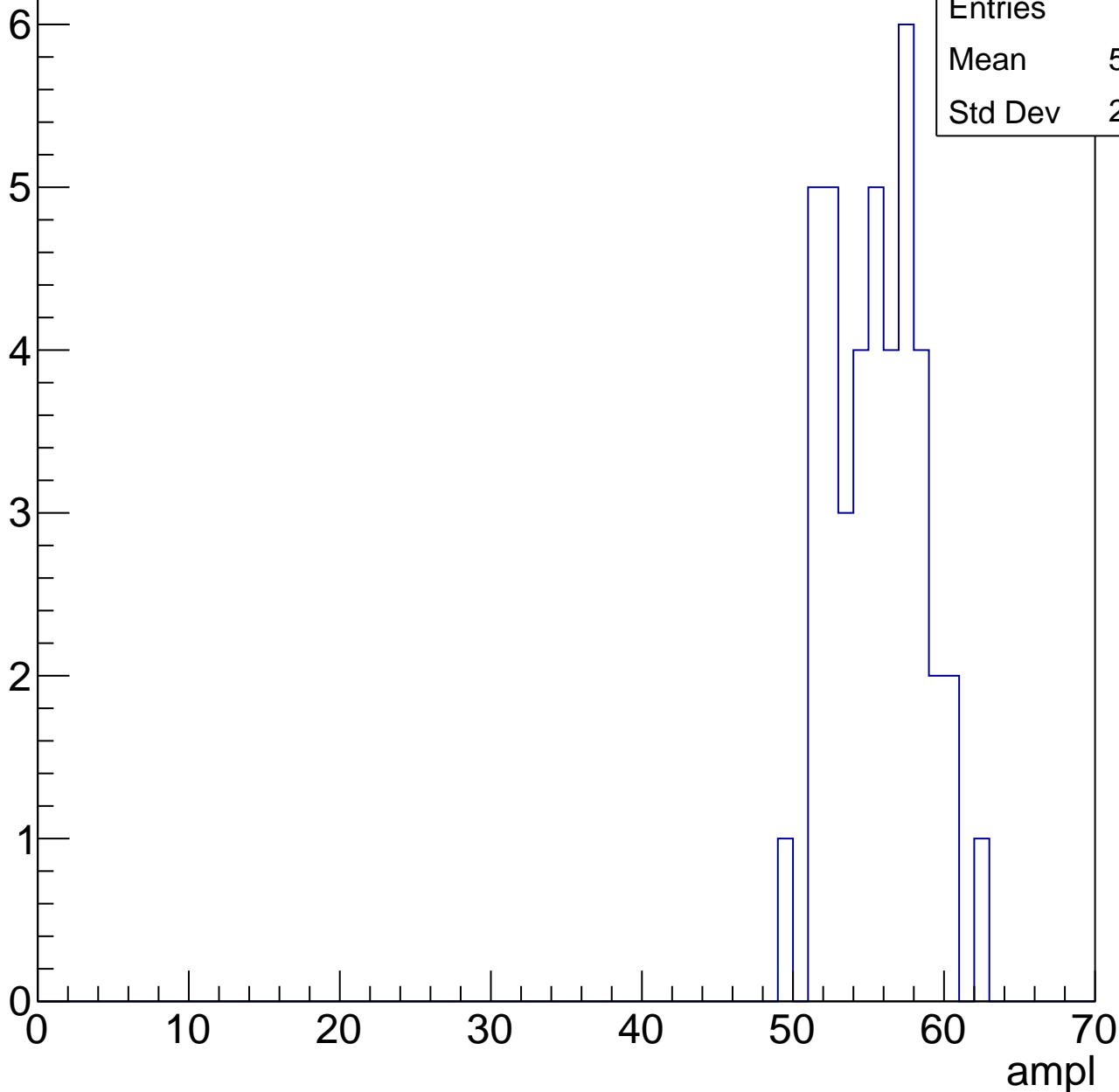


B1L102S, U26-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	55.05
Std Dev	2.976

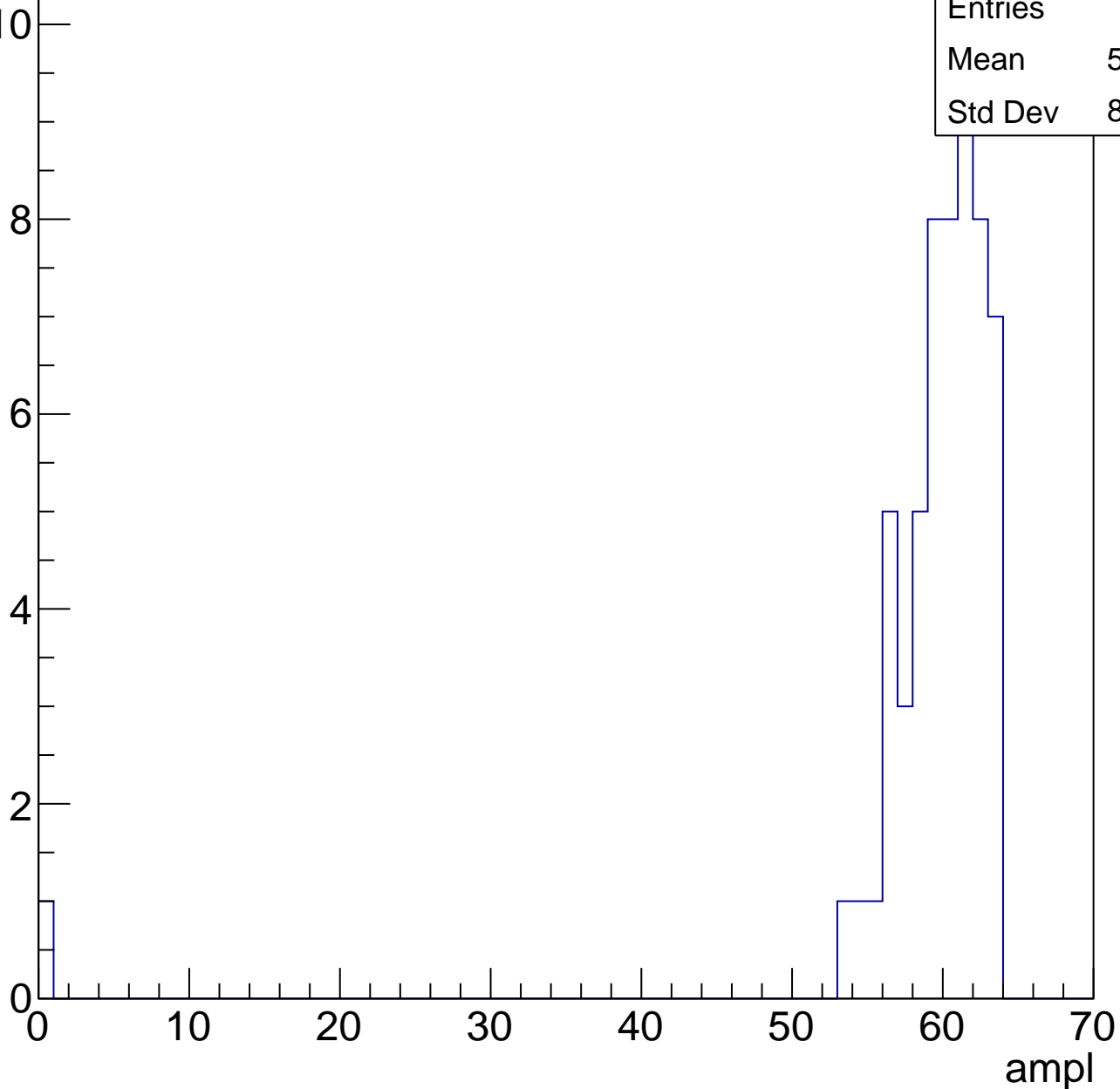


B1L102S, U26-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.66
Std Dev	8.142



B1L102S, U26-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch37, adc0

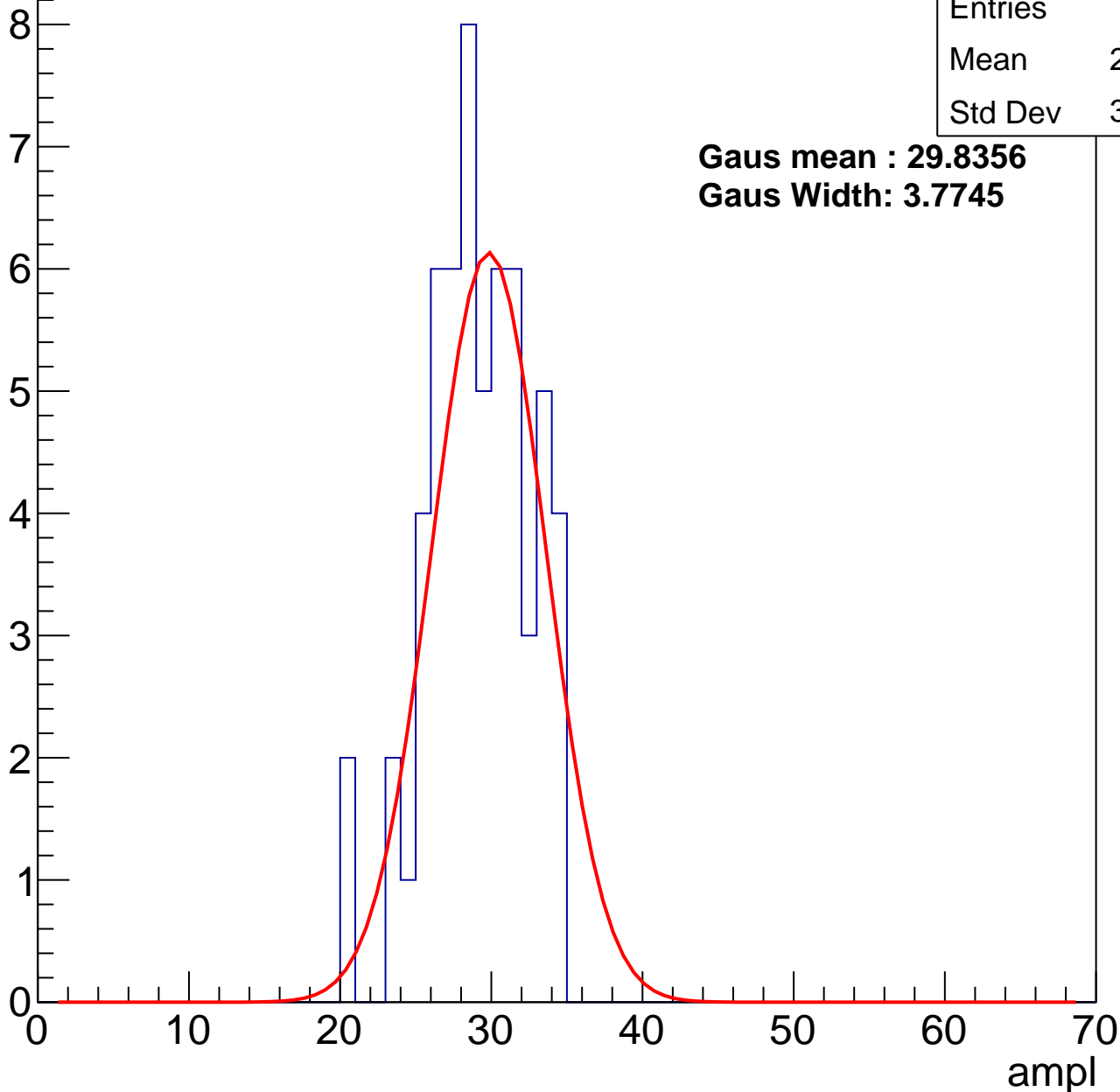
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.62
Std Dev	3.316

Gaus mean : 29.8356

Gaus Width: 3.7745



B1L102S, U26-ch37, adc1

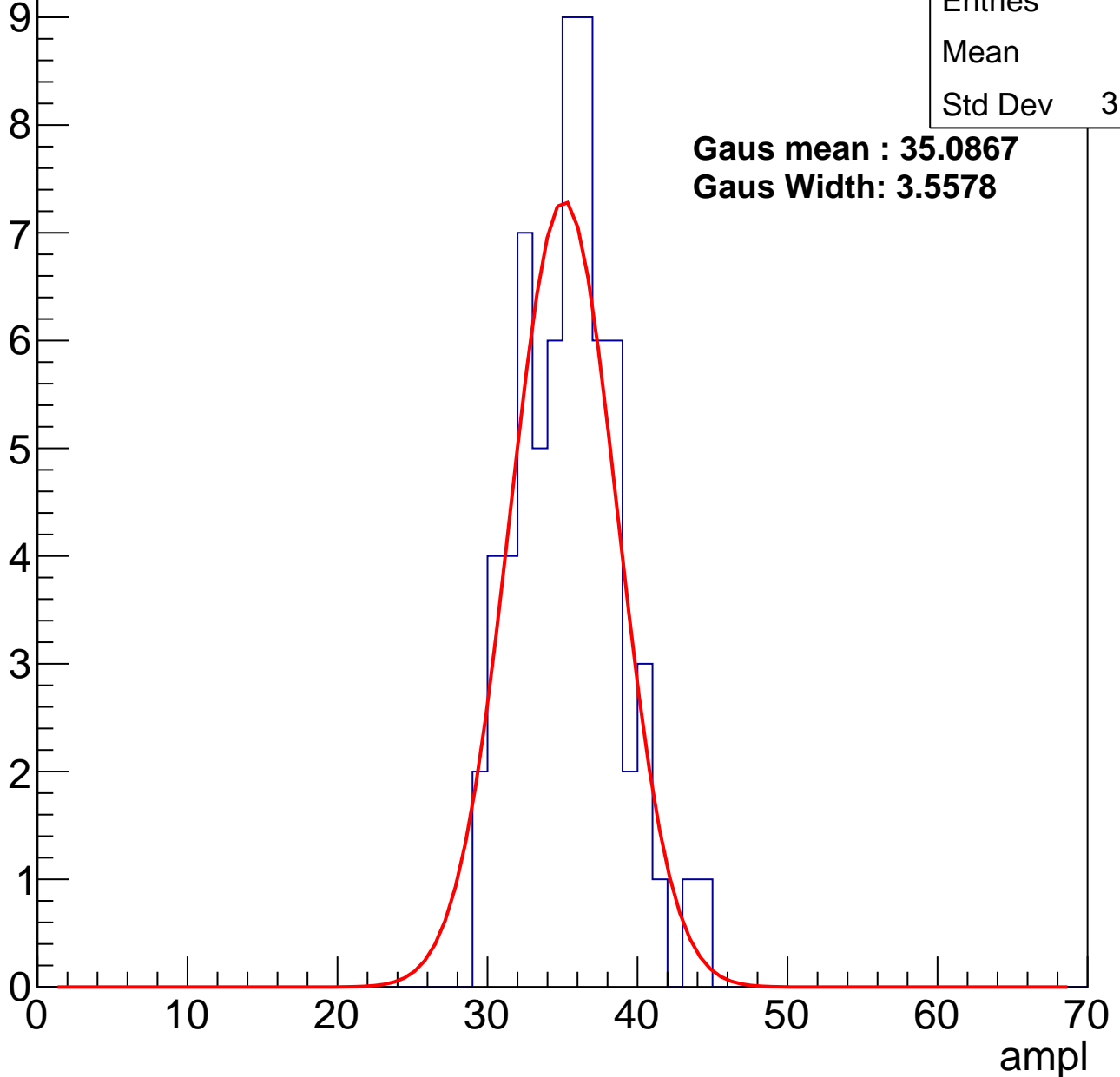
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35
Std Dev	3.257

Gaus mean : 35.0867

Gaus Width: 3.5578



B1L102S, U26-ch37, adc2

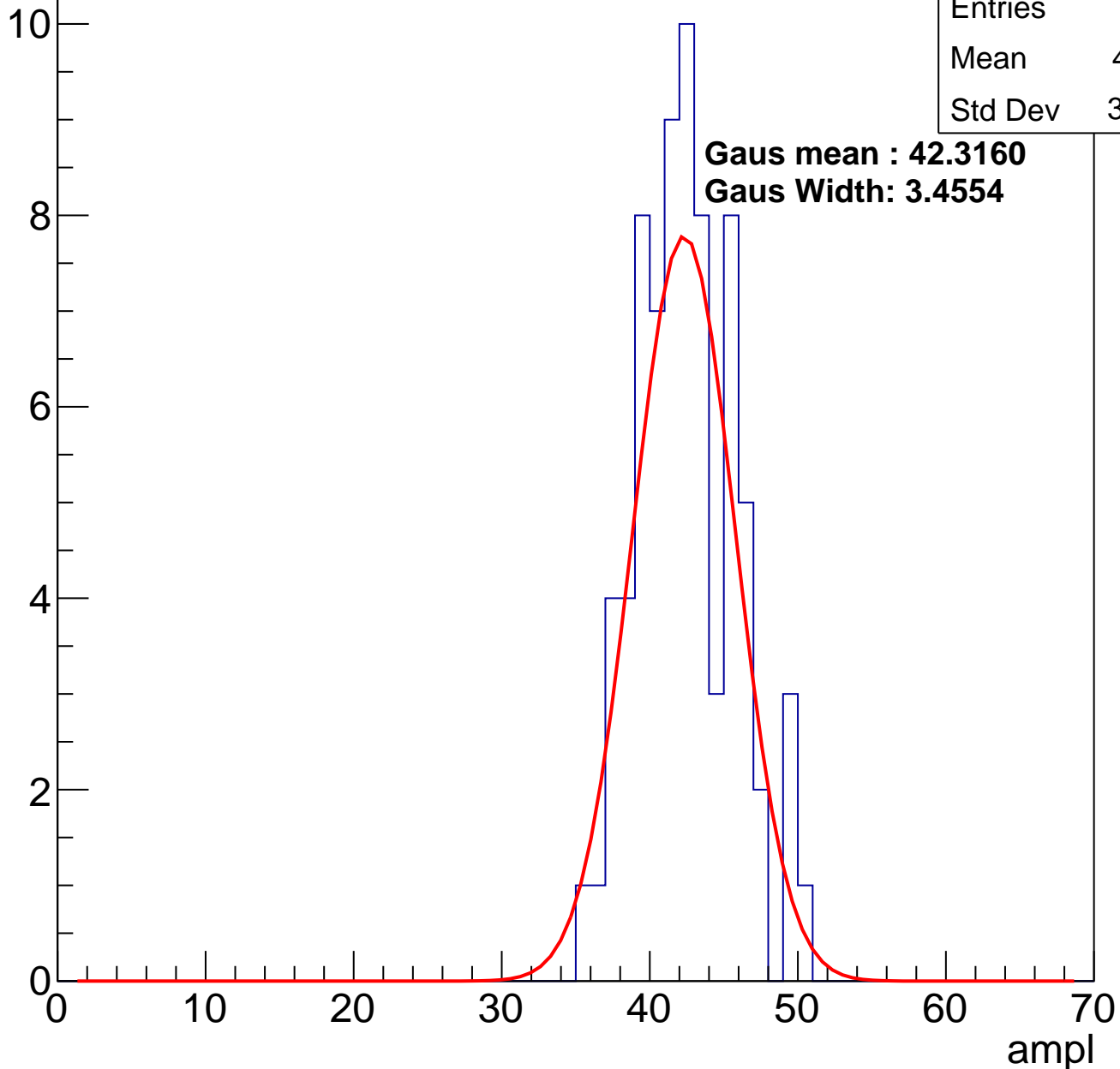
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	42.01
Std Dev	3.269

Gaus mean : 42.3160

Gaus Width: 3.4554

Entry



B1L102S, U26-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

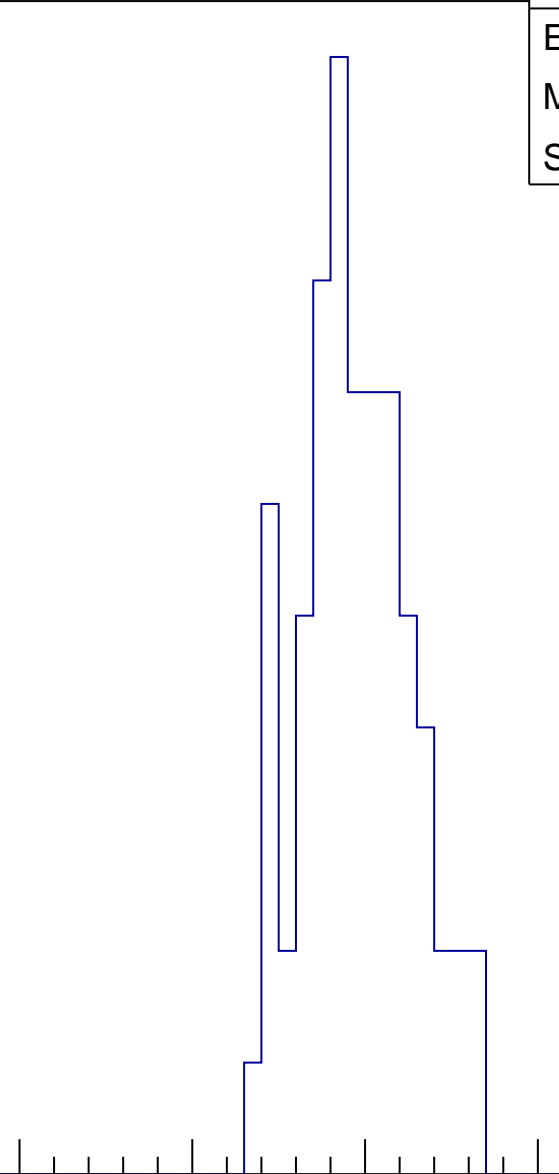
Entries	68
Mean	49.04
Std Dev	3.136

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

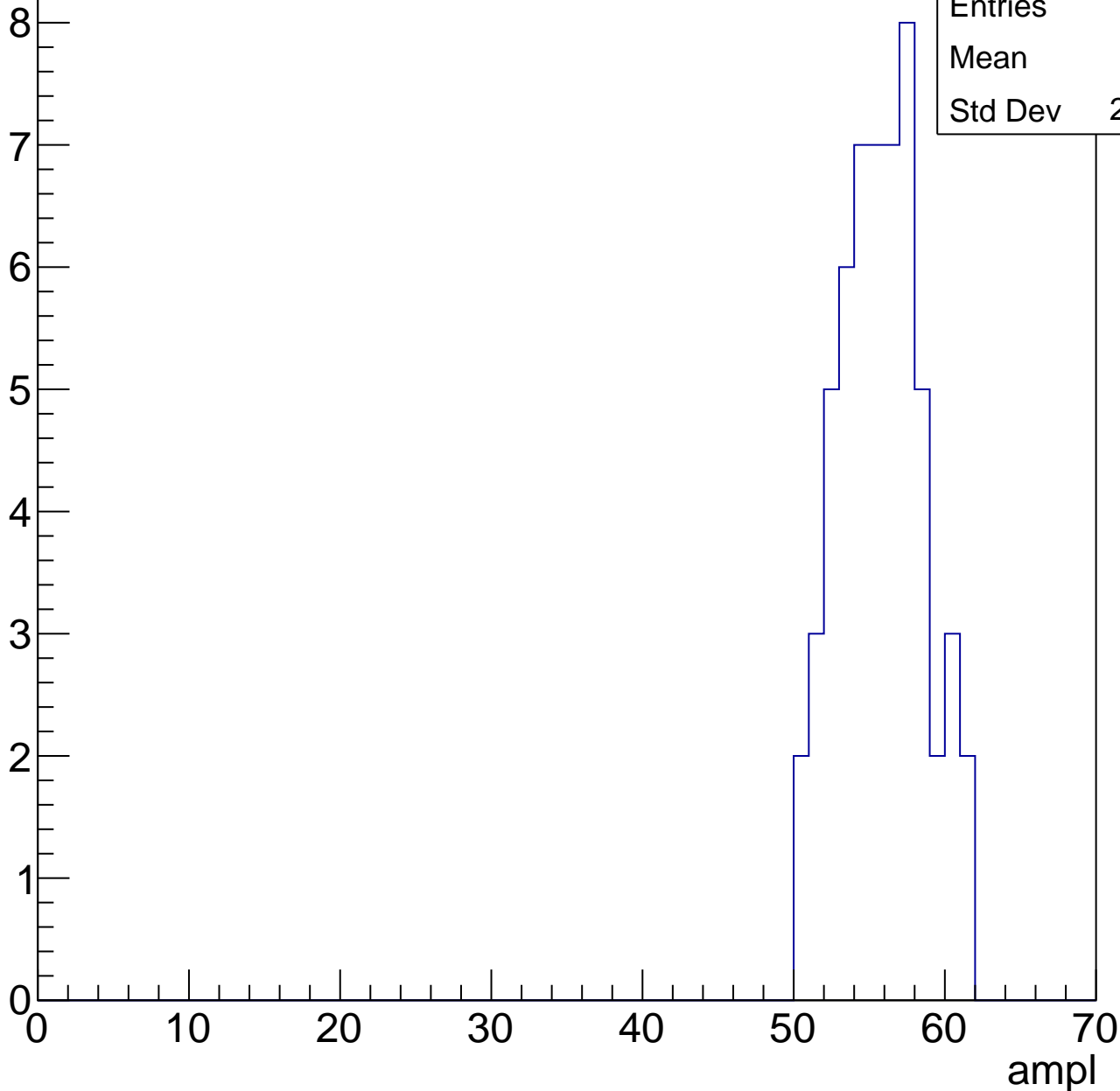


B1L102S, U26-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.3
Std Dev	2.753



B1L102S, U26-ch37, adc5

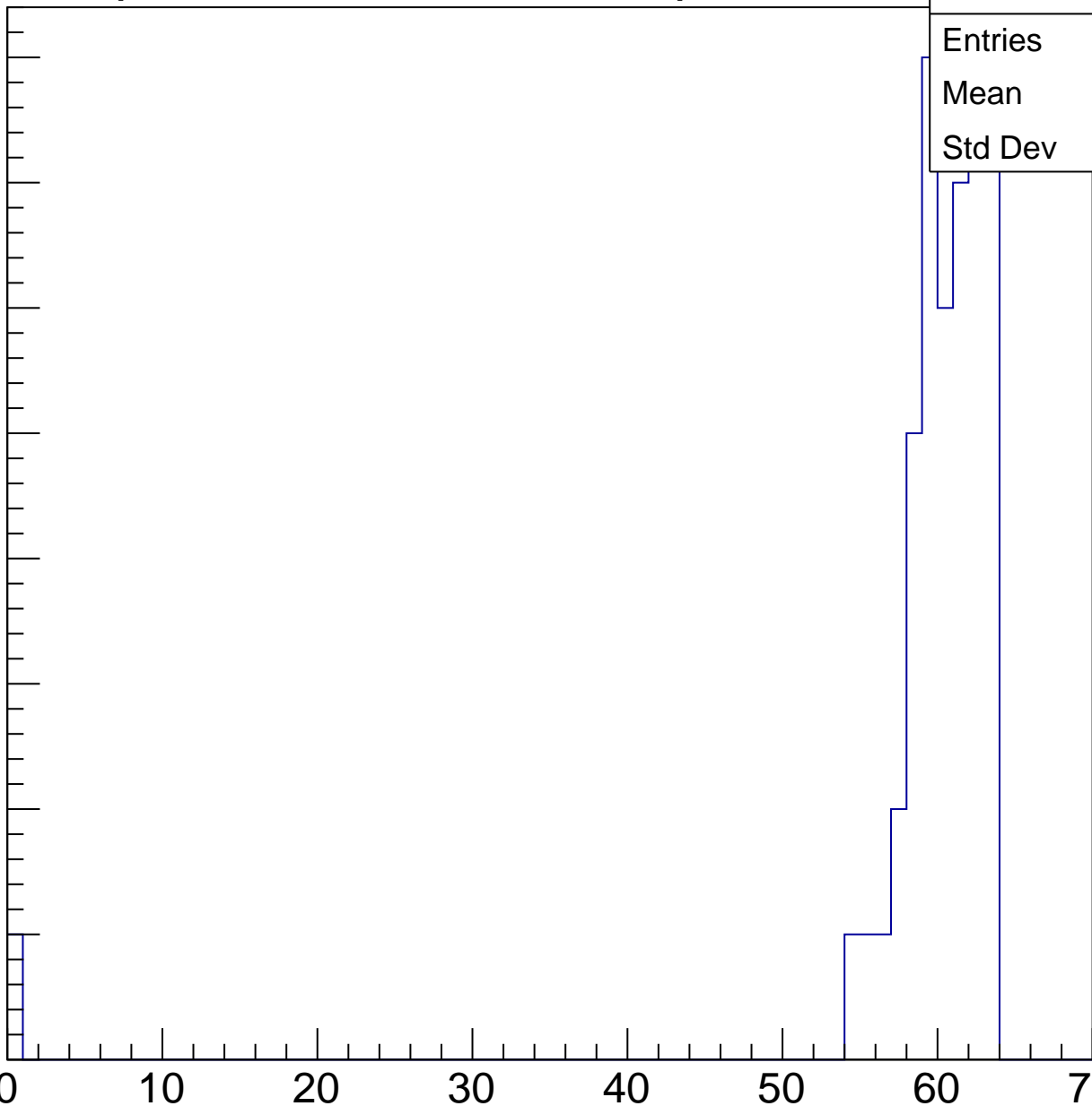
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.92
Std Dev	8.871

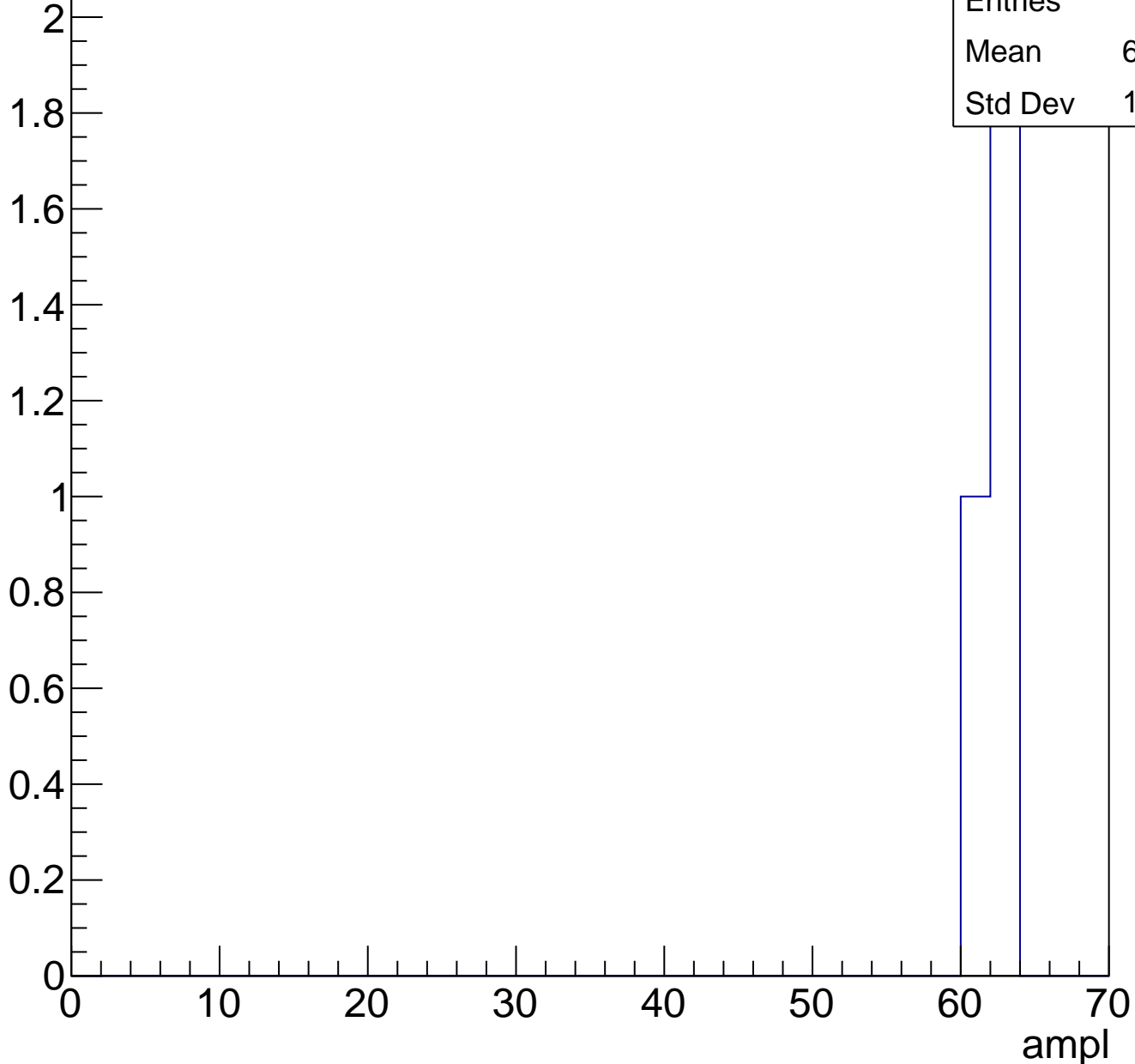
ampl



B1L102S, U26-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch38, adc0

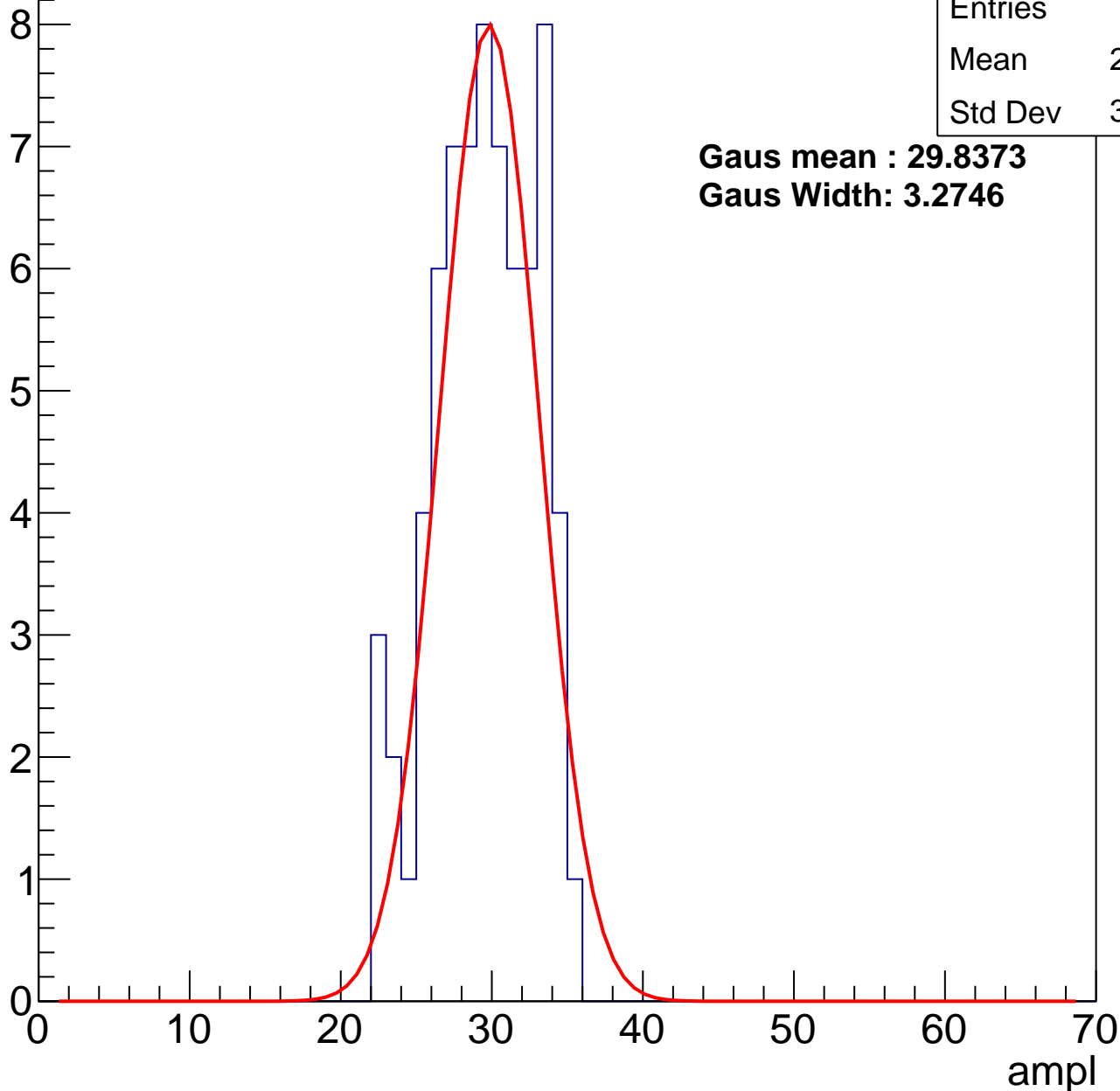
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.03
Std Dev	3.264

Gaus mean : 29.8373

Gaus Width: 3.2746



B1L102S, U26-ch38, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	36.27
Std Dev	3.6

Gaus mean : 36.7749

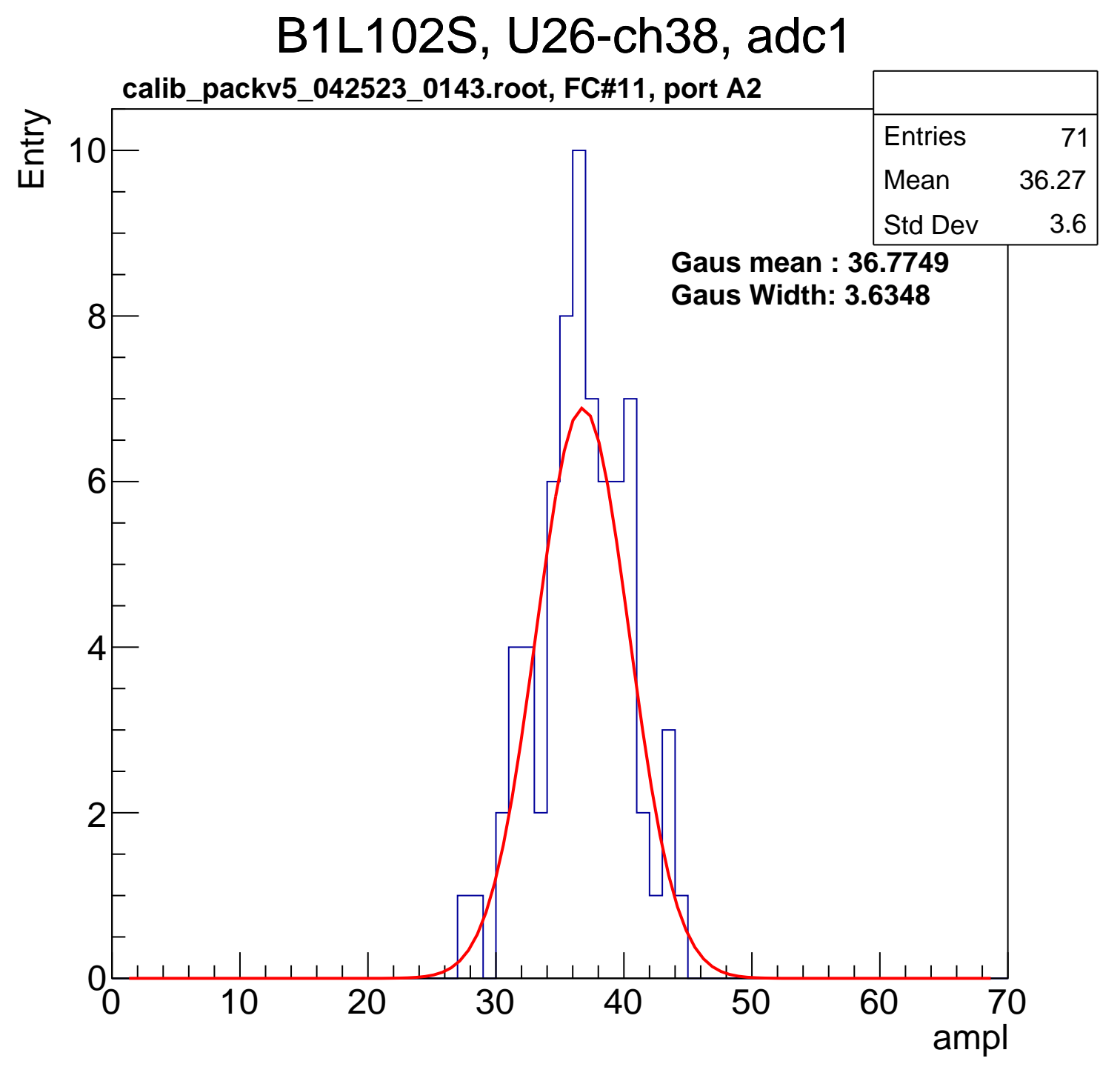
Gaus Width: 3.6348

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch38, adc2

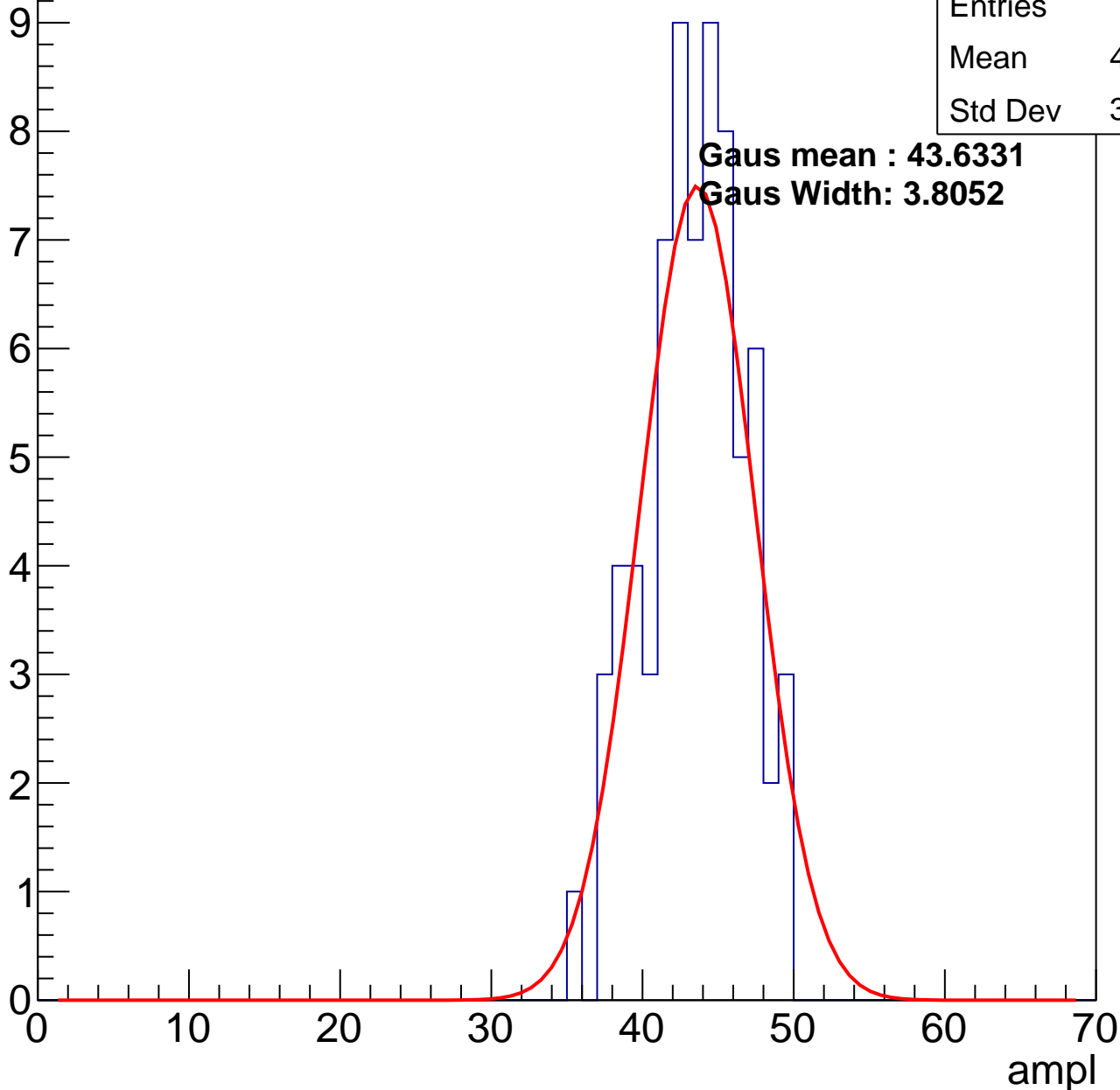
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.97
Std Dev	3.228

Gaus mean : 43.6331

Gaus Width: 3.8052

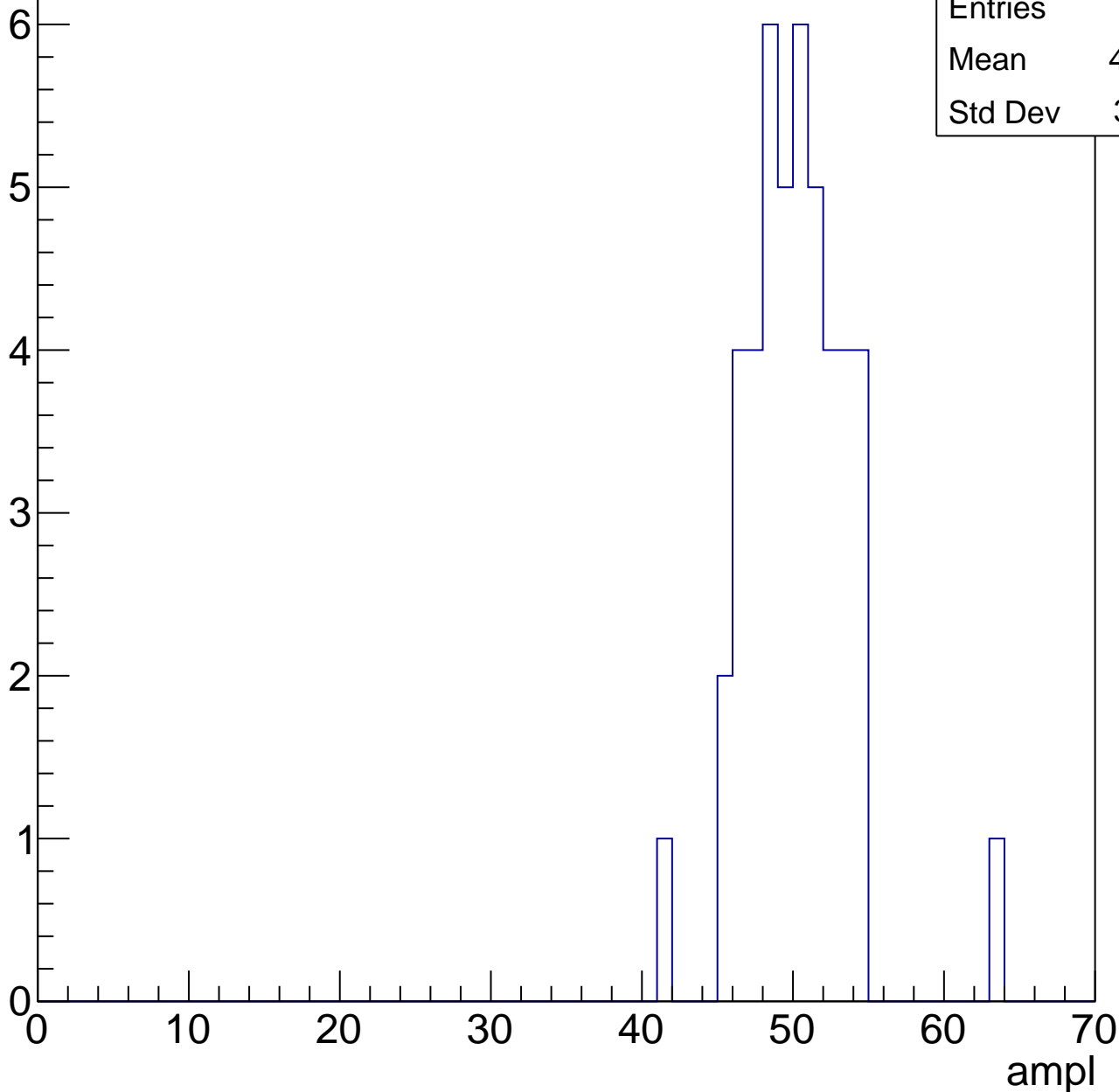


B1L102S, U26-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	49.78
Std Dev	3.451



B1L102S, U26-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	54.93
Std Dev	3.492

Entry

10

8

6

4

2

0

0

10

20

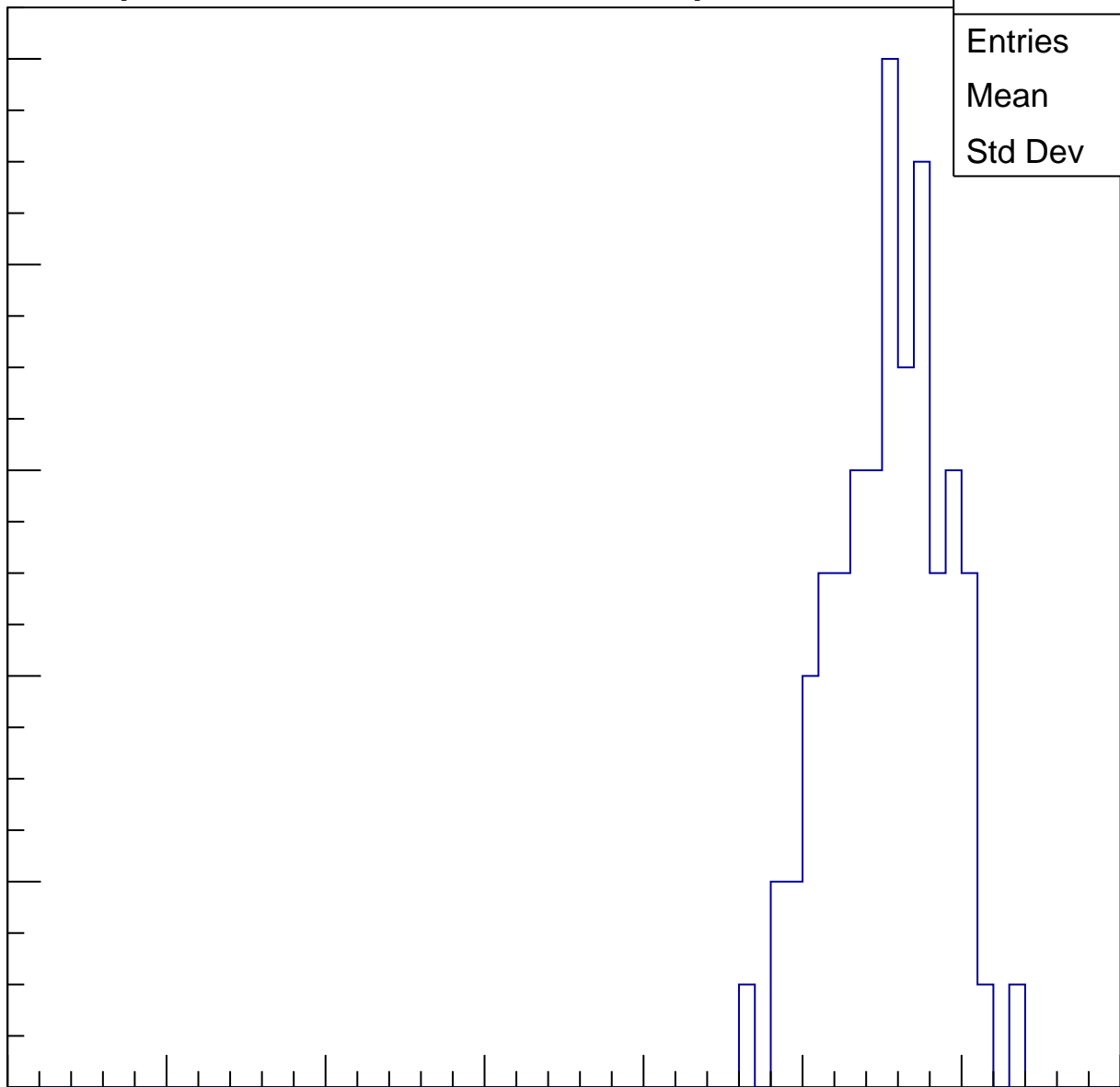
30

40

50

60

ampl

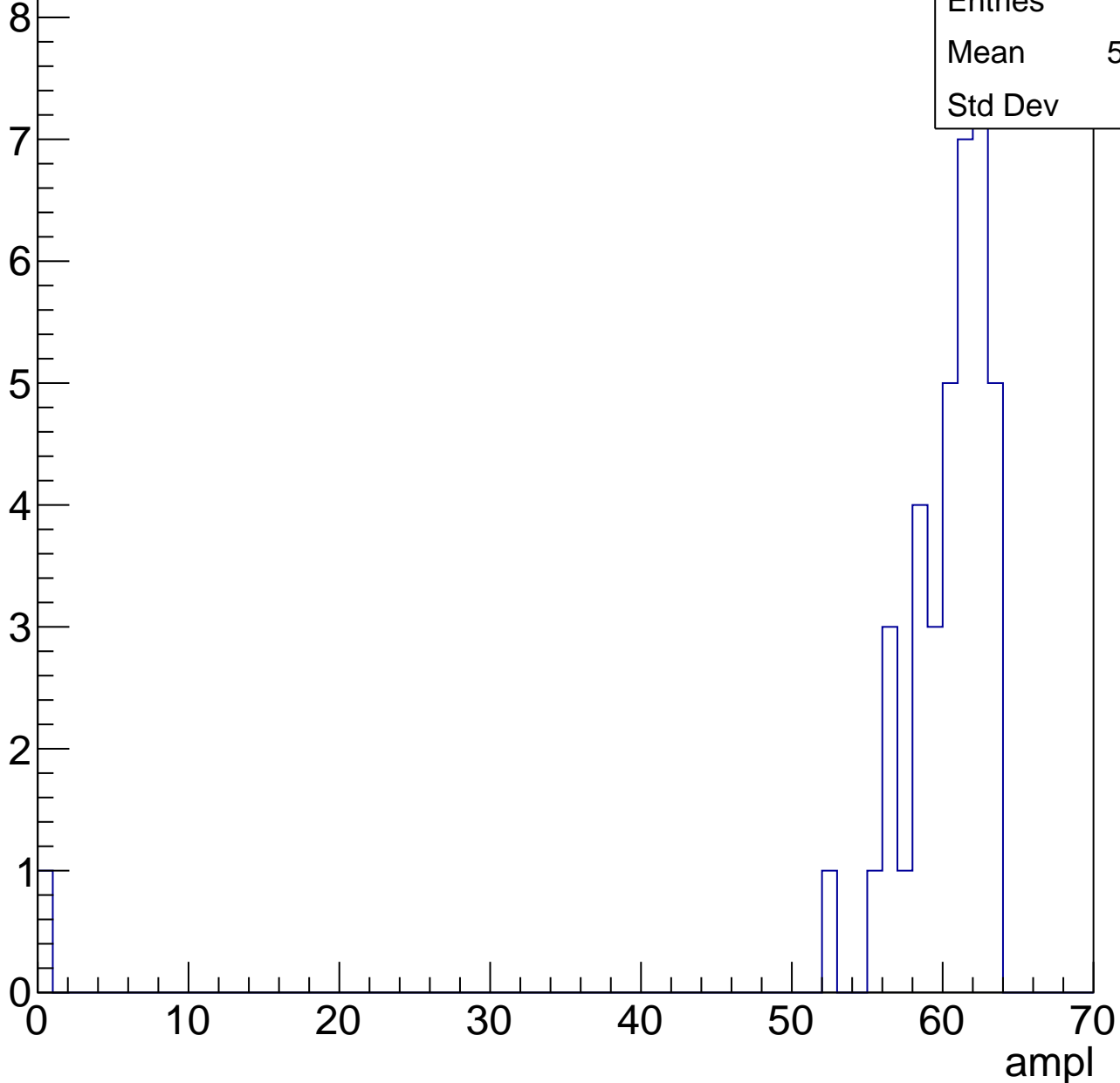


B1L102S, U26-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

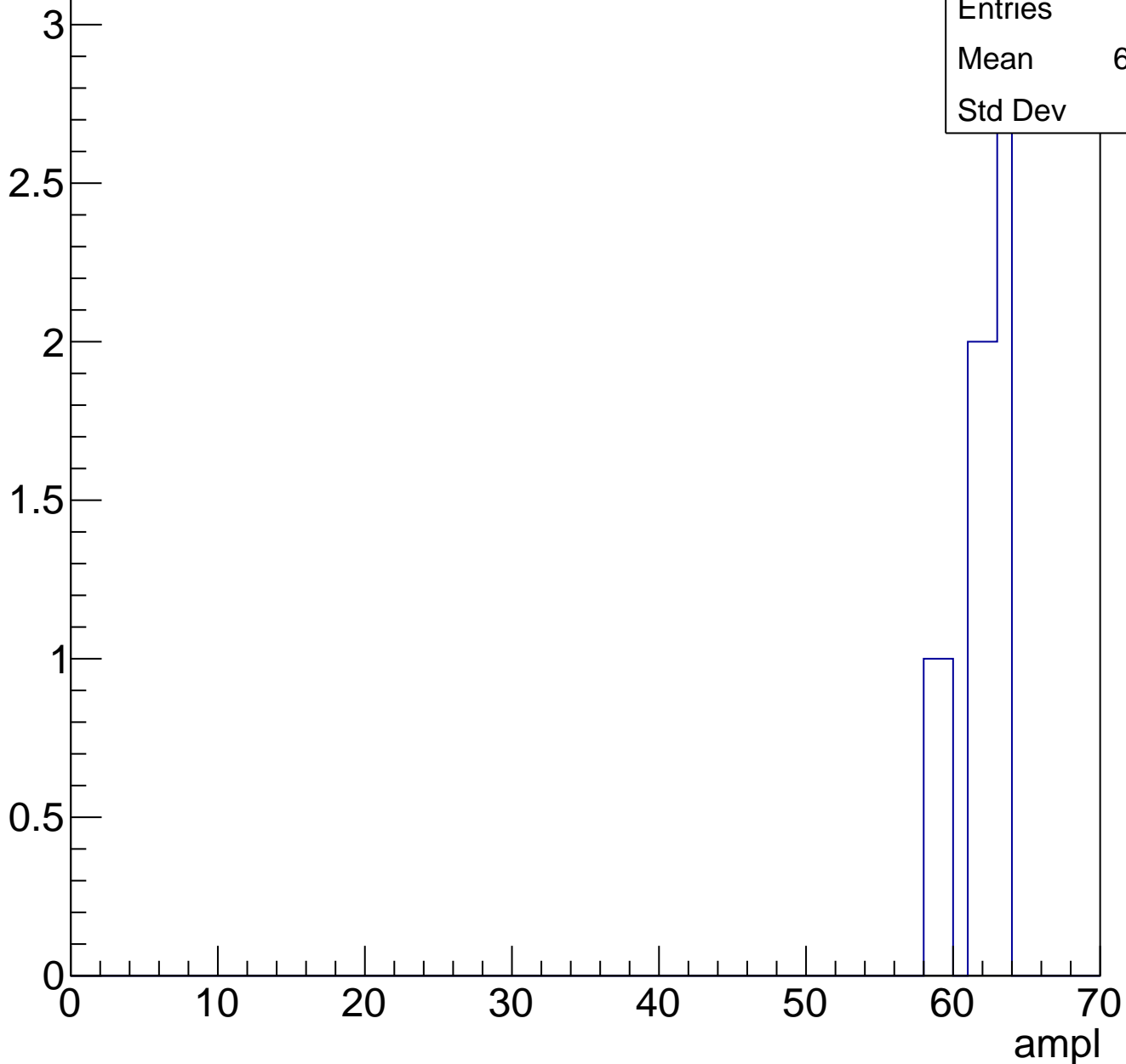
Entries	39
Mean	58.44
Std Dev	9.81



B1L102S, U26-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch39, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	28.87
Std Dev	4.774

Gaus mean : 29.9151

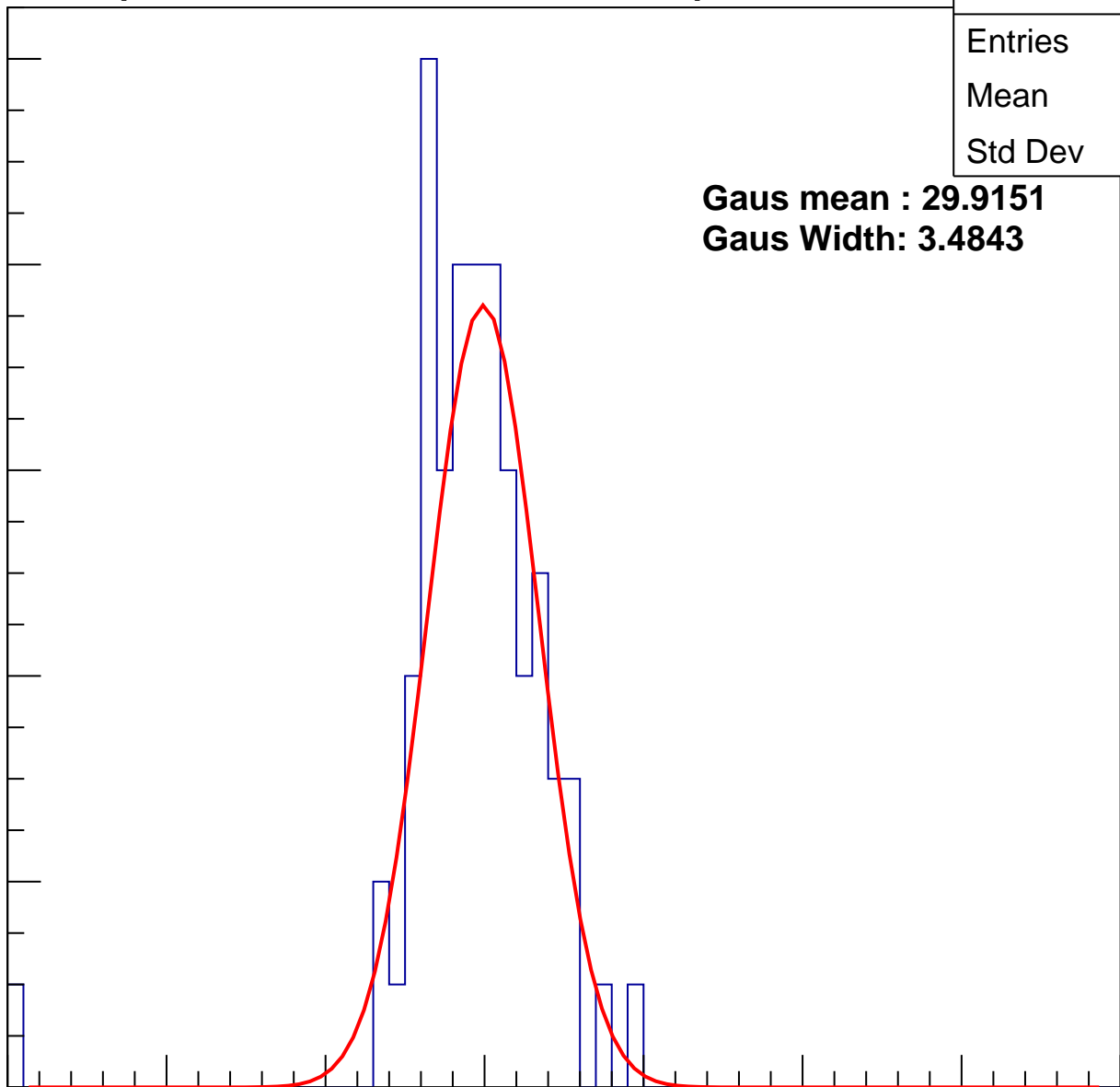
Gaus Width: 3.4843

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch39, adc1

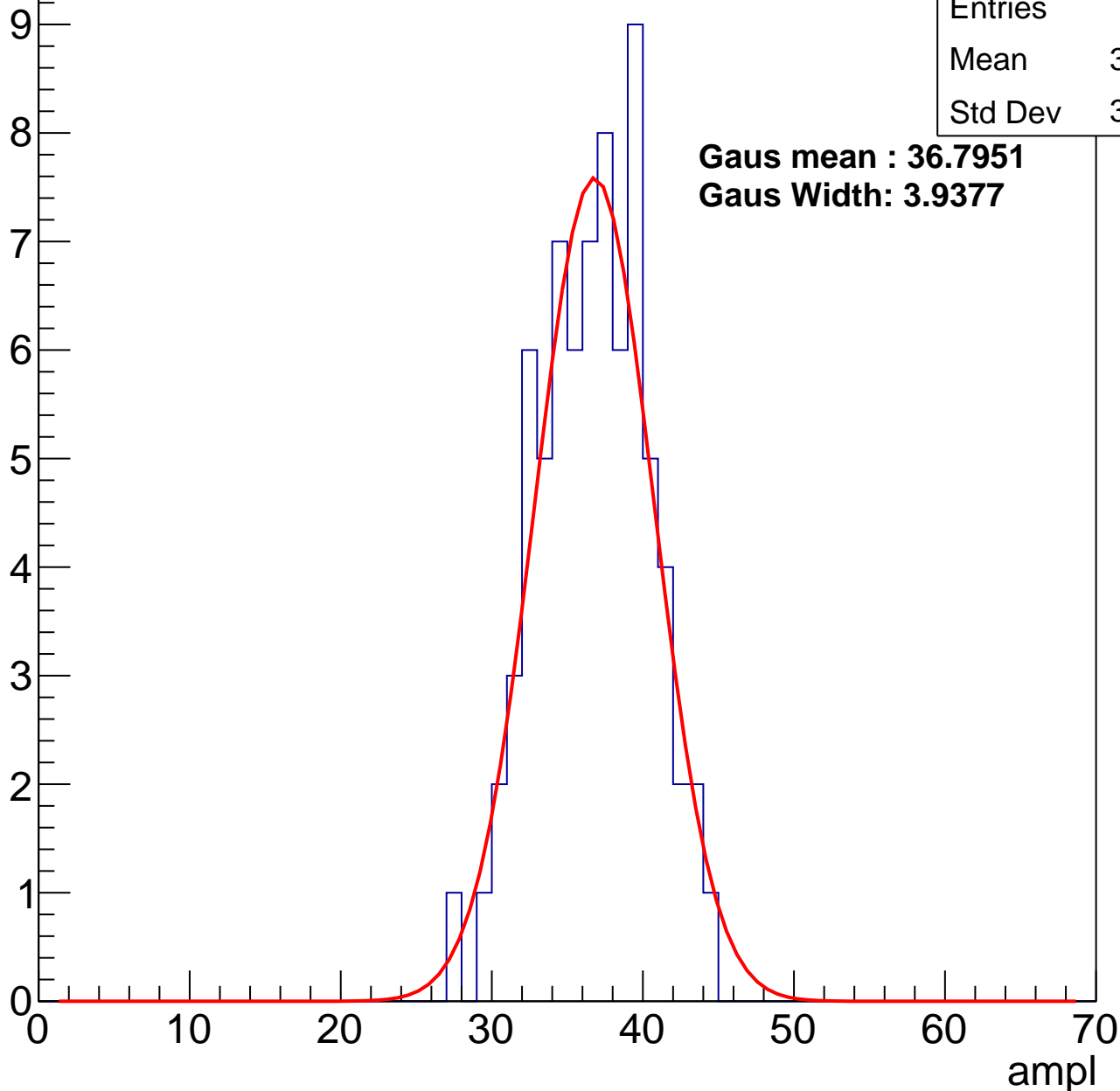
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.25
Std Dev	3.604

Gaus mean : 36.7951

Gaus Width: 3.9377



B1L102S, U26-ch39, adc2

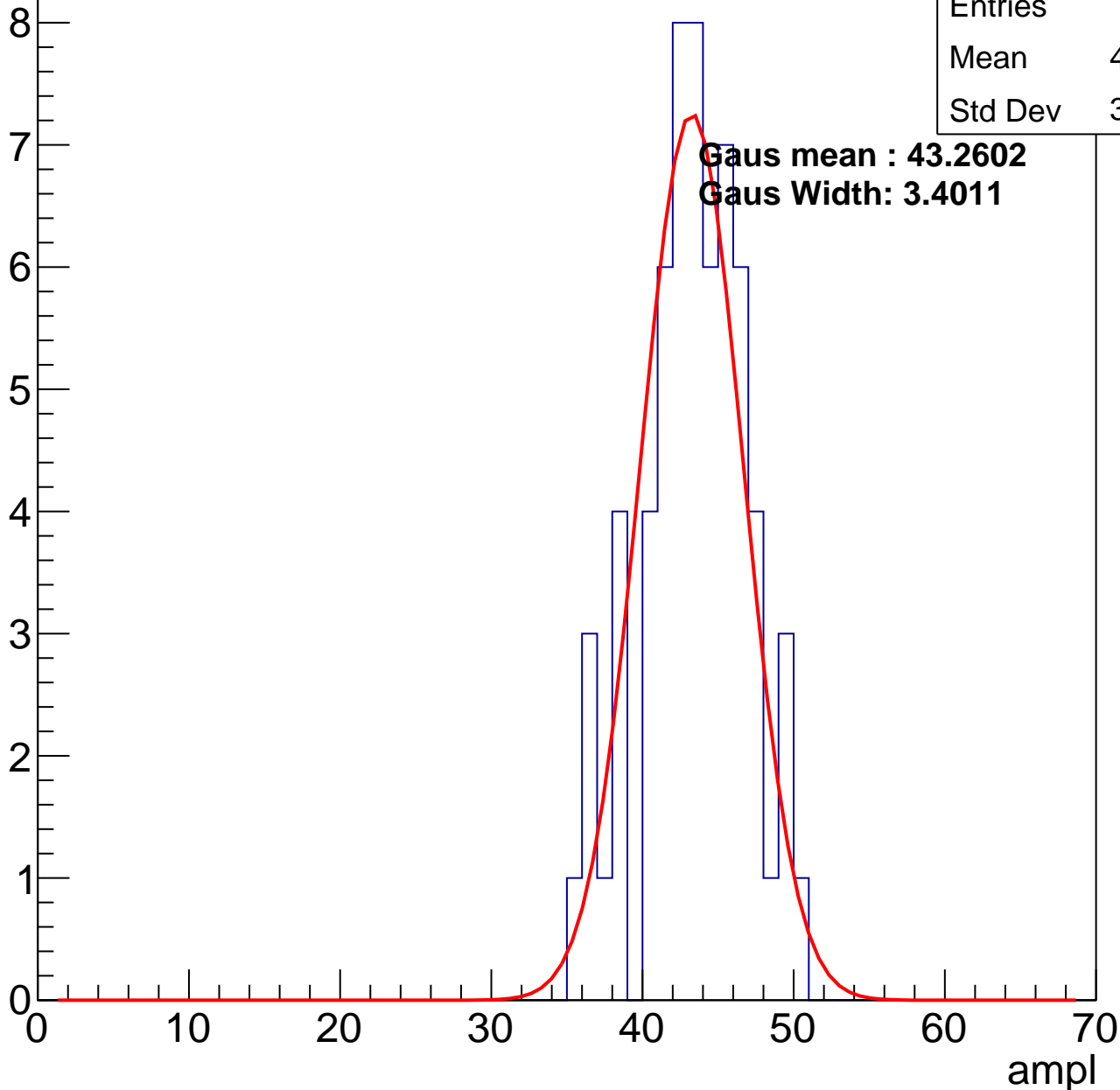
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.95
Std Dev	3.448

Gaus mean : 43.2602

Gaus Width: 3.4011

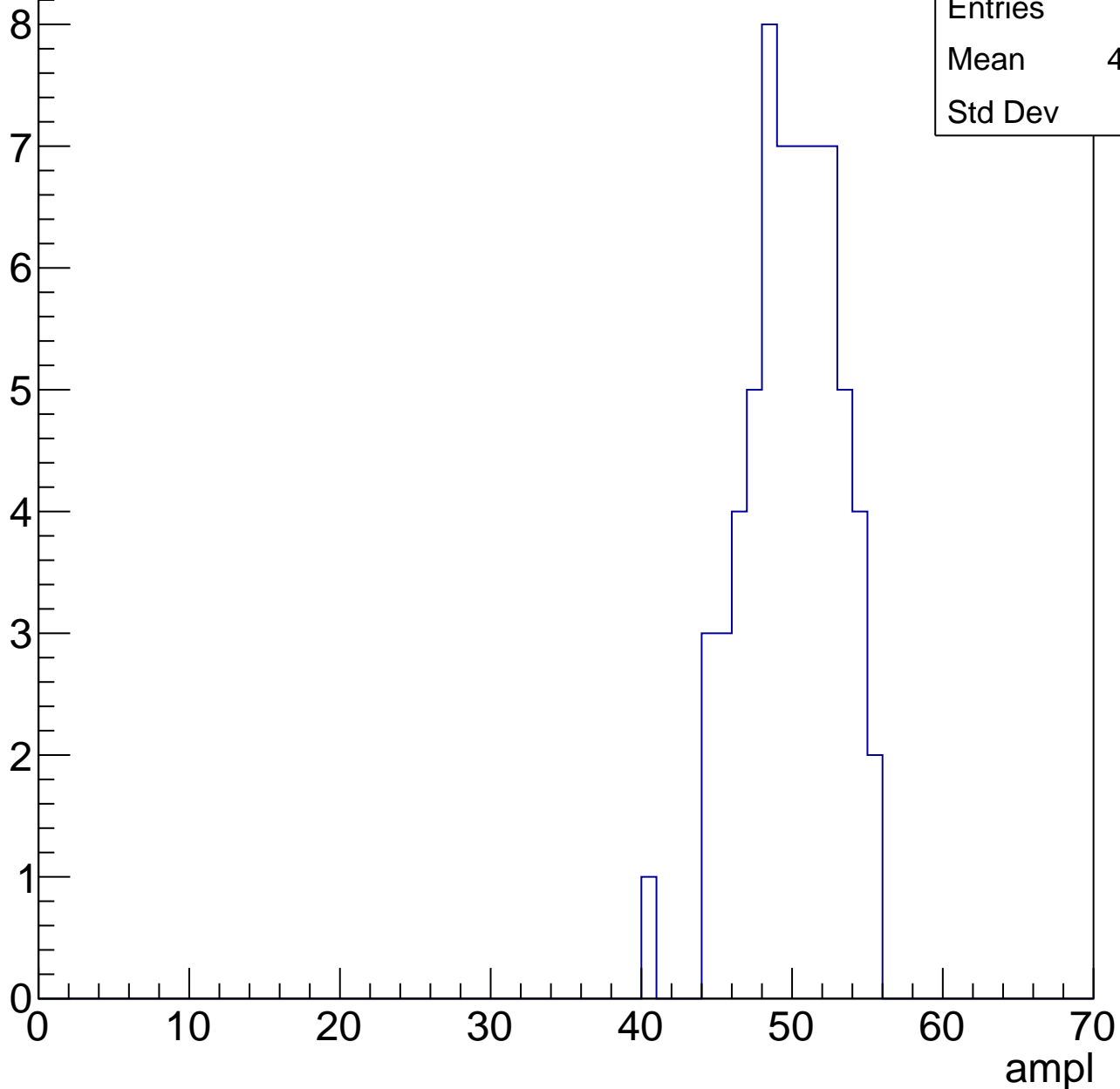


B1L102S, U26-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.44
Std Dev	3.1



B1L102S, U26-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	55.8
Std Dev	3.313

Entry

10

8

6

4

2

0

0

10

20

30

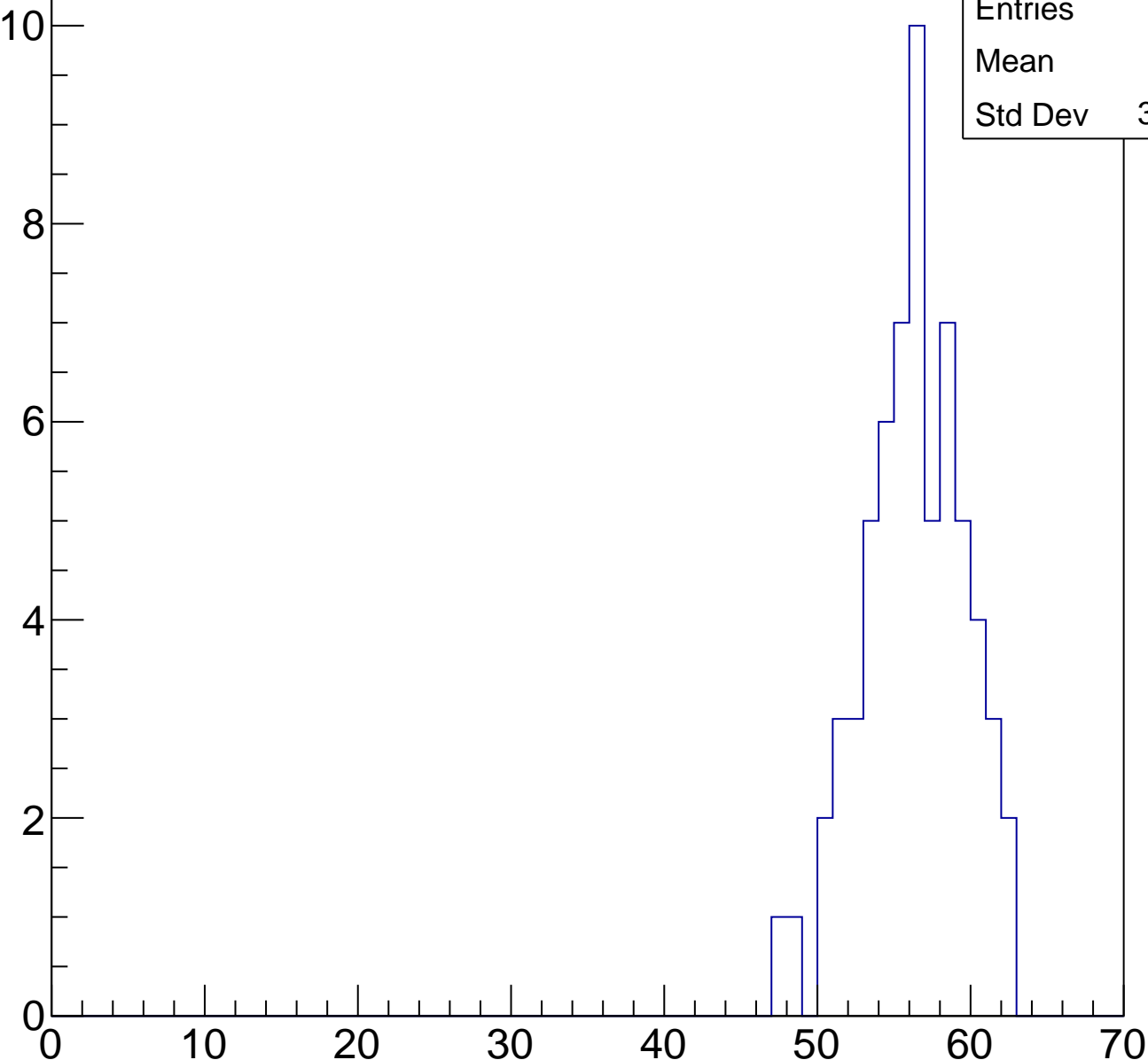
40

50

60

70

ampl

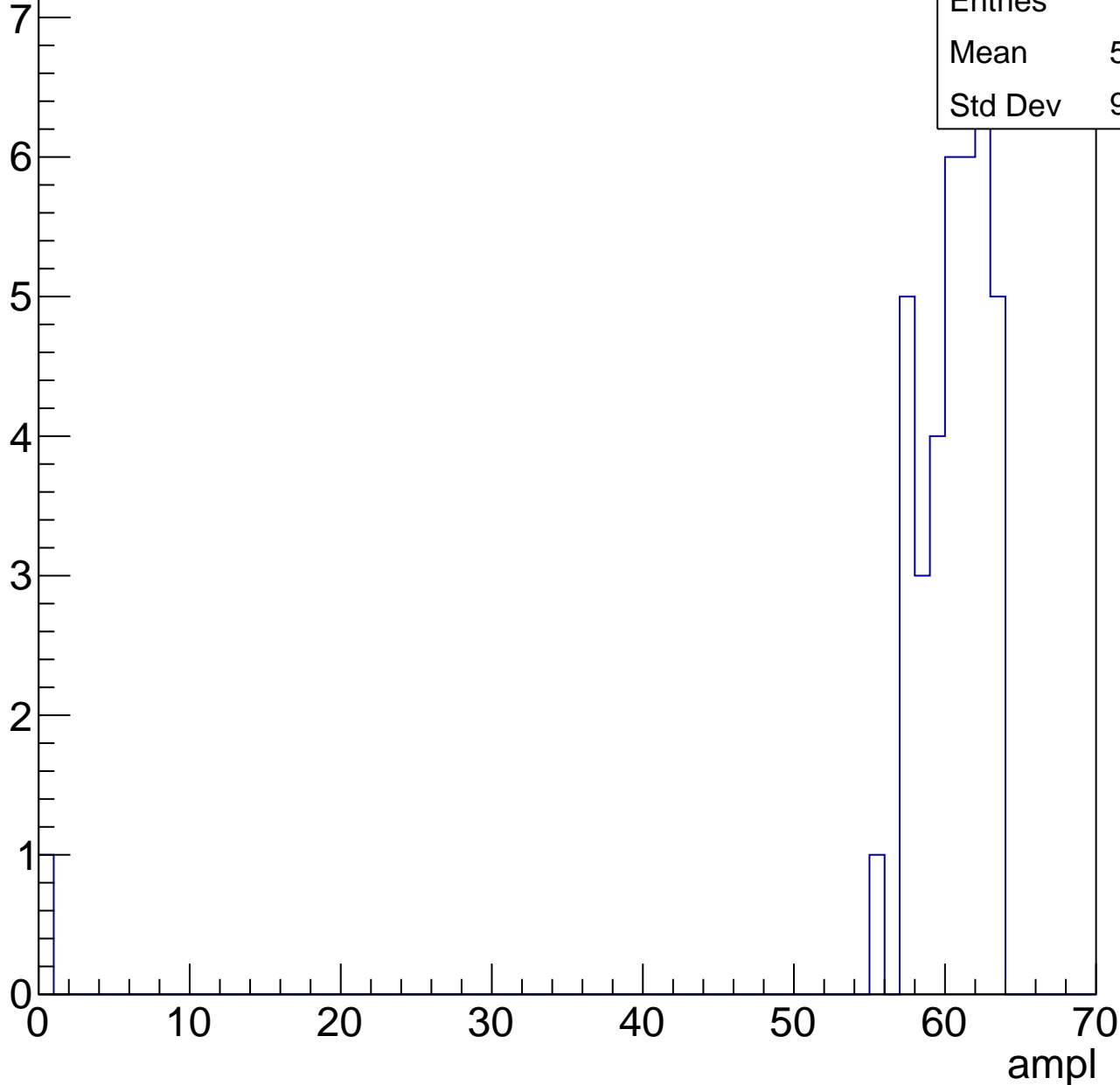


B1L102S, U26-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.55
Std Dev	9.848

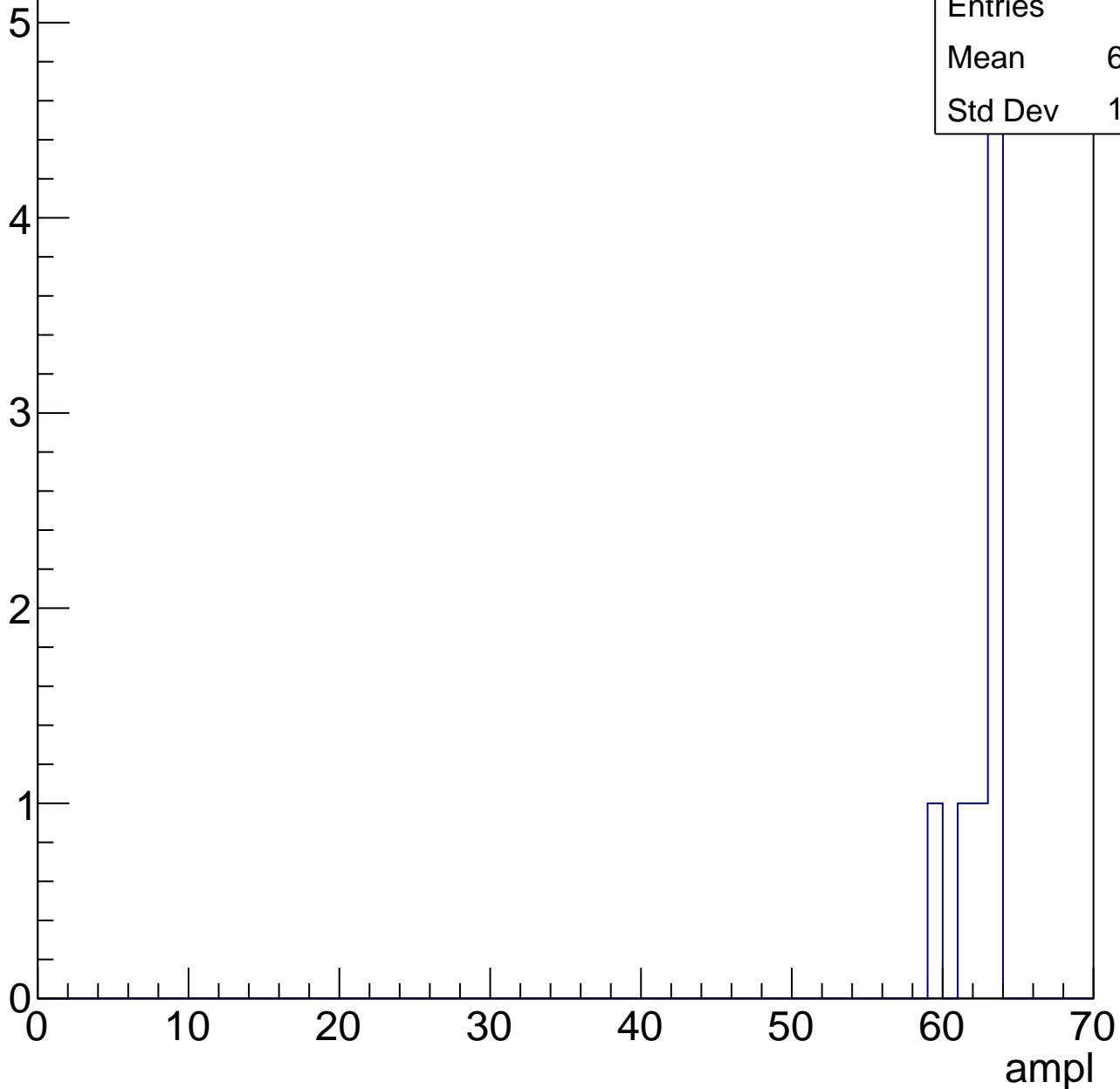


B1L102S, U26-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	62.12
Std Dev	1.364



B1L102S, U26-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U26-ch40, adc0

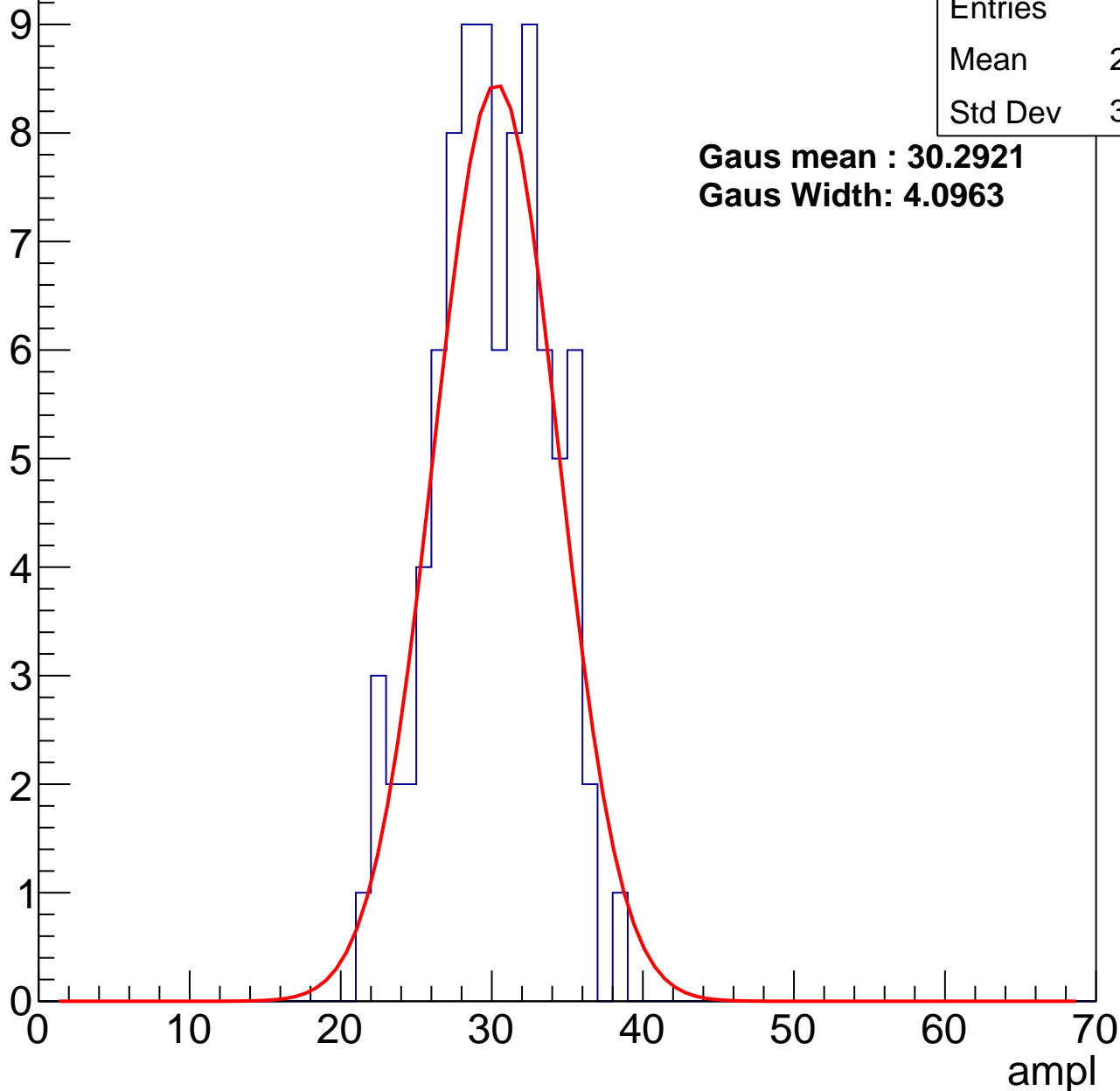
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	29.54
Std Dev	3.716

Gaus mean : 30.2921

Gaus Width: 4.0963



B1L102S, U26-ch40, adc1

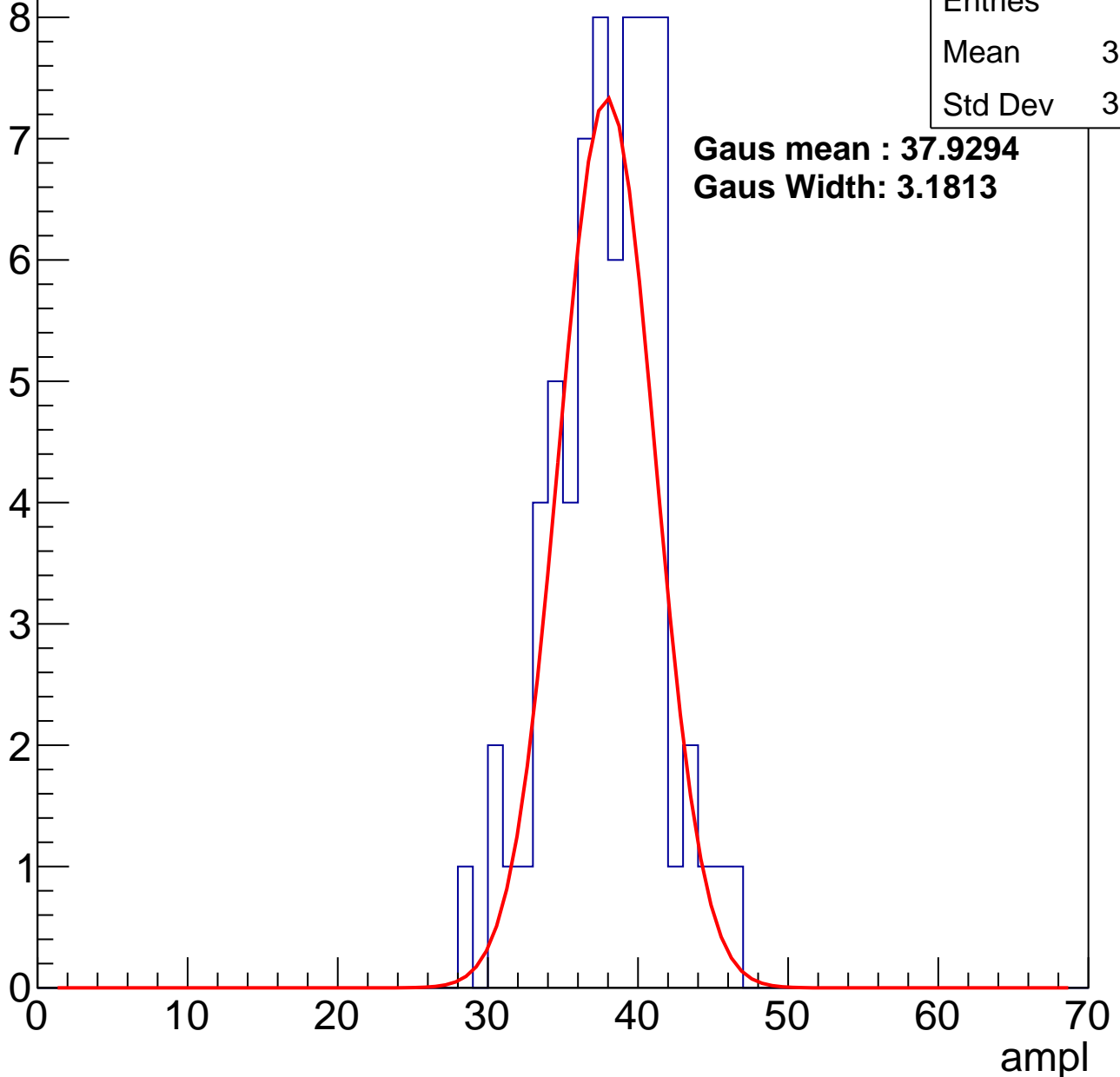
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.57
Std Dev	3.569

Gaus mean : 37.9294

Gaus Width: 3.1813



B1L102S, U26-ch40, adc2

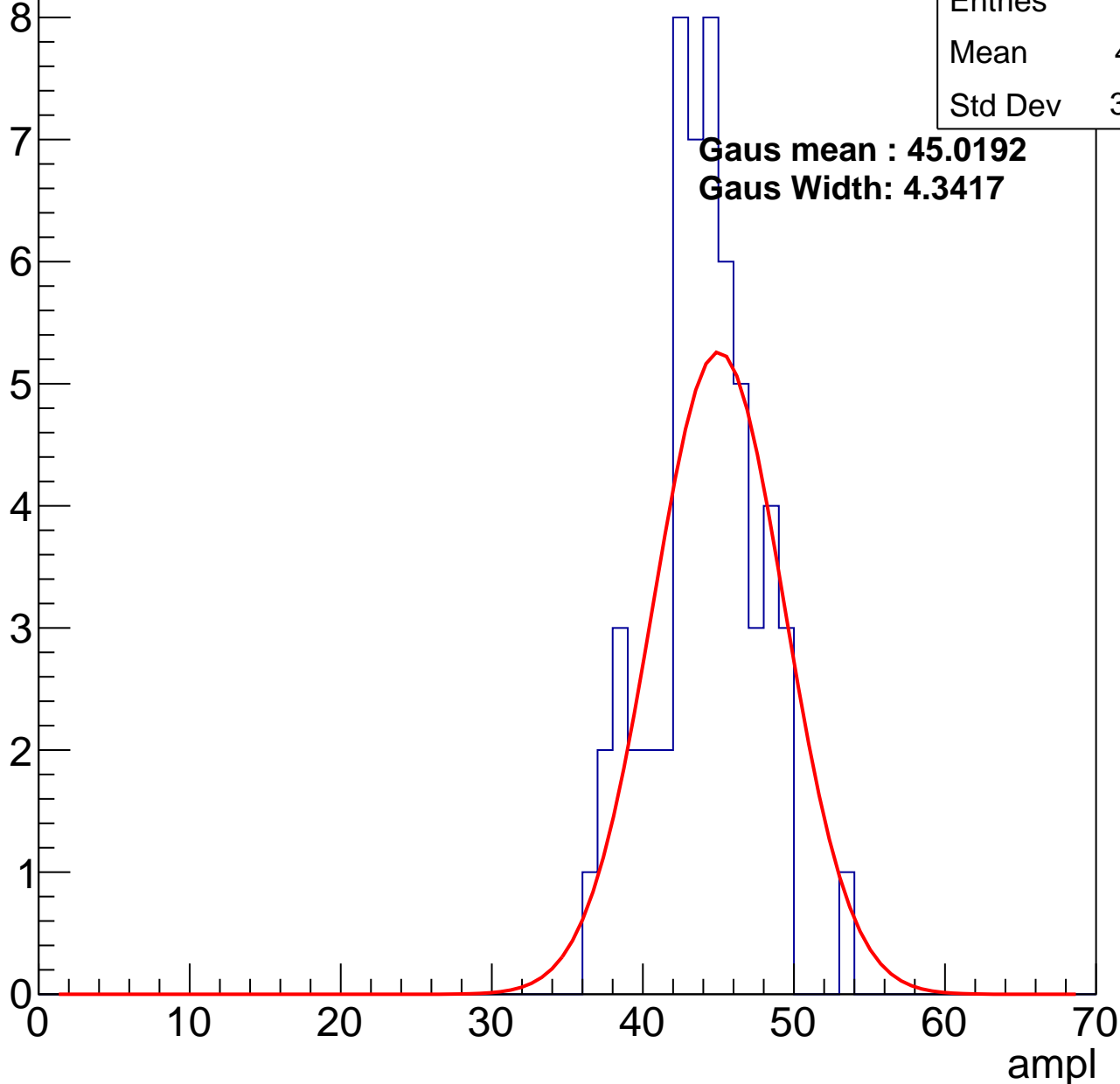
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.61
Std Dev	3.437

Gaus mean : 45.0192

Gaus Width: 4.3417

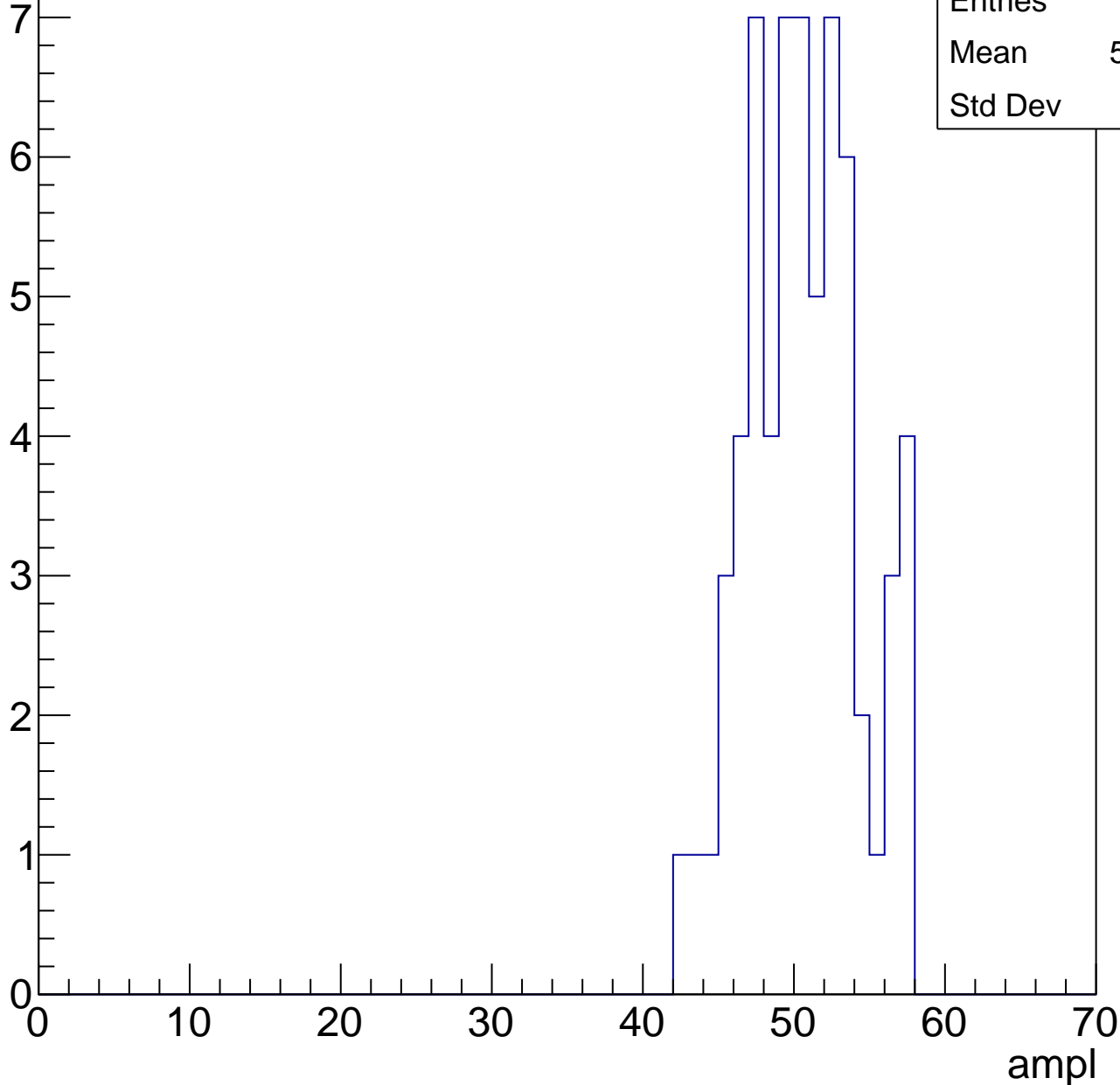


B1L102S, U26-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

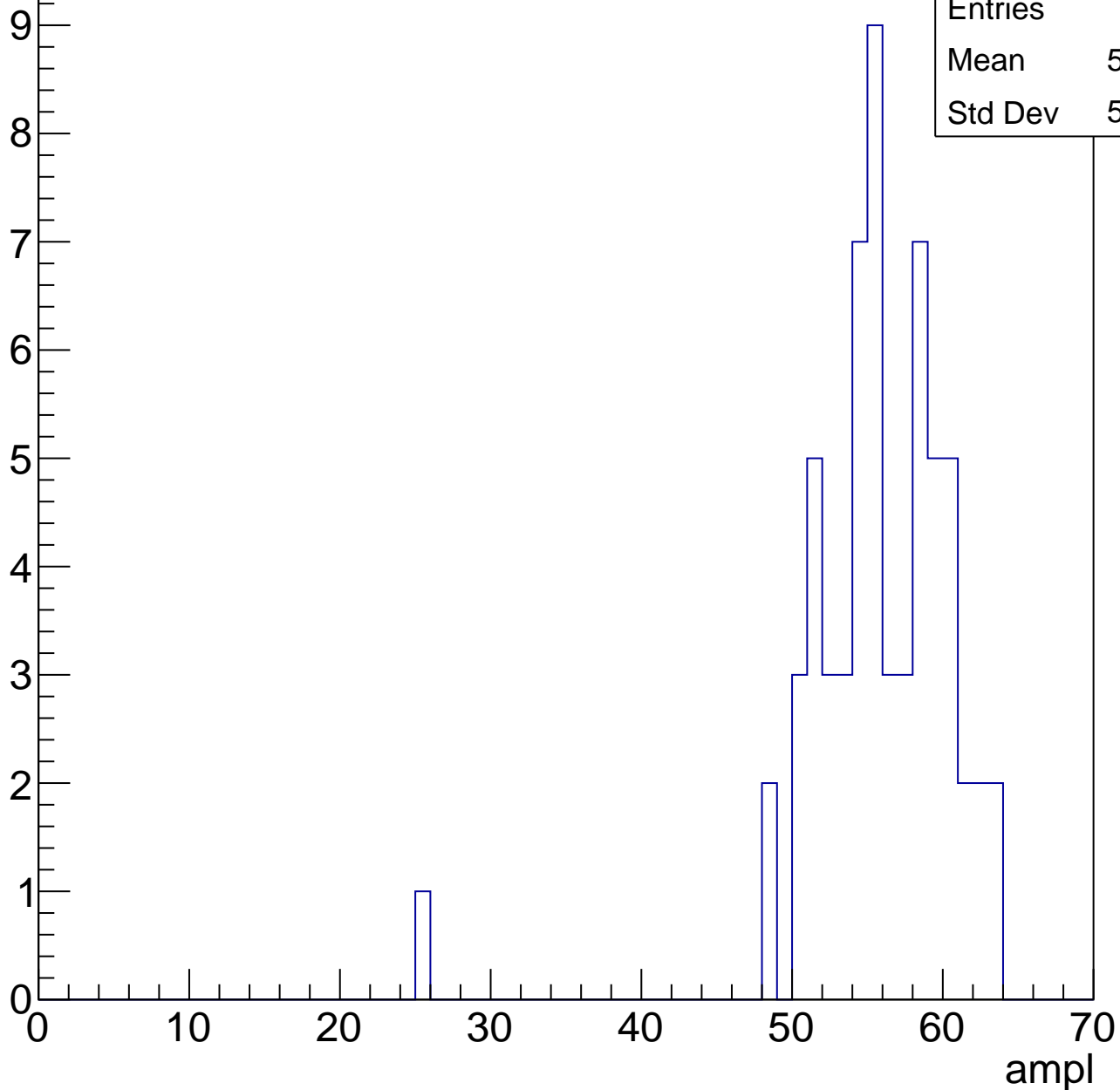
Entries	63
Mean	50.13
Std Dev	3.61



B1L102S, U26-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

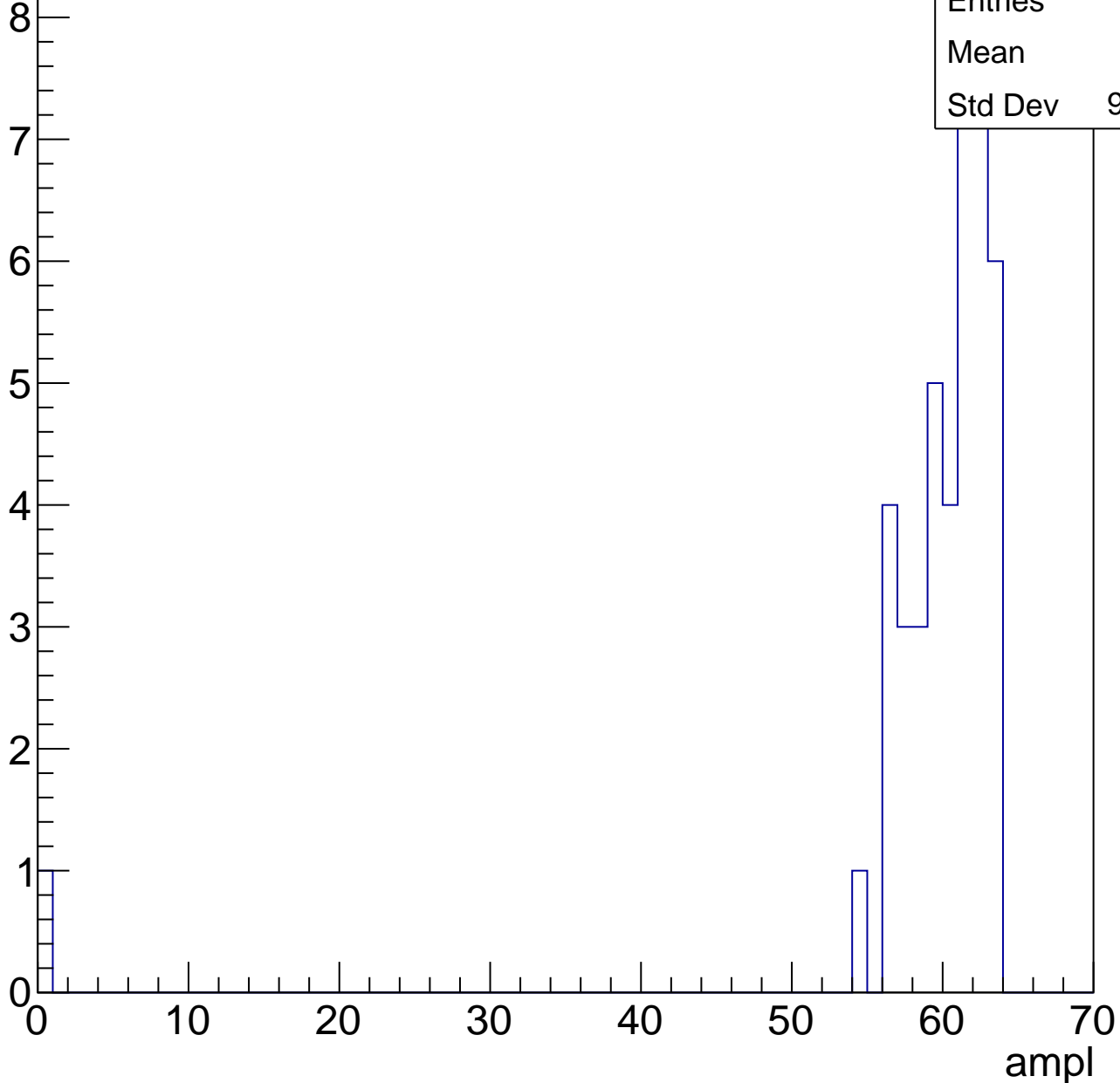


B1L102S, U26-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.6
Std Dev	9.344



B1L102S, U26-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L102S, U26-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch41, adc0

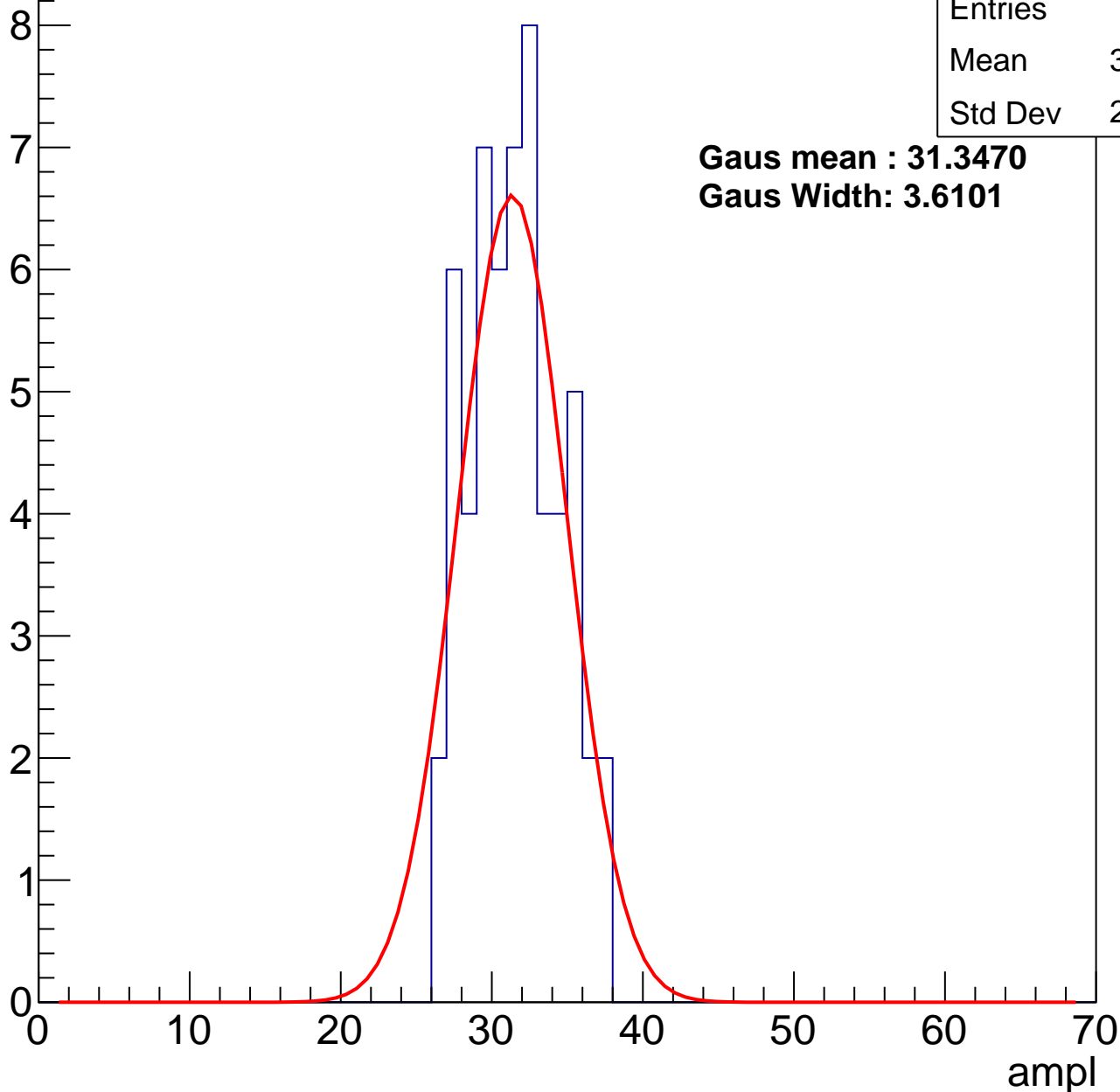
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	31.07
Std Dev	2.895

Gaus mean : 31.3470

Gaus Width: 3.6101



B1L102S, U26-ch41, adc1

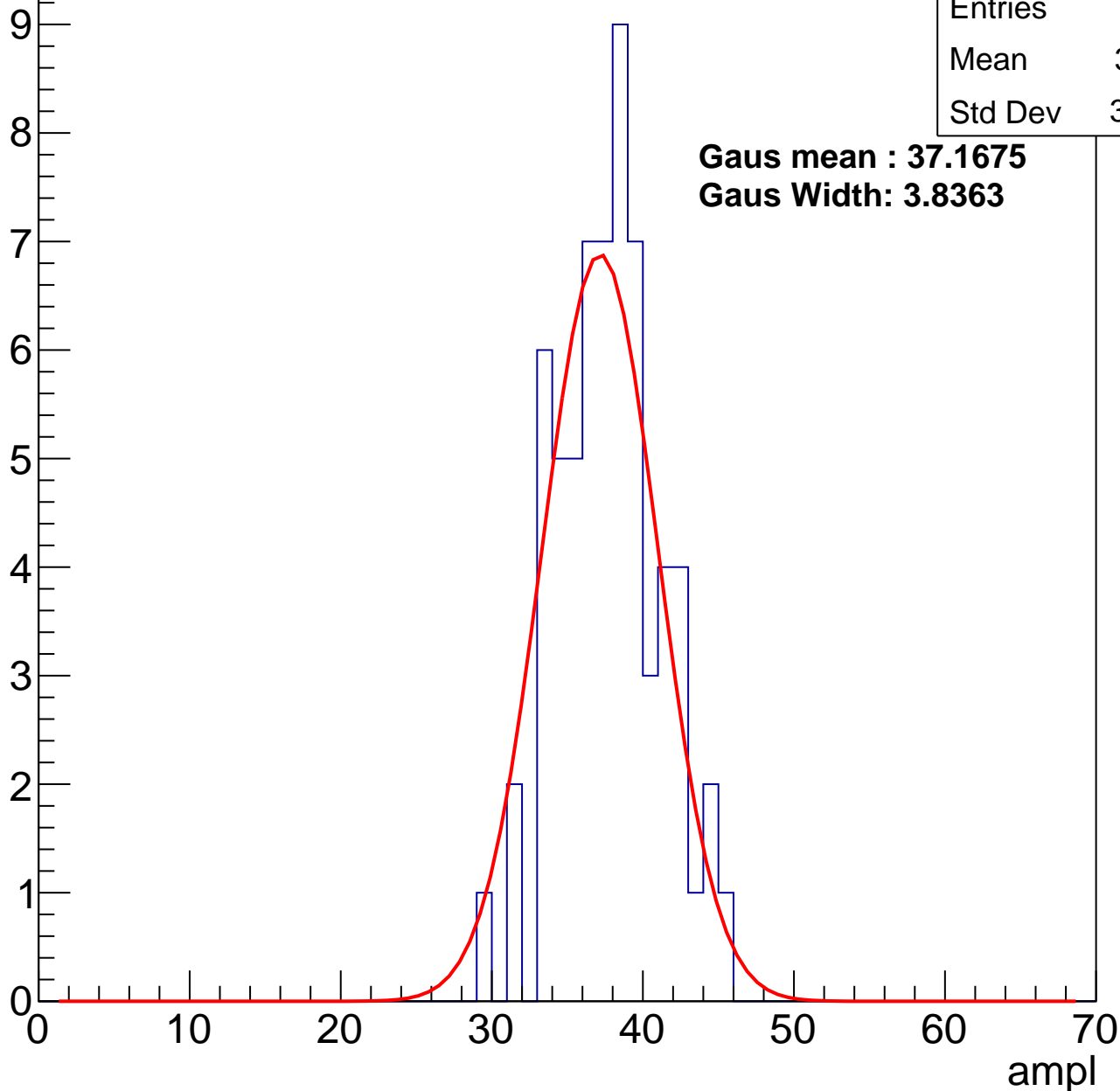
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.31
Std Dev	3.363

Gaus mean : 37.1675

Gaus Width: 3.8363



B1L102S, U26-ch41, adc2

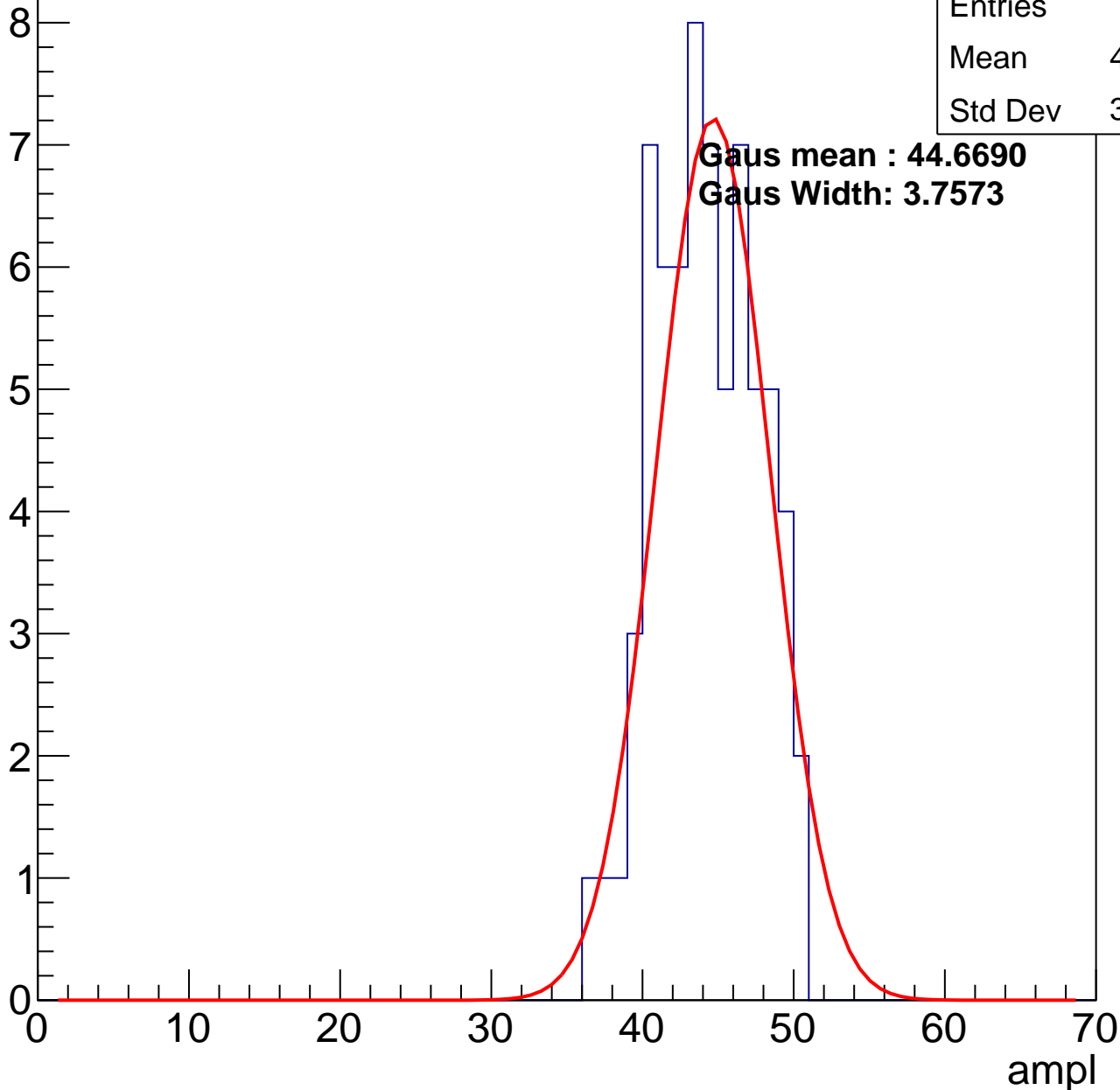
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.76
Std Dev	3.317

Gaus mean : 44.6690

Gaus Width: 3.7573

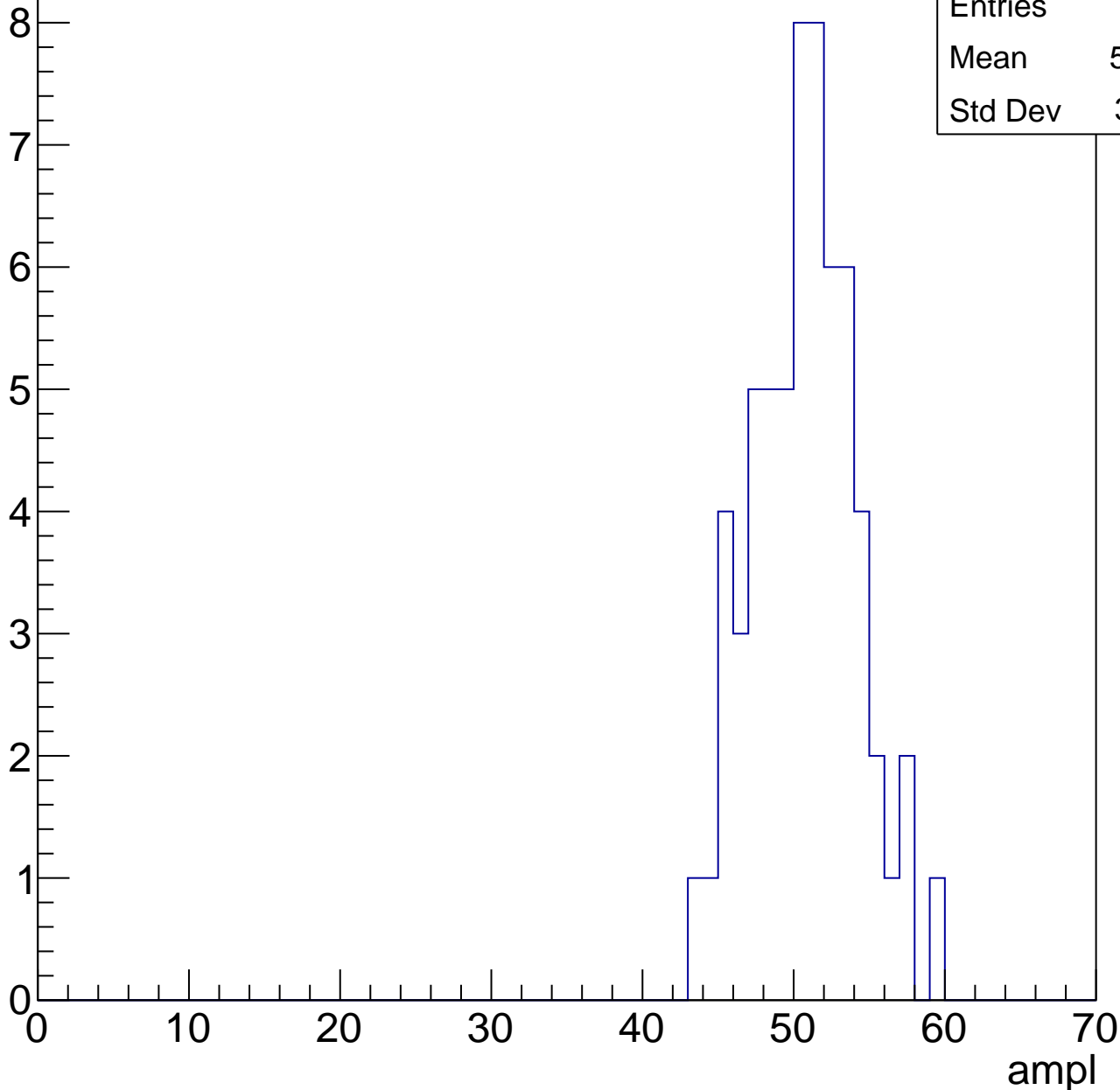


B1L102S, U26-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.29
Std Dev	3.391

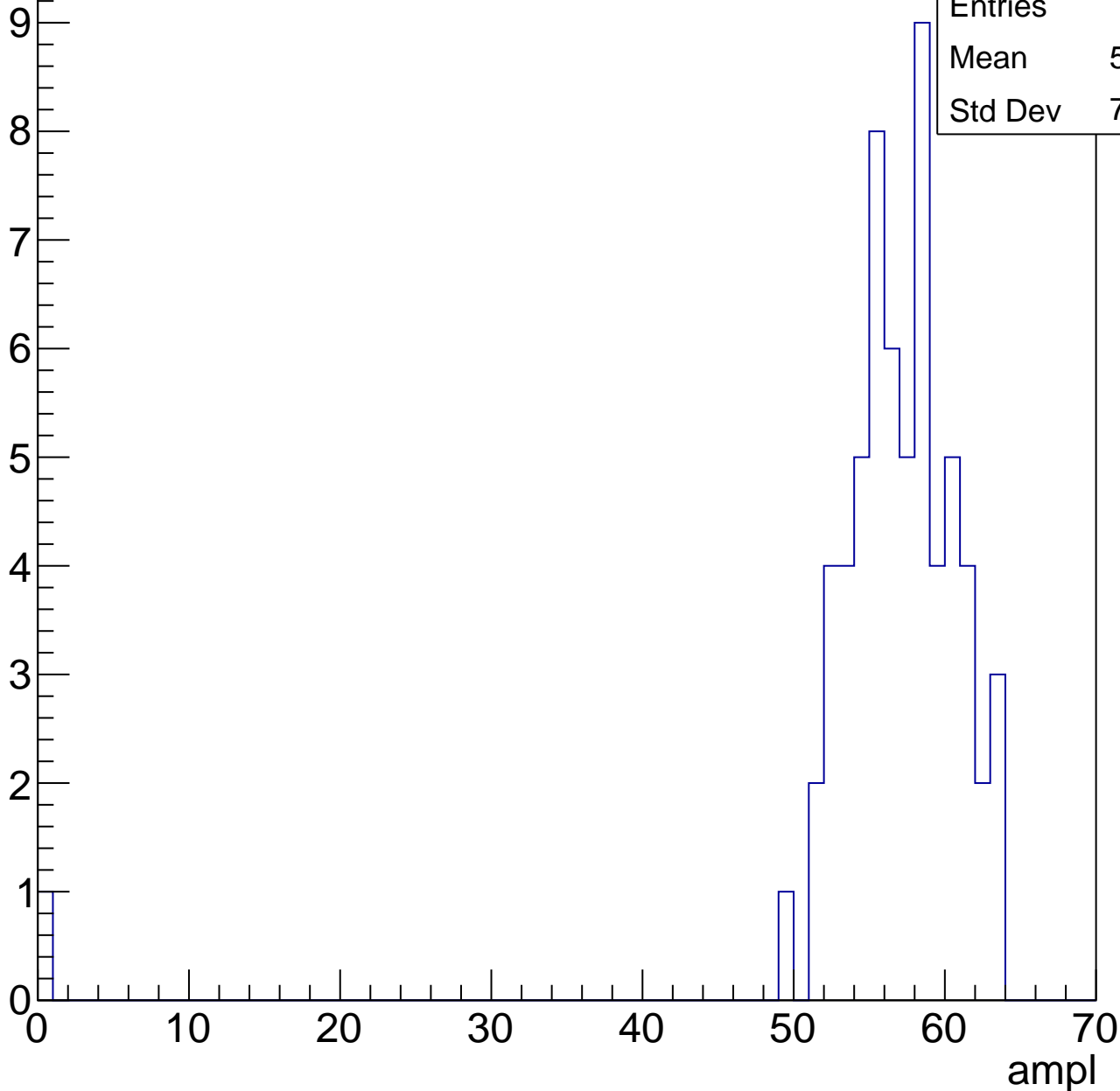


B1L102S, U26-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.83
Std Dev	7.804

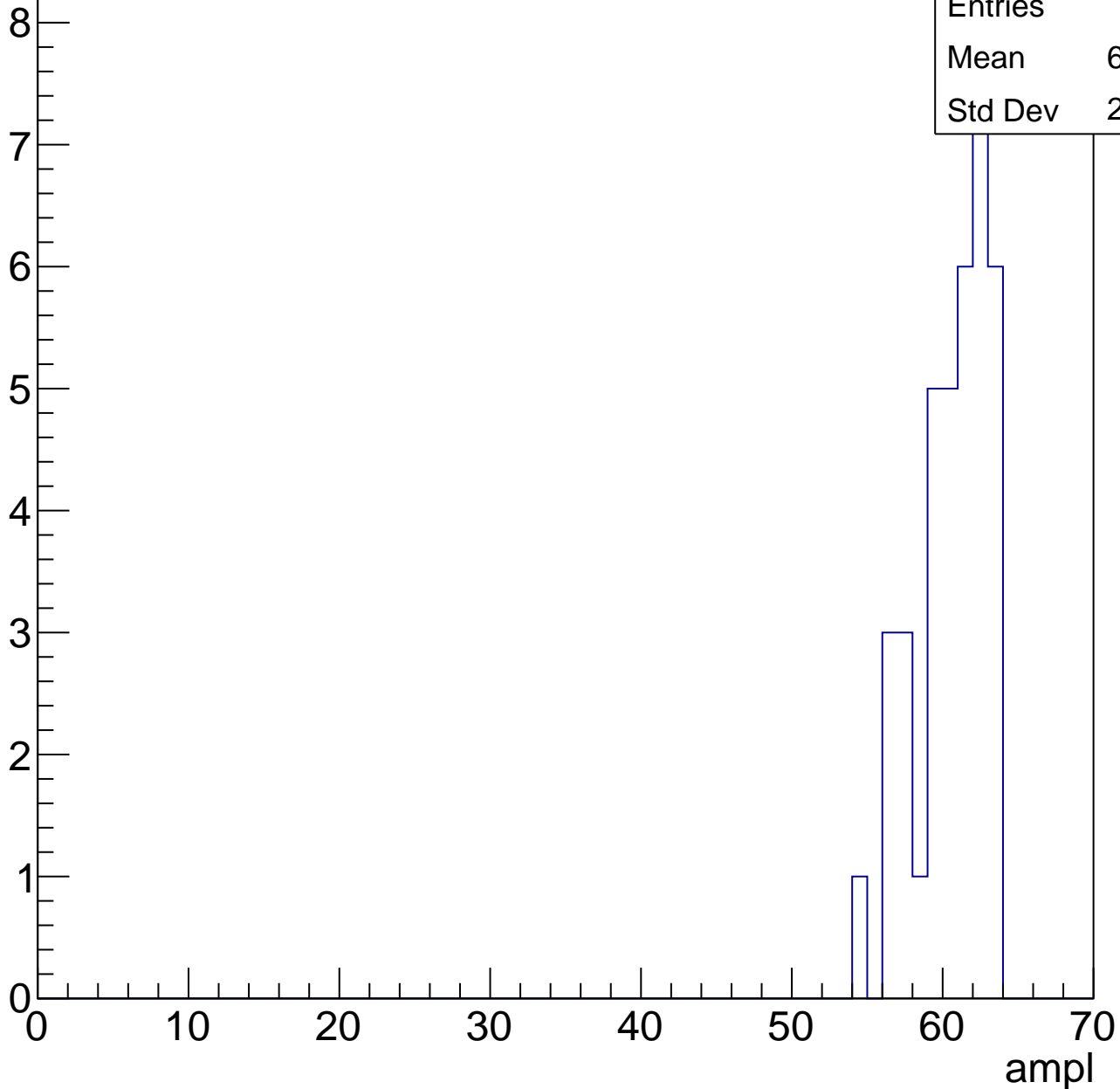


B1L102S, U26-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	60.16
Std Dev	2.357



B1L102S, U26-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L102S, U26-ch42, adc0

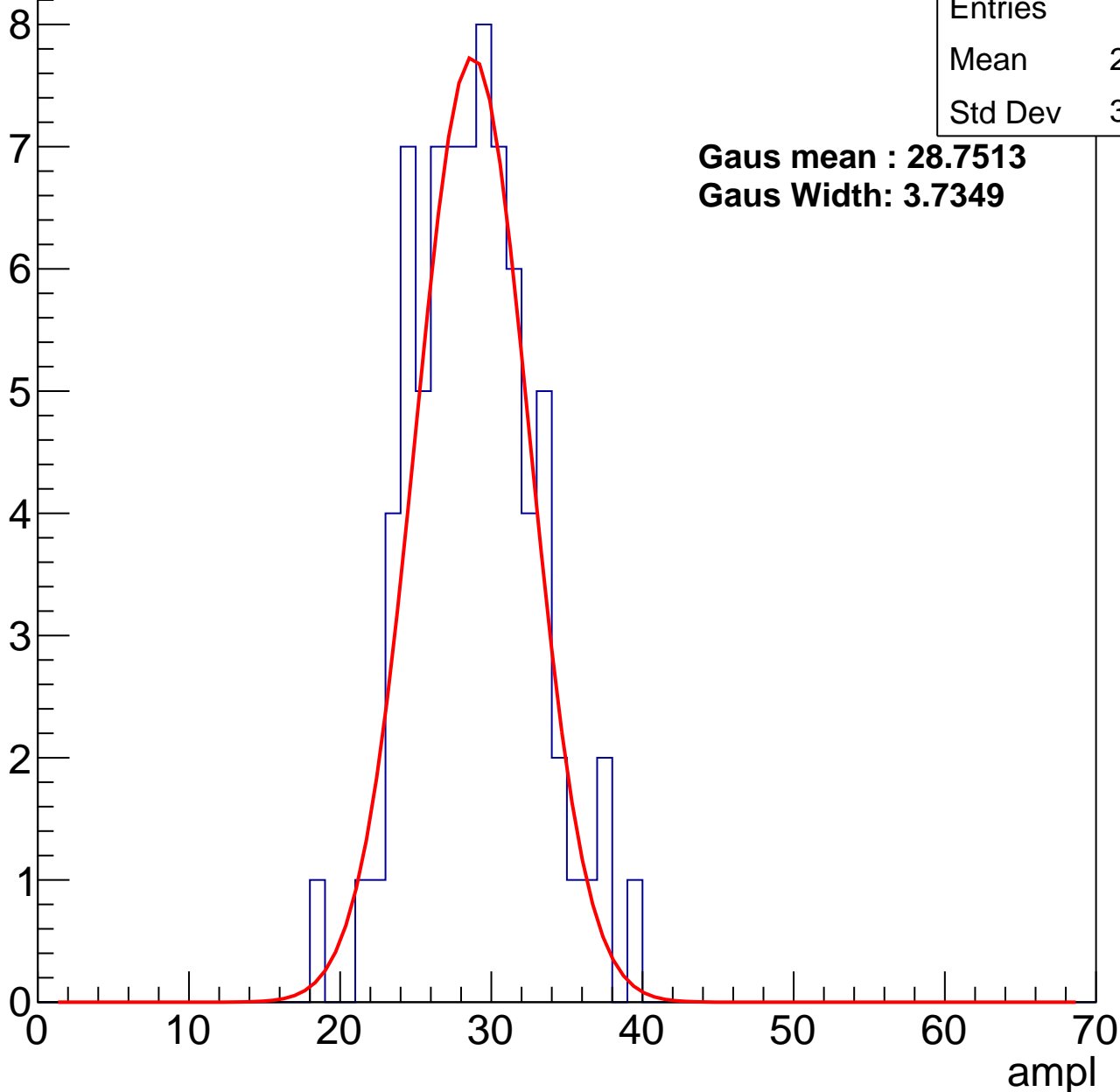
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.39
Std Dev	3.968

Gaus mean : 28.7513

Gaus Width: 3.7349



B1L102S, U26-ch42, adc1

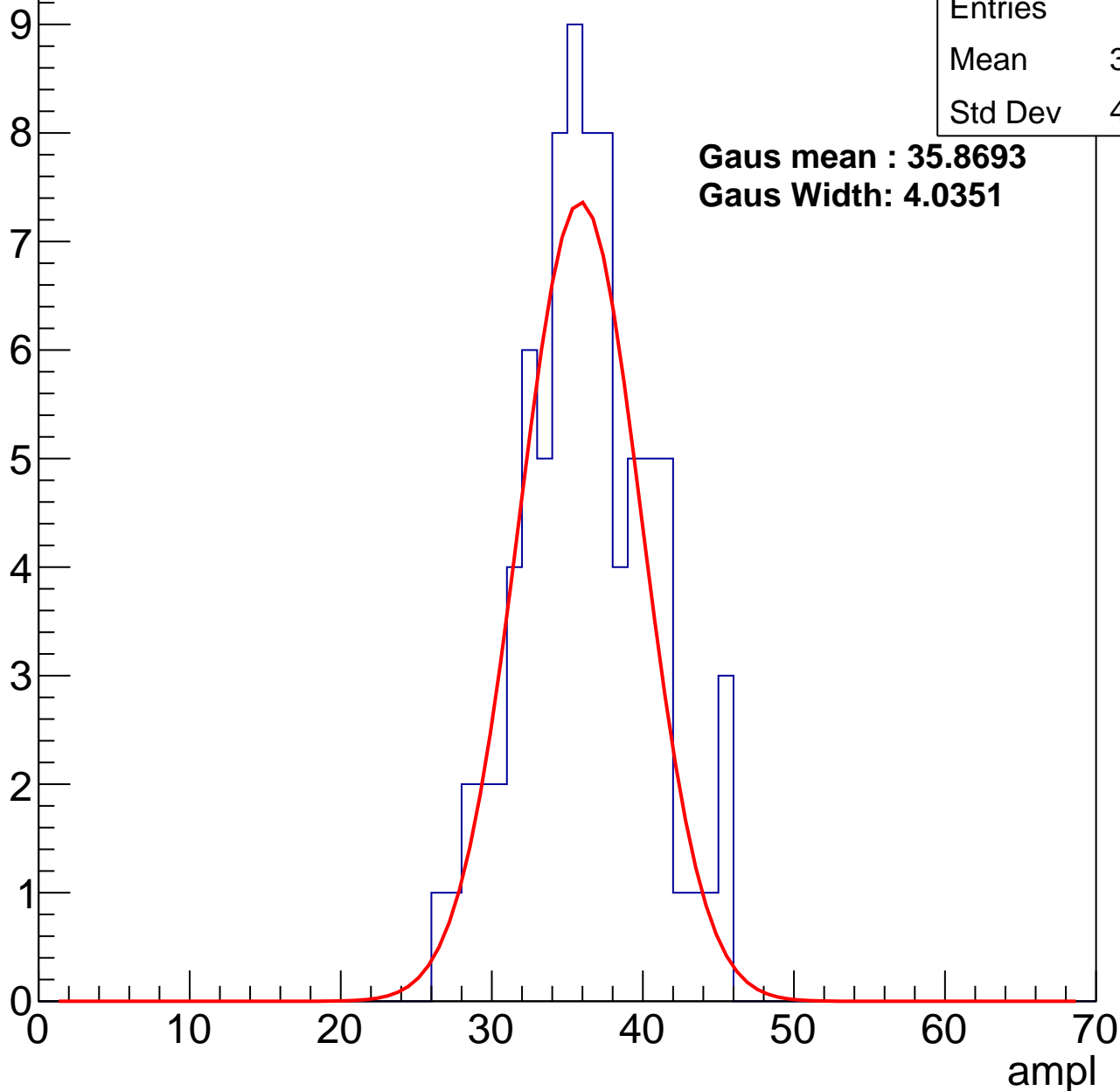
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.74
Std Dev	4.189

Gaus mean : 35.8693

Gaus Width: 4.0351



B1L102S, U26-ch42, adc2

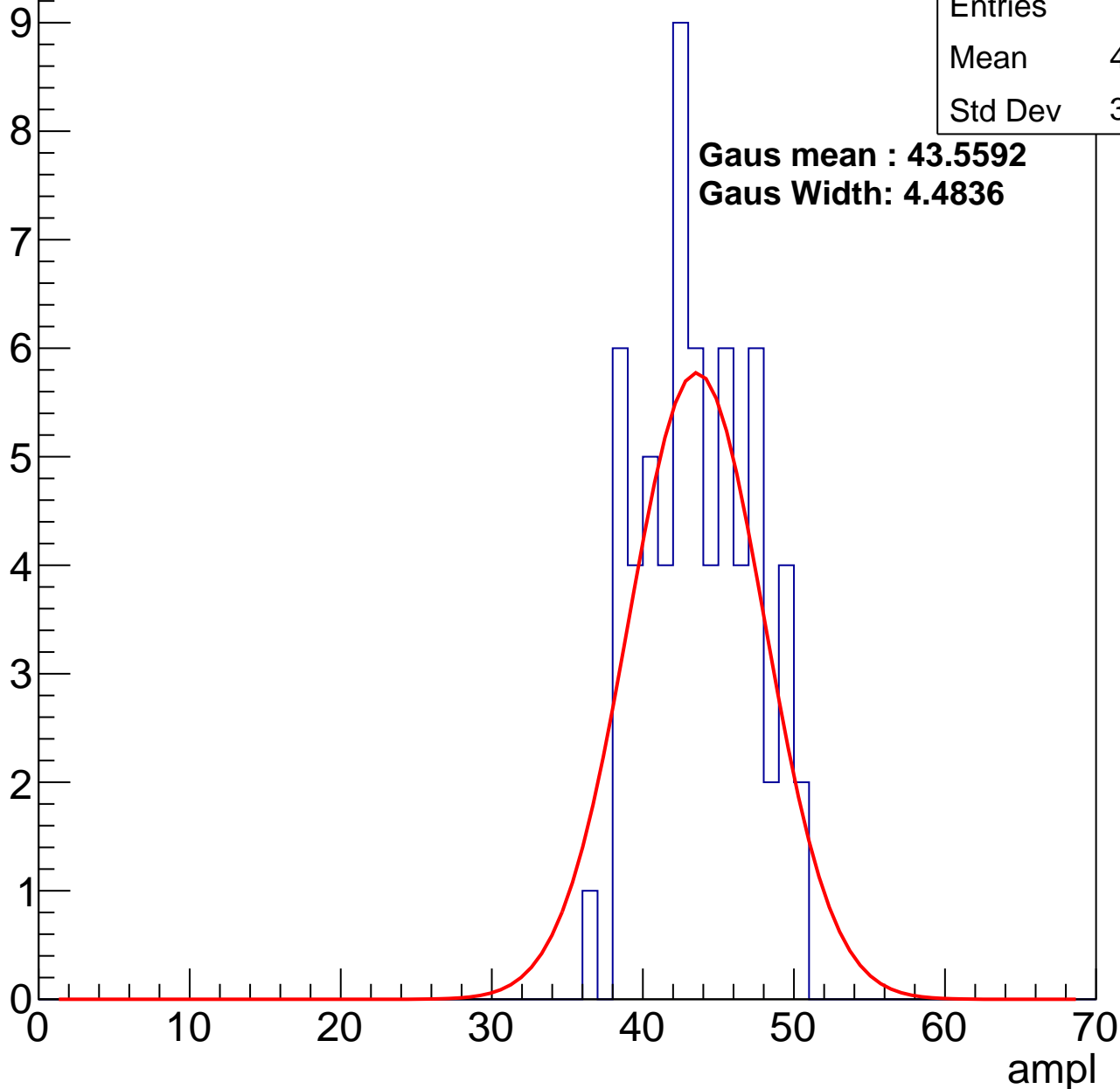
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.24
Std Dev	3.522

Gaus mean : 43.5592

Gaus Width: 4.4836

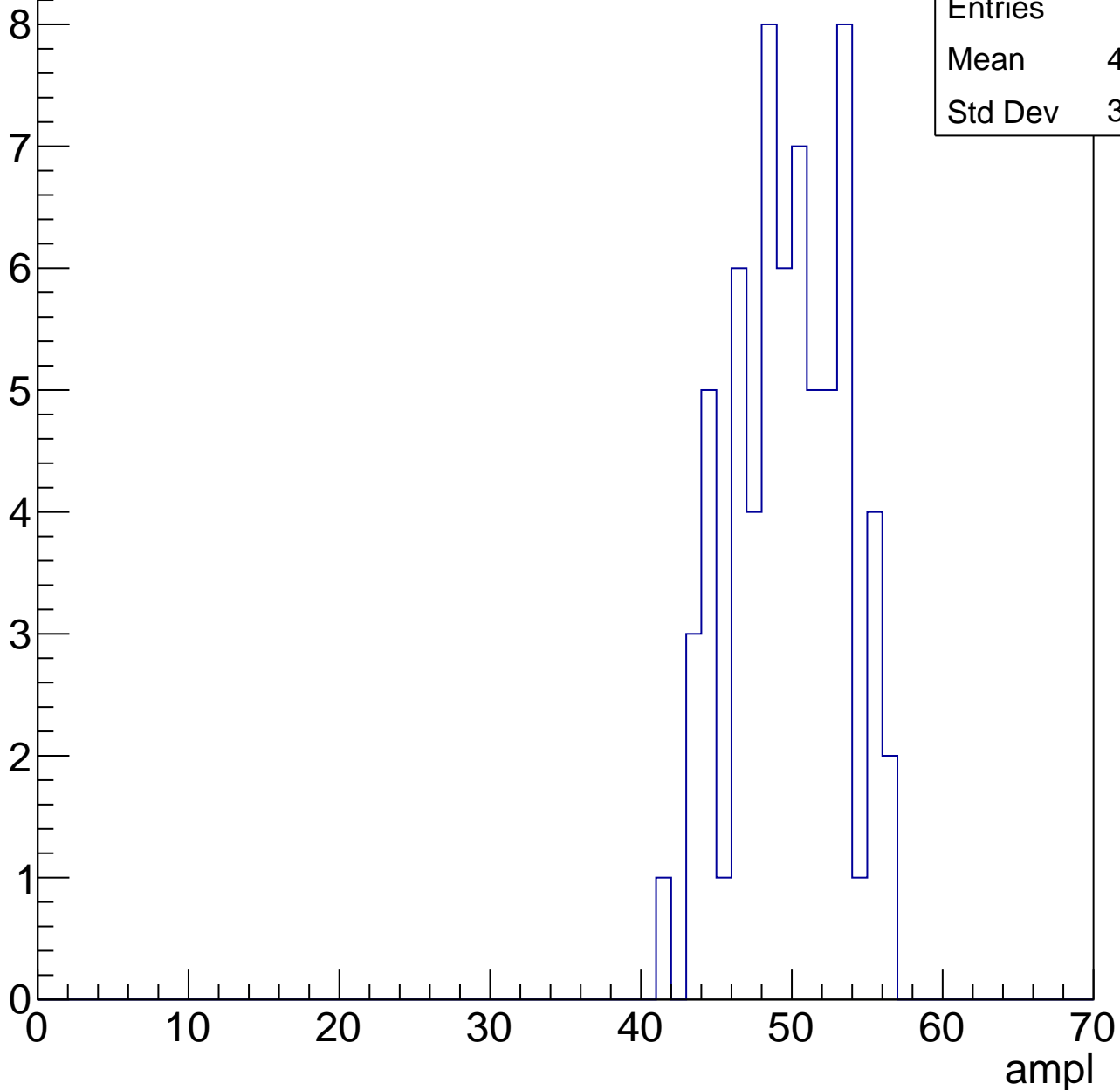


B1L102S, U26-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.27
Std Dev	3.595

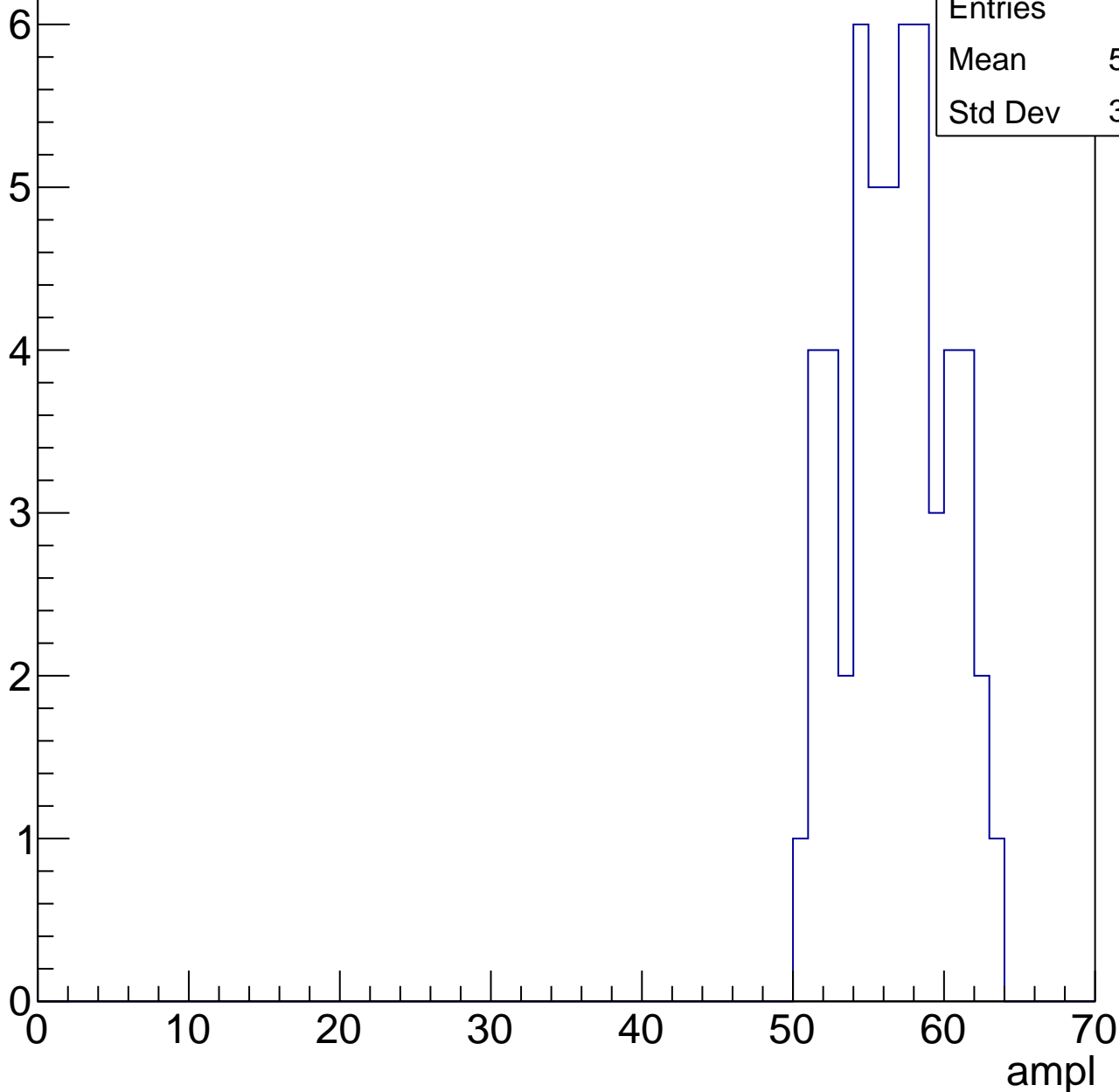


B1L102S, U26-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.32
Std Dev	3.318

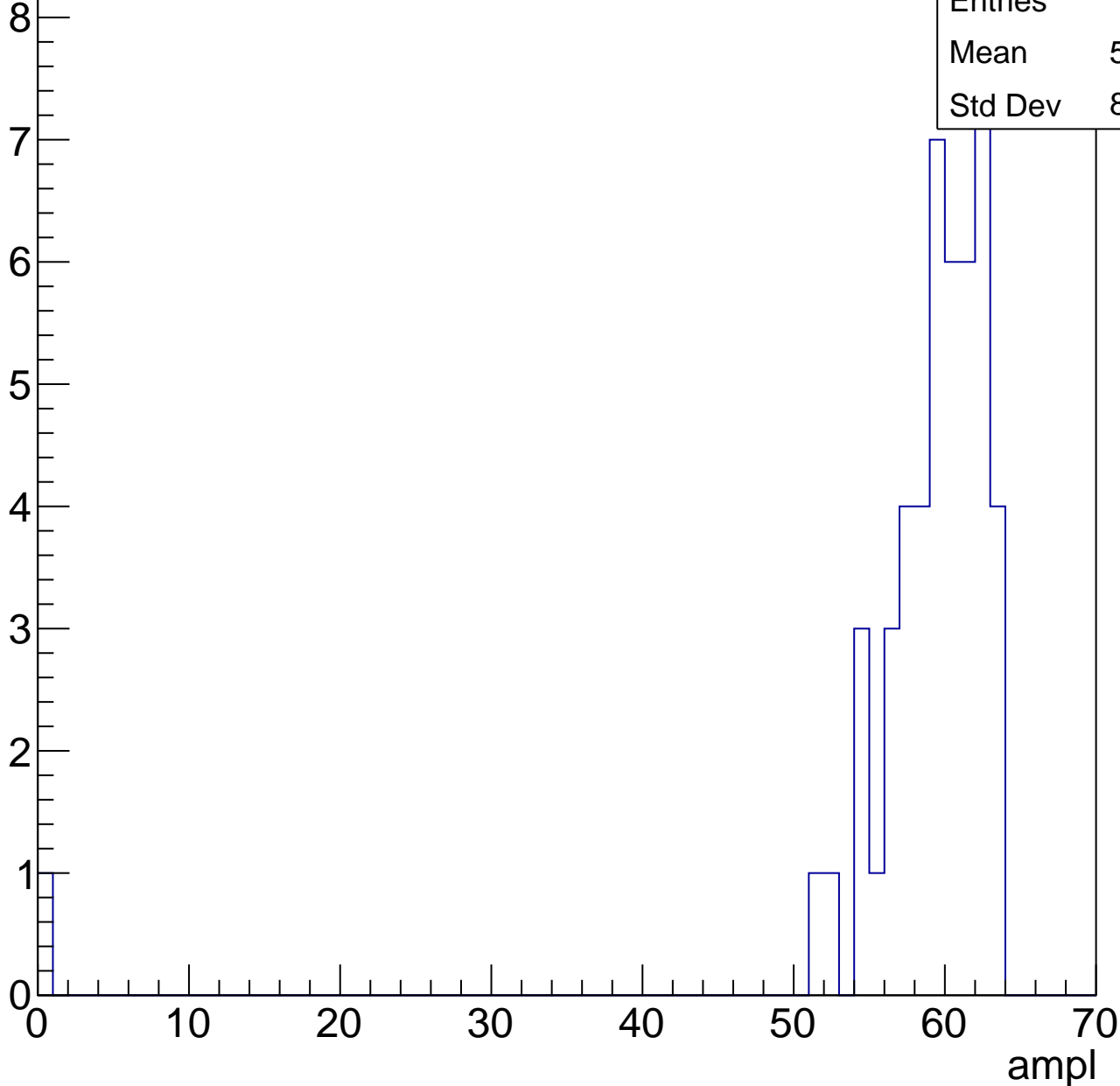


B1L102S, U26-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

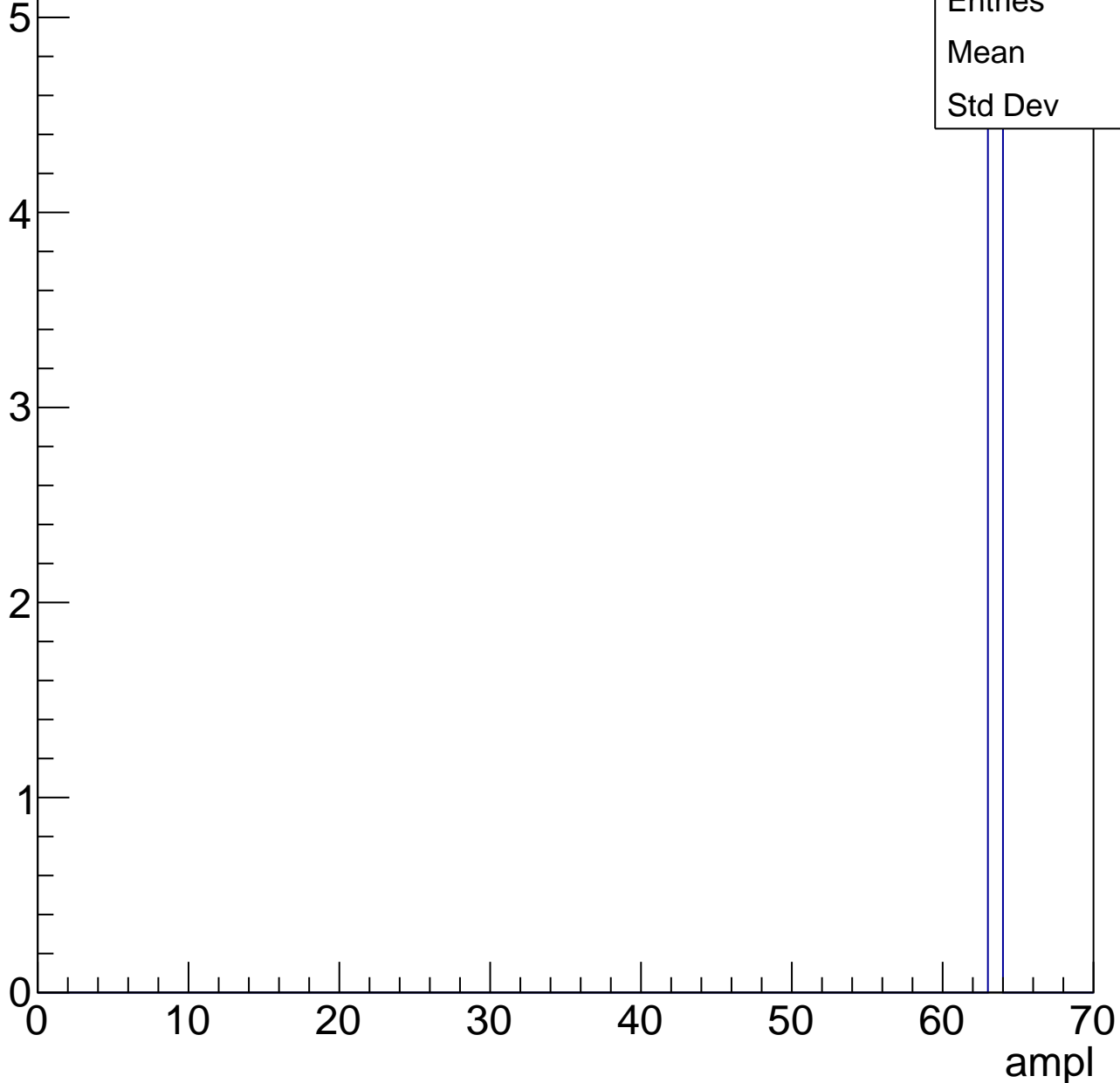
Entries	49
Mean	57.86
Std Dev	8.846



B1L102S, U26-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch43, adc0

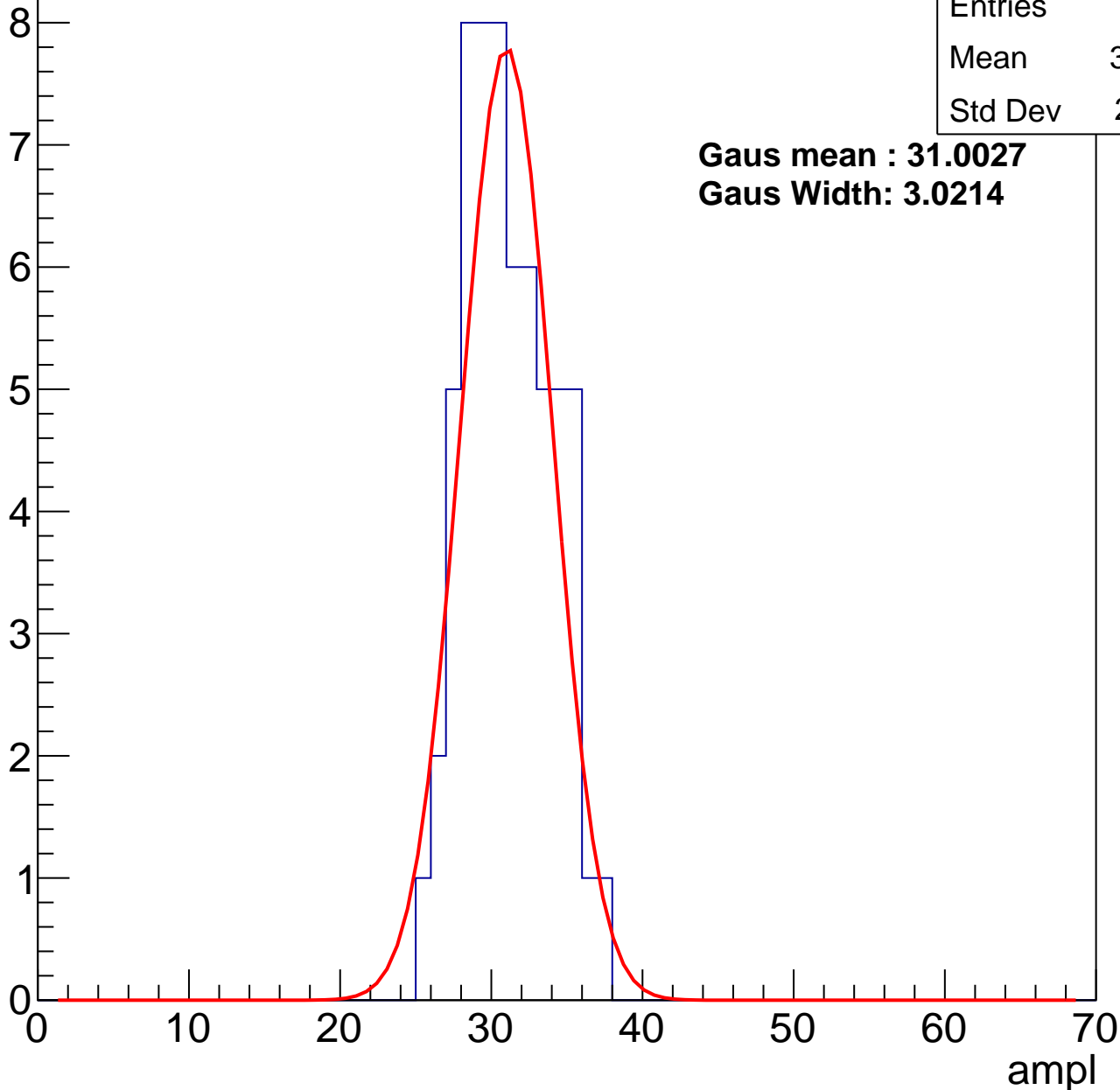
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.64
Std Dev	2.811

Gaus mean : 31.0027

Gaus Width: 3.0214



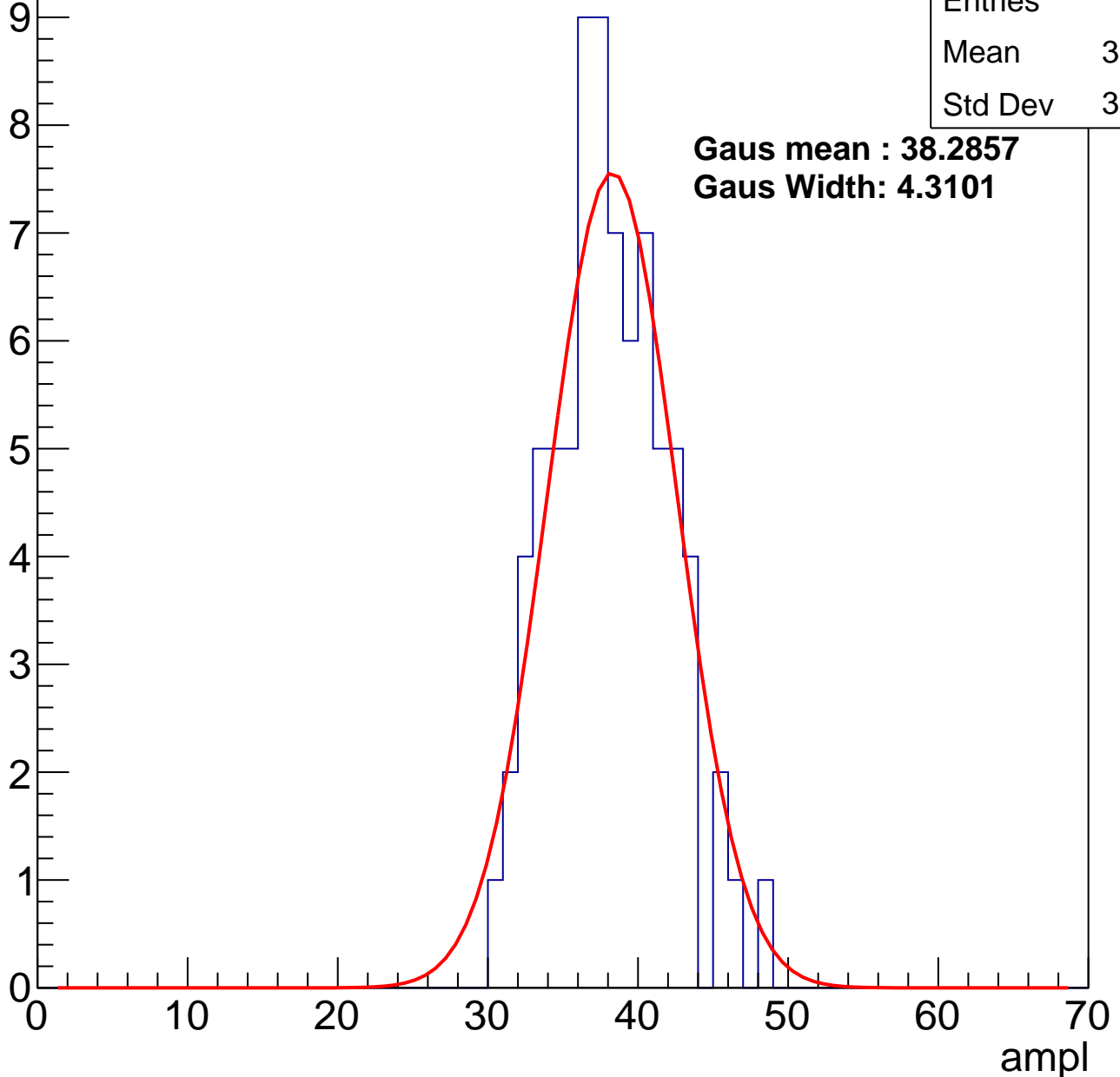
B1L102S, U26-ch43, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	37.67
Std Dev	3.778

Gaus mean : 38.2857
Gaus Width: 4.3101



B1L102S, U26-ch43, adc2

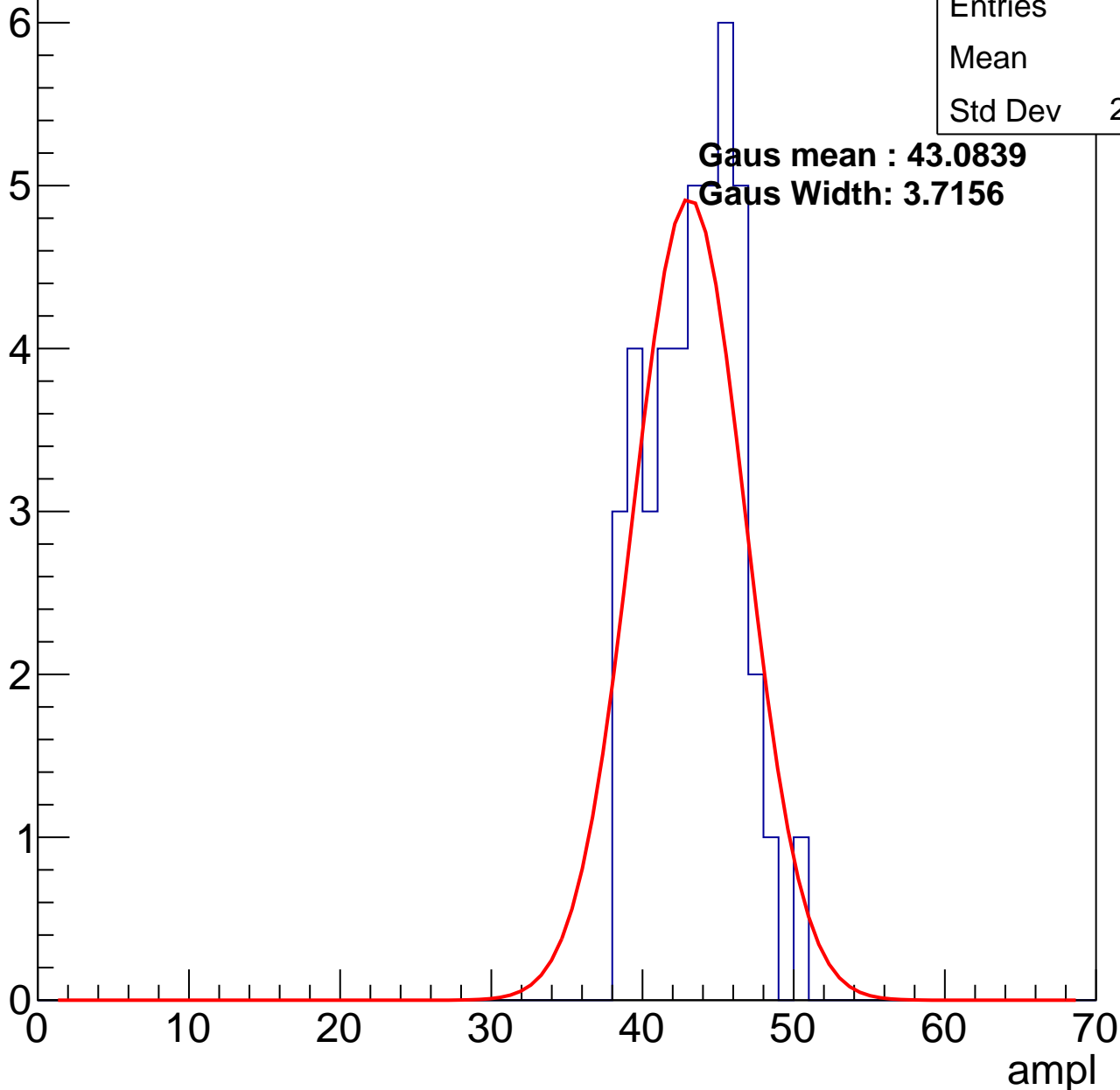
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	43
Std Dev	2.917

Gaus mean : 43.0839

Gaus Width: 3.7156

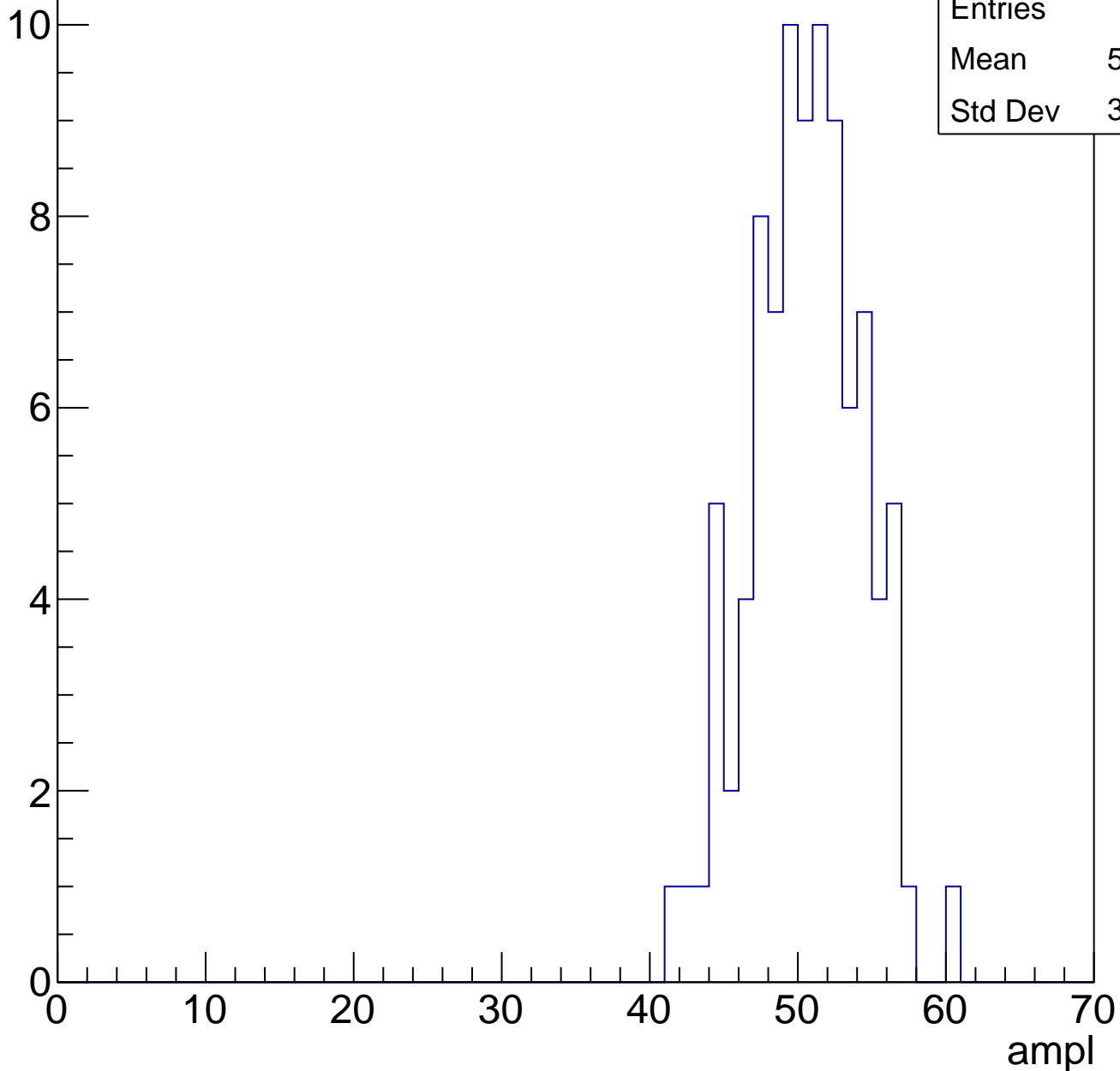


B1L102S, U26-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	50.14
Std Dev	3.687

Entry

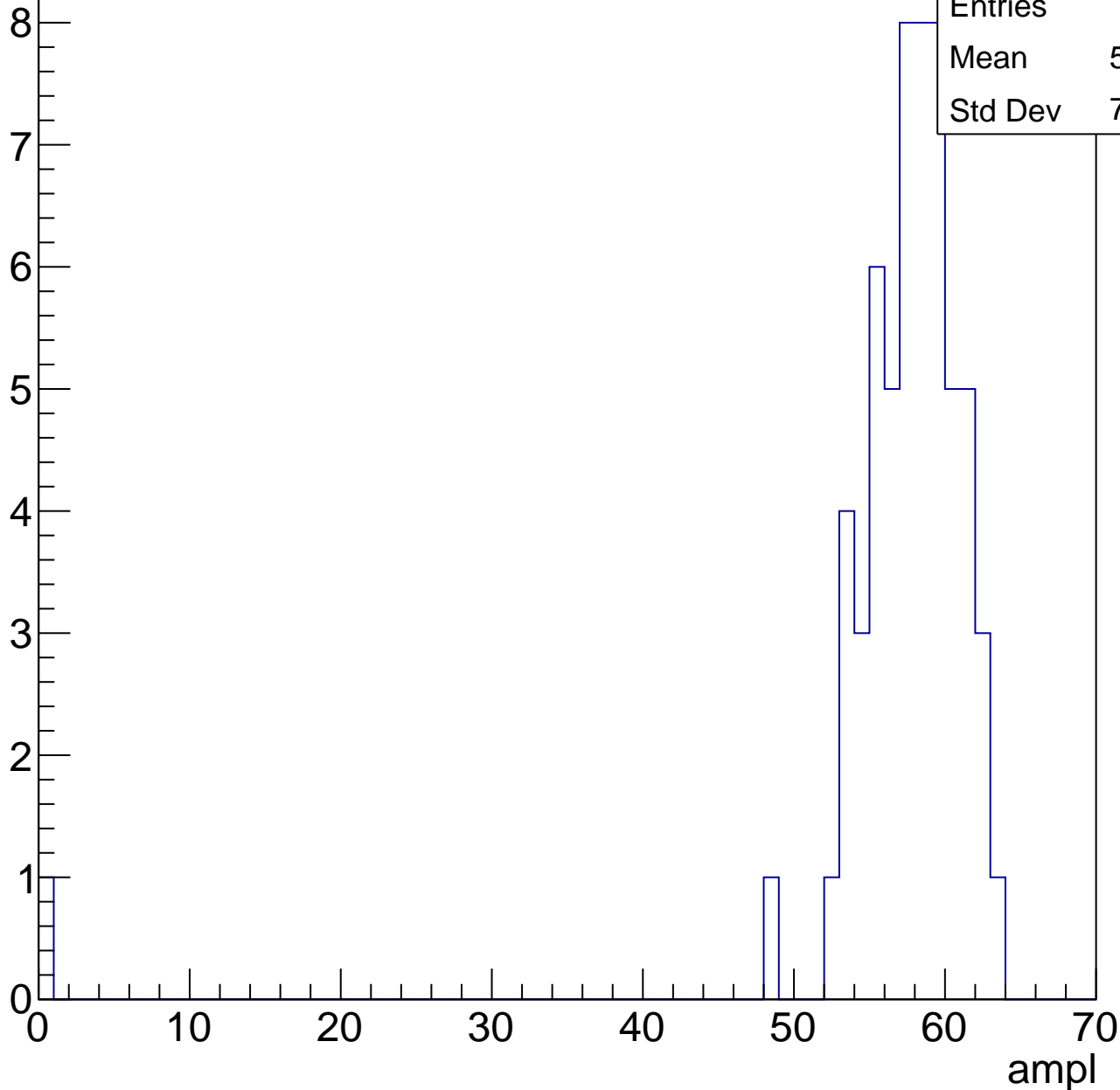


B1L102S, U26-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.44
Std Dev	7.954

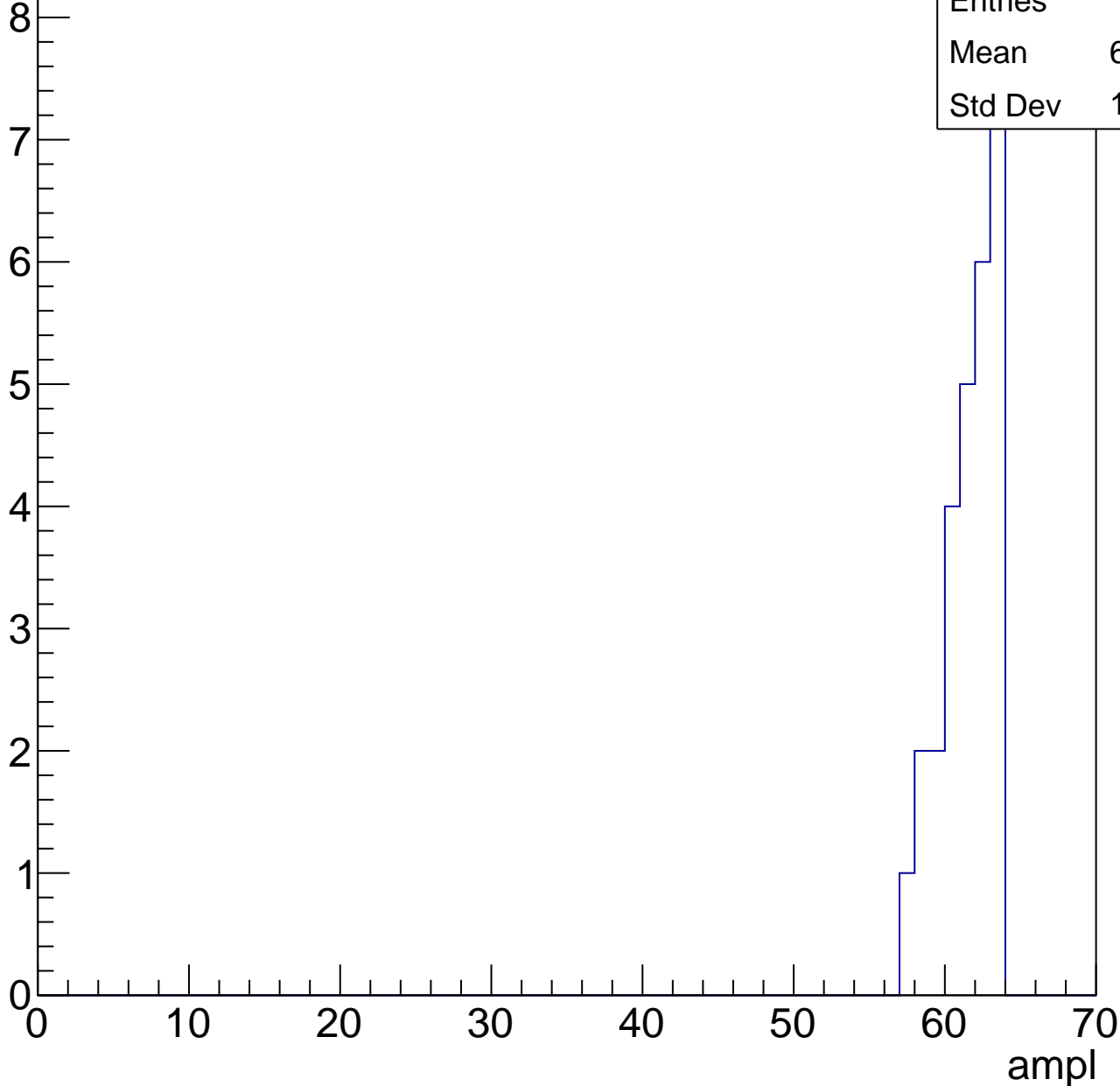


B1L102S, U26-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	61.14
Std Dev	1.726



B1L102S, U26-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

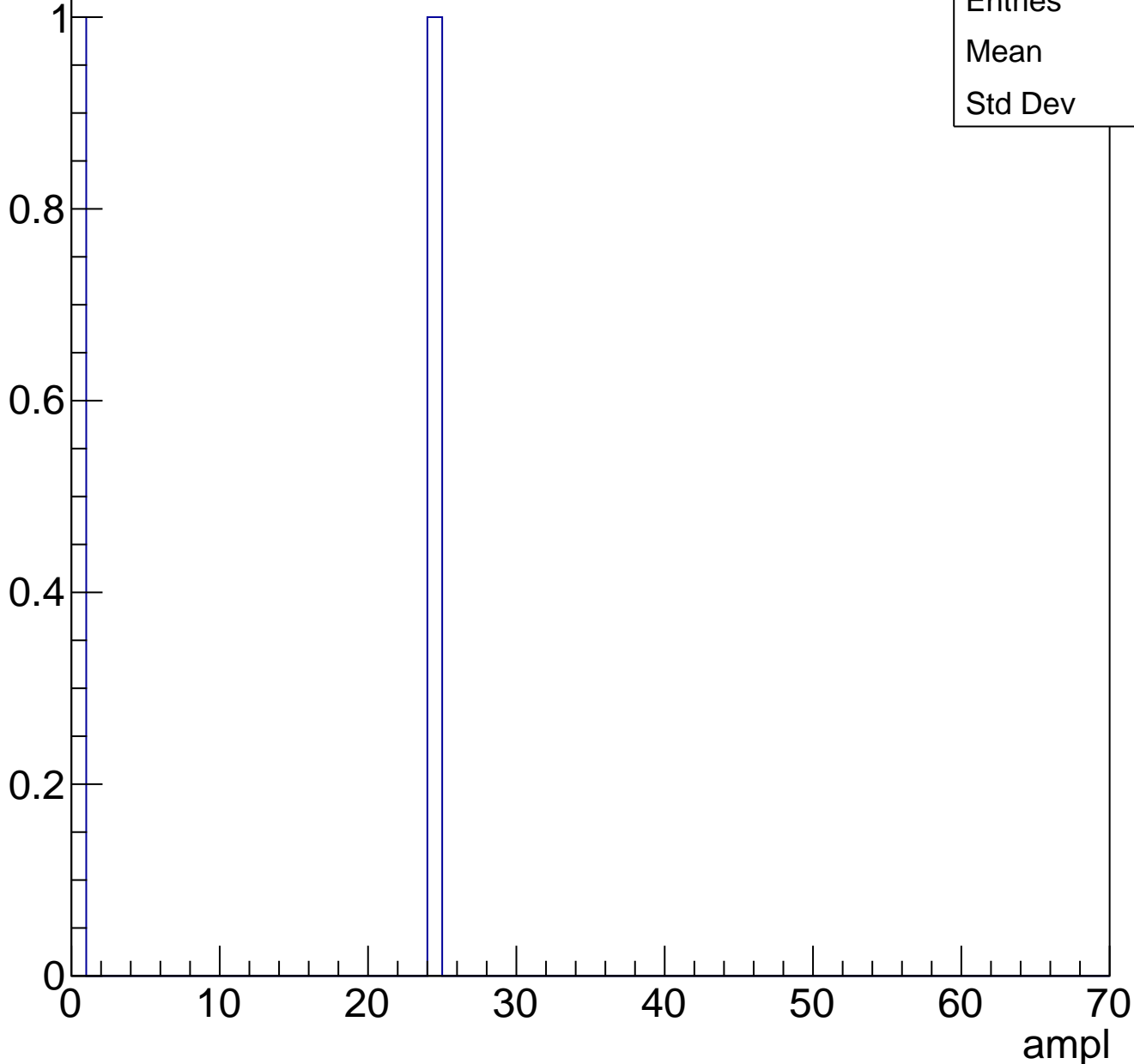
Entry



B1L102S, U26-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch44, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	28.78
Std Dev	4.931

Gaus mean : 29.6302

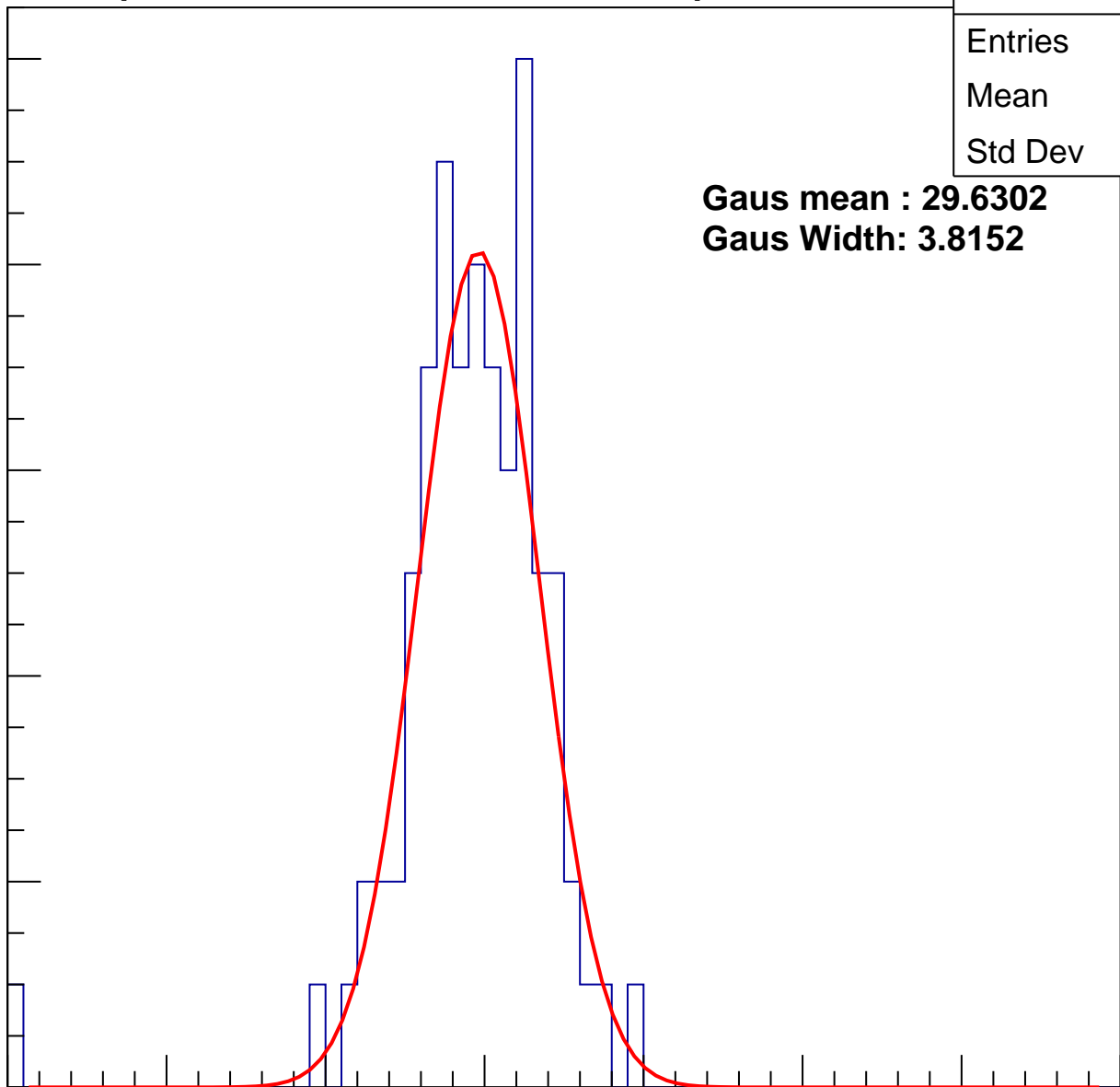
Gaus Width: 3.8152

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch44, adc1

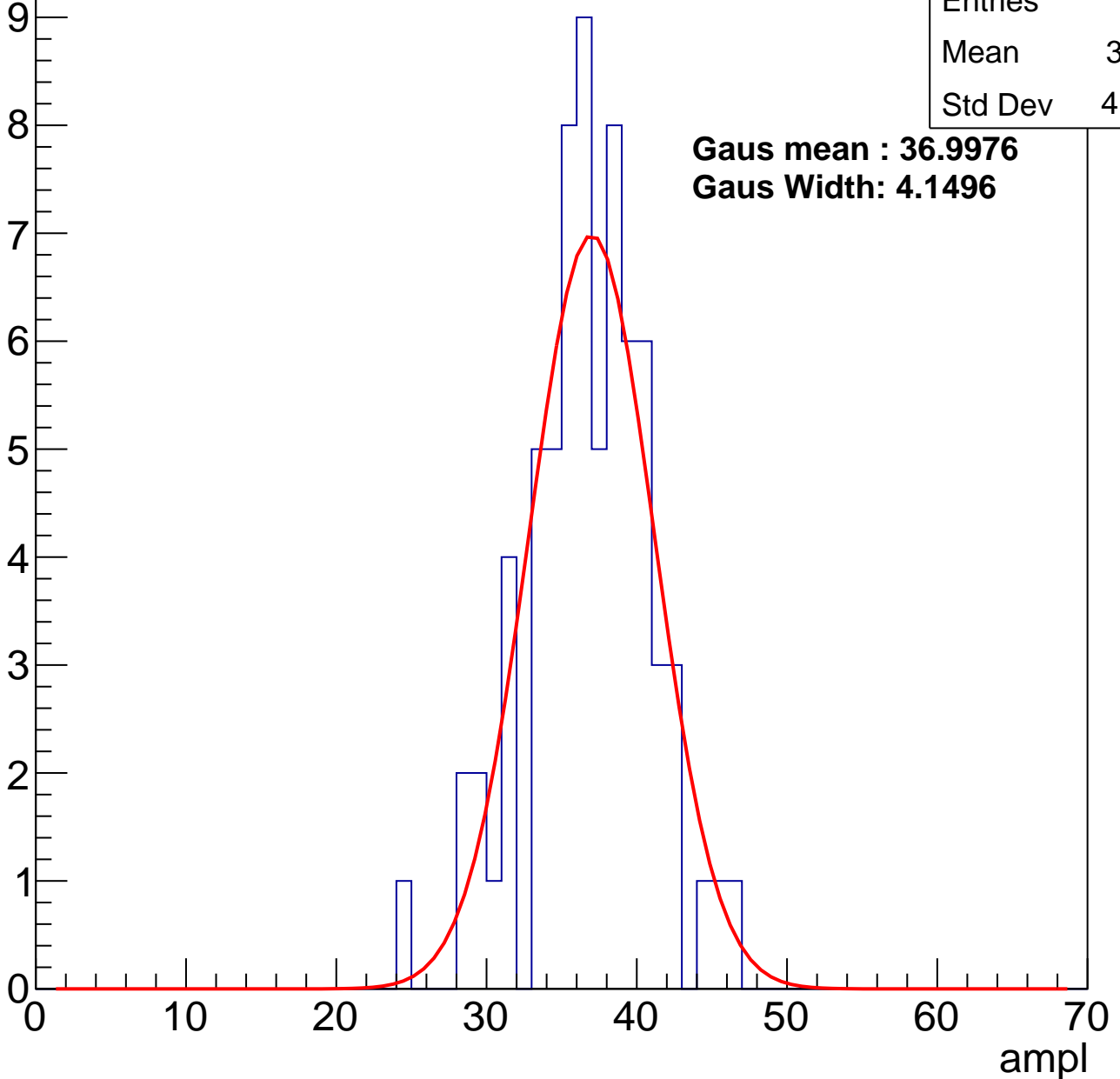
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.31
Std Dev	4.089

Gaus mean : 36.9976

Gaus Width: 4.1496



B1L102S, U26-ch44, adc2

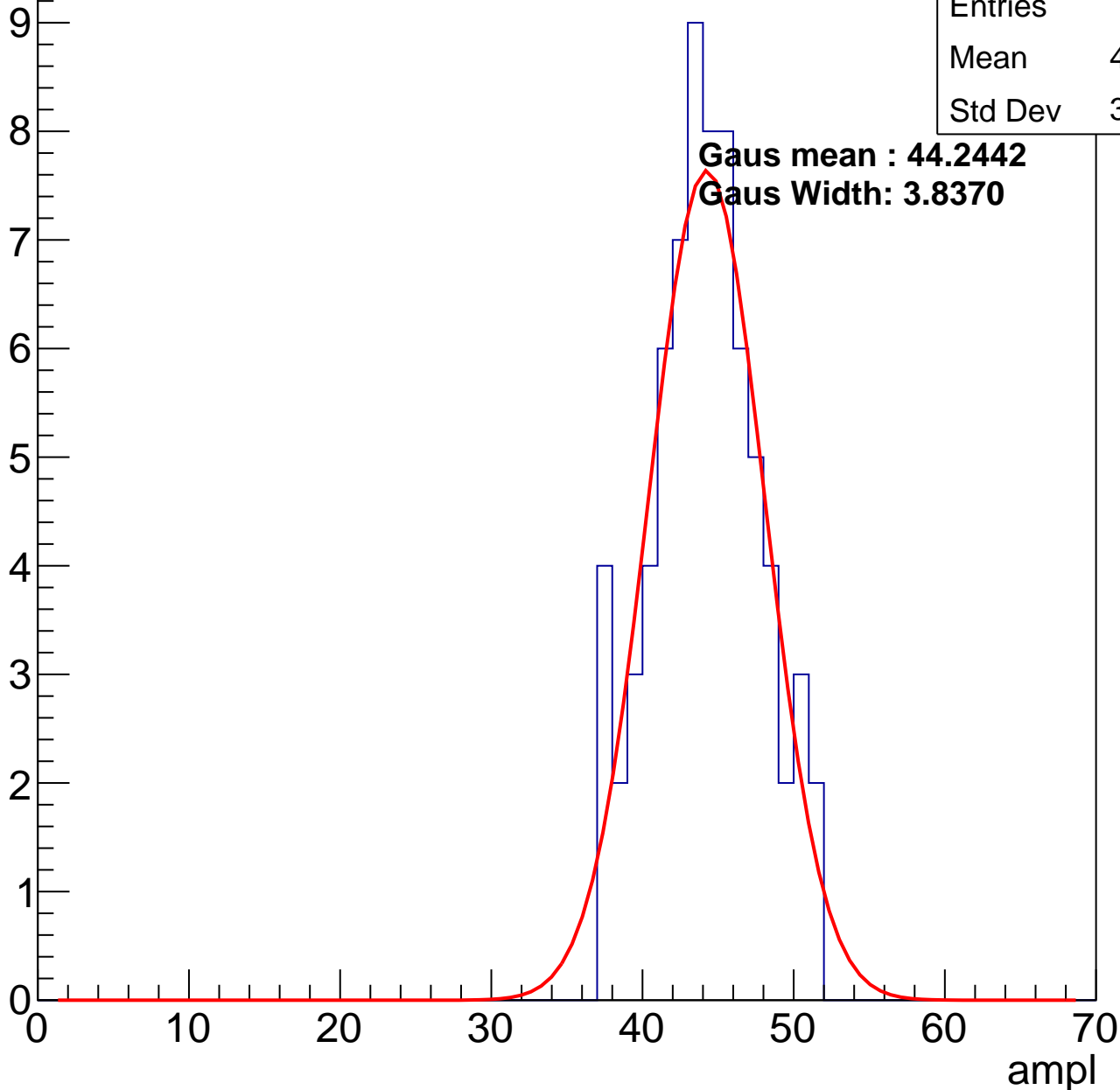
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.74
Std Dev	3.492

Gaus mean : 44.2442

Gaus Width: 3.8370

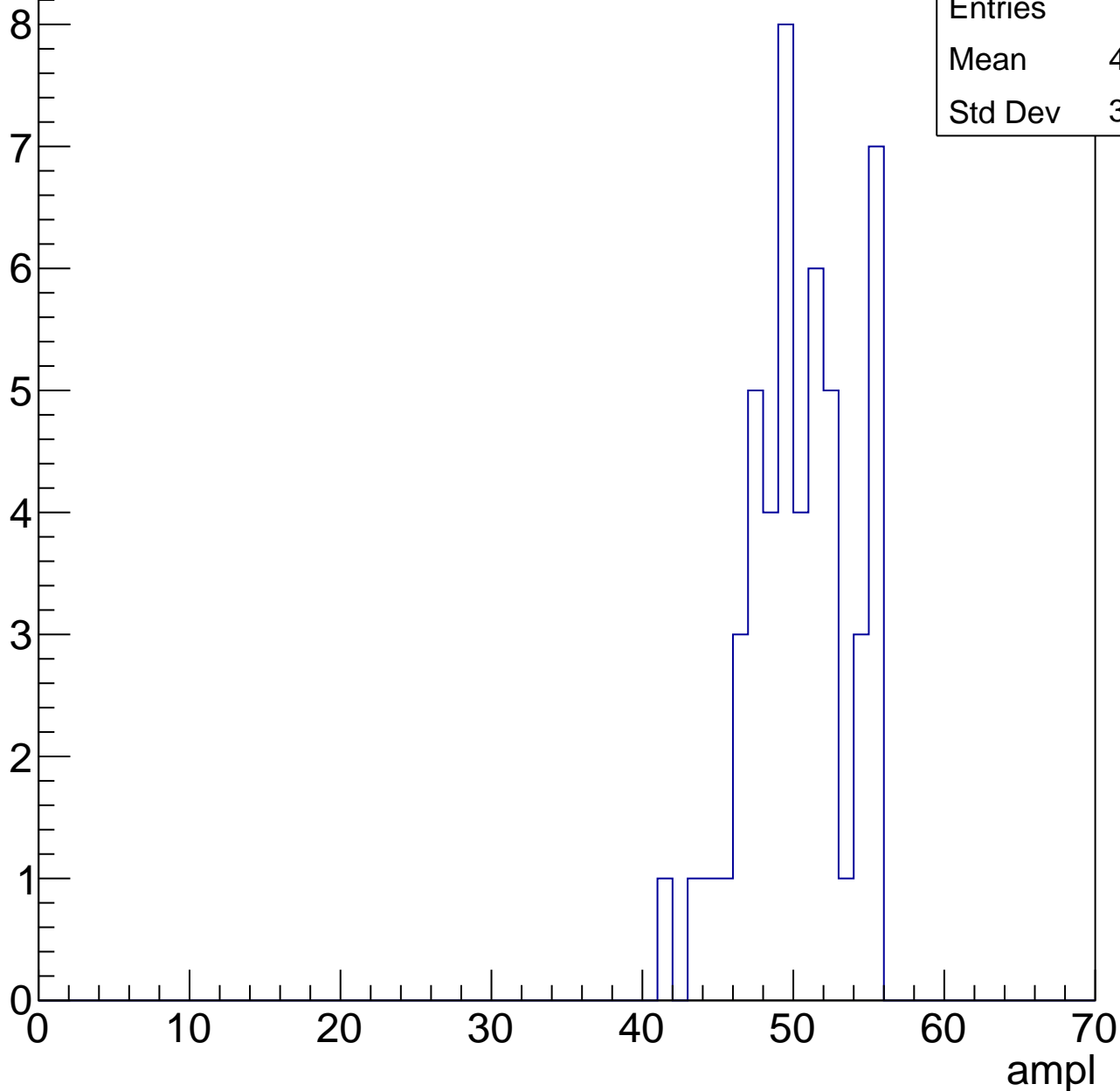


B1L102S, U26-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	49.92
Std Dev	3.364

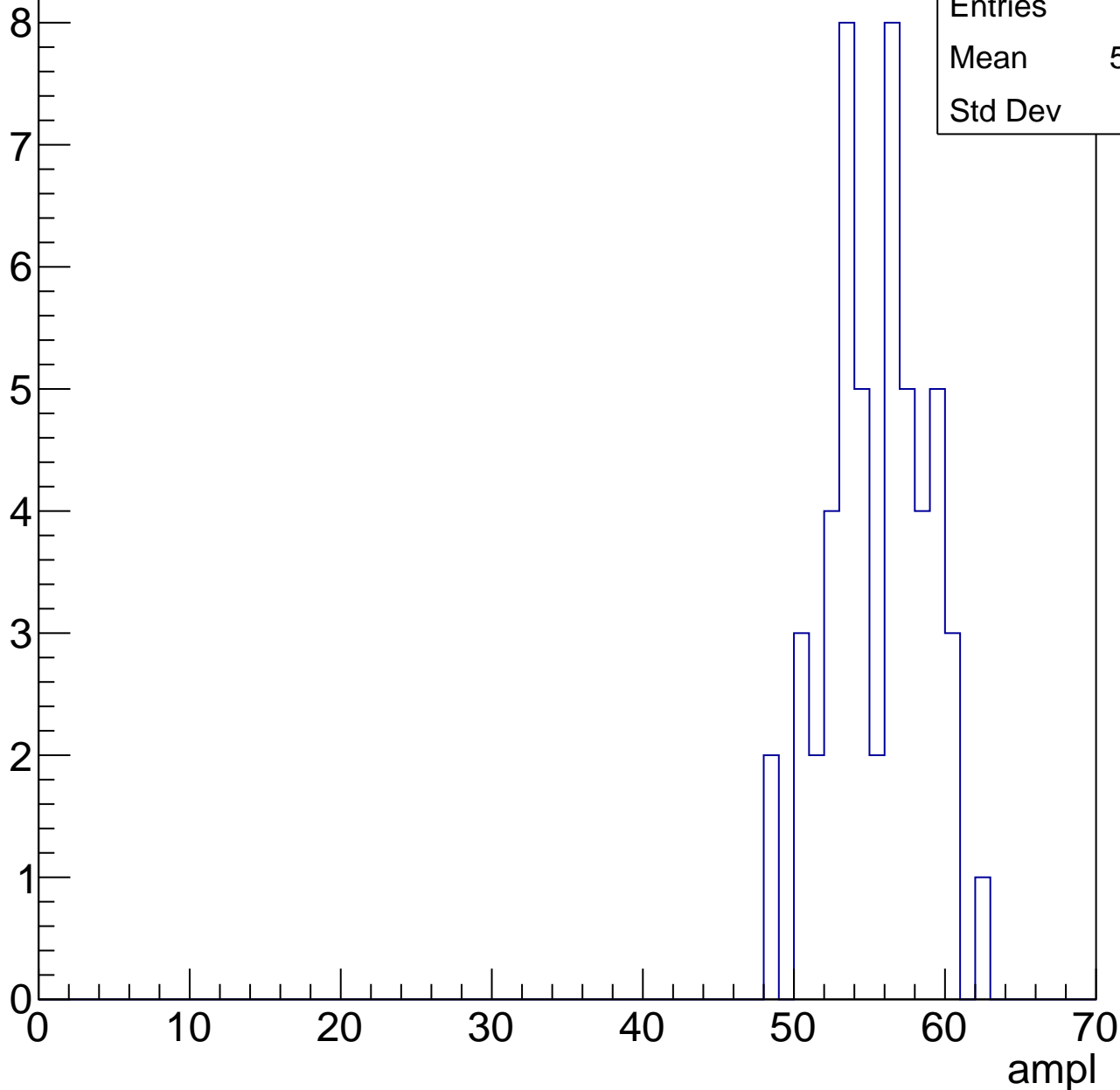


B1L102S, U26-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.04
Std Dev	3.24

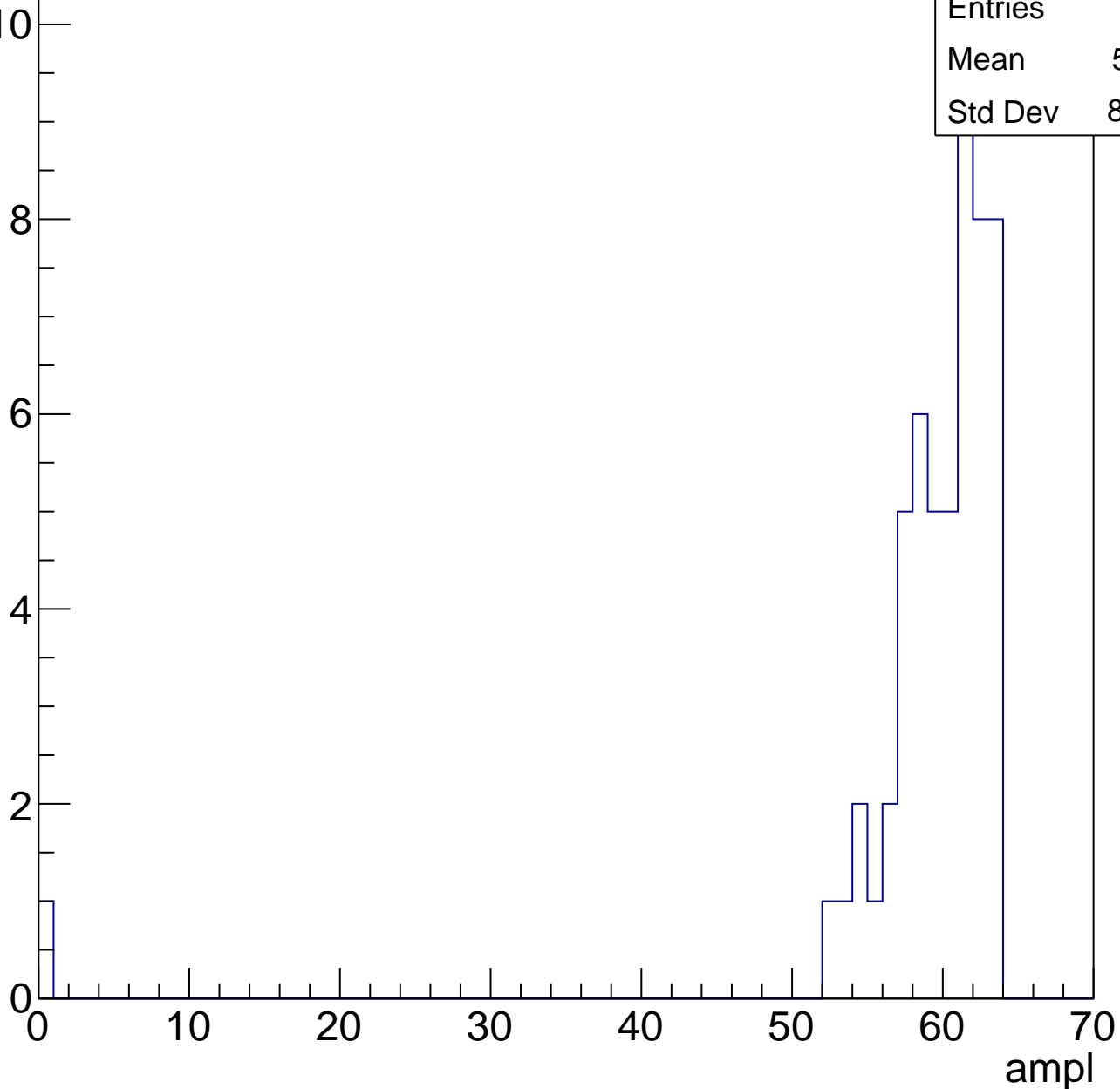


B1L102S, U26-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.51
Std Dev	8.429



B1L102S, U26-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch45, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	30.18
Std Dev	4.771

Gaus mean : 30.9189

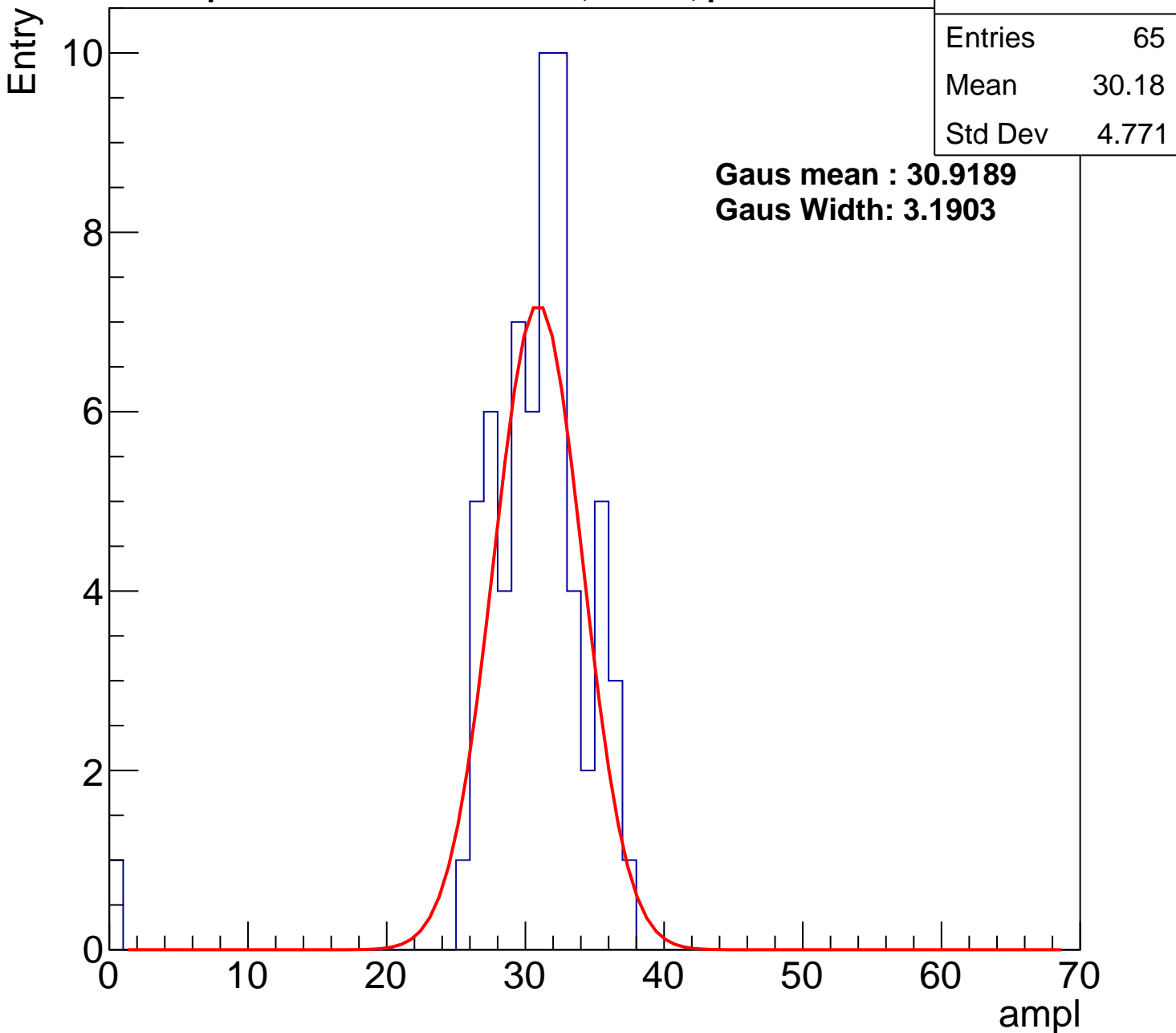
Gaus Width: 3.1903

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch45, adc1

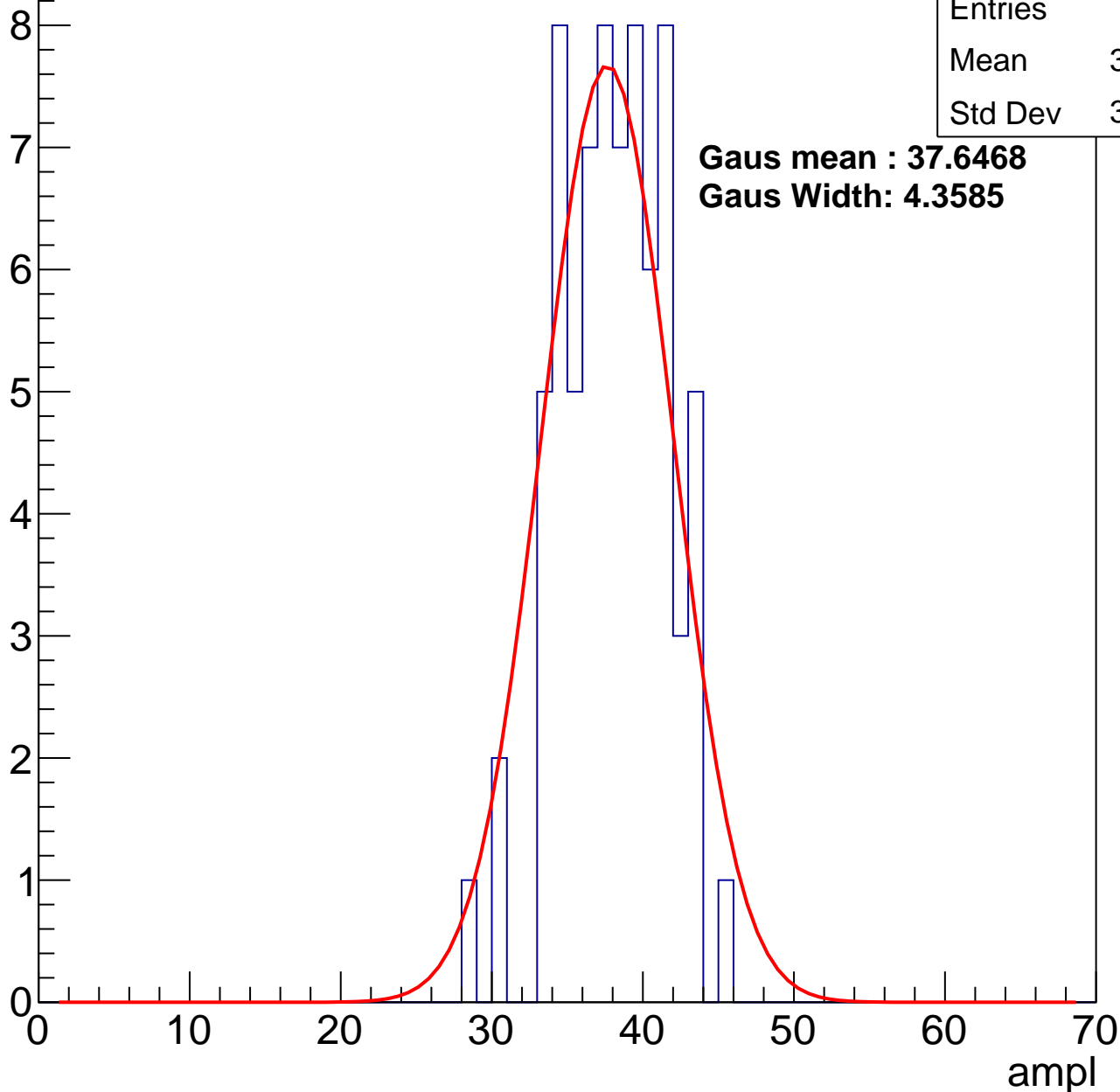
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.57
Std Dev	3.437

Gaus mean : 37.6468

Gaus Width: 4.3585



B1L102S, U26-ch45, adc2

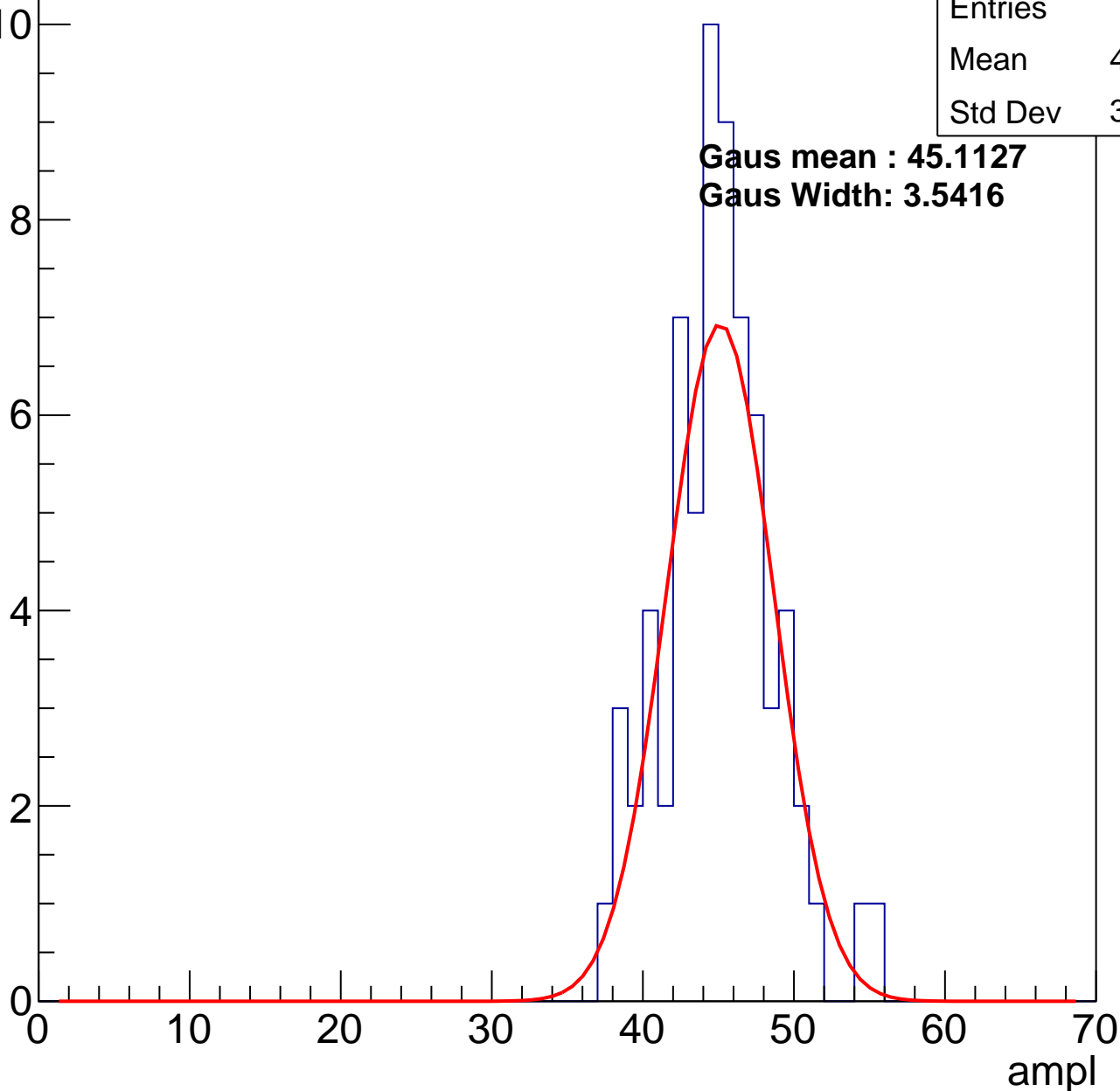
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.54
Std Dev	3.603

Gaus mean : 45.1127

Gaus Width: 3.5416

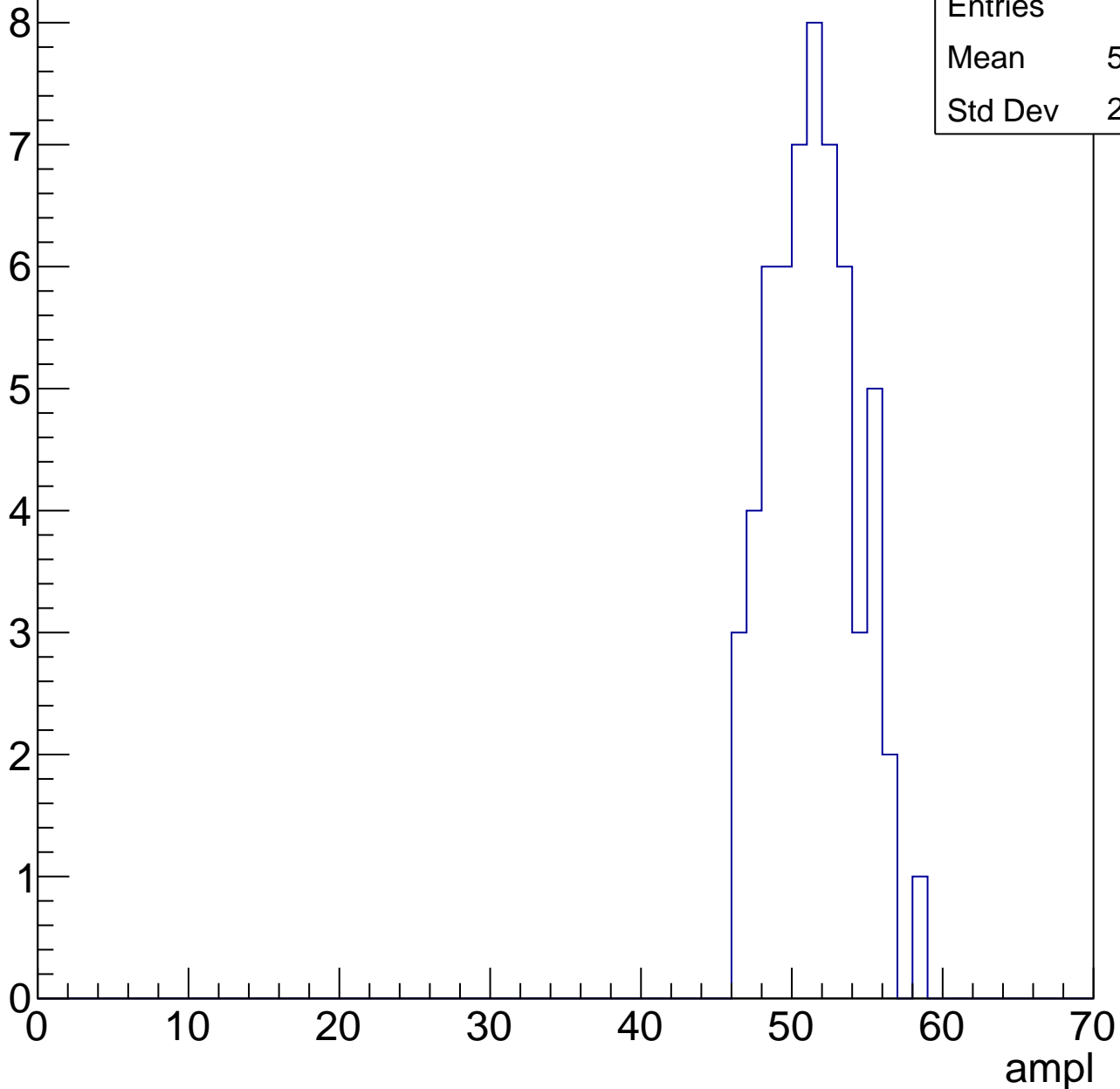


B1L102S, U26-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.95
Std Dev	2.819

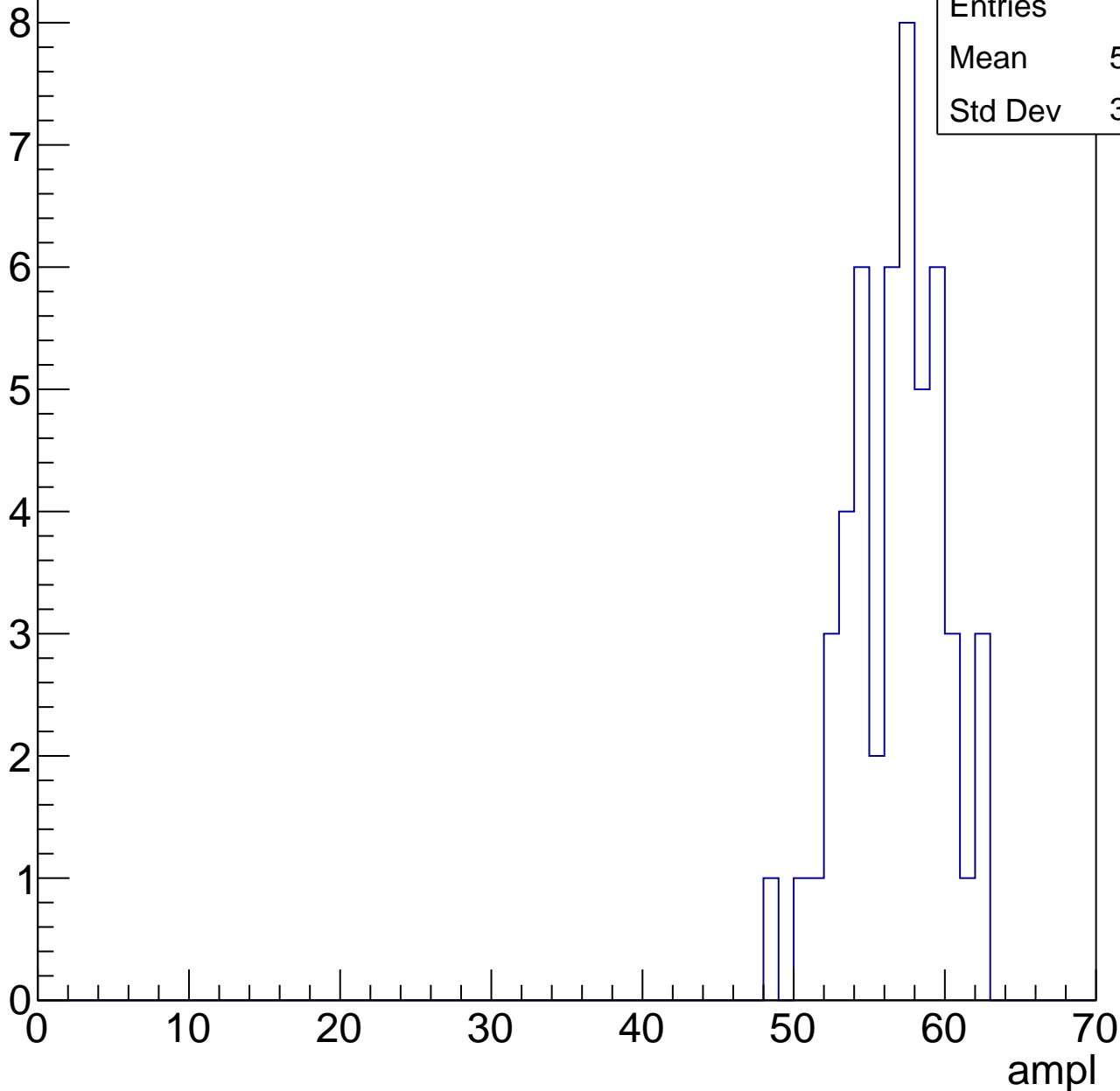


B1L102S, U26-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

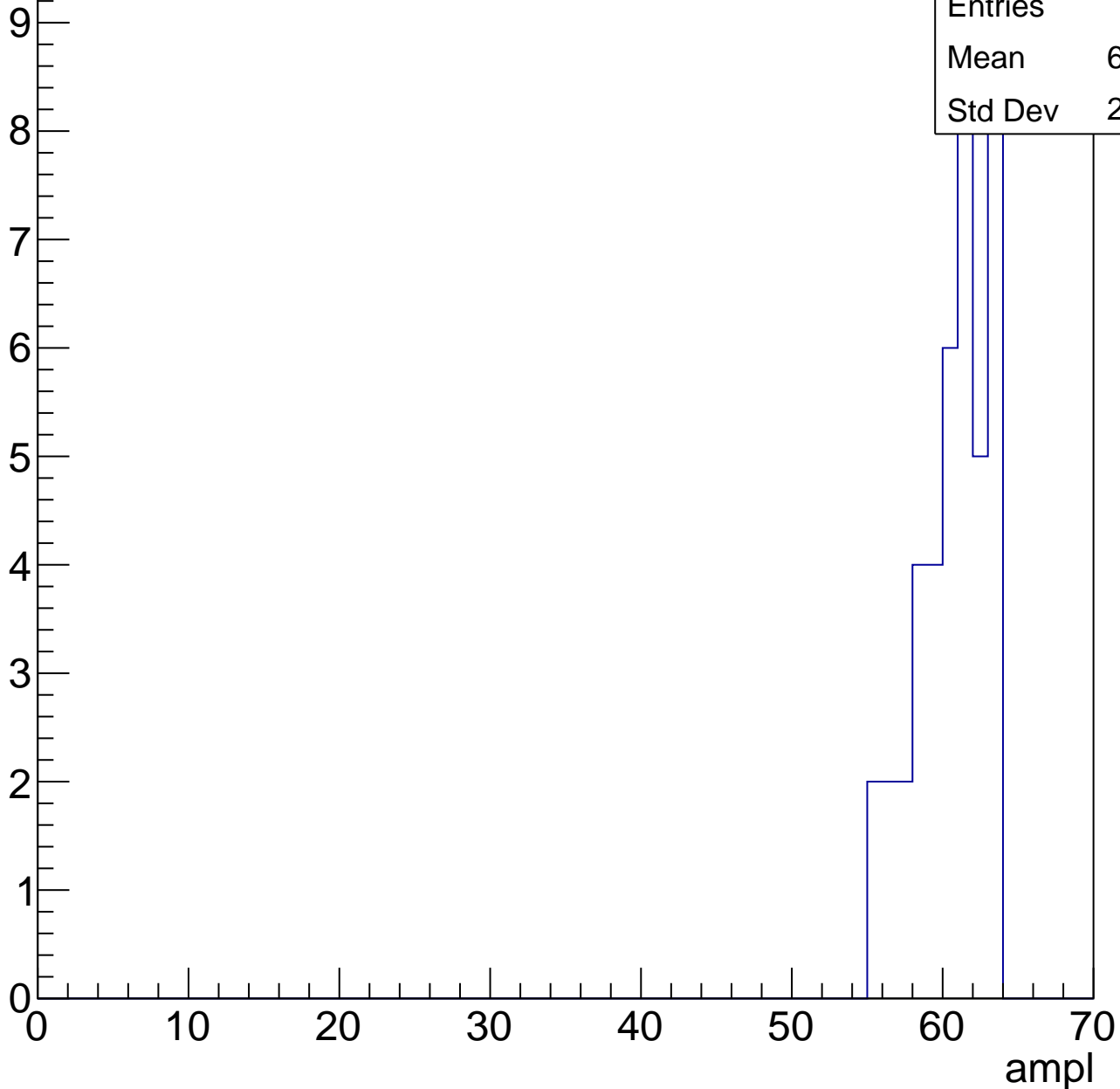
Entries	50
Mean	56.28
Std Dev	3.144



B1L102S, U26-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	42
Mean	60.17
Std Dev	2.288

B1L102S, U26-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

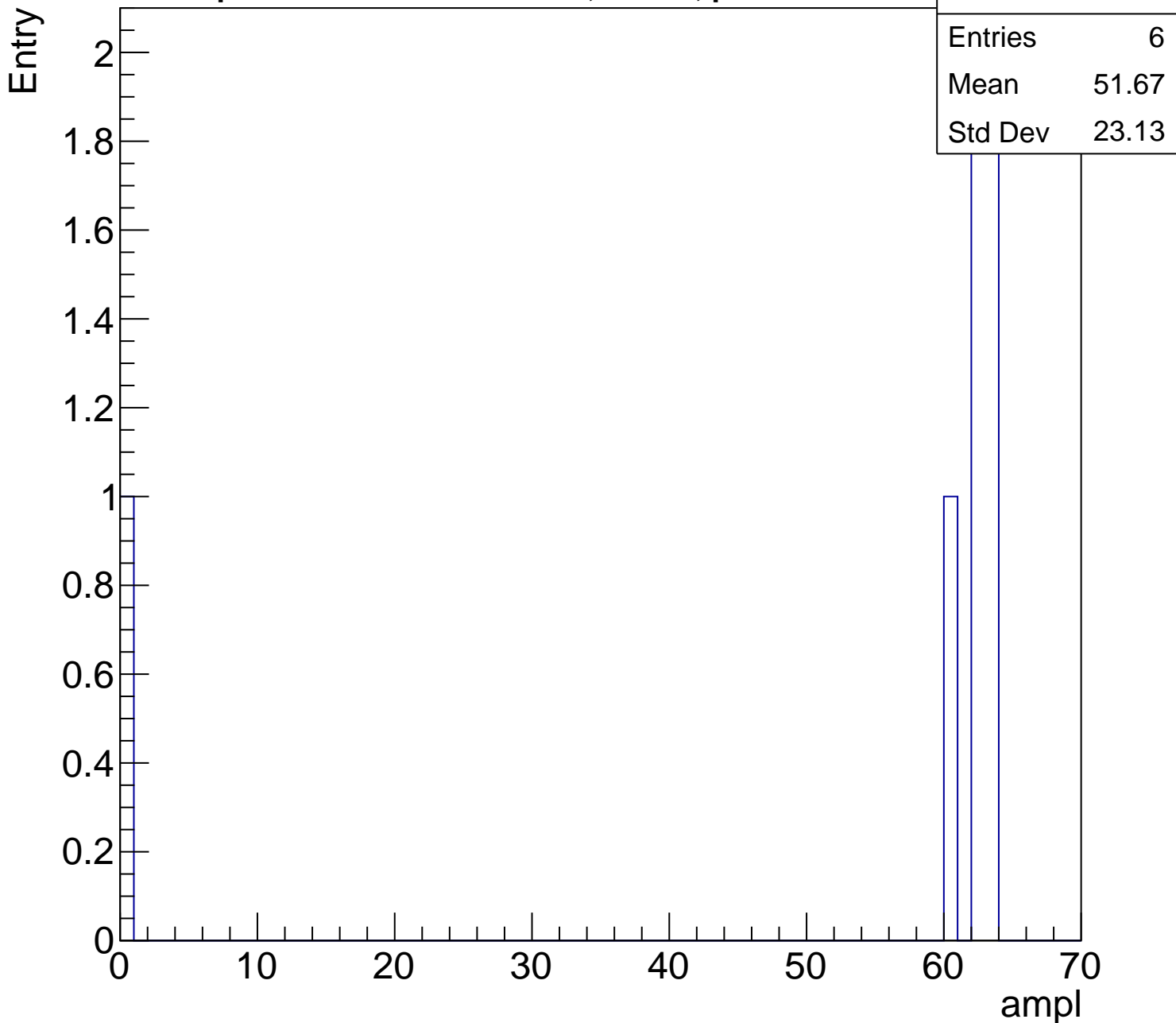
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.13

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch46, adc0

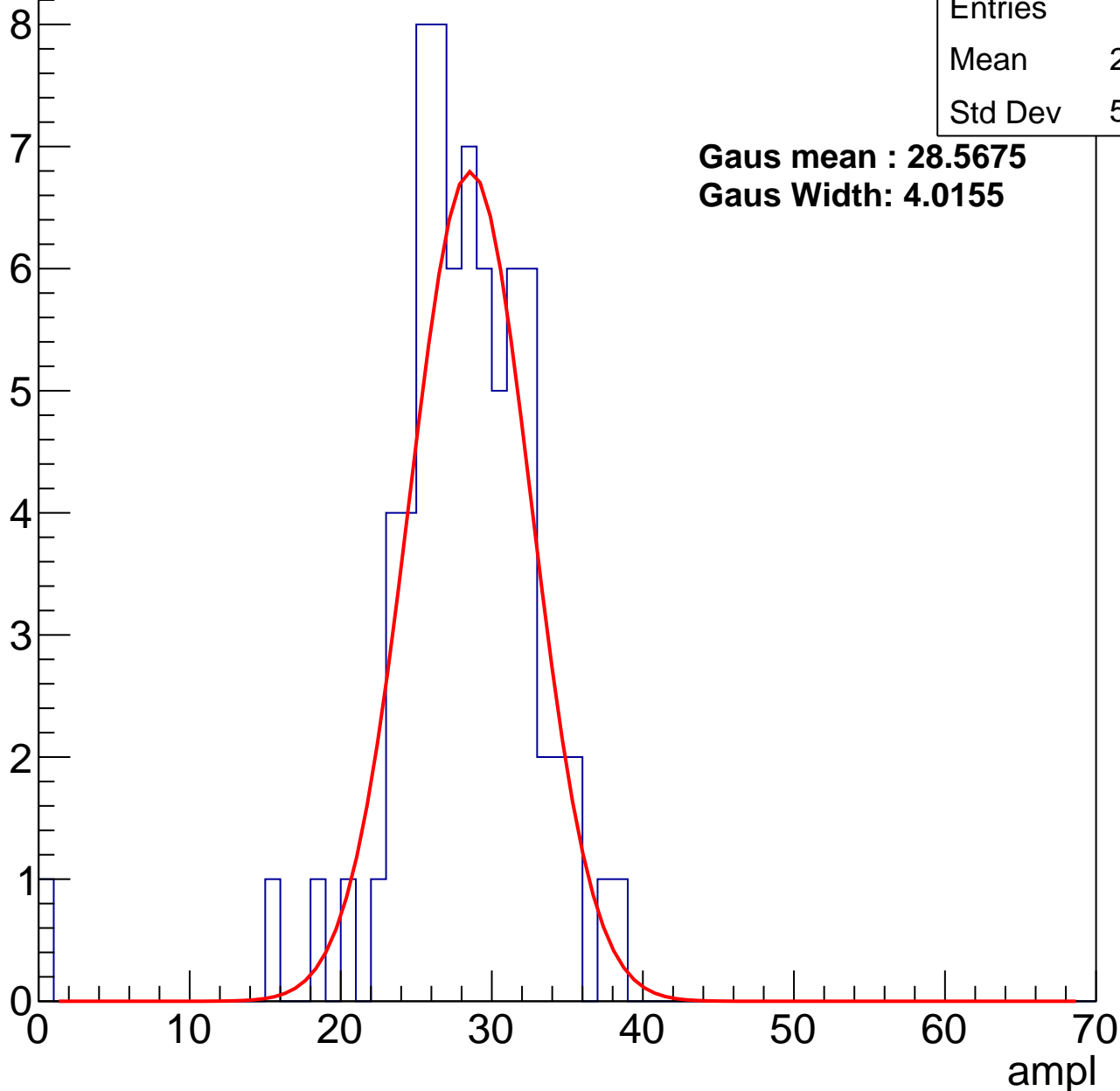
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.53
Std Dev	5.219

Gaus mean : 28.5675

Gaus Width: 4.0155



B1L102S, U26-ch46, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	34.74
Std Dev	3.518

Gaus mean : 35.0863

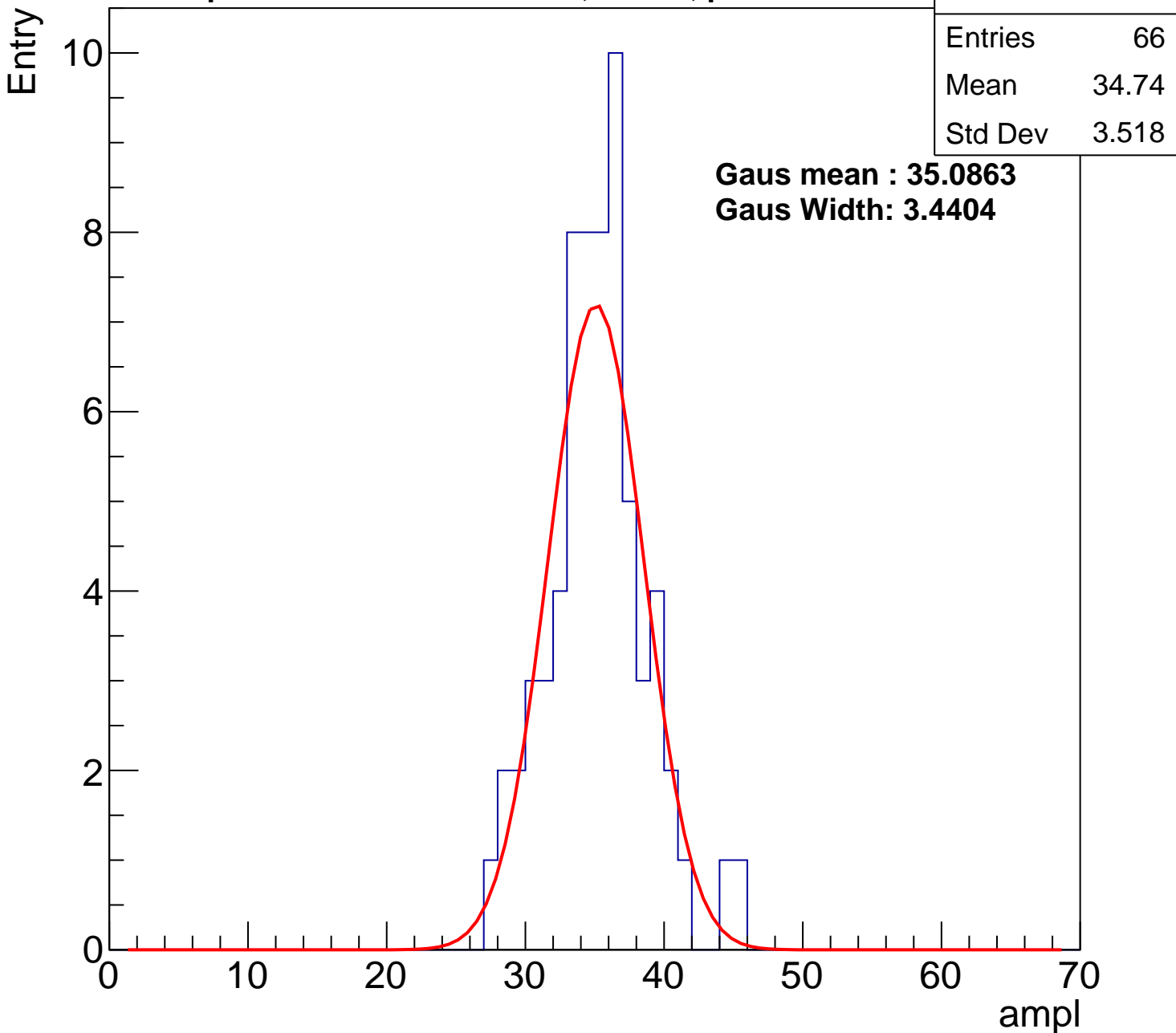
Gaus Width: 3.4404

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch46, adc2

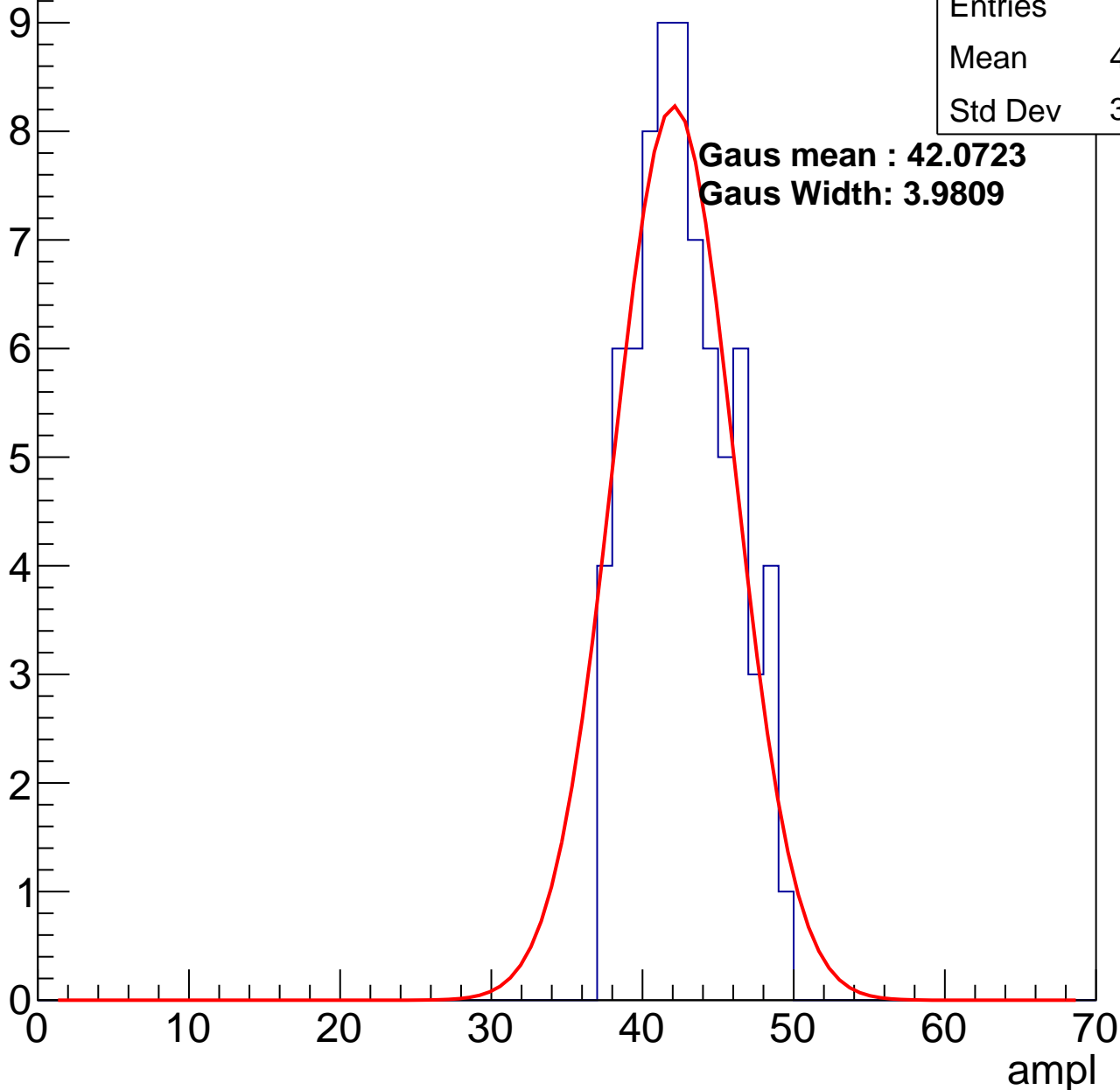
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.23
Std Dev	3.135

Gaus mean : 42.0723

Gaus Width: 3.9809

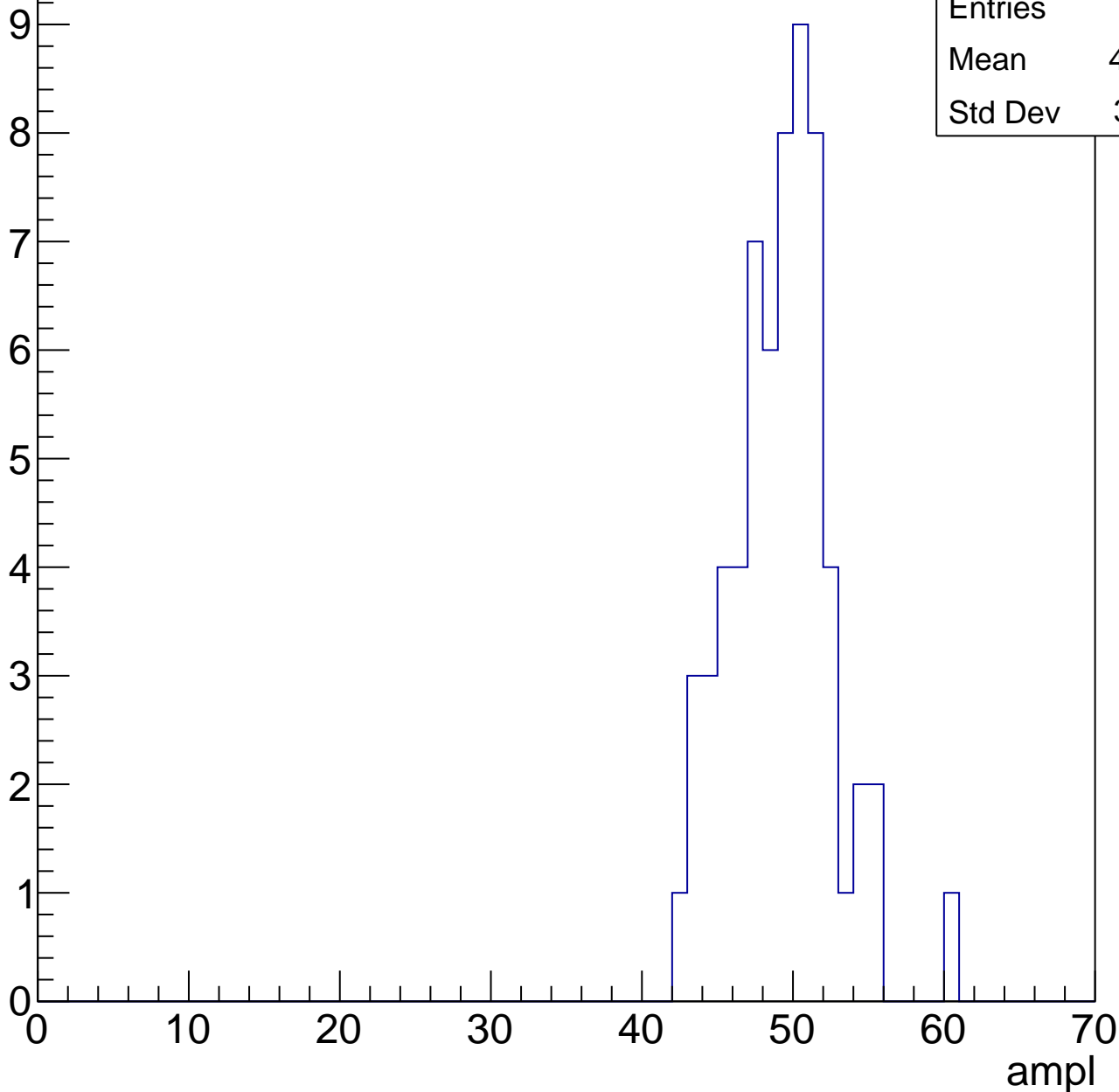


B1L102S, U26-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.78
Std Dev	3.331

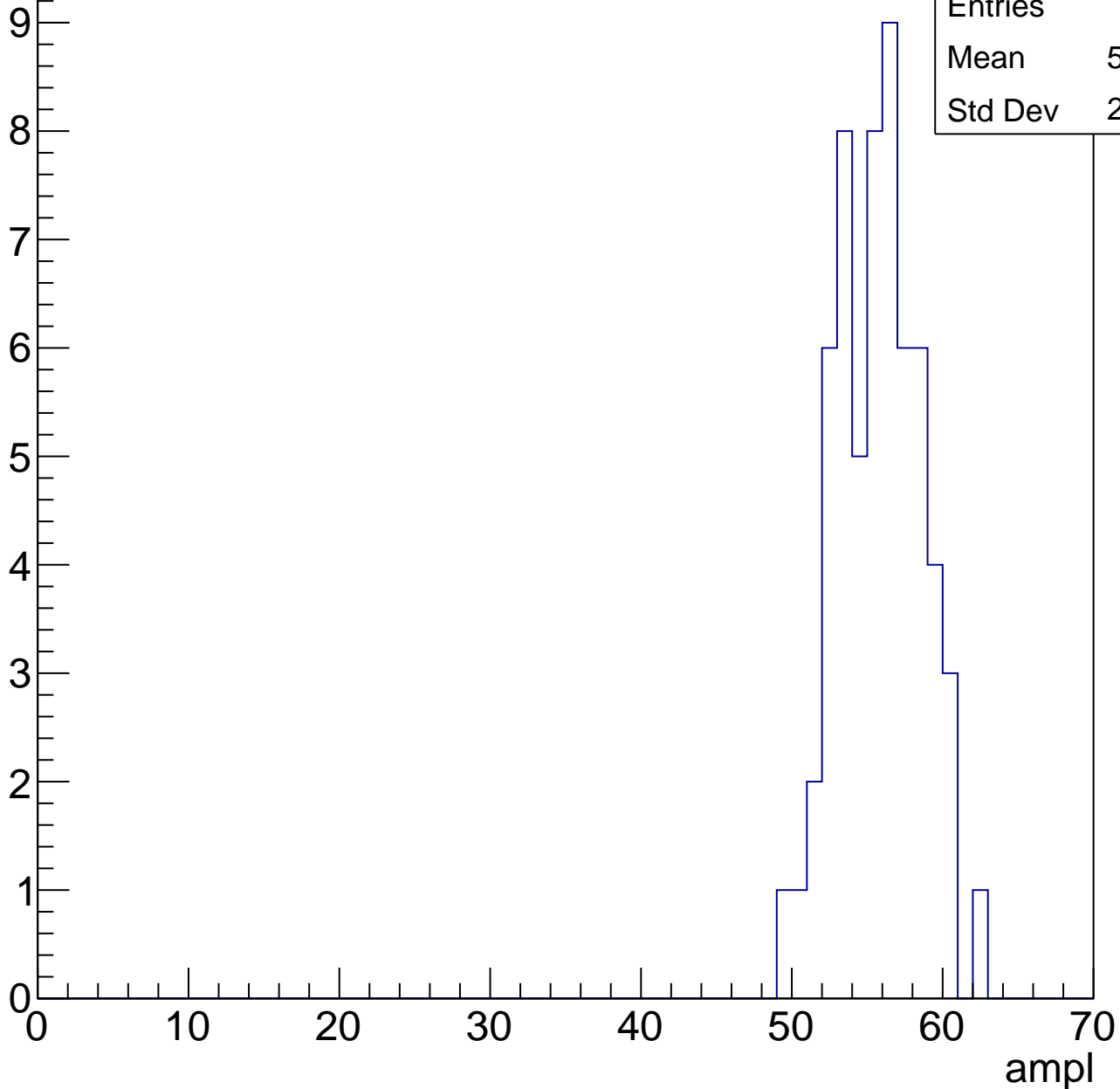


B1L102S, U26-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.32
Std Dev	2.748

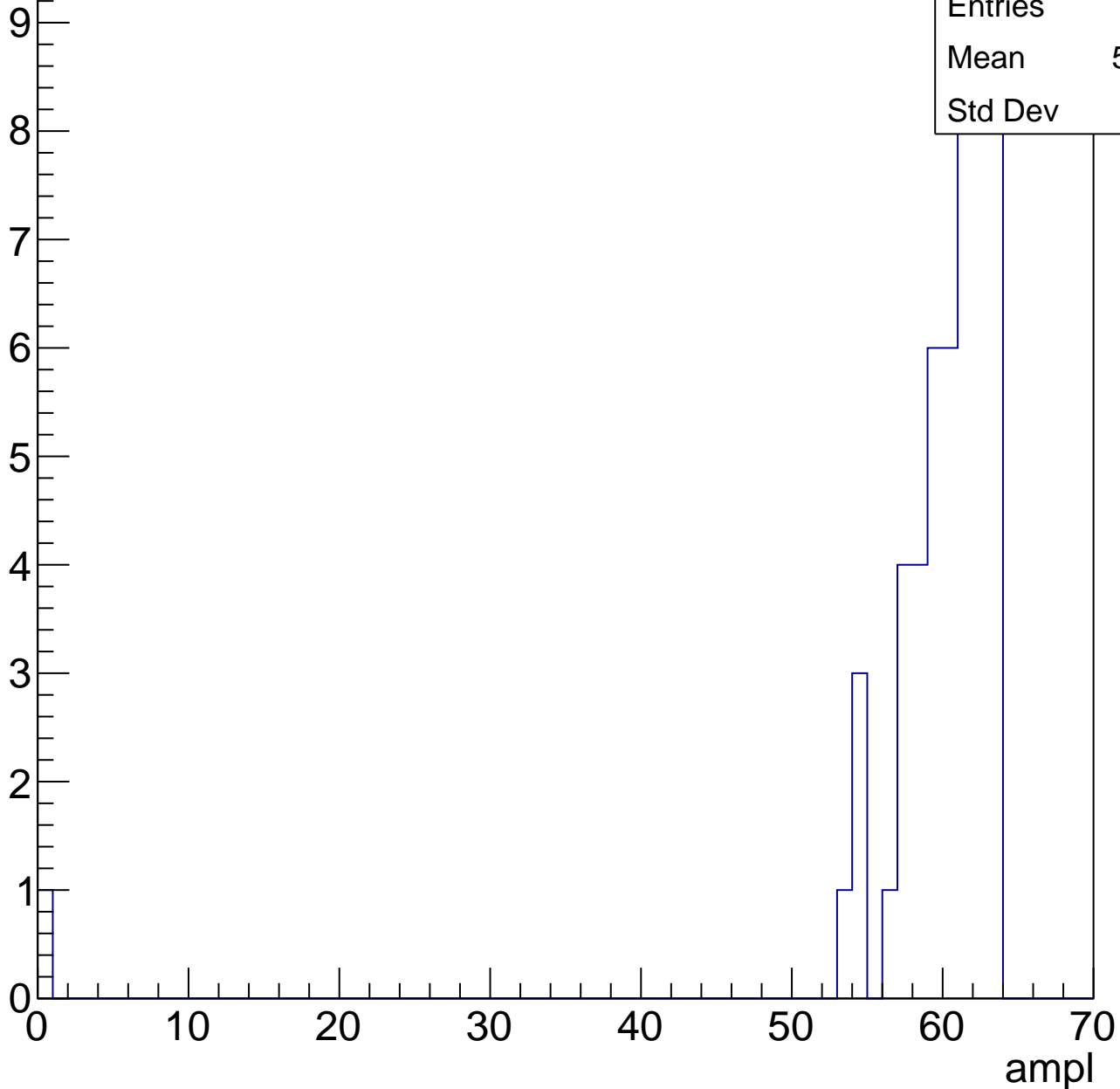


B1L102S, U26-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.81
Std Dev	8.64



B1L102S, U26-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch47, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	30.11
Std Dev	3.449

Gaus mean : 30.5663

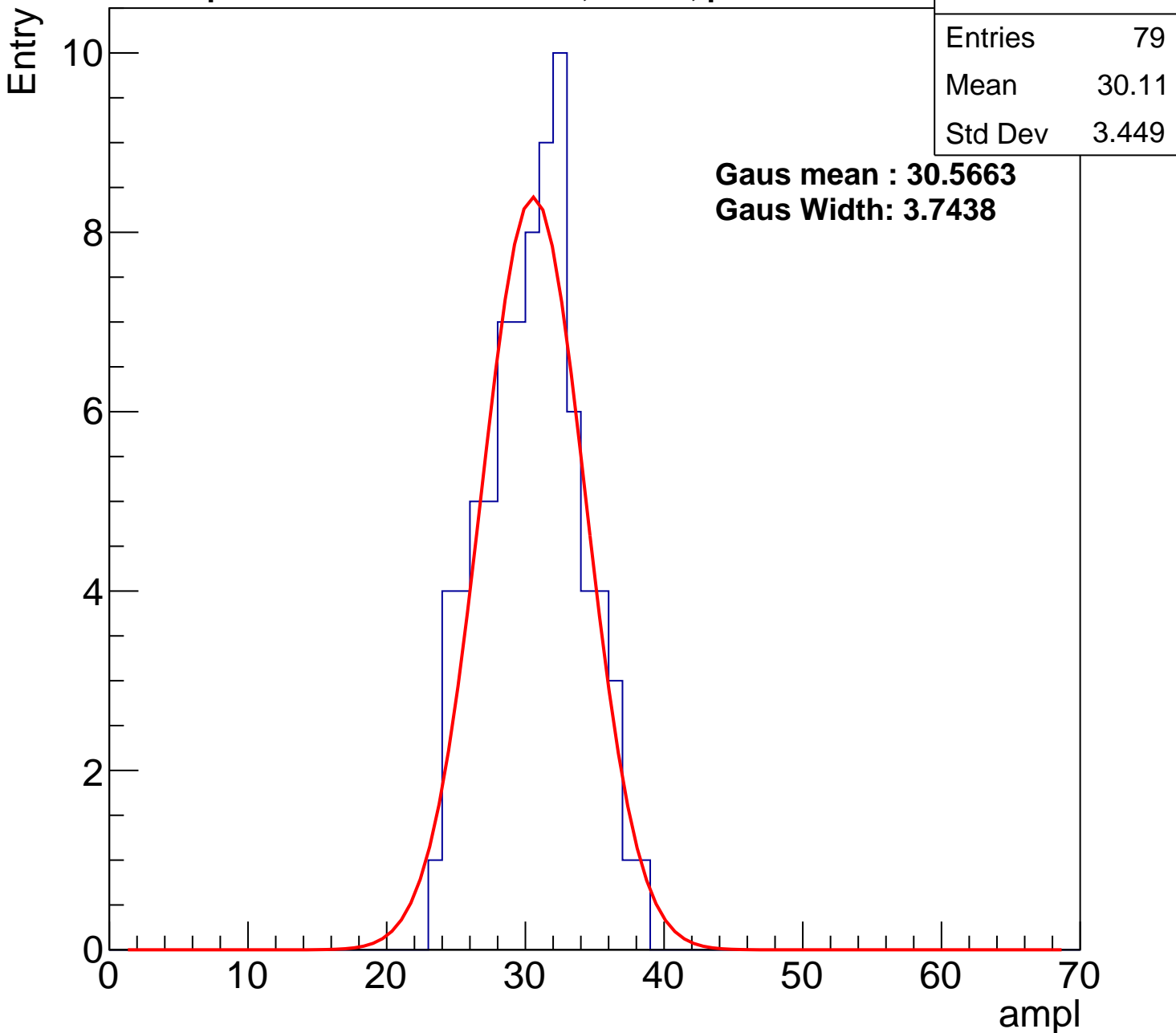
Gaus Width: 3.7438

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



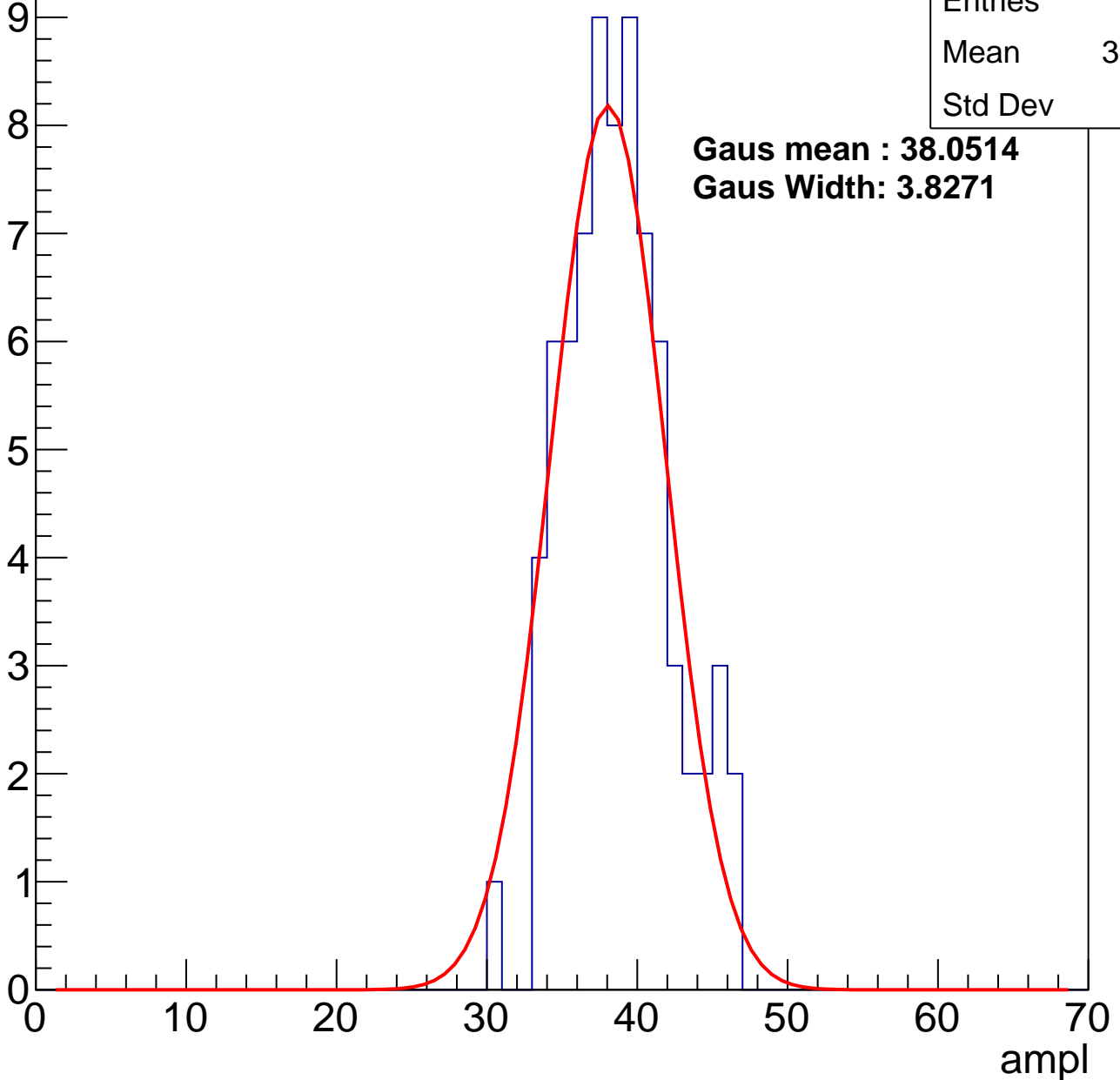
B1L102S, U26-ch47, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	38.25
Std Dev	3.43

Gaus mean : 38.0514
Gaus Width: 3.8271



B1L102S, U26-ch47, adc2

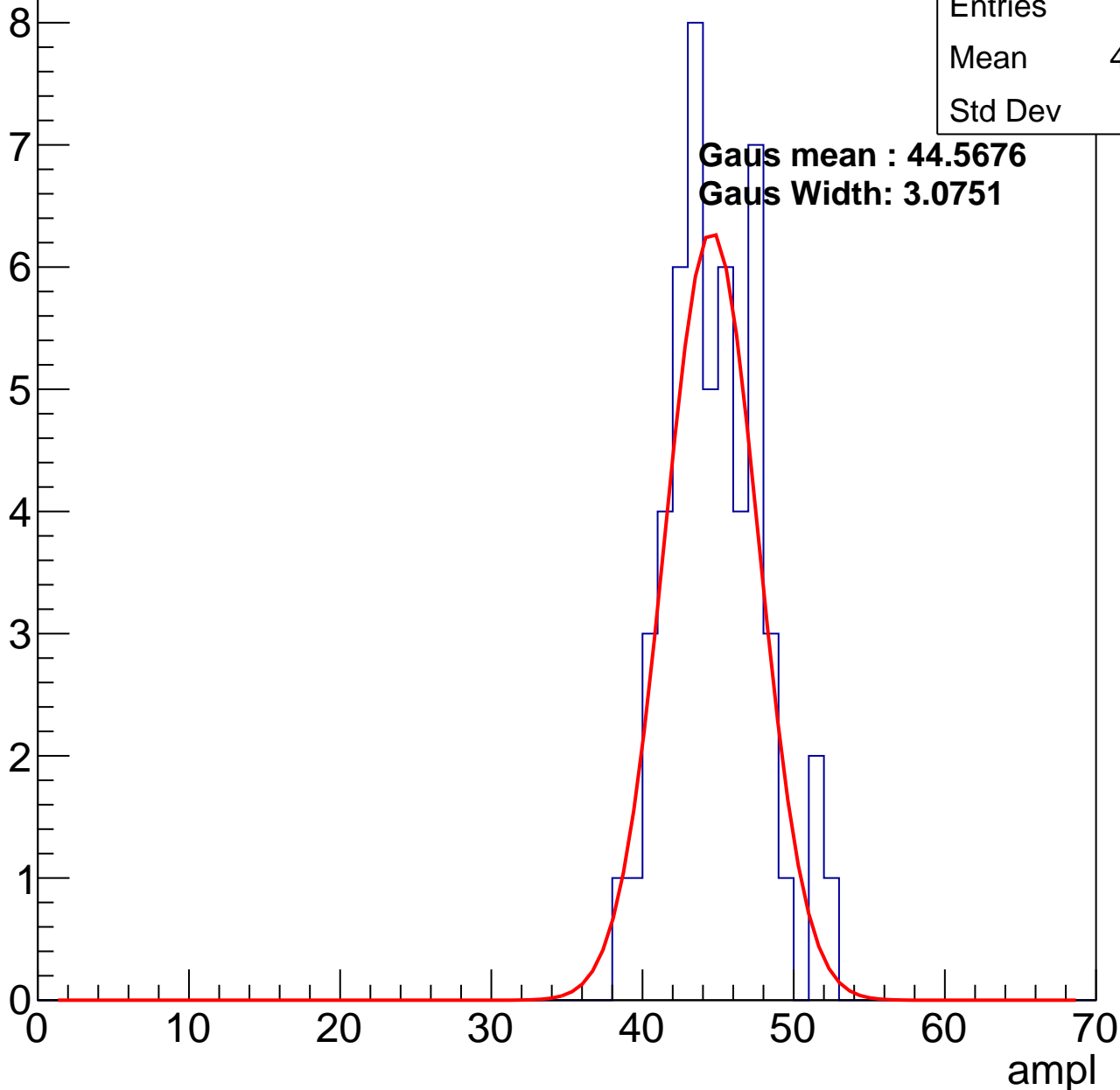
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	44.37
Std Dev	3.07

Gaus mean : 44.5676

Gaus Width: 3.0751

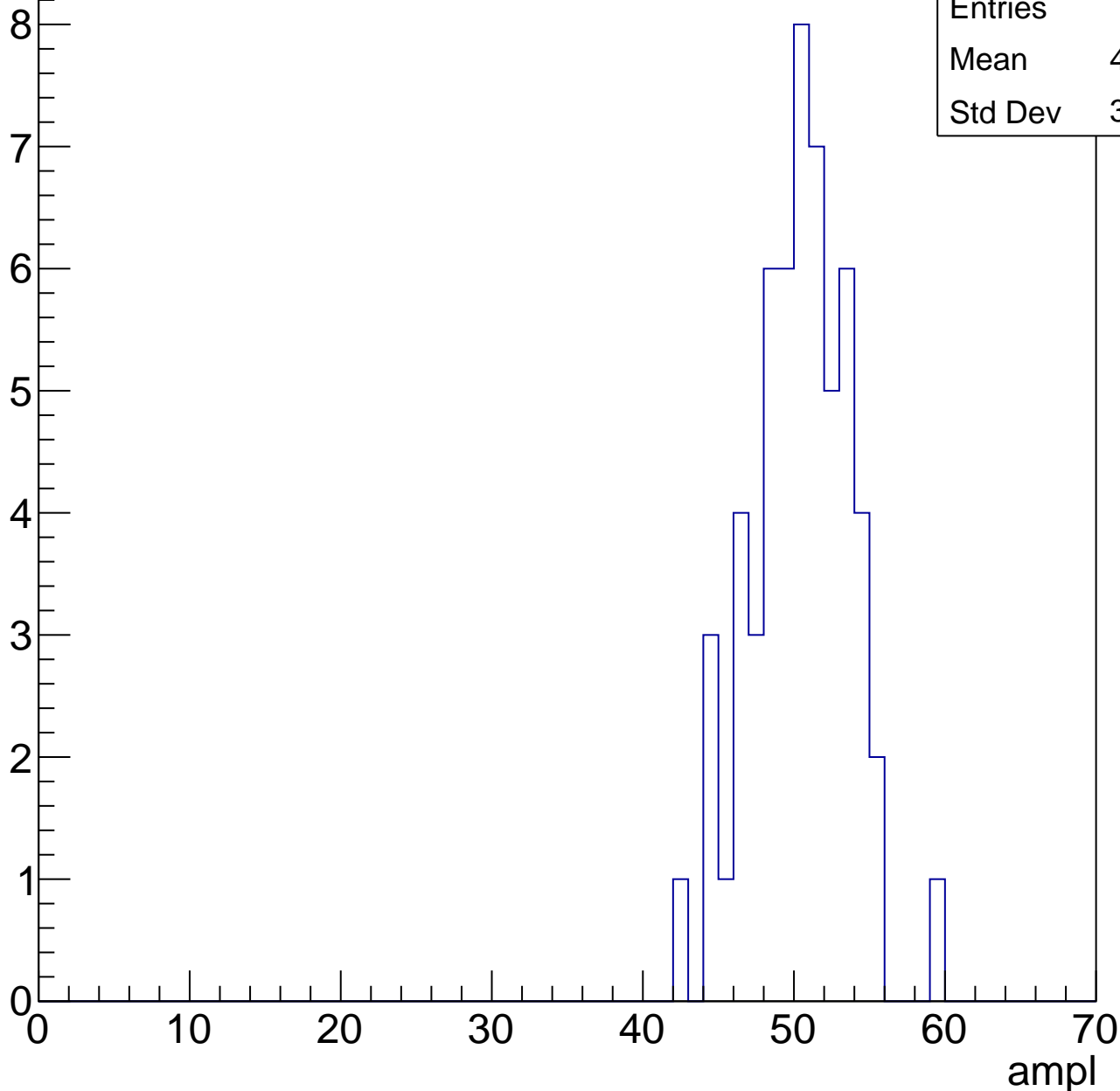


B1L102S, U26-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.93
Std Dev	3.227

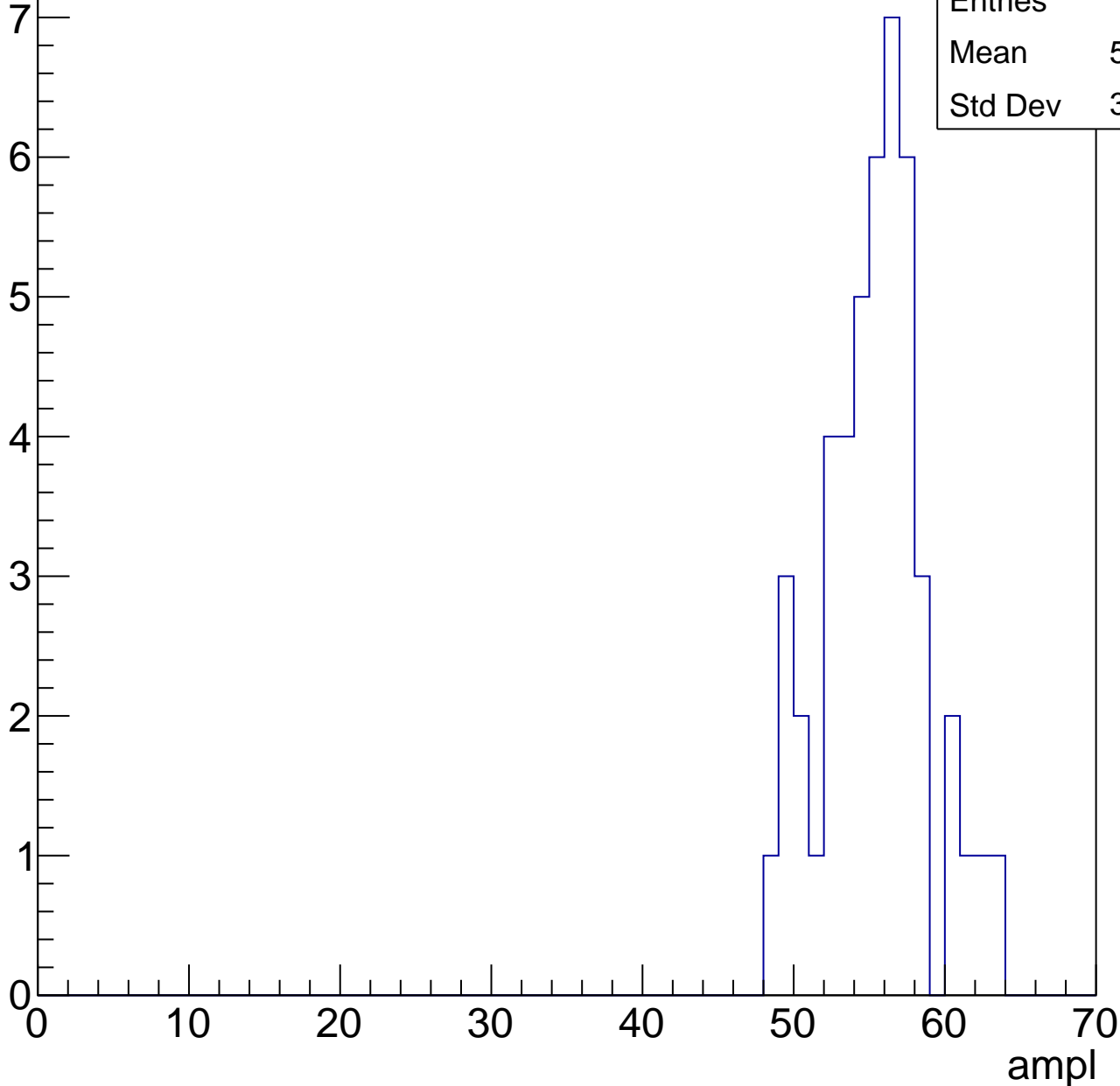


B1L102S, U26-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.89
Std Dev	3.378

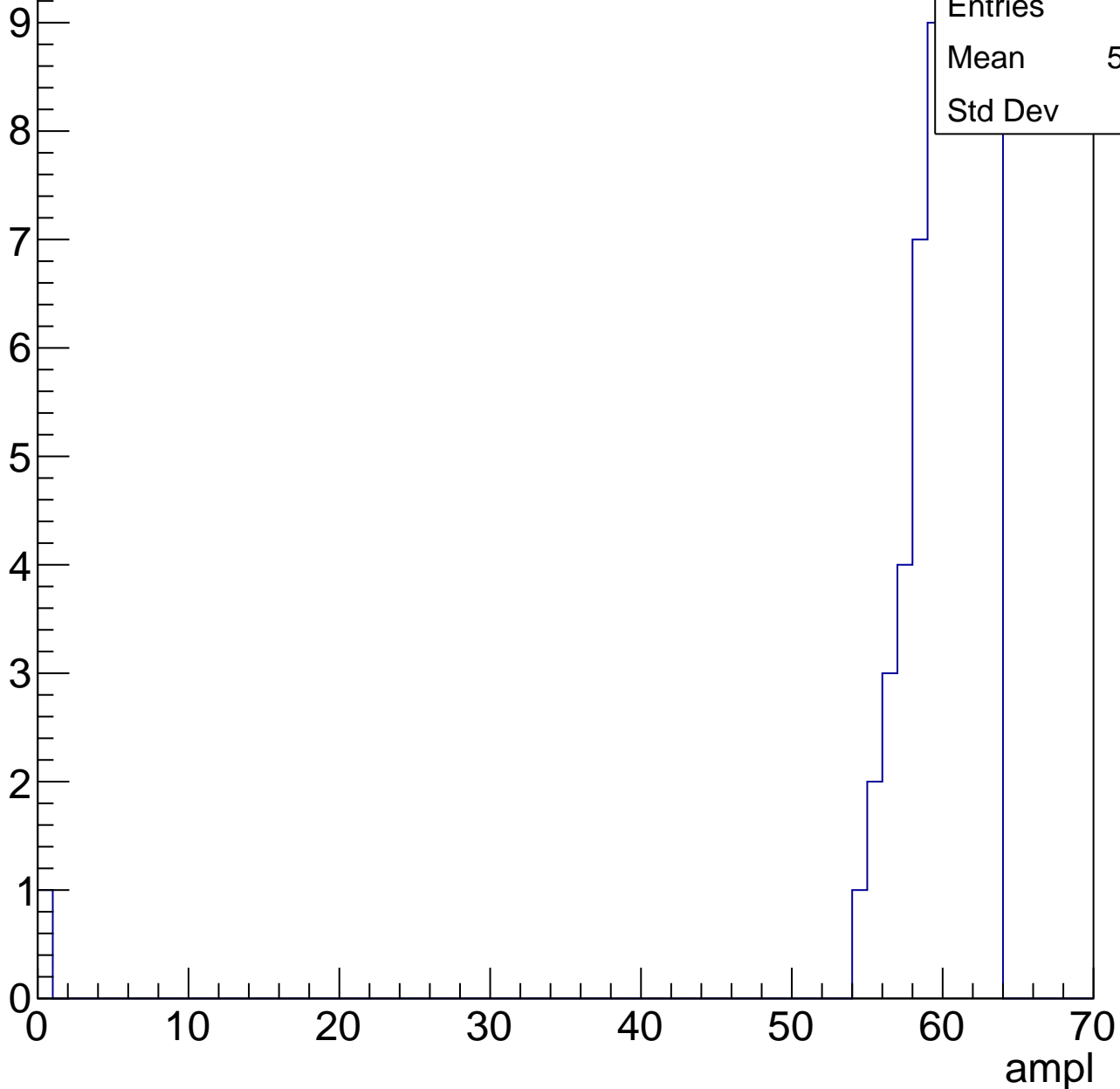


B1L102S, U26-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.73
Std Dev	8.05



B1L102S, U26-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U26-ch48, adc0

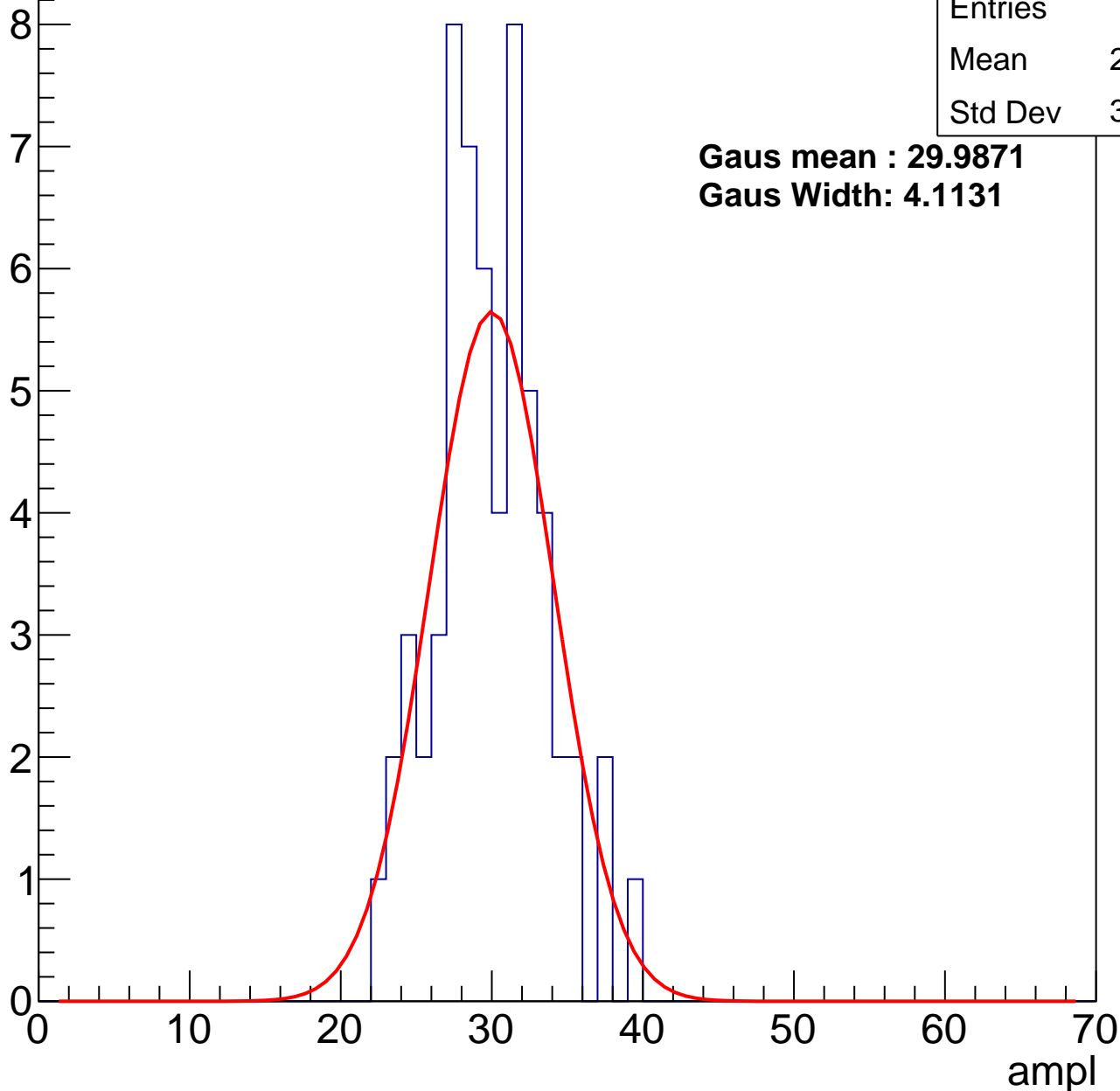
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.42
Std Dev	3.602

Gaus mean : 29.9871

Gaus Width: 4.1131



B1L102S, U26-ch48, adc1

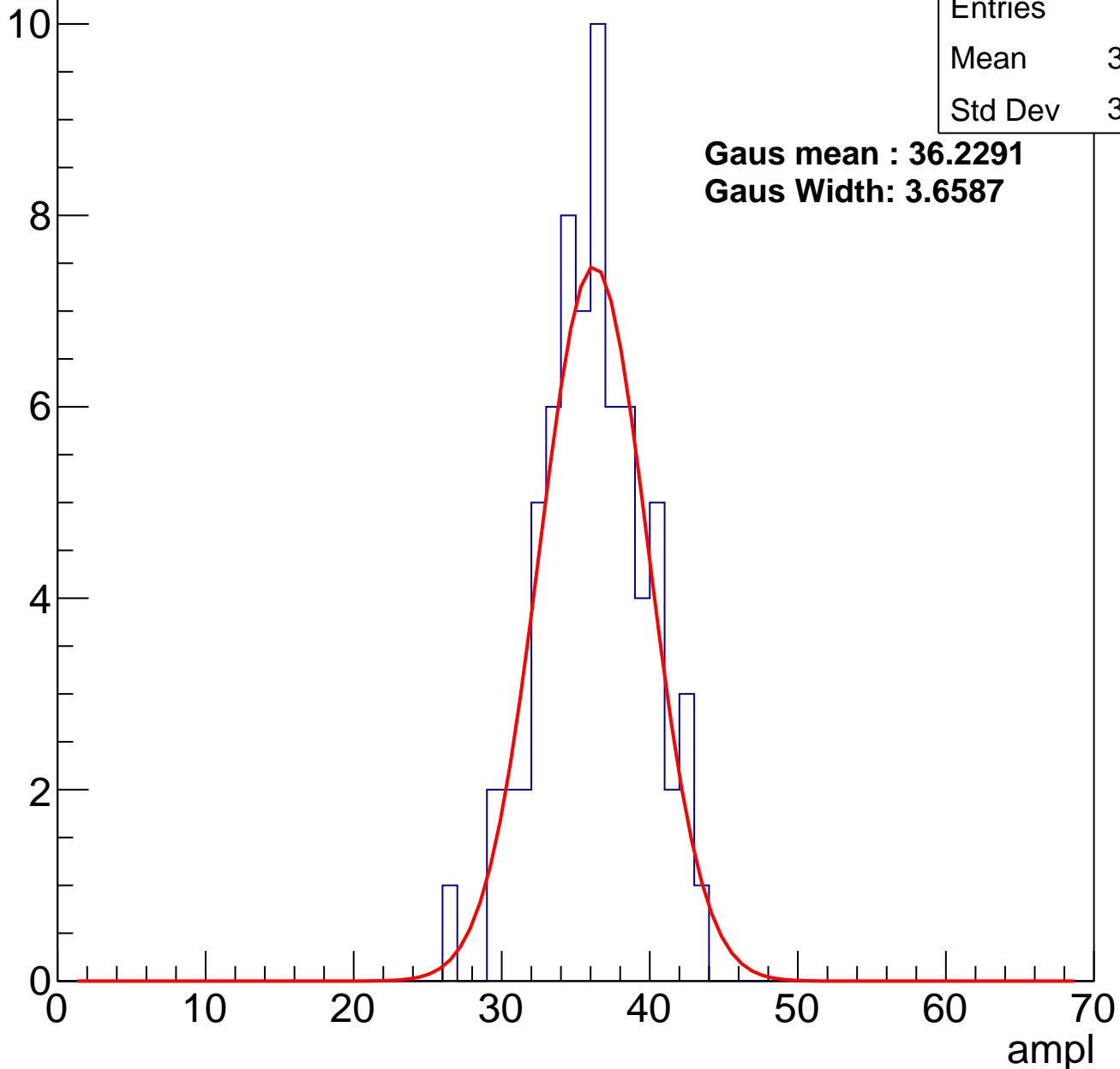
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	35.69
Std Dev	3.462

Gaus mean : 36.2291

Gaus Width: 3.6587

Entry



B1L102S, U26-ch48, adc2

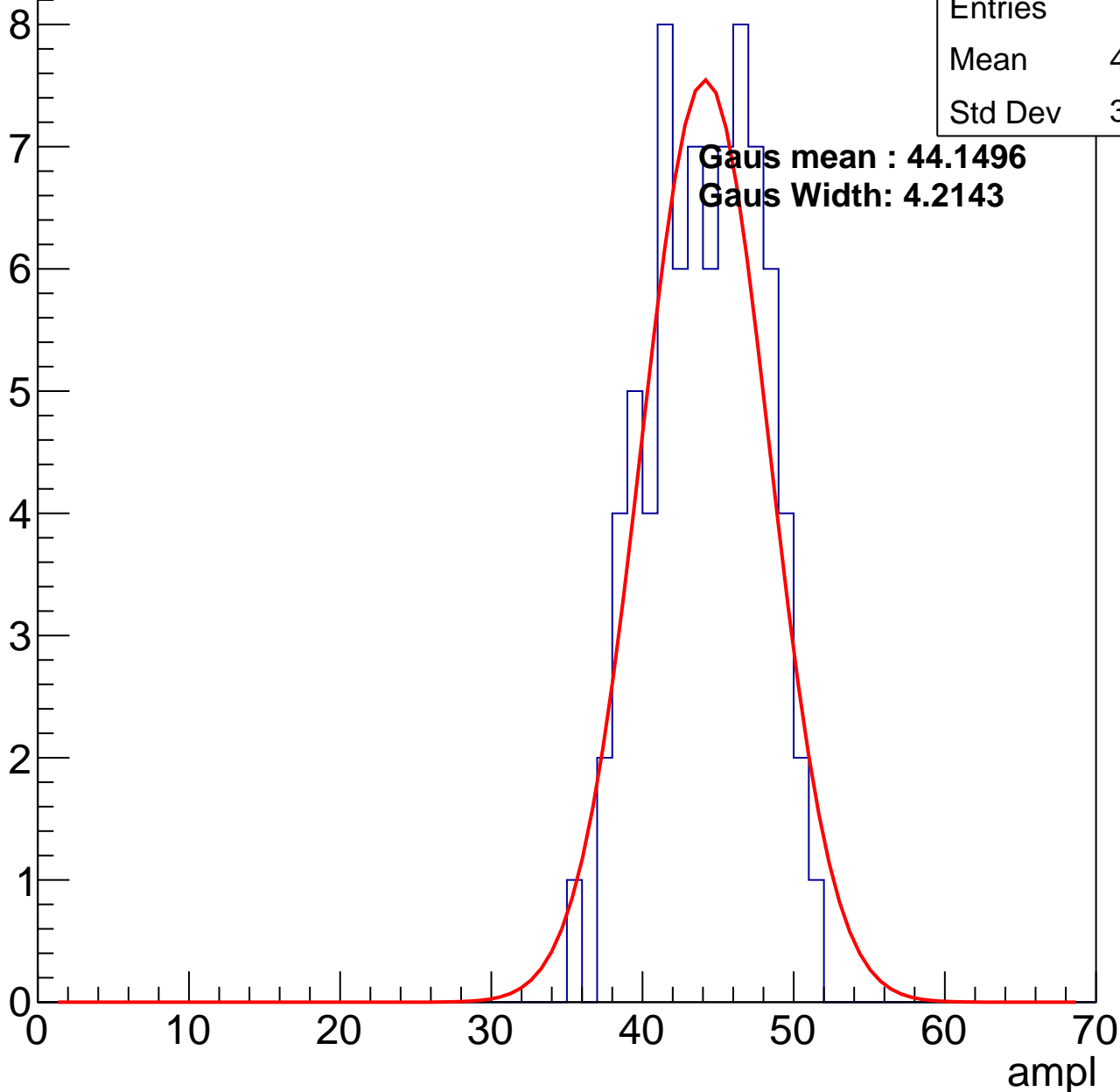
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	43.69
Std Dev	3.628

Gaus mean : 44.1496

Gaus Width: 4.2143

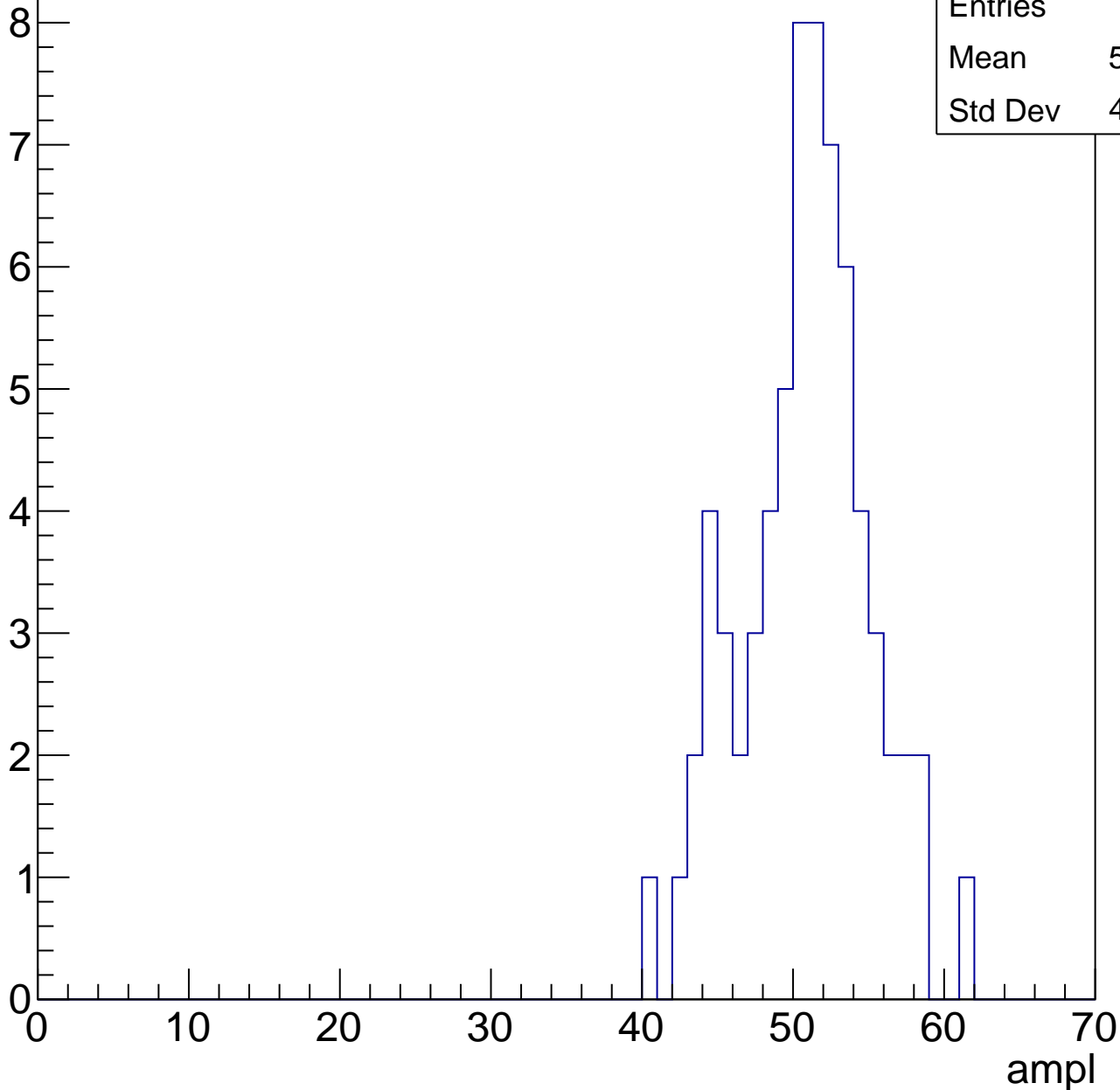


B1L102S, U26-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.34
Std Dev	4.203

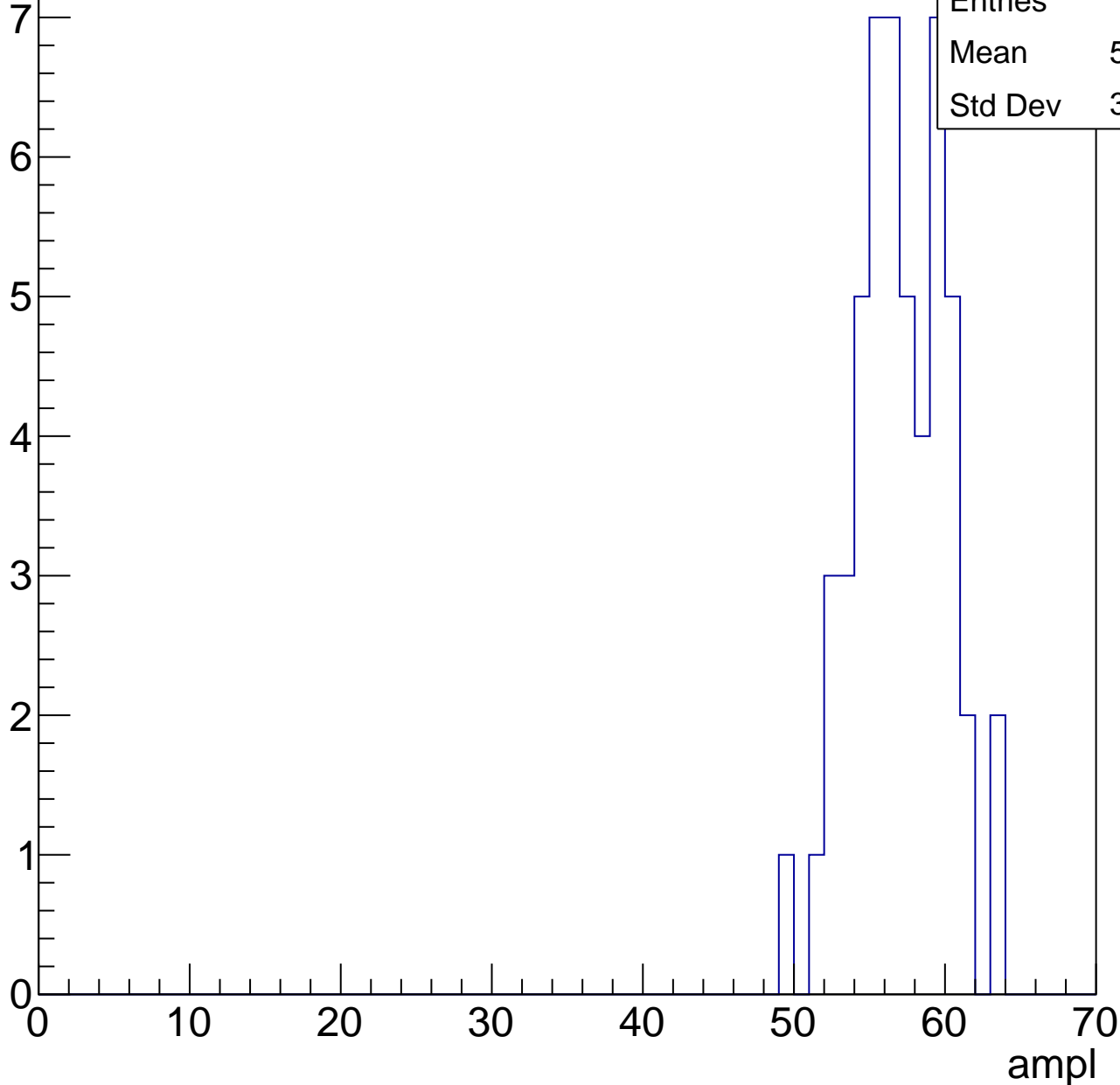


B1L102S, U26-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.54
Std Dev	3.016

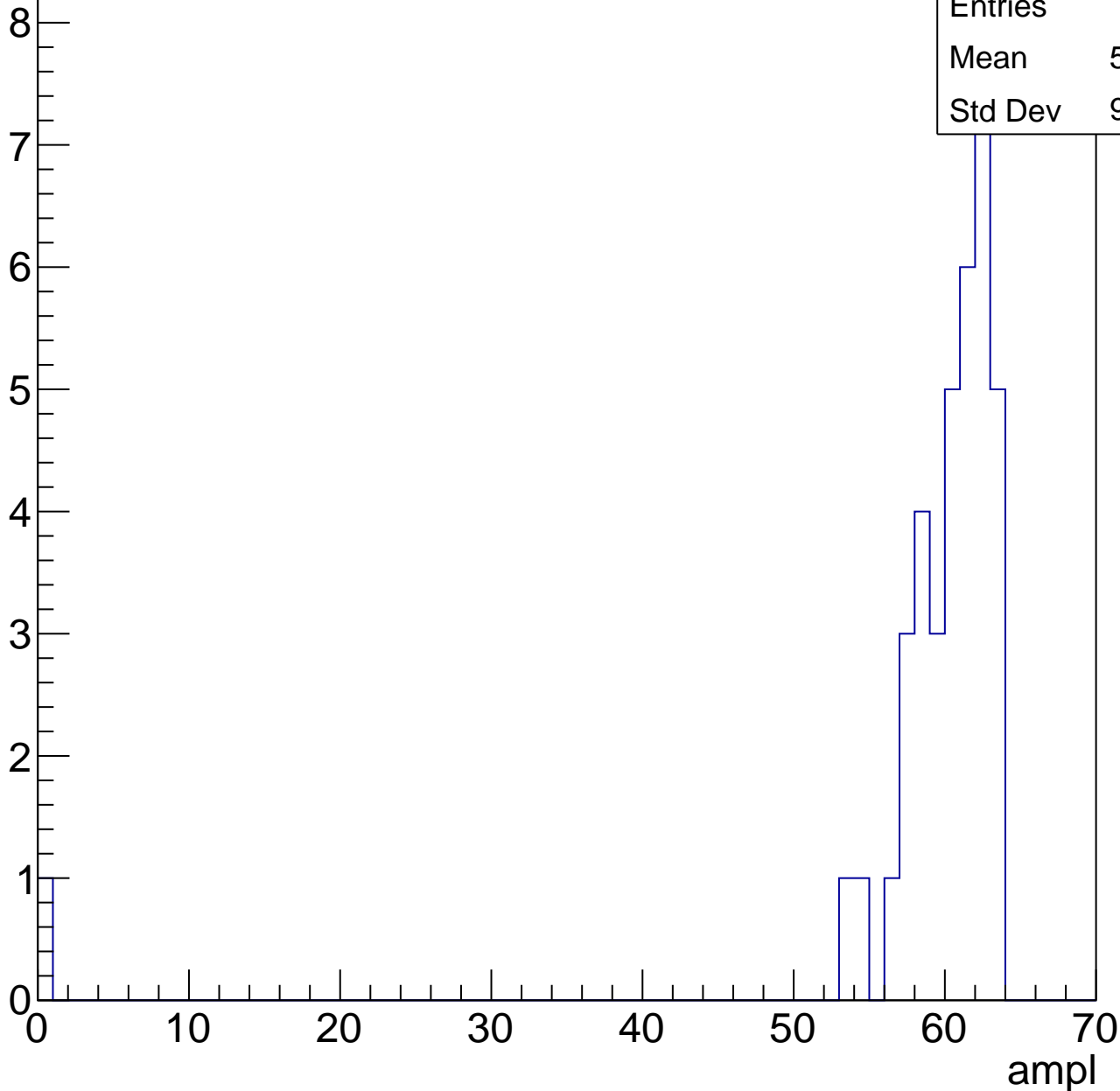


B1L102S, U26-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.42
Std Dev	9.914



B1L102S, U26-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch49, adc0

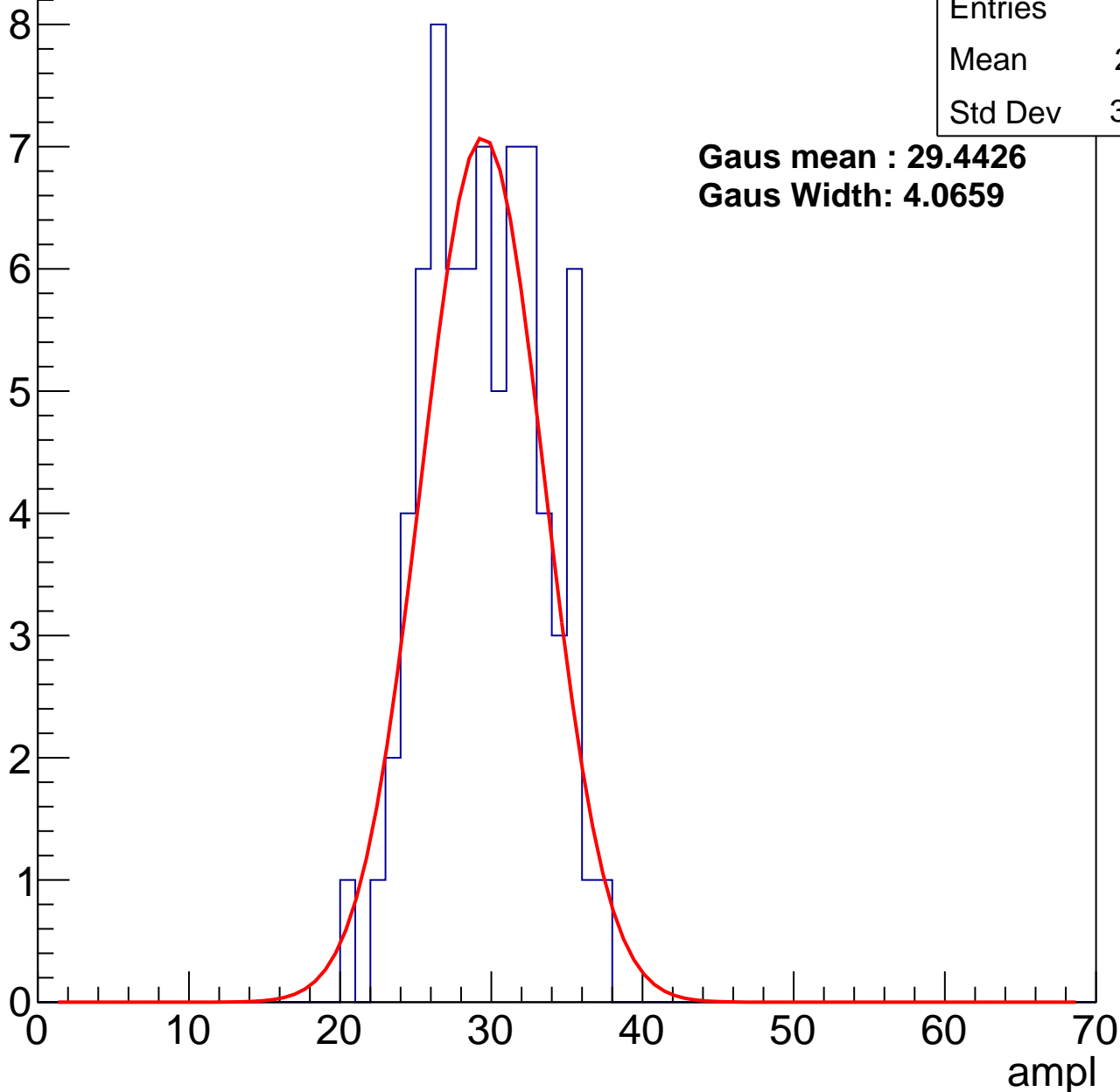
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.11
Std Dev	3.765

Gaus mean : 29.4426

Gaus Width: 4.0659



B1L102S, U26-ch49, adc1

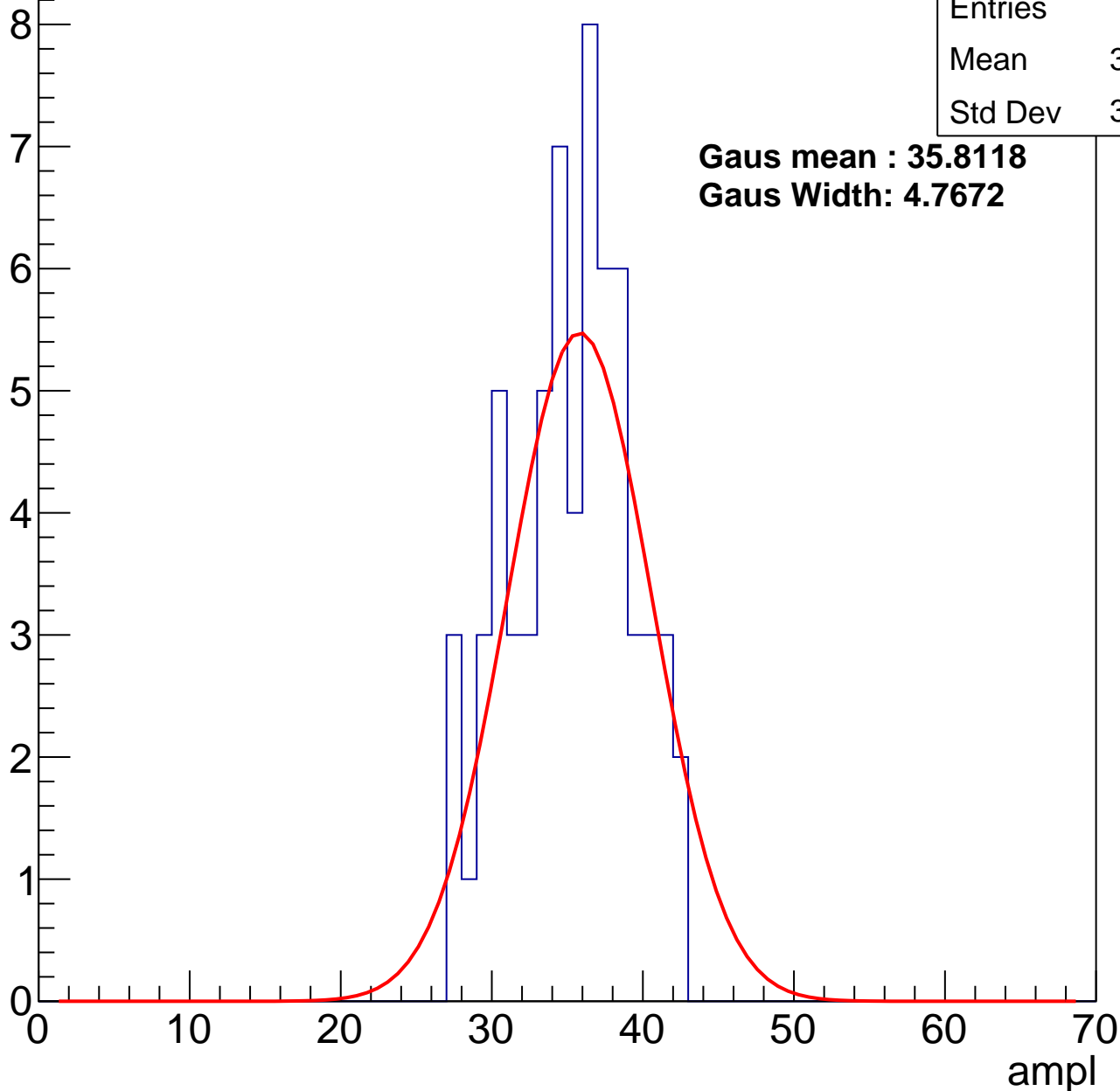
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.77
Std Dev	3.898

Gaus mean : 35.8118

Gaus Width: 4.7672



B1L102S, U26-ch49, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.09
Std Dev	3.179

Gaus mean : 42.5517

Gaus Width: 3.3237

10

8

6

4

2

0

0

10

20

30

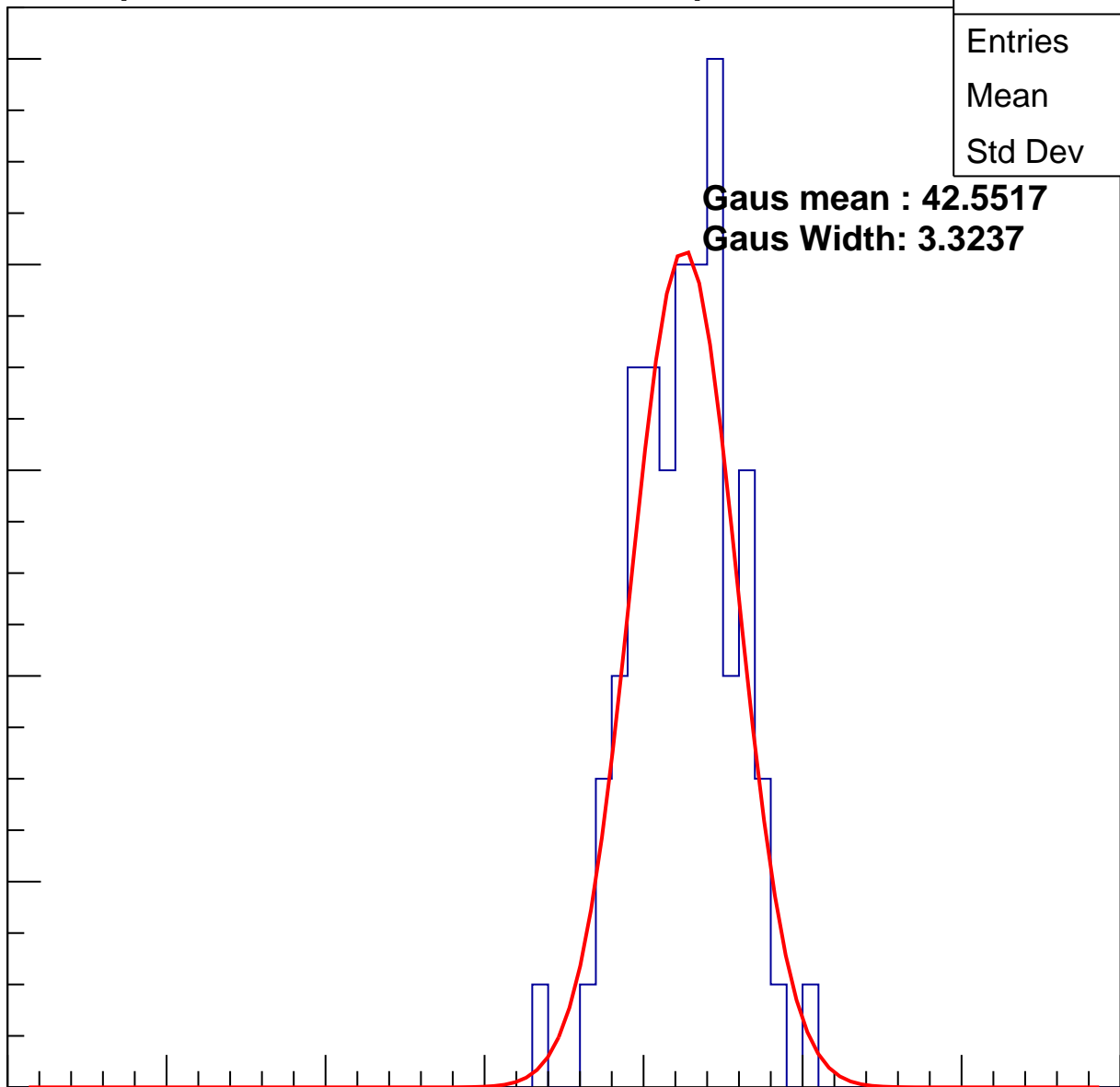
40

50

60

70

ampl



B1L102S, U26-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	49.33
Std Dev	3.46

Entry

10

8

6

4

2

0

0

10

20

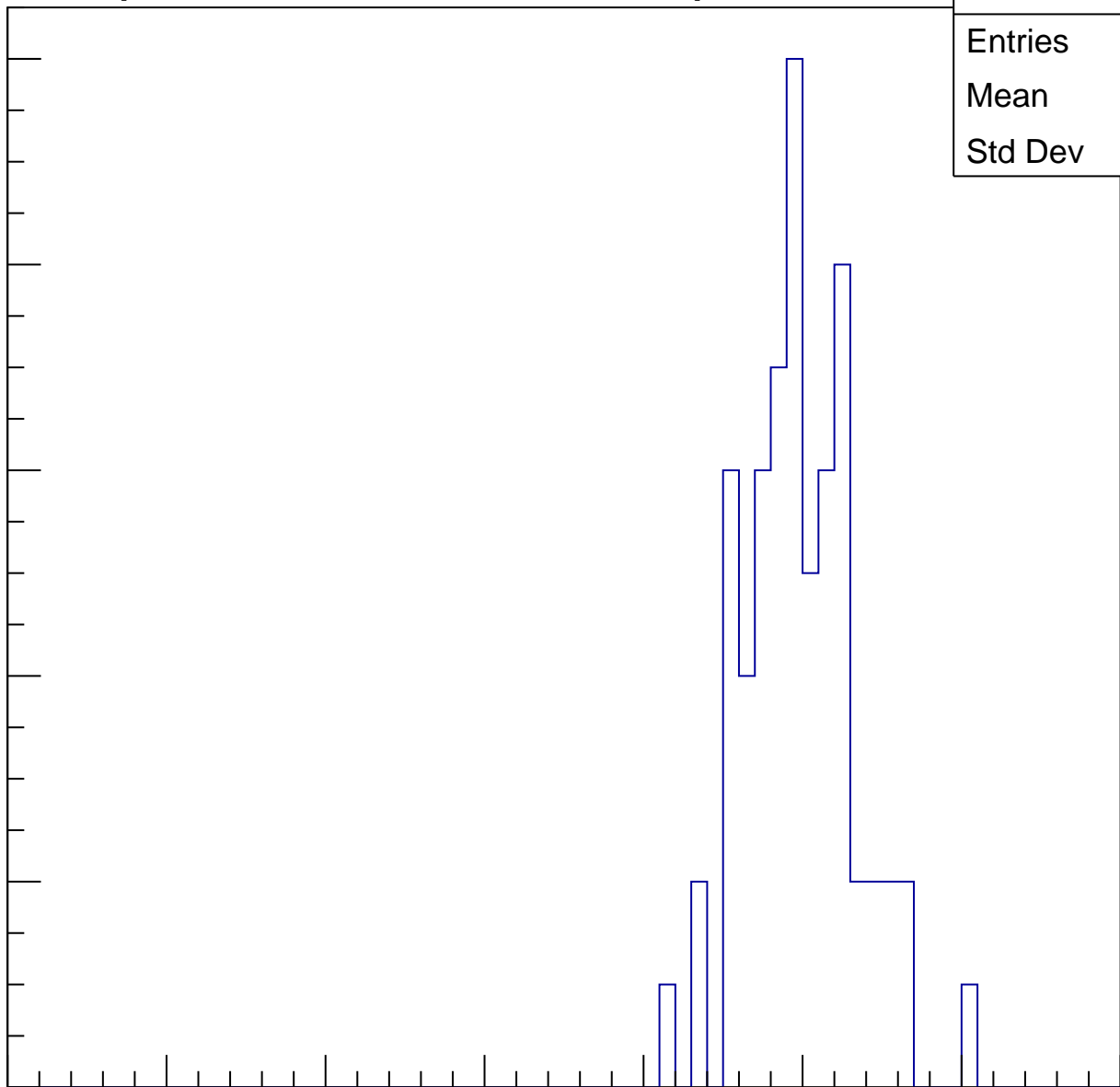
30

40

50

60

ampl

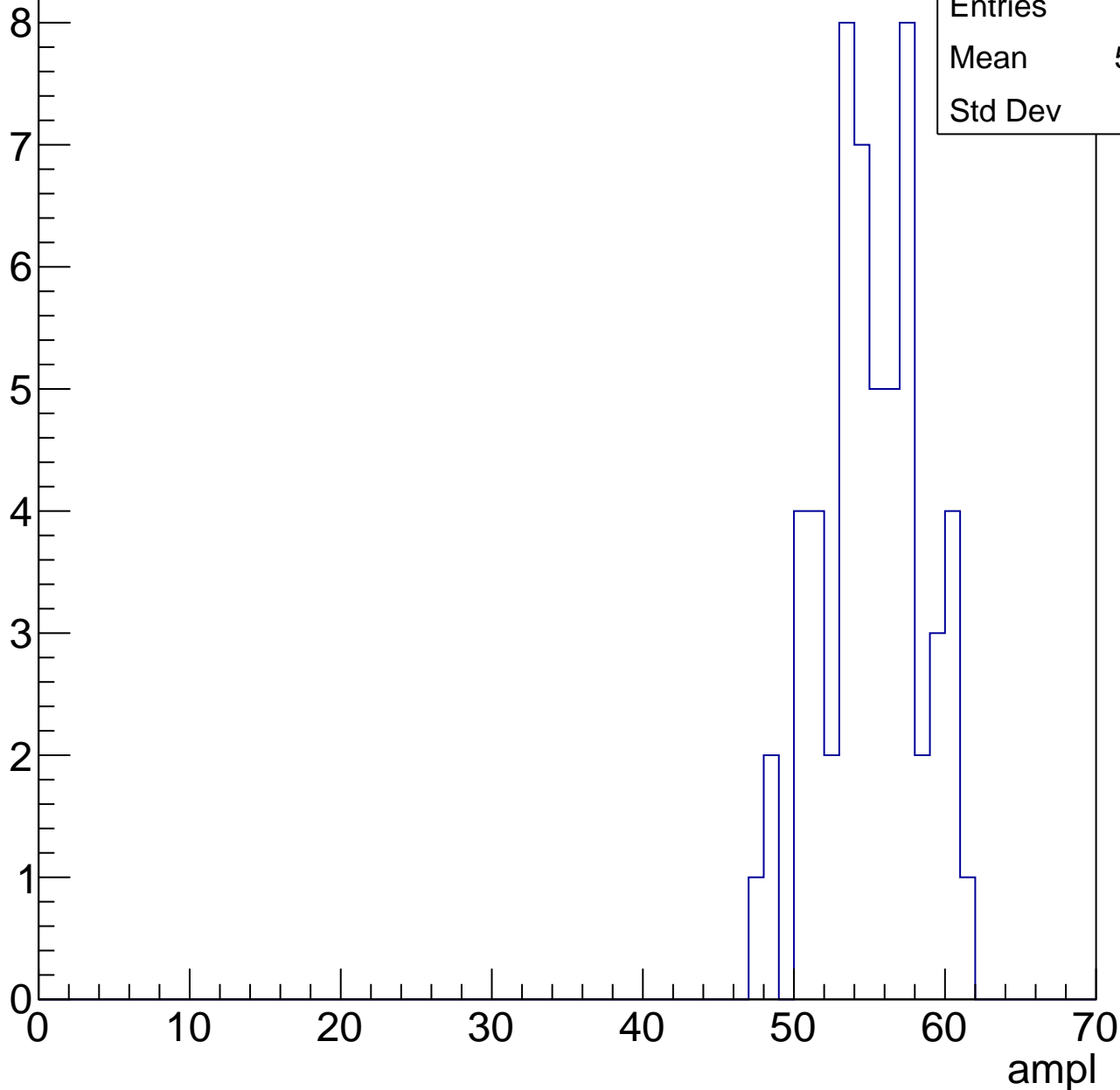


B1L102S, U26-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.61
Std Dev	3.31

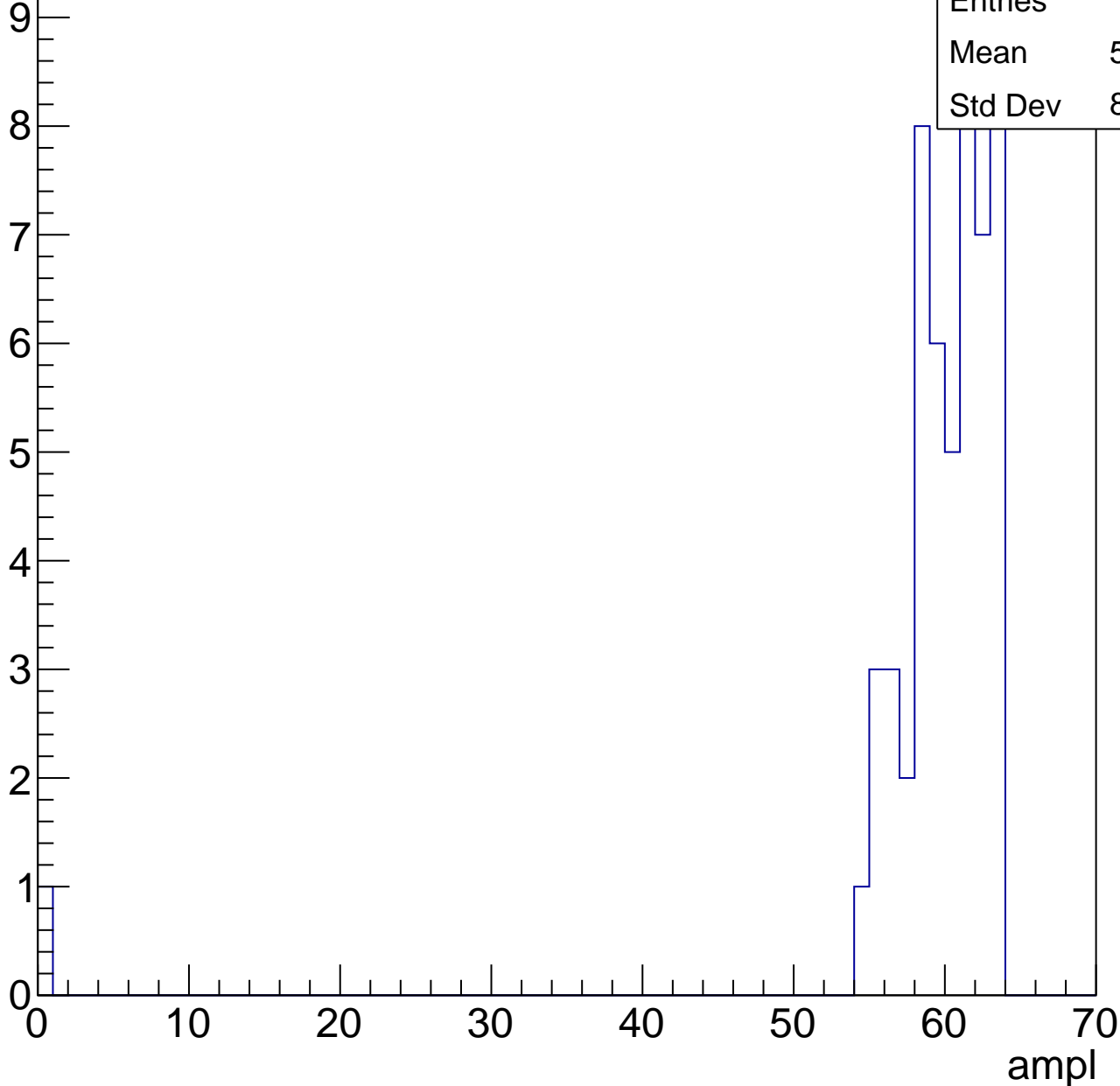


B1L102S, U26-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

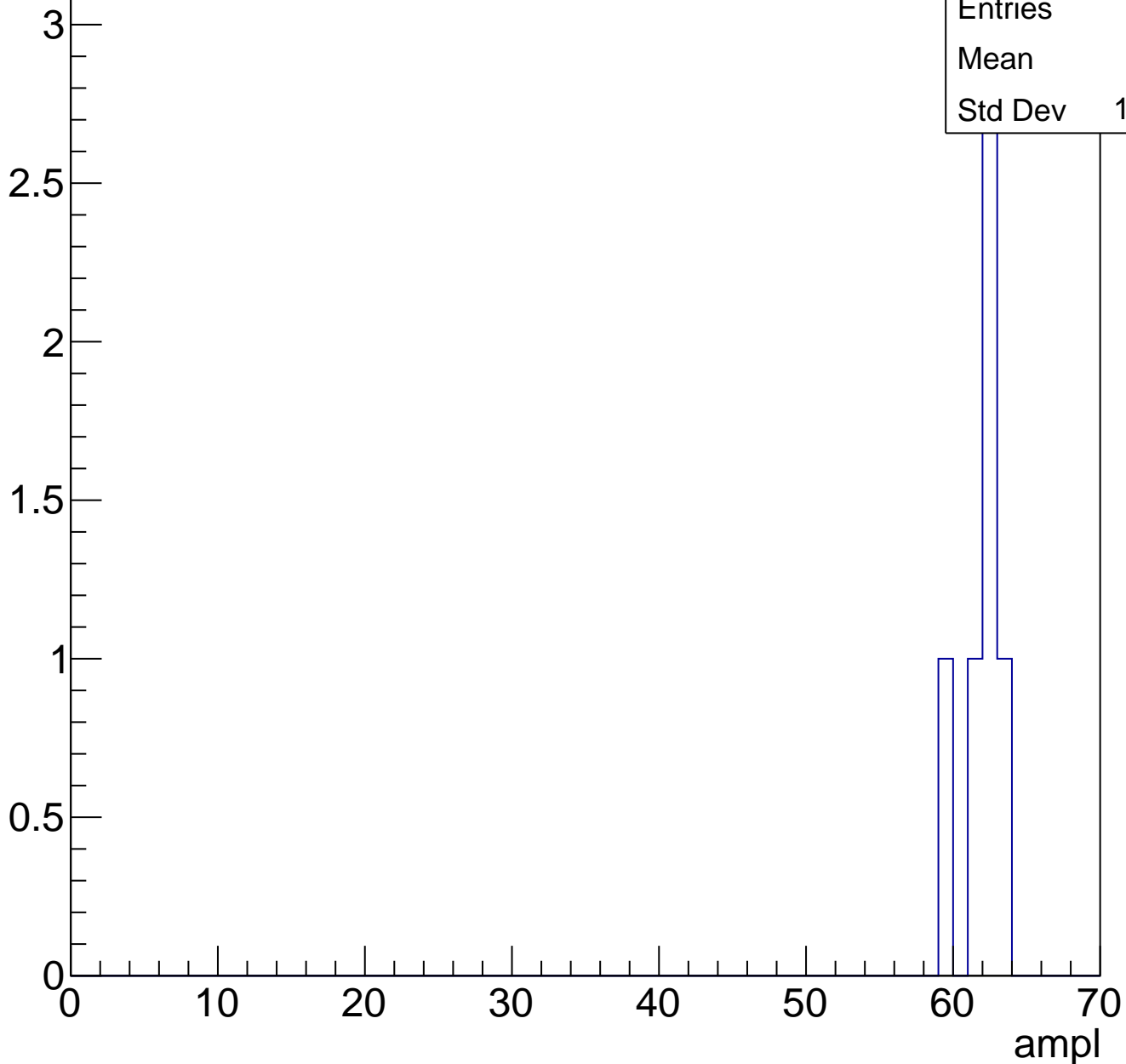
Entries	53
Mean	58.64
Std Dev	8.505



B1L102S, U26-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch50, adc0

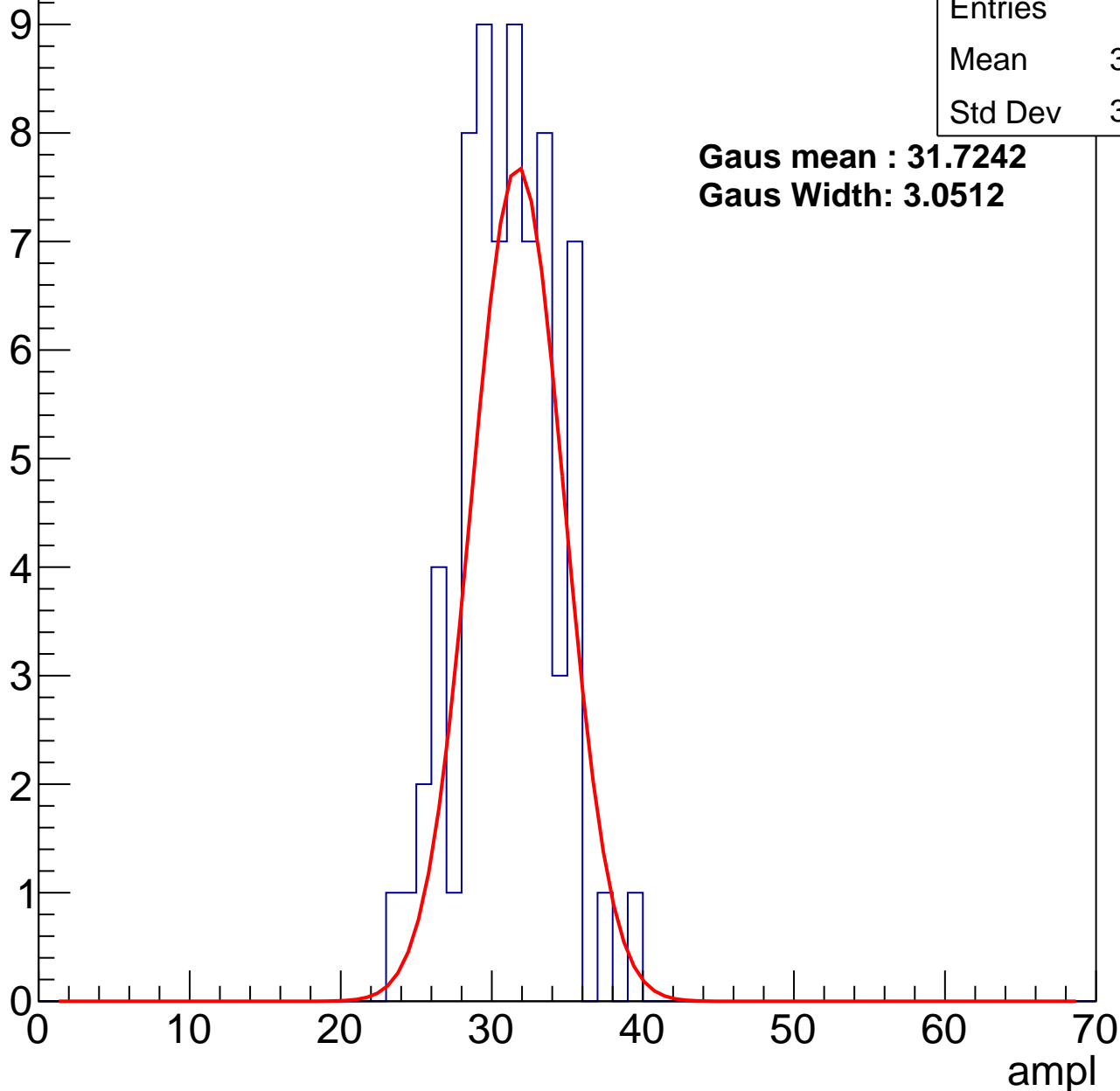
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.62
Std Dev	3.149

Gaus mean : 31.7242

Gaus Width: 3.0512



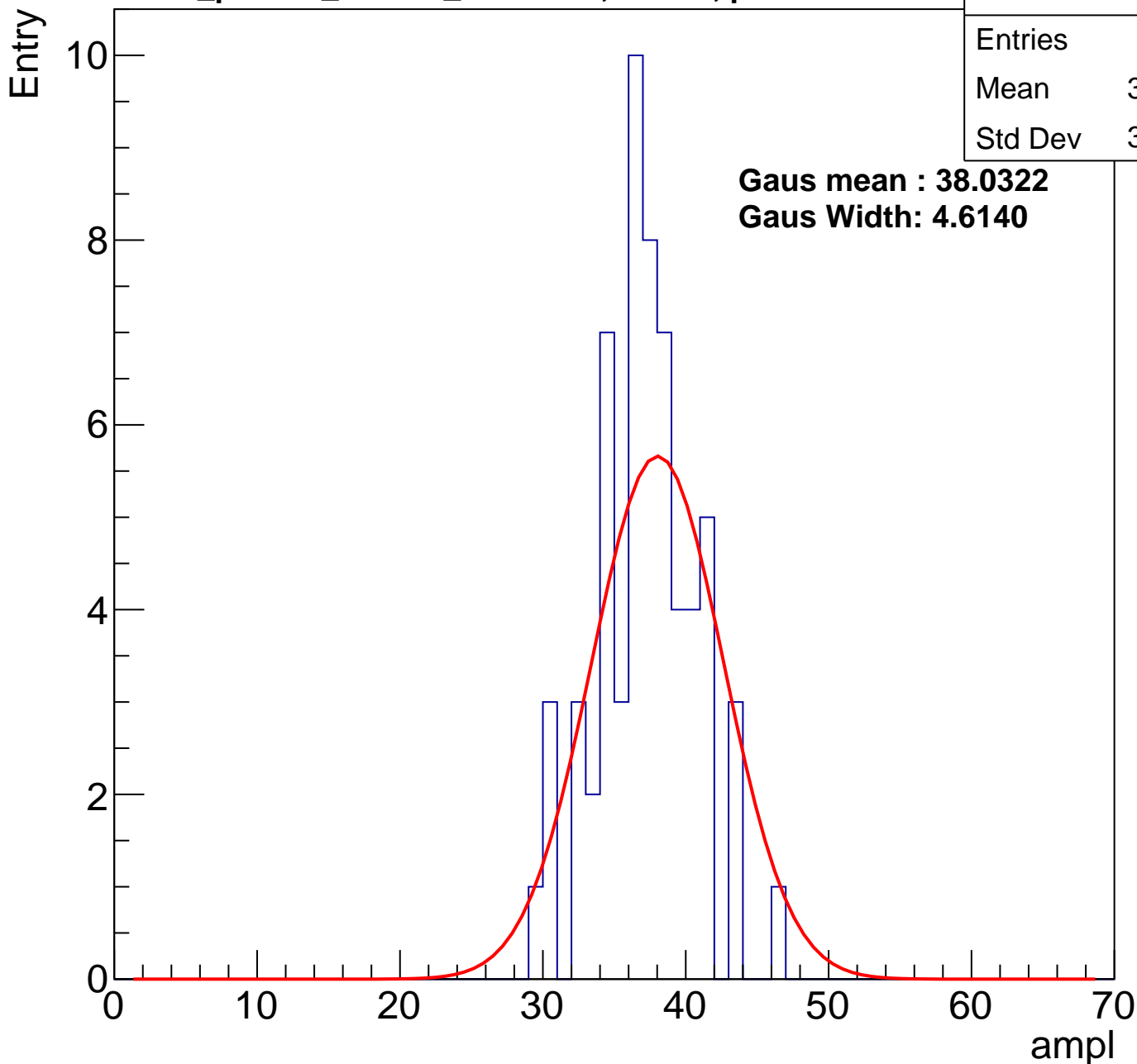
B1L102S, U26-ch50, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	61
Mean	36.75
Std Dev	3.453

Gaus mean : 38.0322

Gaus Width: 4.6140



B1L102S, U26-ch50, adc2

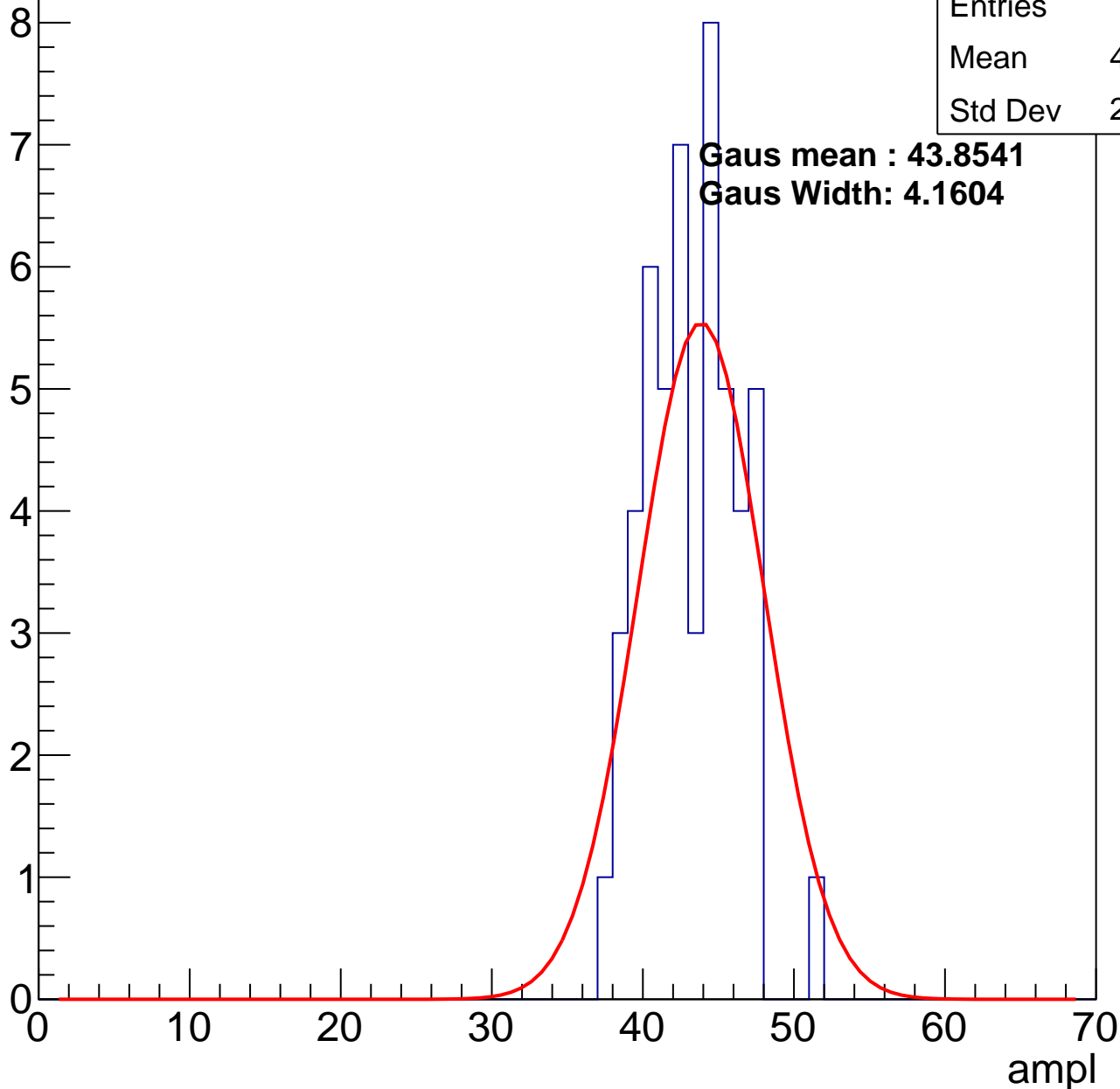
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.73
Std Dev	2.975

Gaus mean : 43.8541

Gaus Width: 4.1604



B1L102S, U26-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

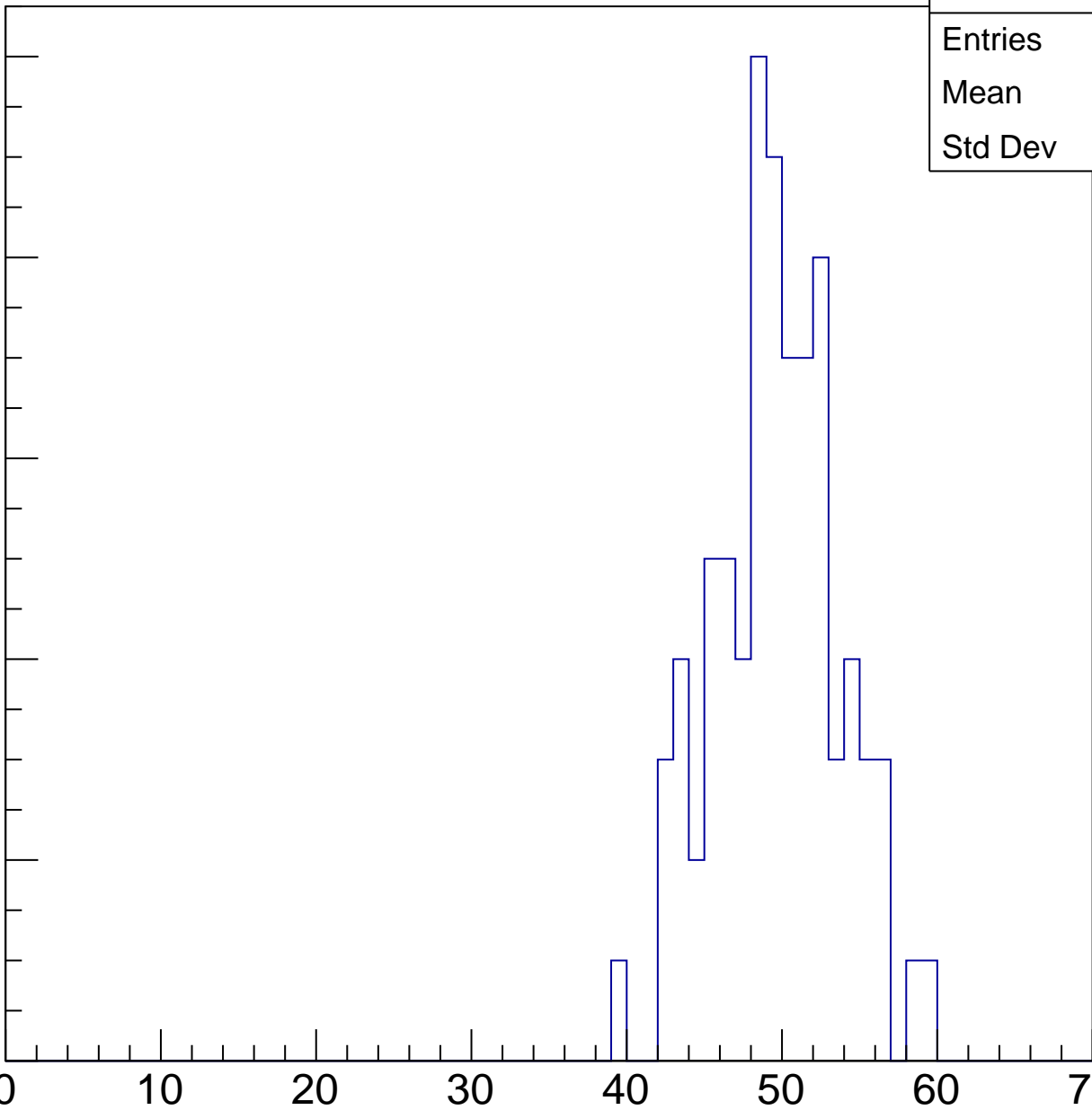
Entries	80
Mean	49.21
Std Dev	3.999

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

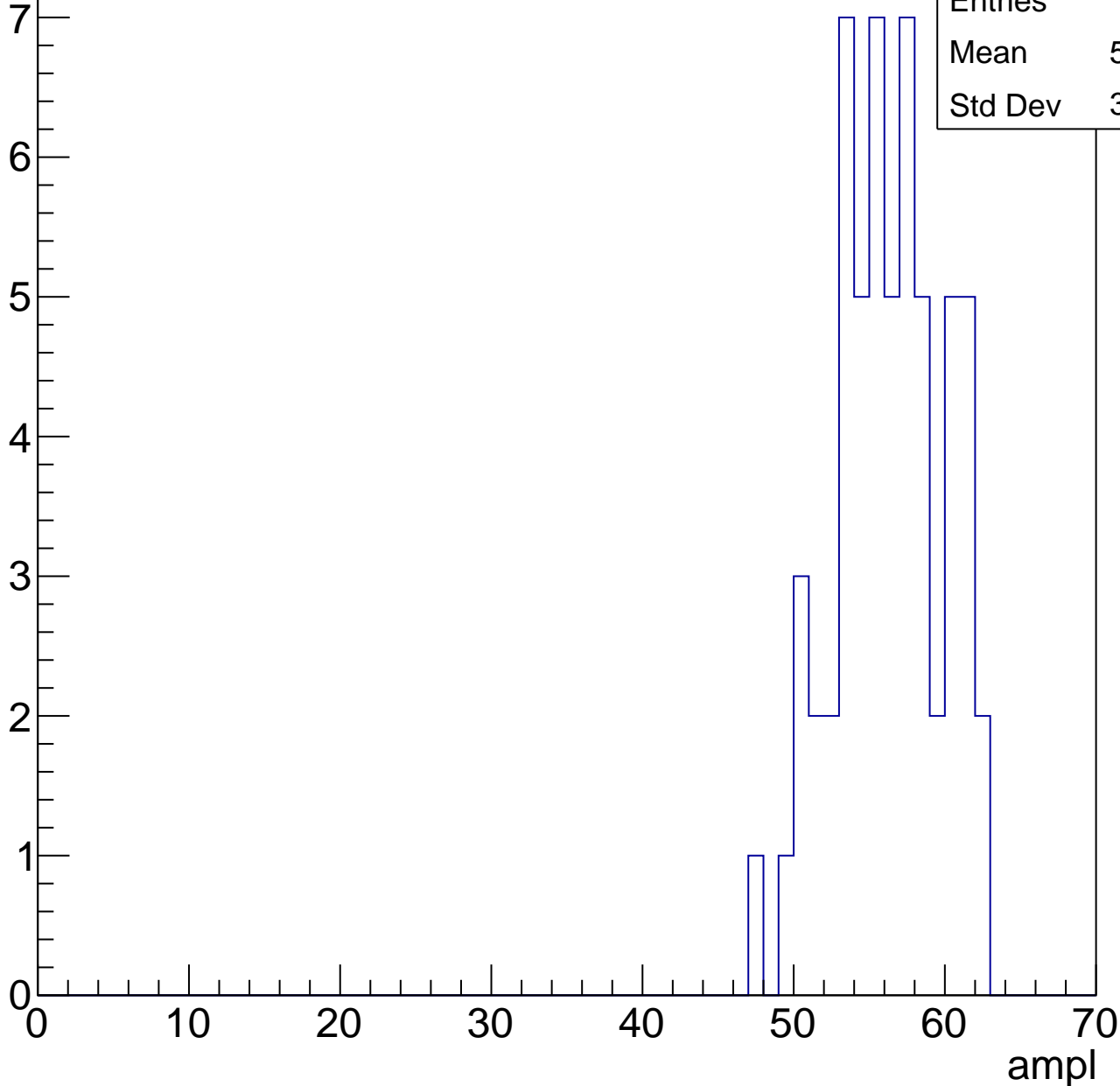


B1L102S, U26-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.83
Std Dev	3.518

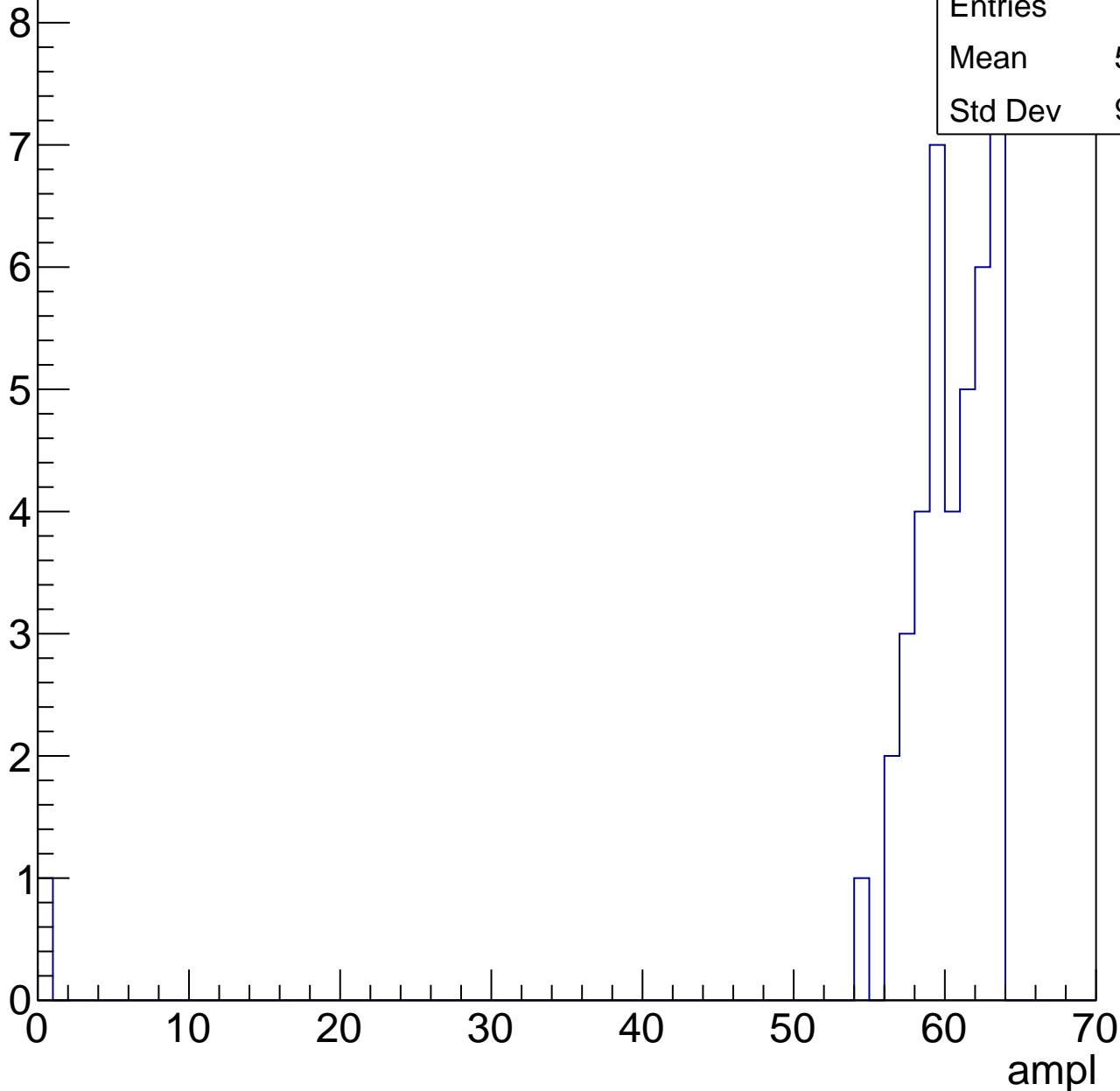


B1L102S, U26-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.61
Std Dev	9.551



B1L102S, U26-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch51, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	29.01
Std Dev	6.81

Gaus mean : 30.2732

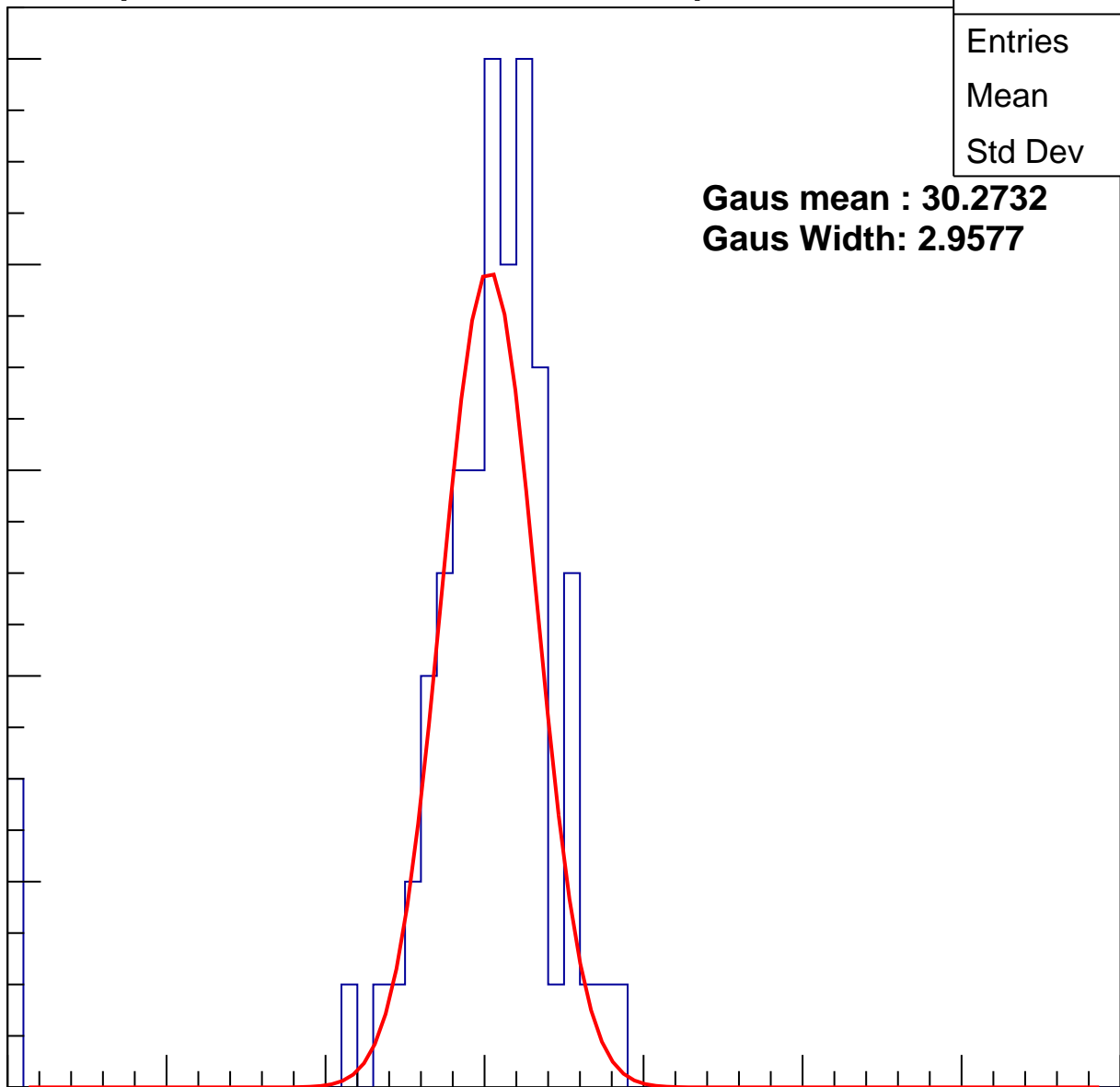
Gaus Width: 2.9577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch51, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	37.34
Std Dev	3.644

Gaus mean : 37.9830

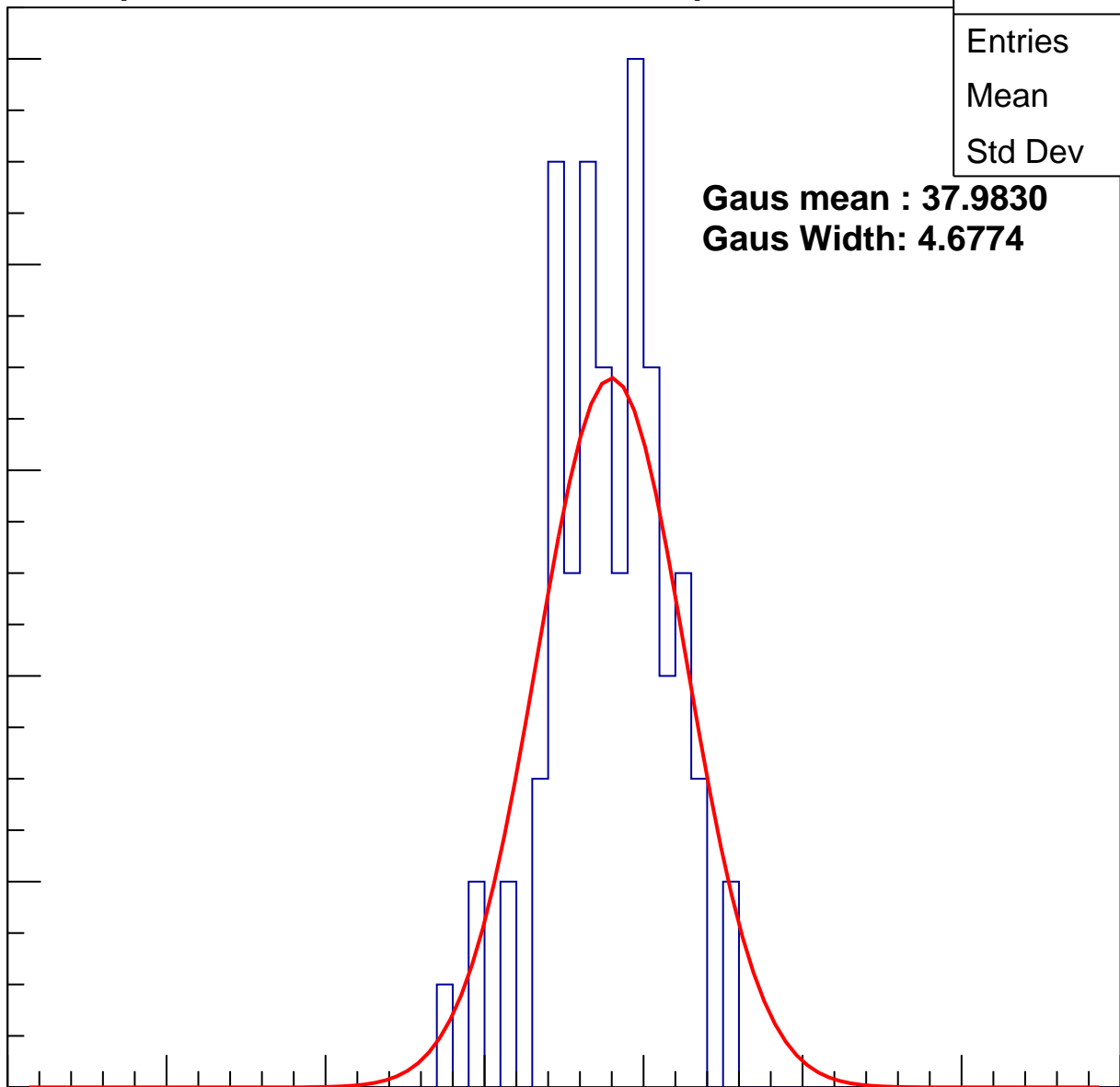
Gaus Width: 4.6774

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch51, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.27
Std Dev	3.445

Gaus mean : 44.1590

Gaus Width: 3.7127

10

8

6

4

2

0

0

10

20

30

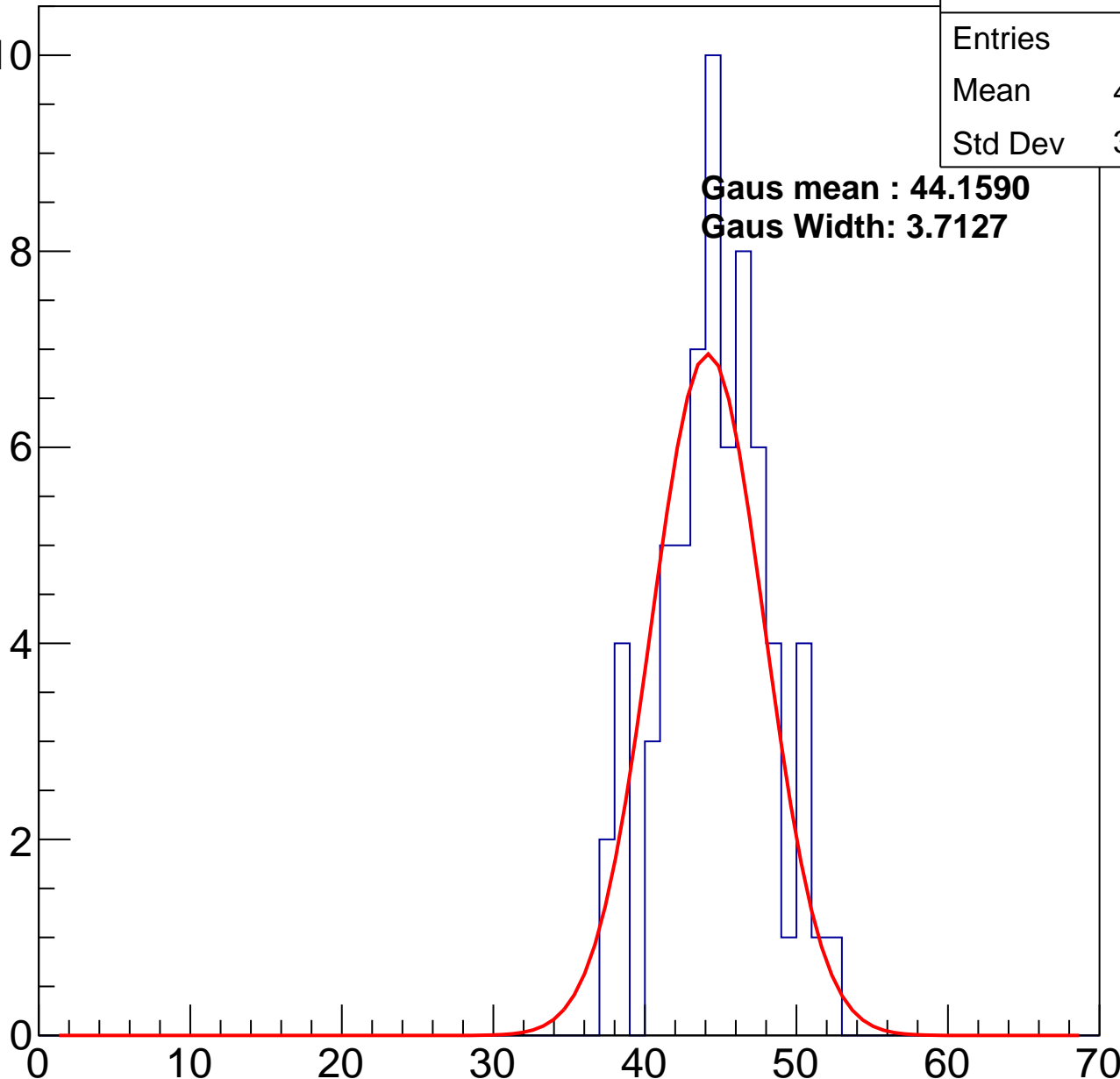
40

50

60

70

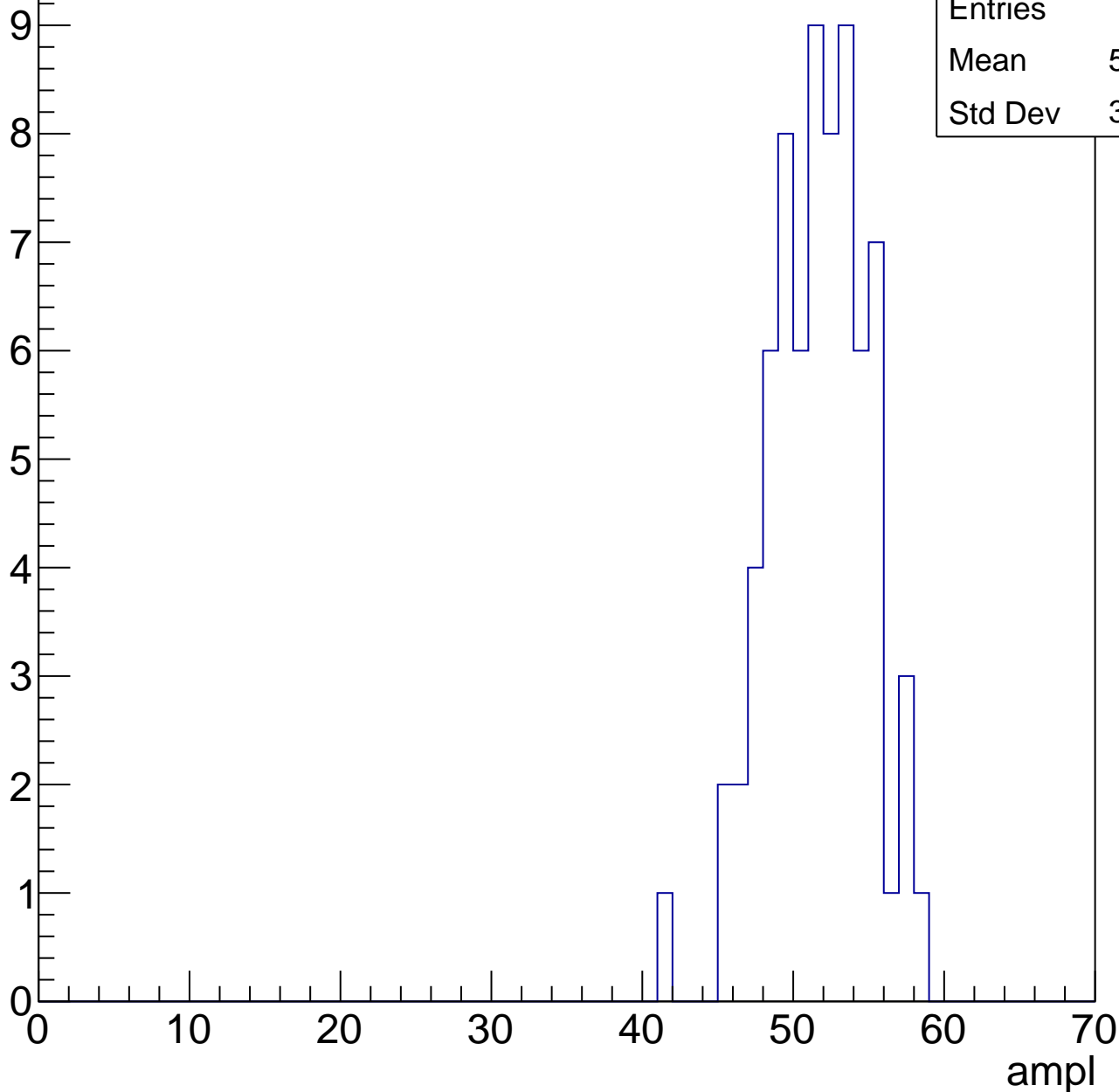
ampl



B1L102S, U26-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

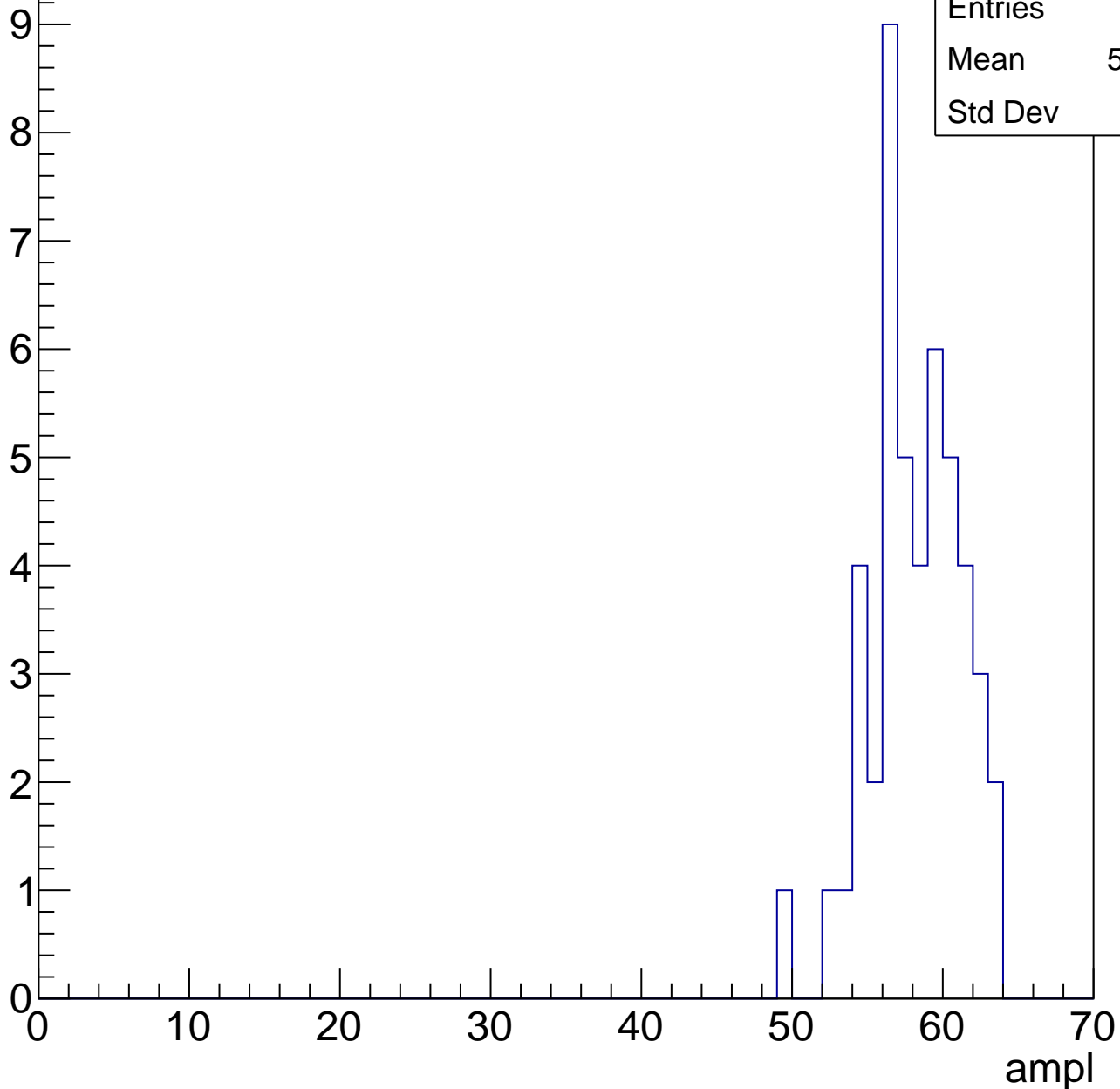
Entry



B1L102S, U26-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



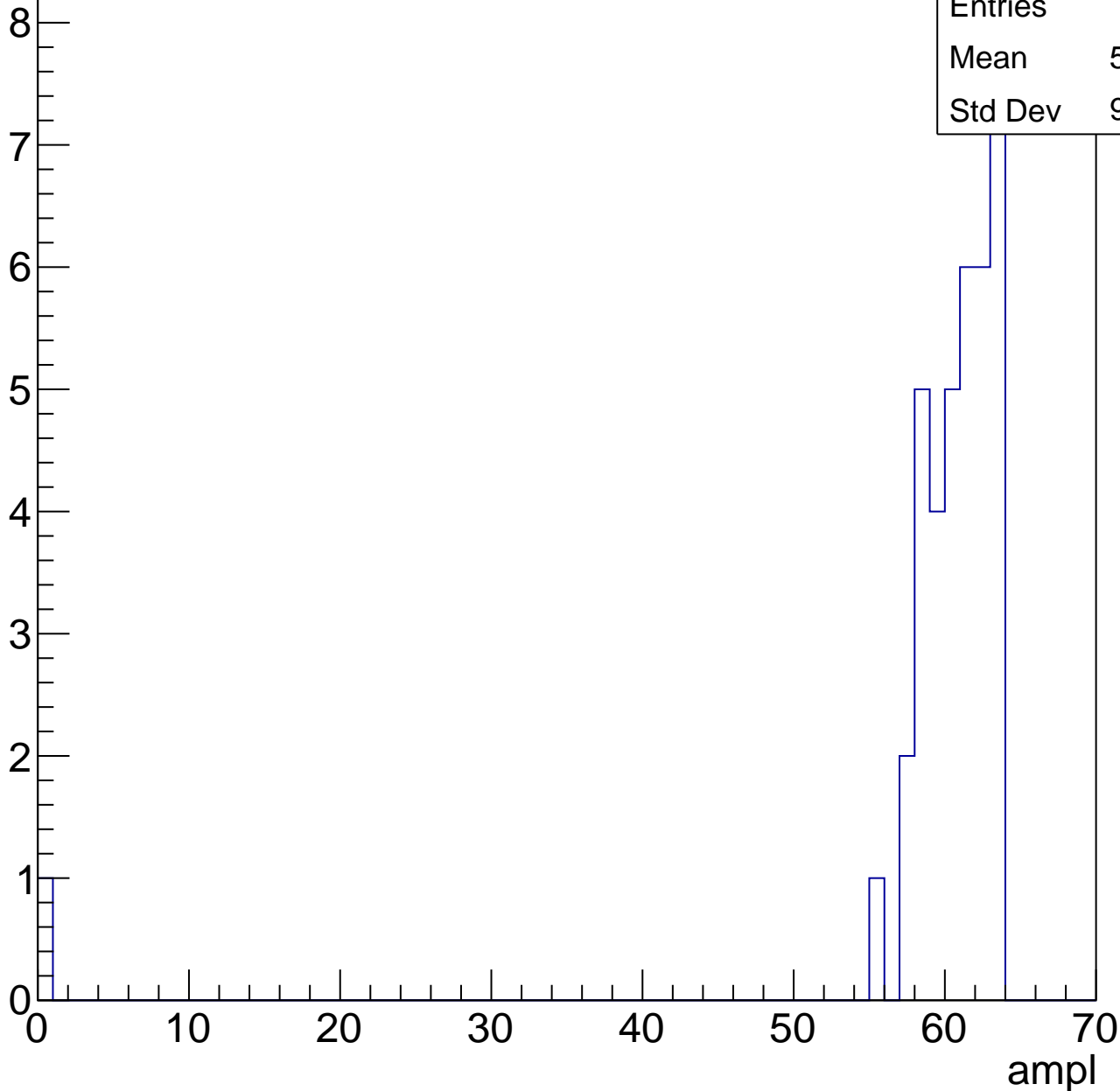
Entries	47
Mean	57.68
Std Dev	2.99

B1L102S, U26-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.87
Std Dev	9.895



B1L102S, U26-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch52, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	29.53
Std Dev	4.662

Gaus mean : 30.2454

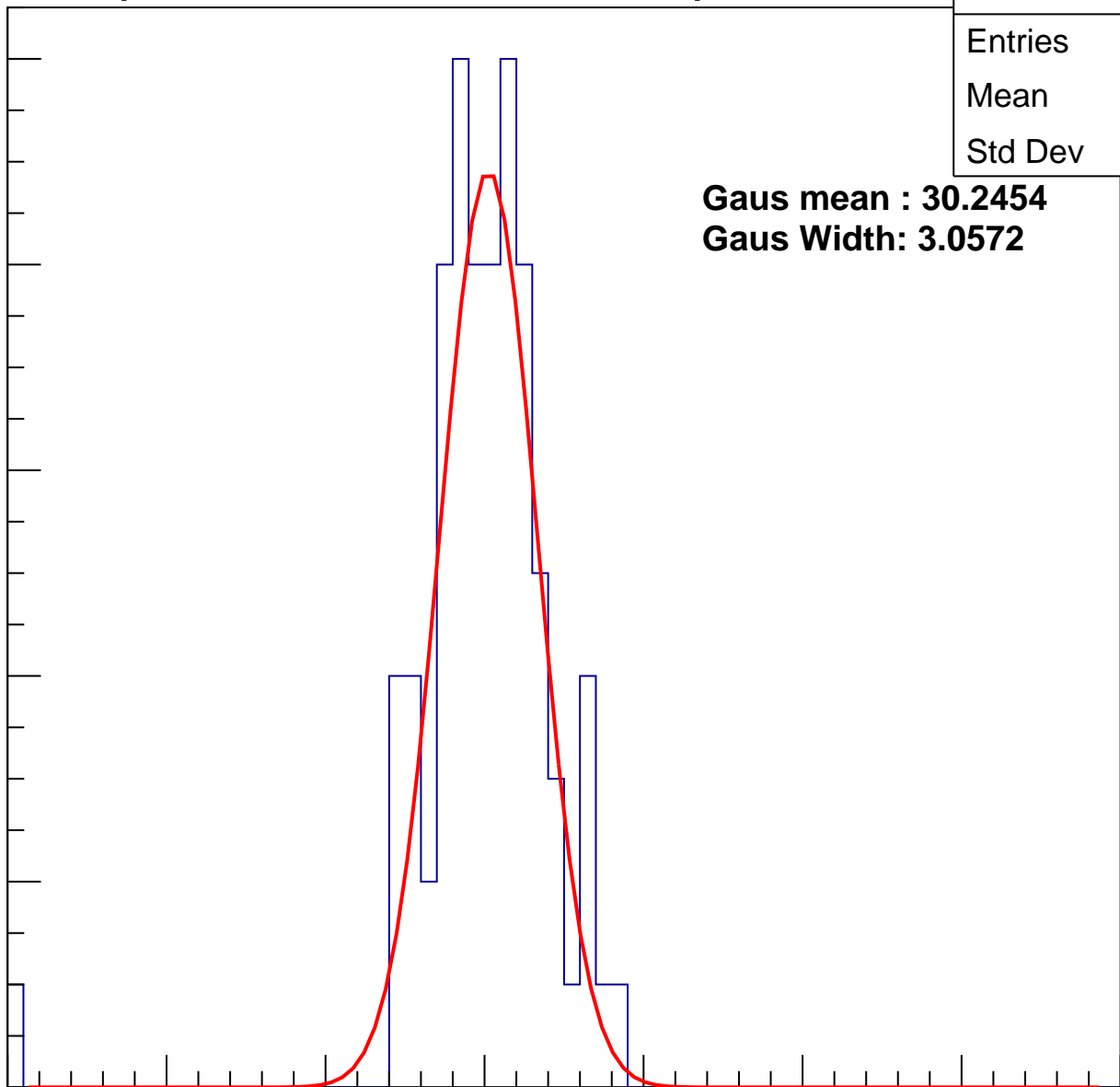
Gaus Width: 3.0572

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch52, adc1

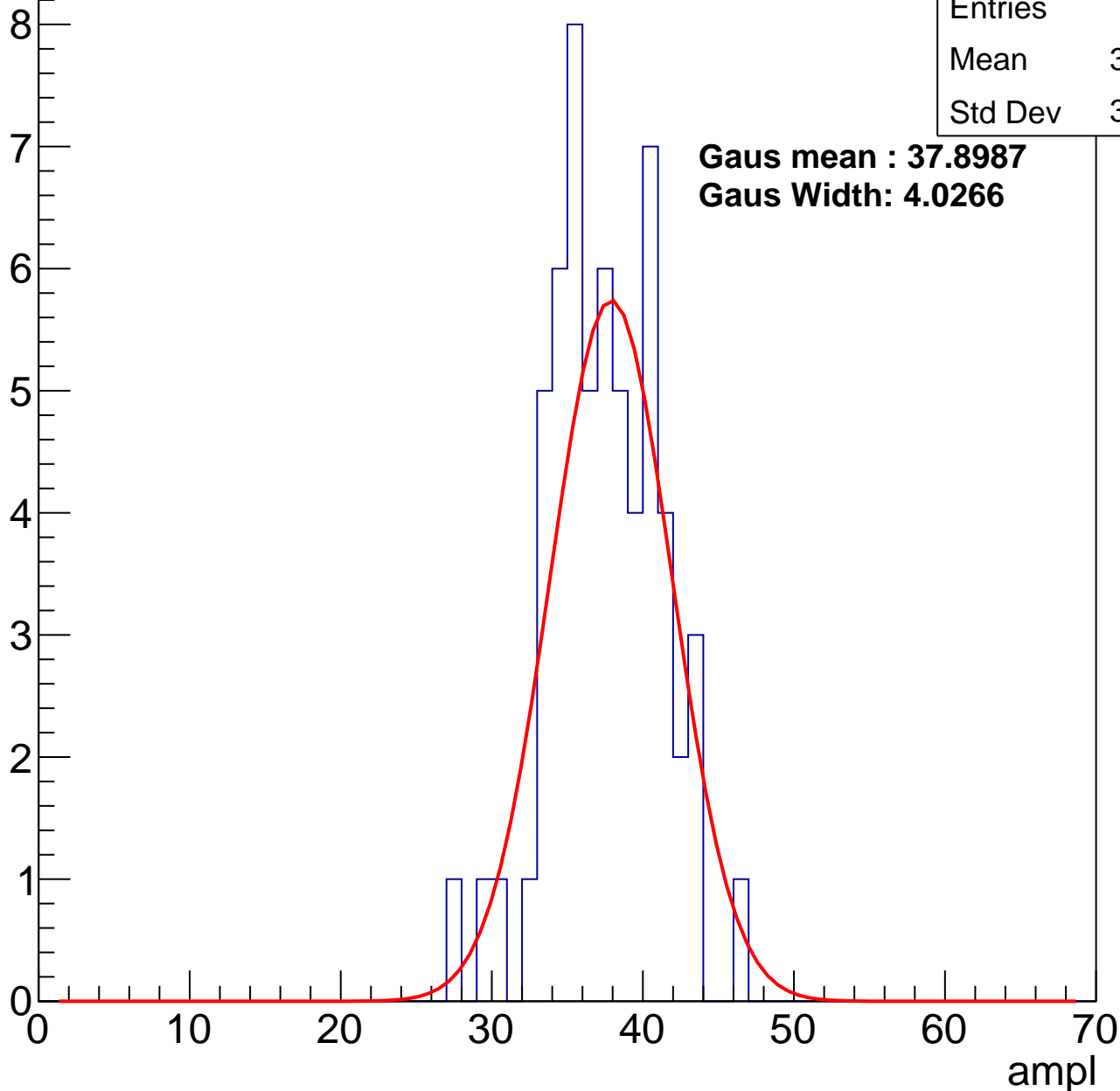
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.97
Std Dev	3.642

Gaus mean : 37.8987

Gaus Width: 4.0266



B1L102S, U26-ch52, adc2

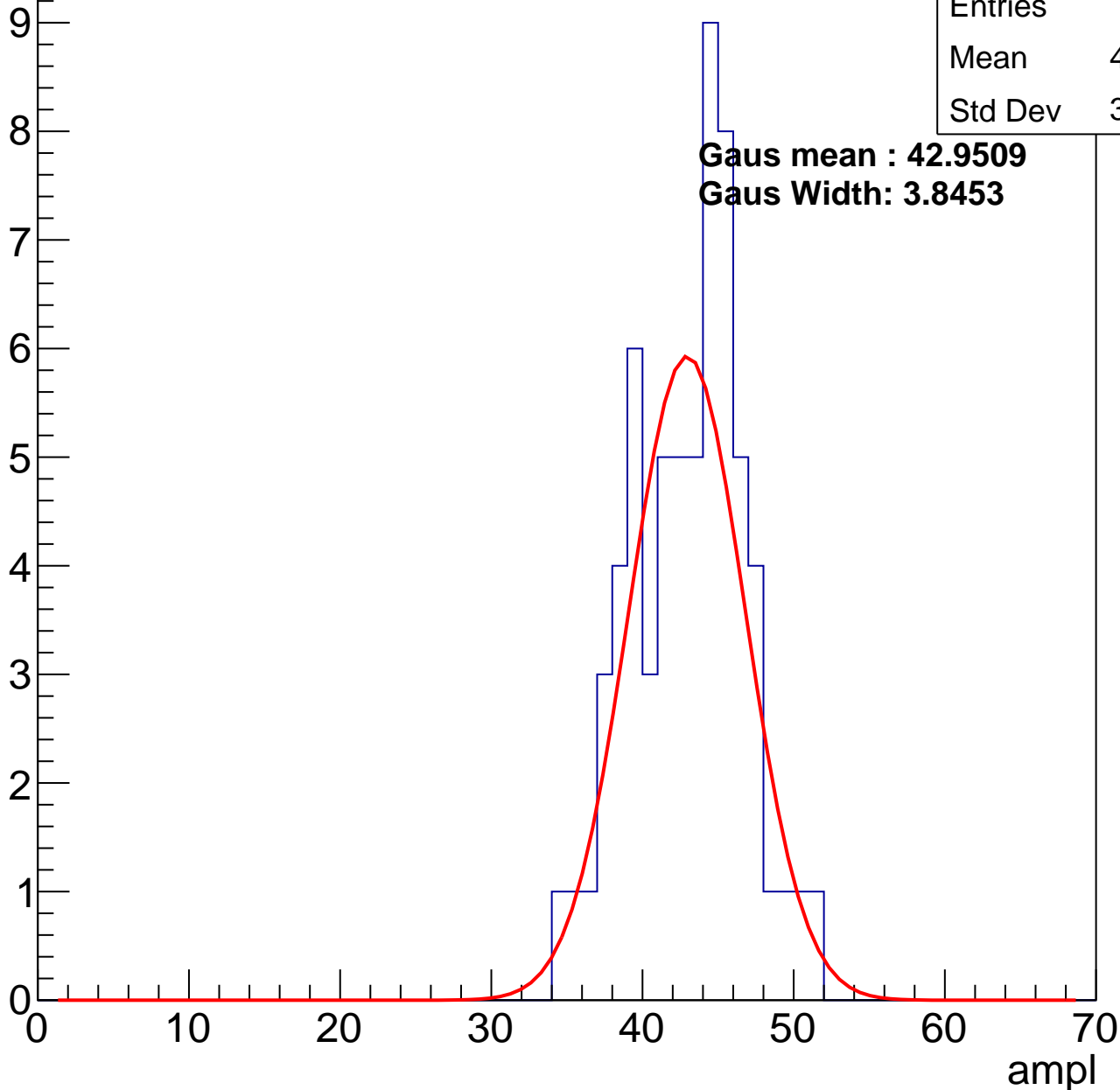
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.56
Std Dev	3.669

Gaus mean : 42.9509

Gaus Width: 3.8453

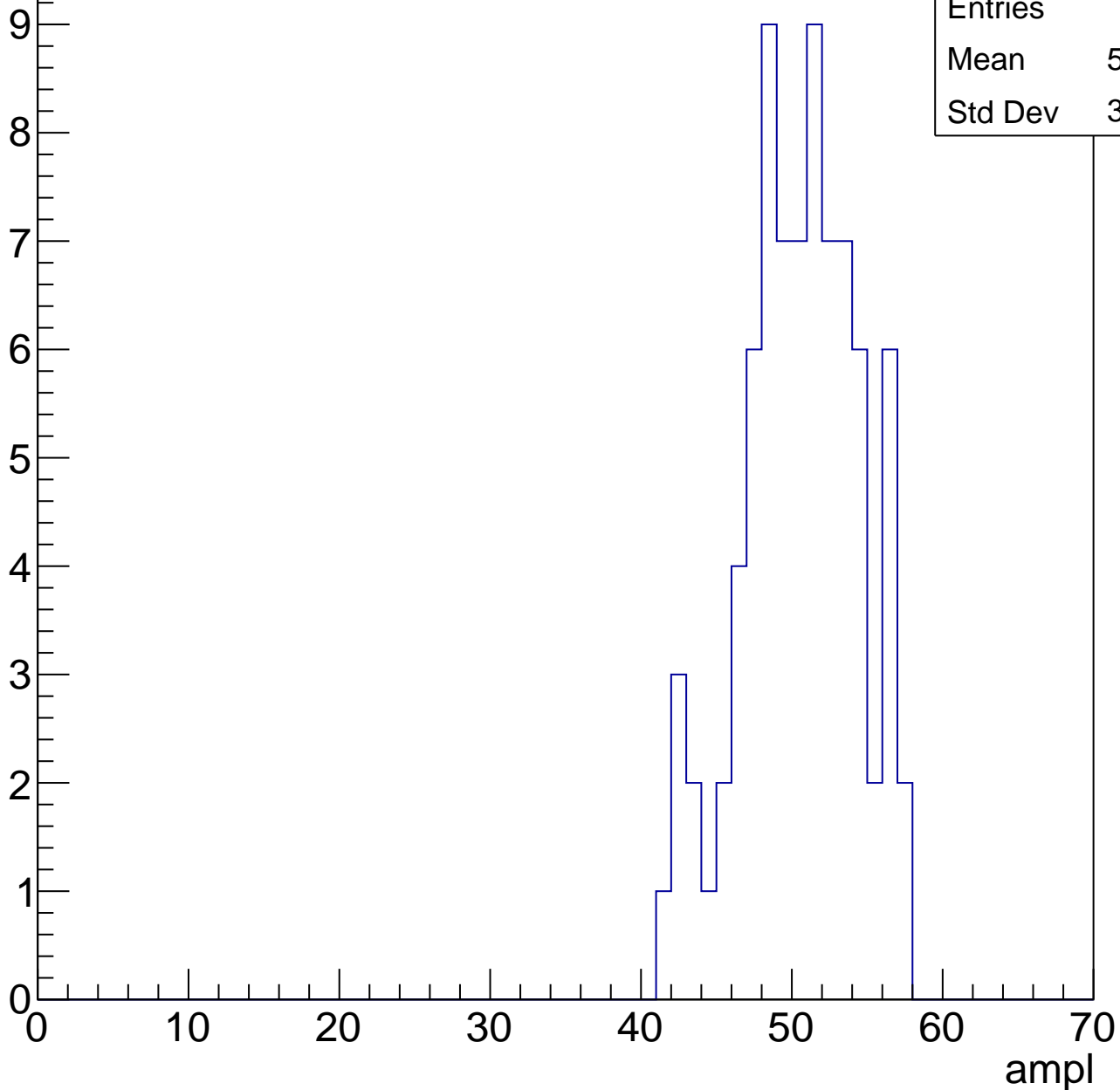


B1L102S, U26-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	50.07
Std Dev	3.813

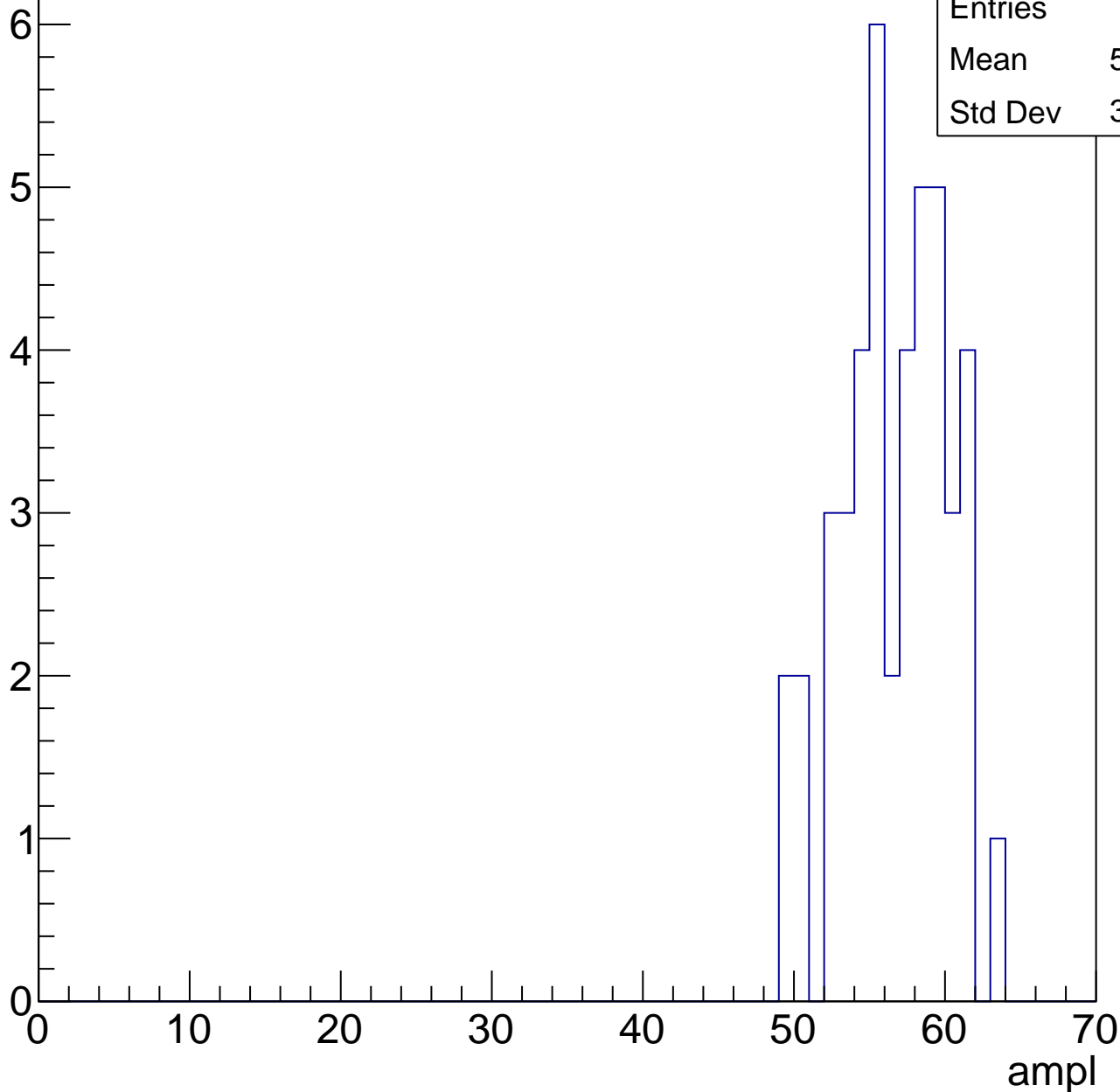


B1L102S, U26-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.16
Std Dev	3.477

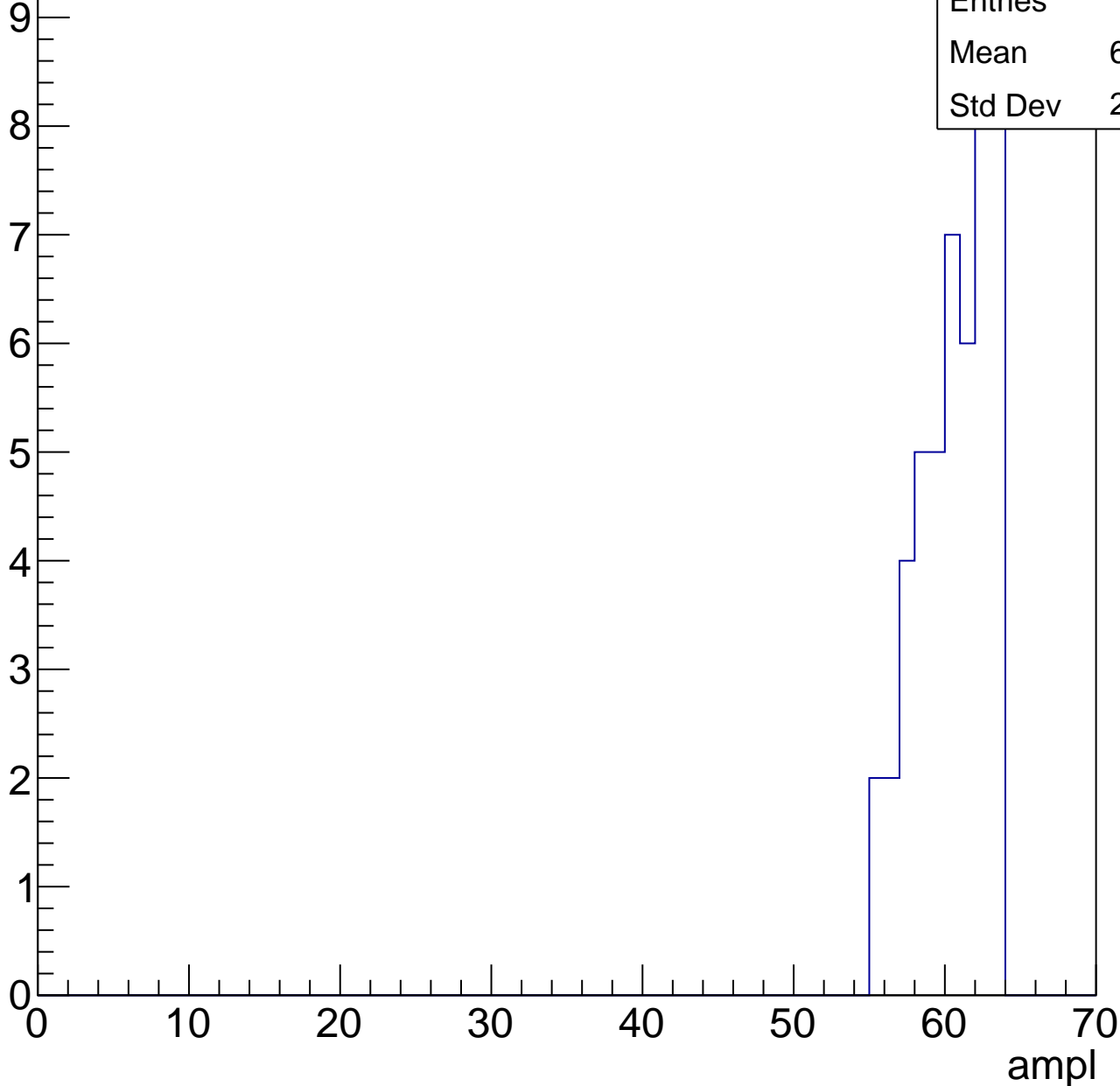


B1L102S, U26-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	60.12
Std Dev	2.327



B1L102S, U26-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U26-ch53, adc0

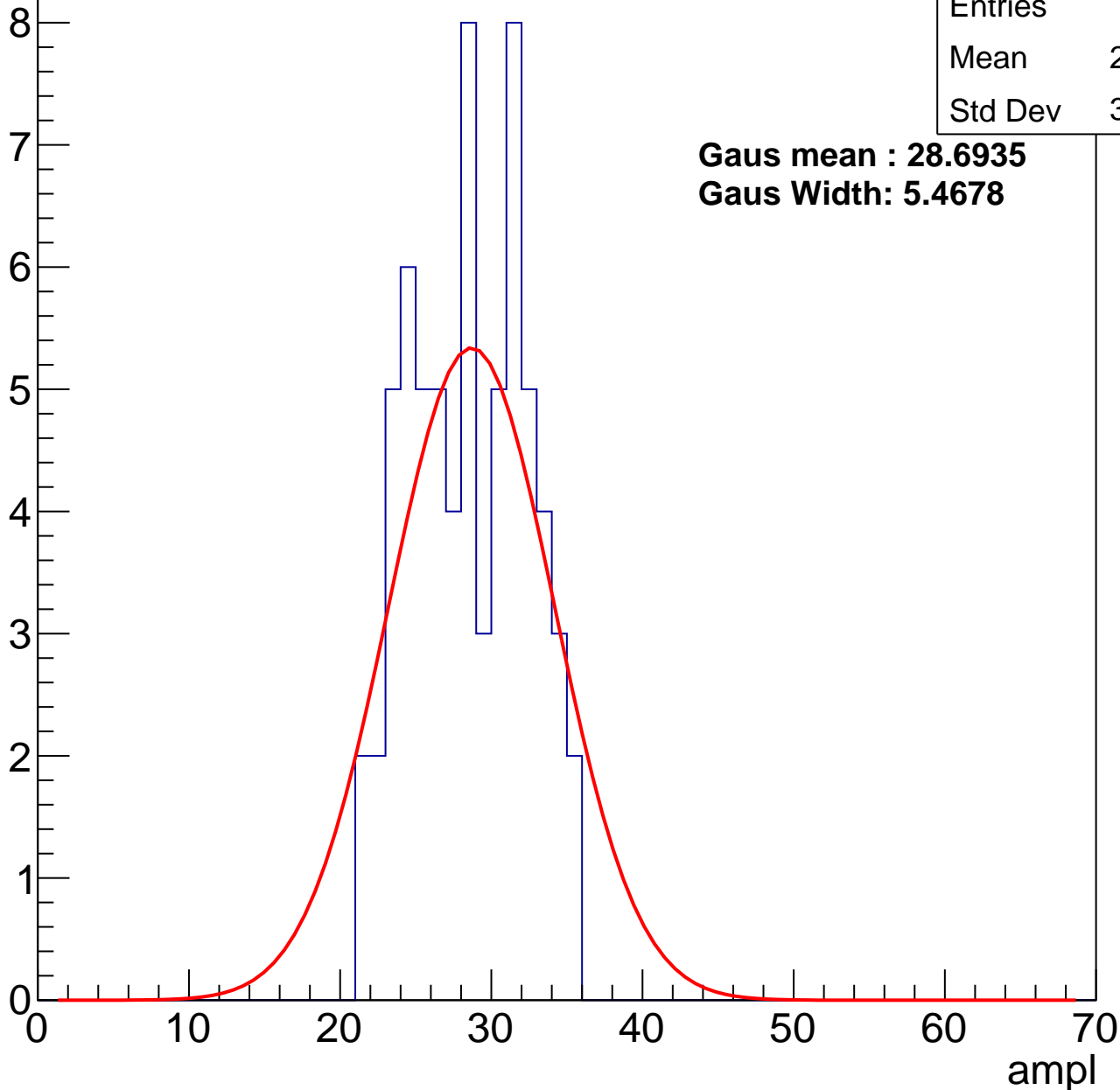
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.07
Std Dev	3.747

Gaus mean : 28.6935

Gaus Width: 5.4678



B1L102S, U26-ch53, adc1

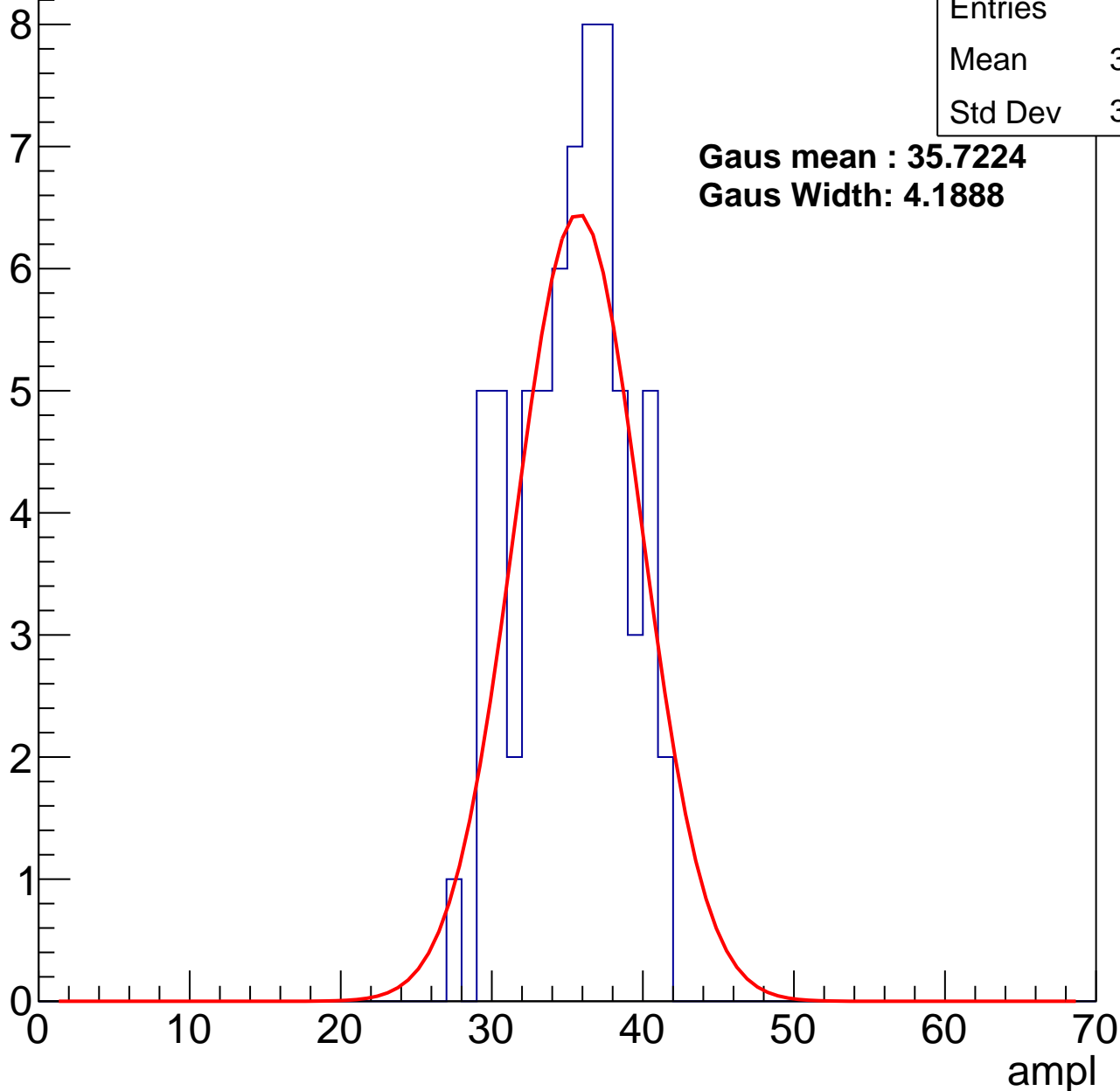
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.79
Std Dev	3.453

Gaus mean : 35.7224

Gaus Width: 4.1888



B1L102S, U26-ch53, adc2

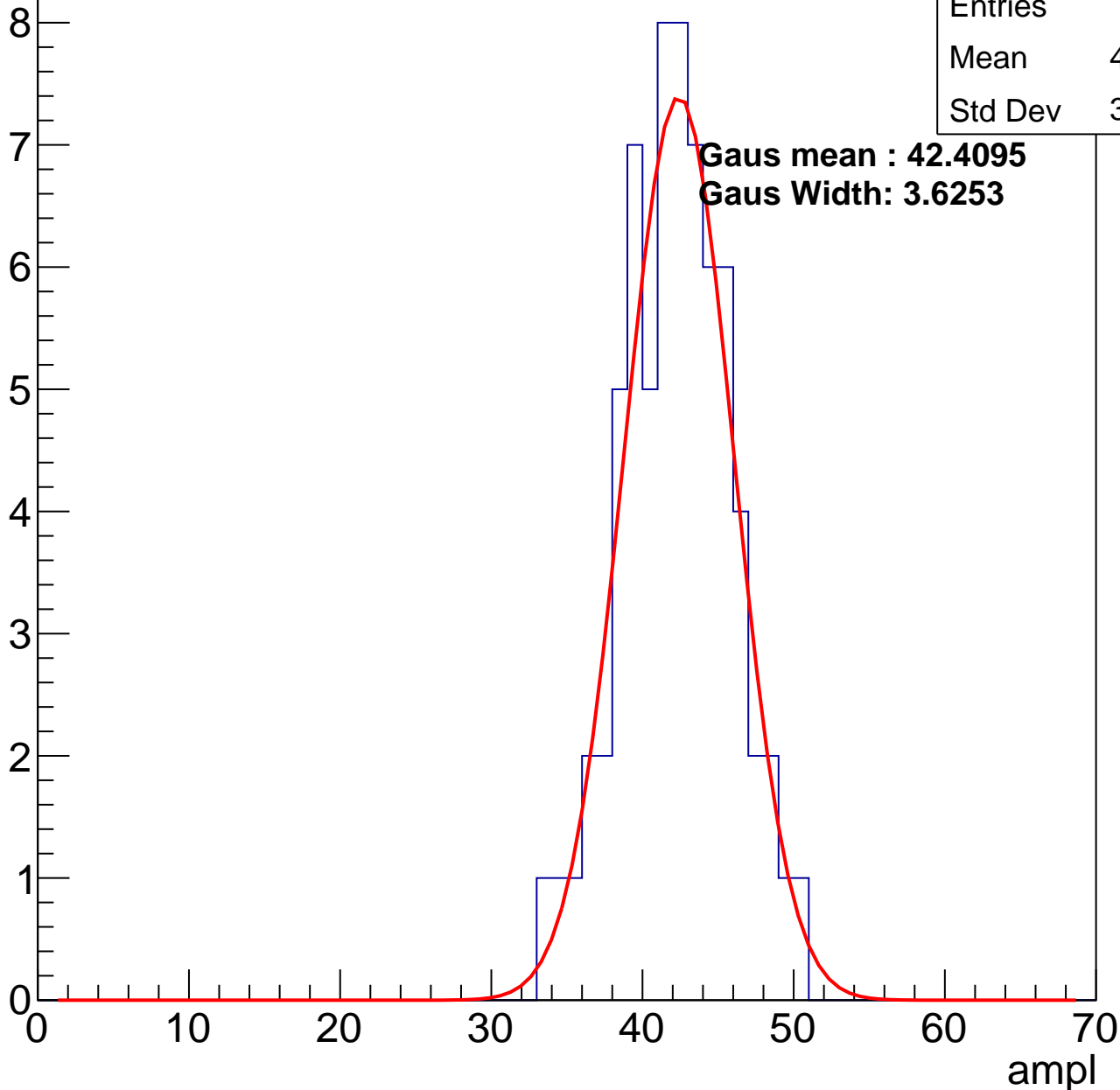
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.78
Std Dev	3.546

Gaus mean : 42.4095

Gaus Width: 3.6253

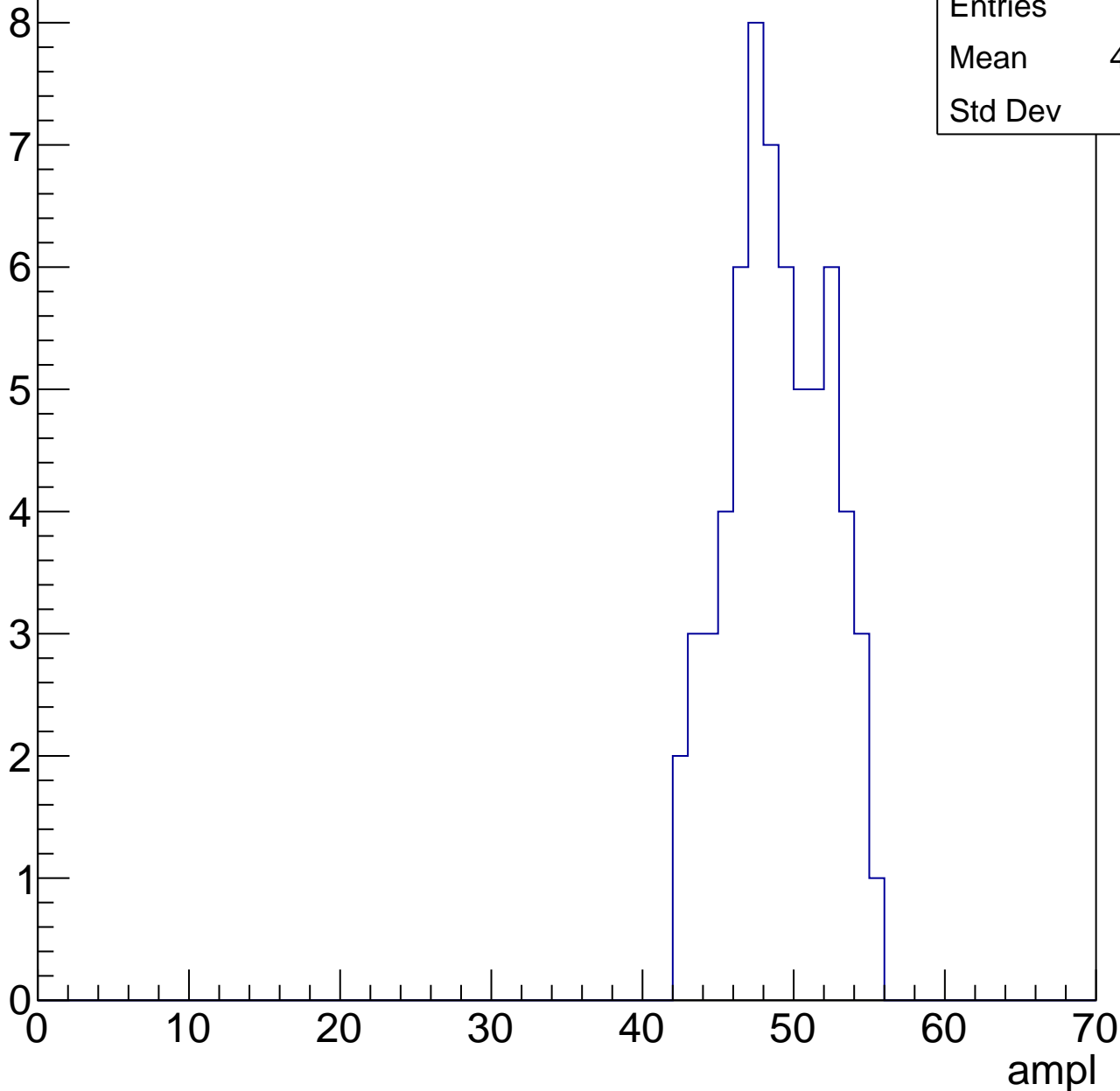


B1L102S, U26-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

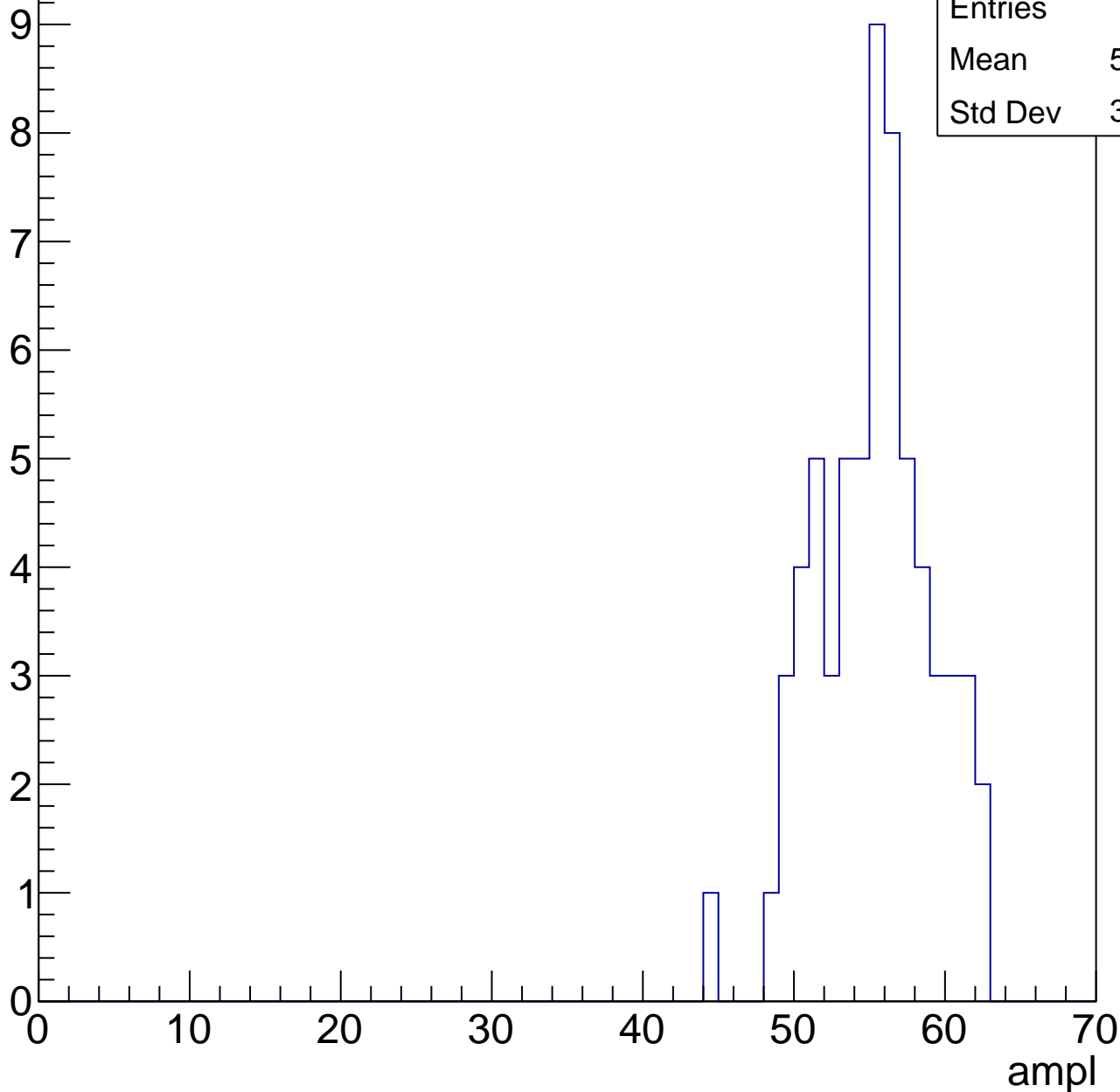
Entries	63
Mean	48.46
Std Dev	3.27



B1L102S, U26-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

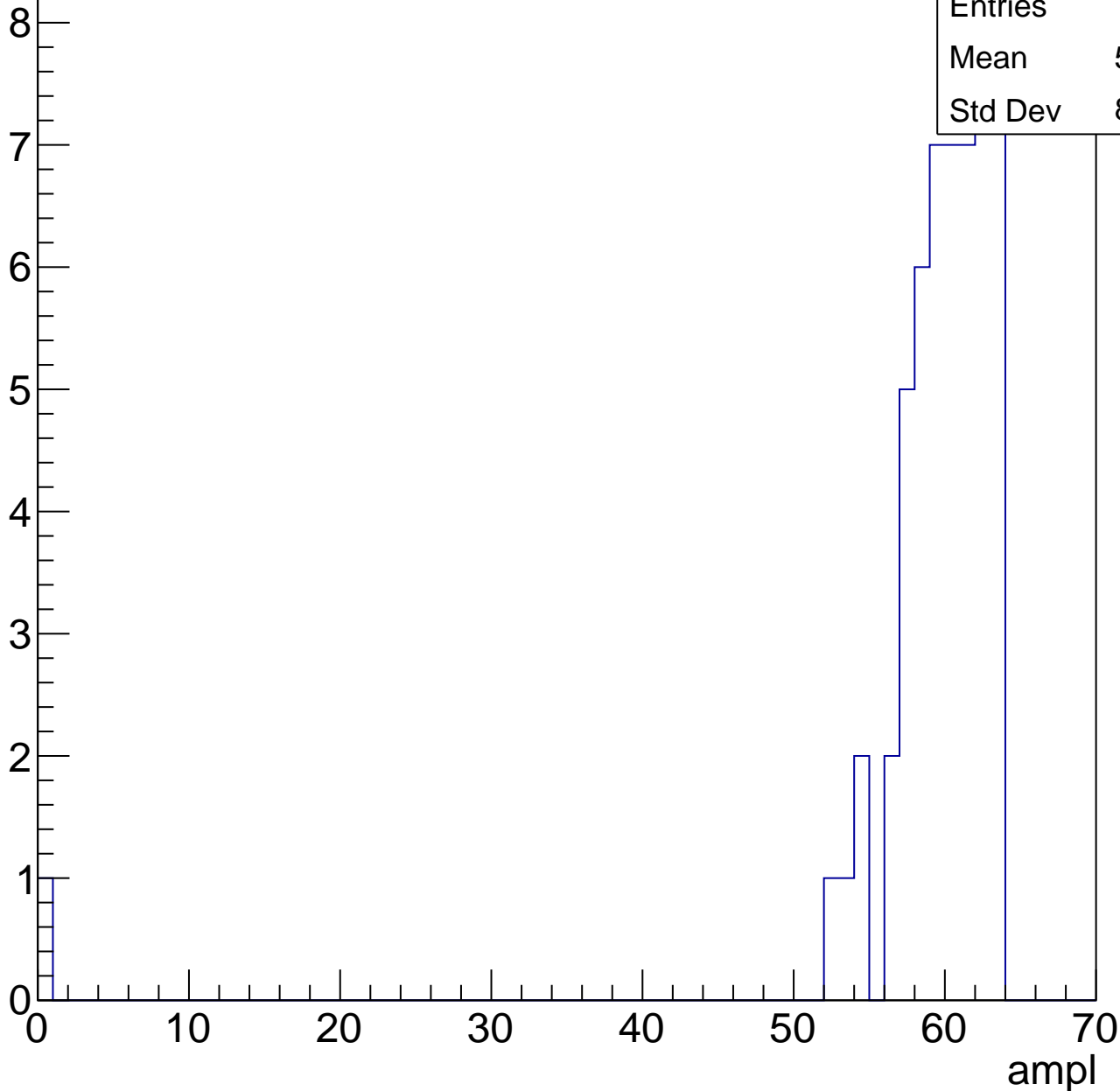


B1L102S, U26-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.51
Std Dev	8.401



B1L102S, U26-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch54, adc0

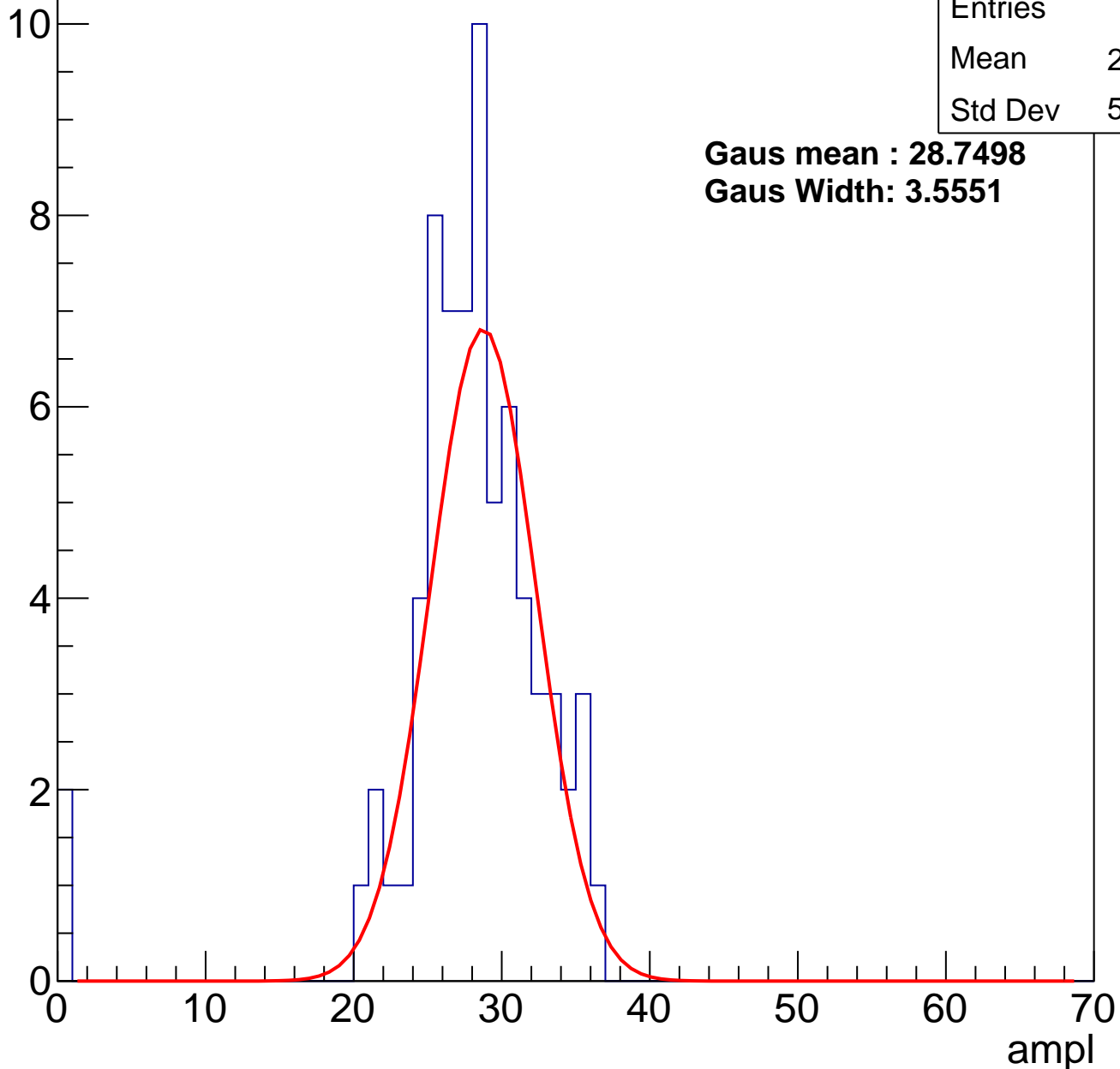
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	27.24
Std Dev	5.847

Gaus mean : 28.7498

Gaus Width: 3.5551

Entry



B1L102S, U26-ch54, adc1

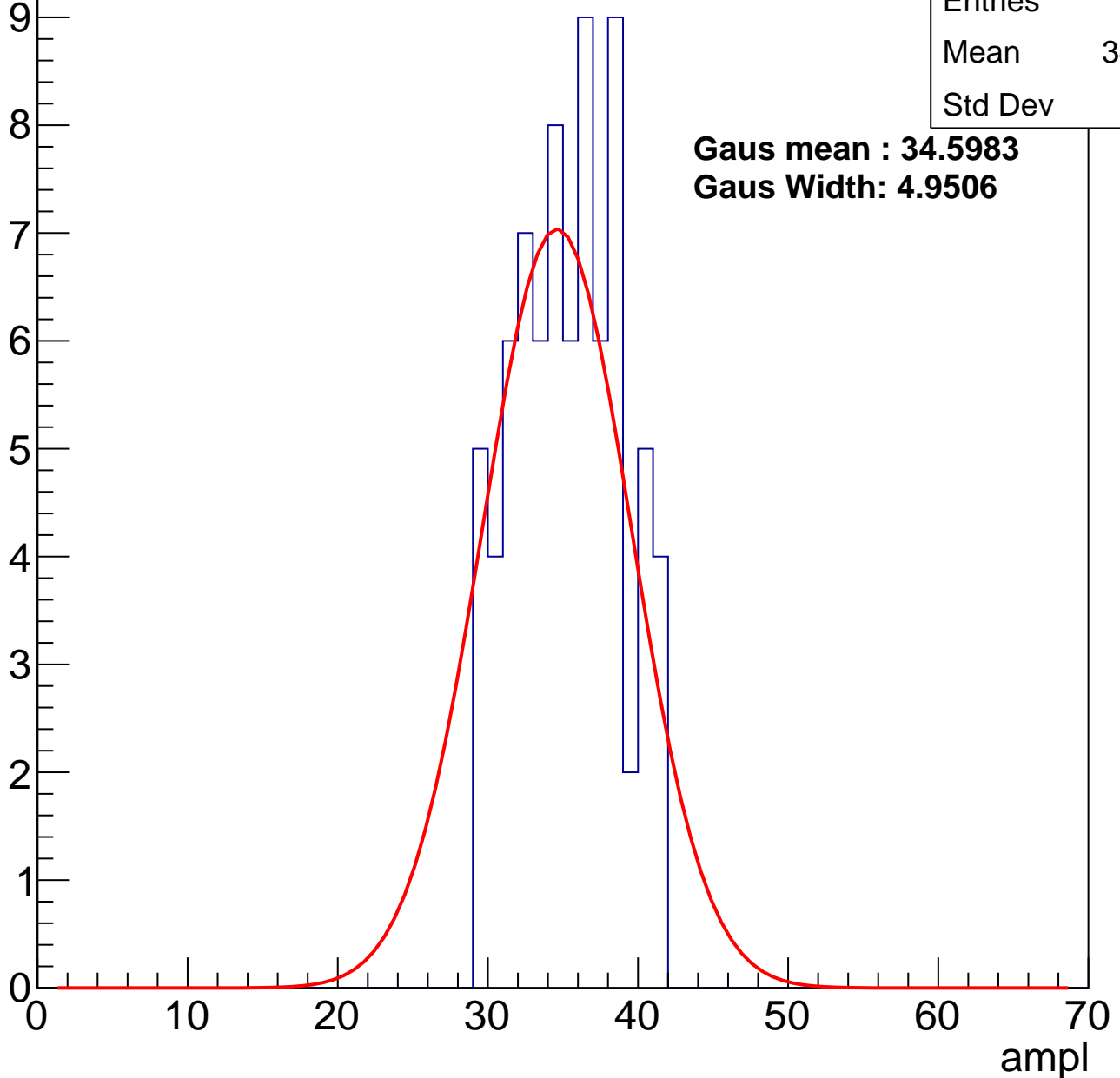
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.87
Std Dev	3.39

Gaus mean : 34.5983

Gaus Width: 4.9506



B1L102S, U26-ch54, adc2

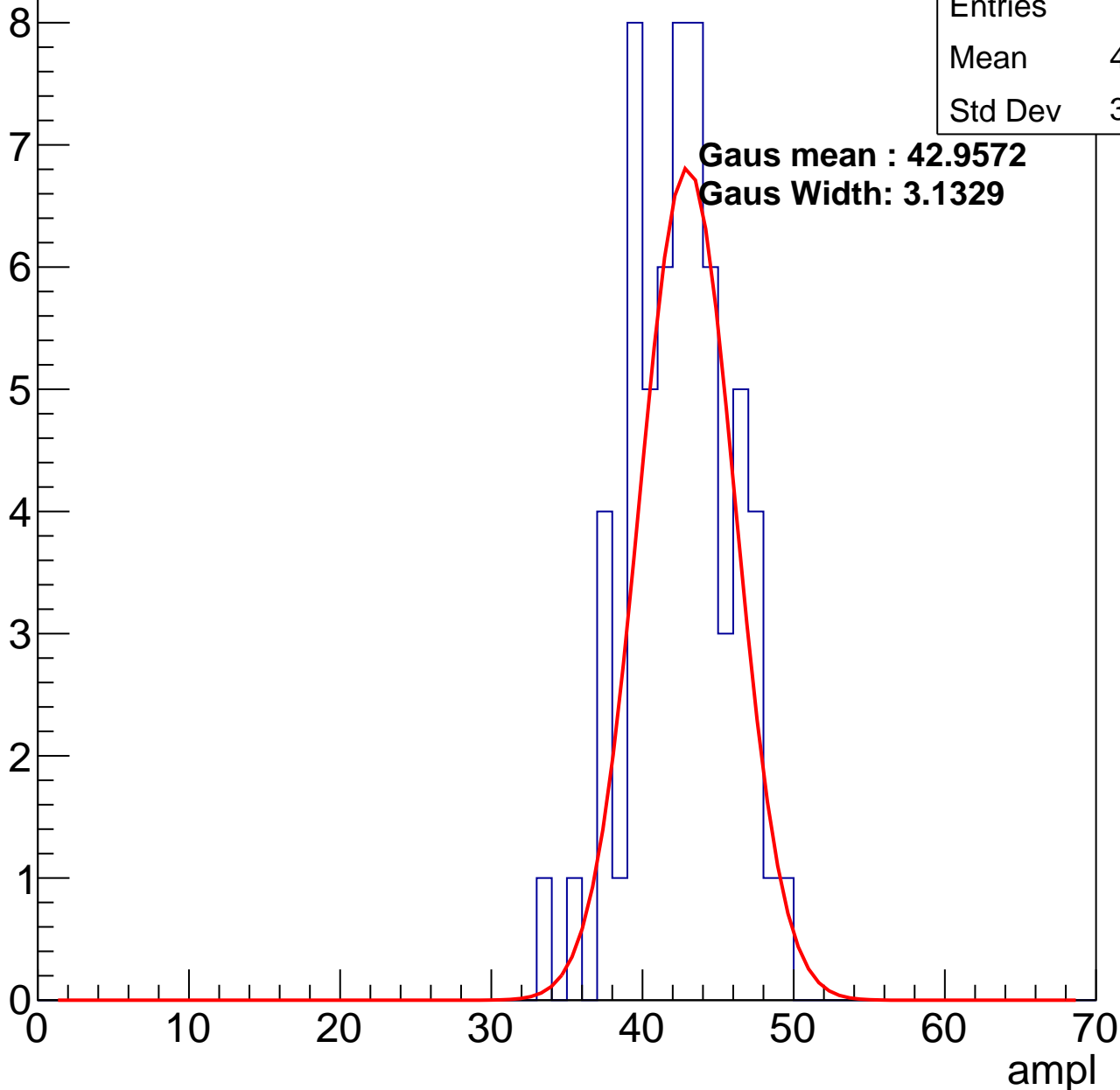
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.03
Std Dev	3.282

Gaus mean : 42.9572

Gaus Width: 3.1329

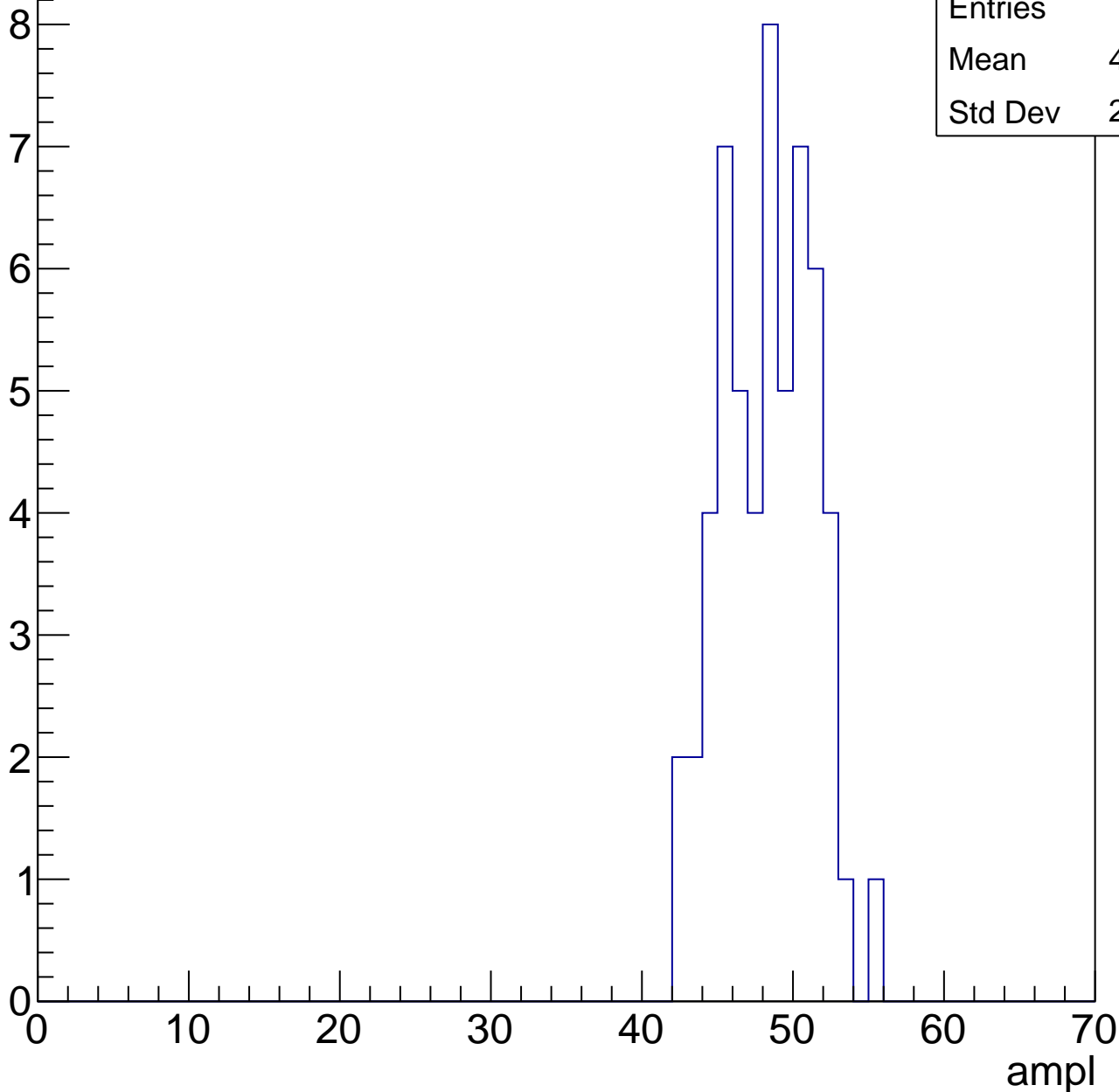


B1L102S, U26-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.86
Std Dev	2.979



B1L102S, U26-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	54.76
Std Dev	3.547

Entry

10

8

6

4

2

0

0

10

20

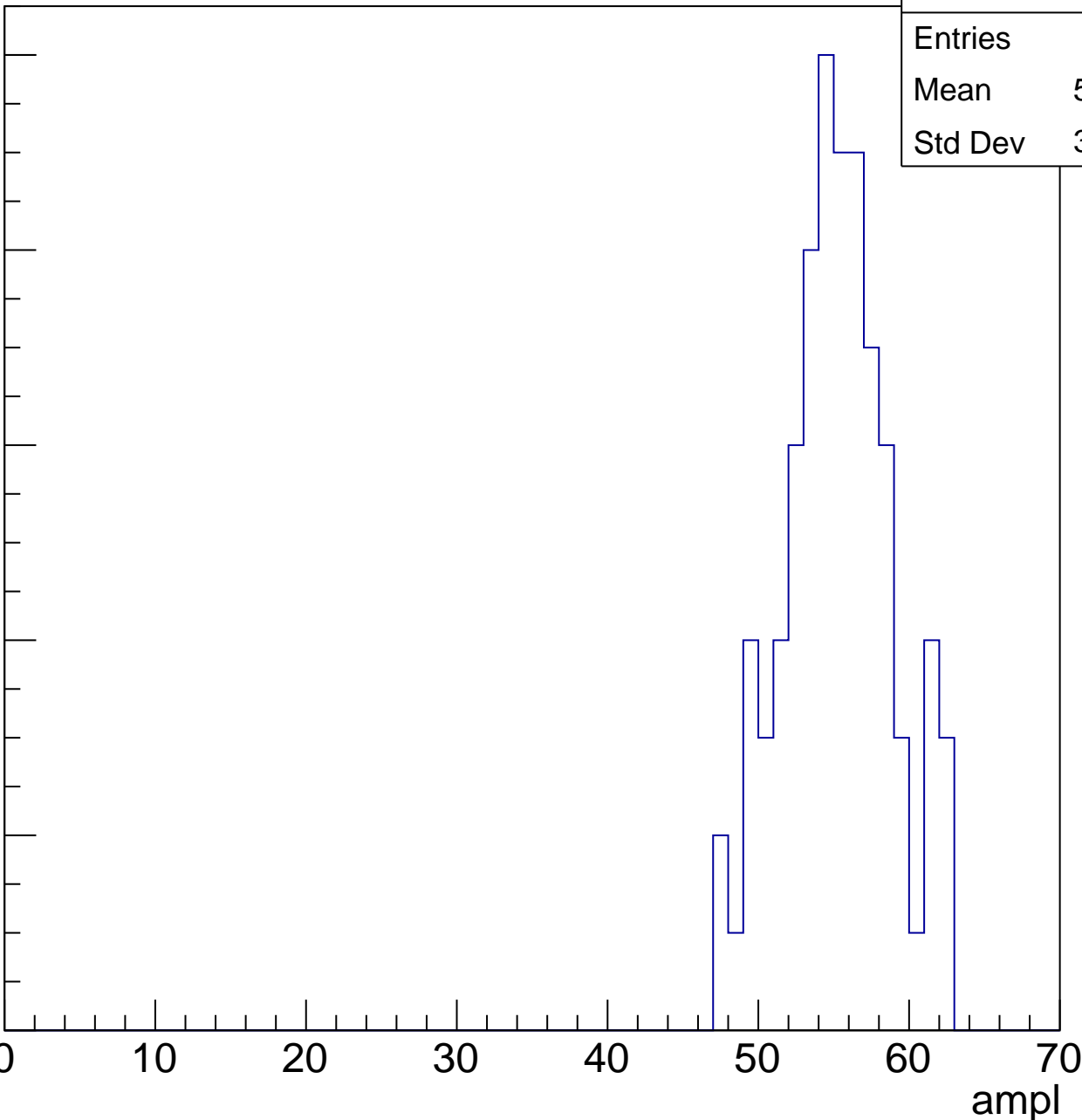
30

40

50

60

ampl

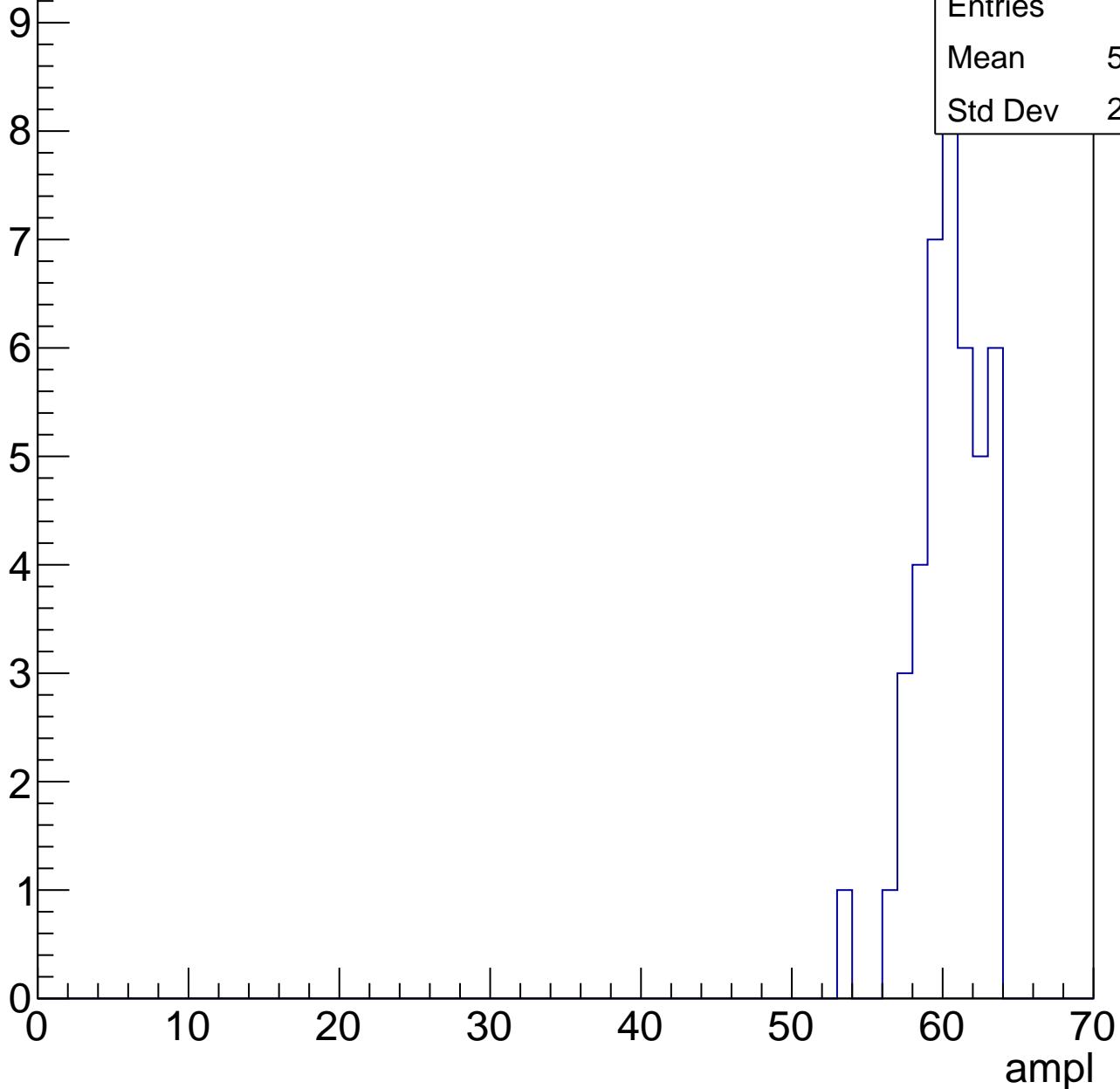


B1L102S, U26-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

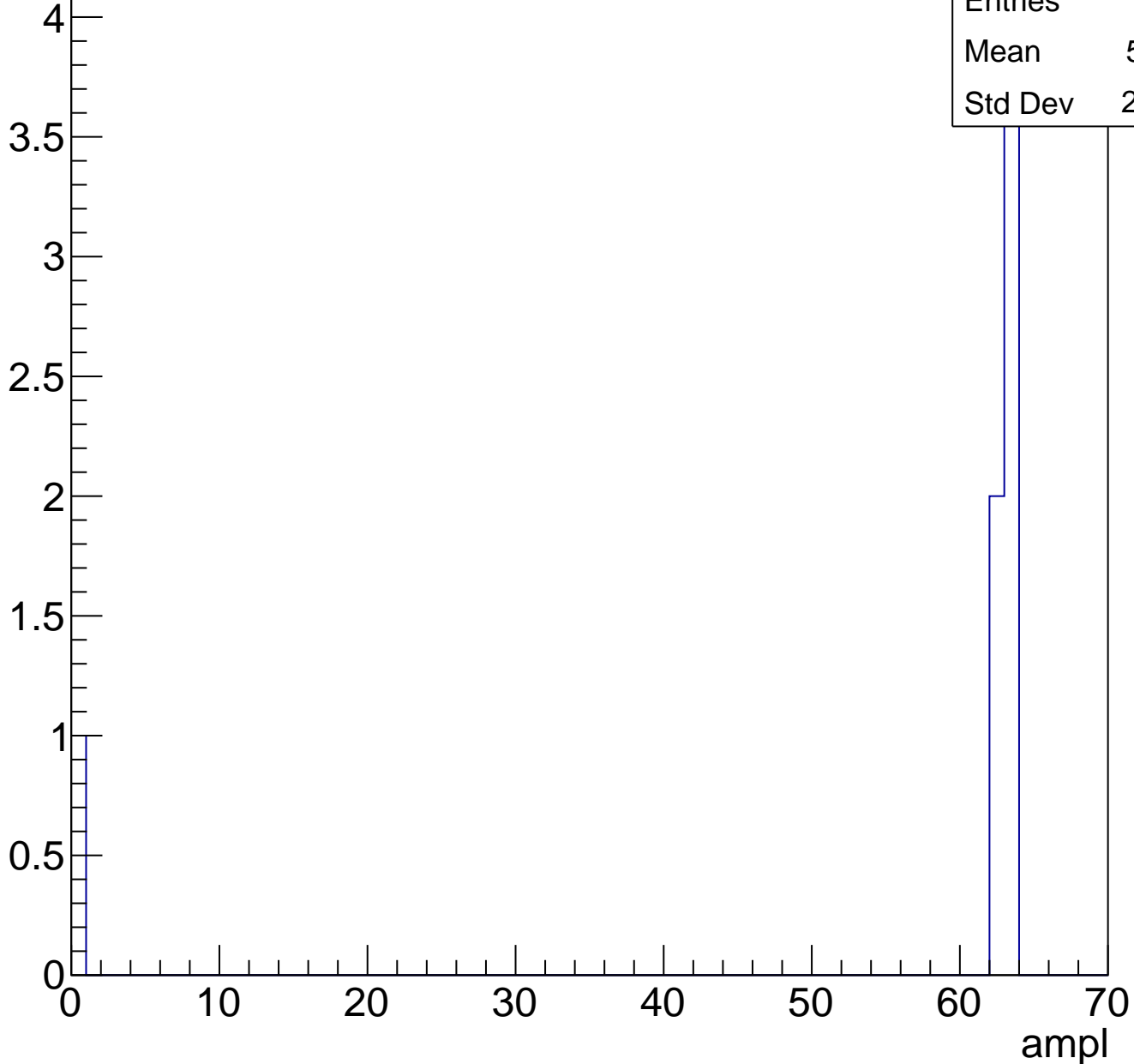
Entries	42
Mean	59.98
Std Dev	2.155



B1L102S, U26-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch55, adc0

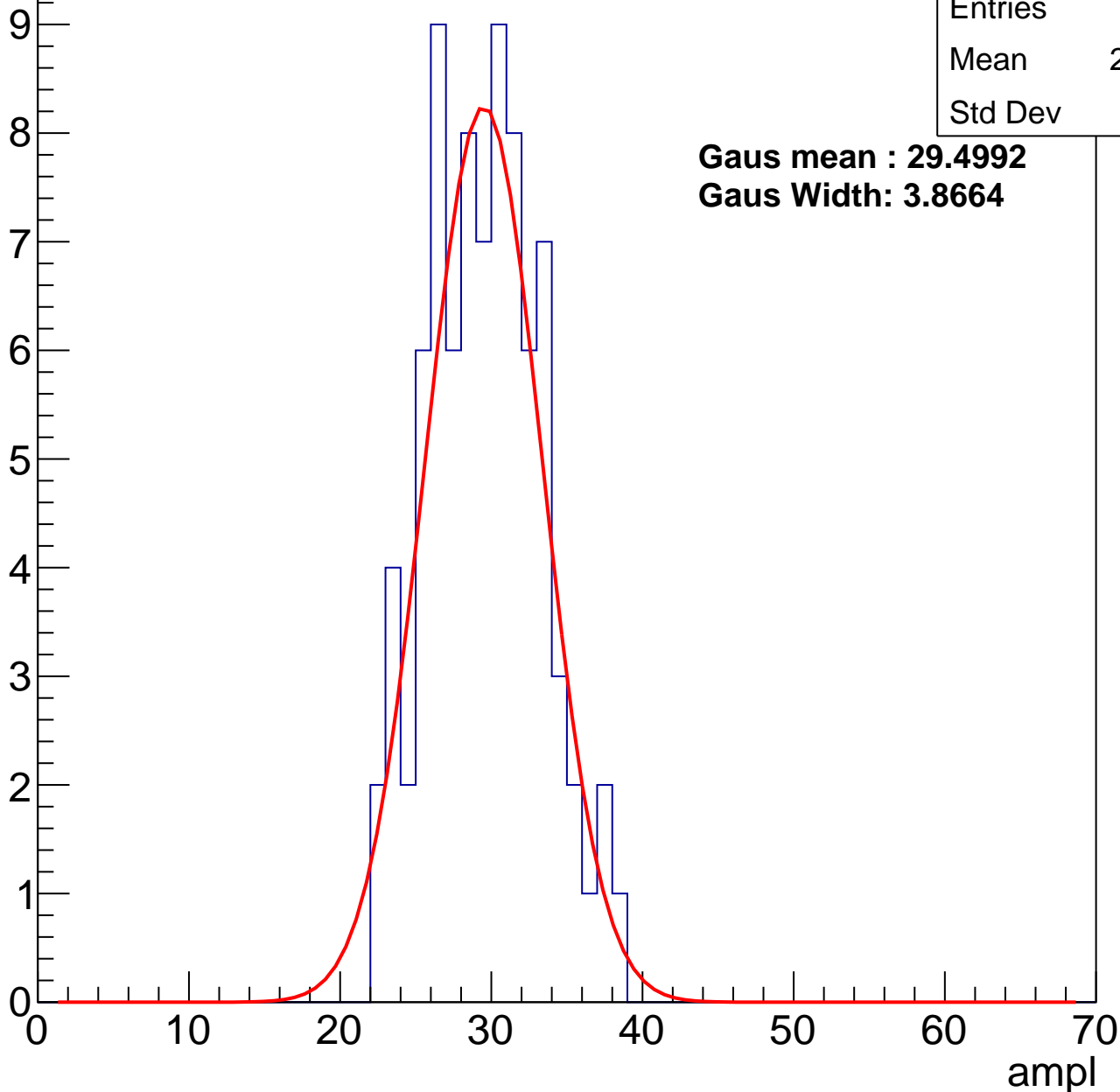
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.13
Std Dev	3.66

Gaus mean : 29.4992

Gaus Width: 3.8664



B1L102S, U26-ch55, adc1

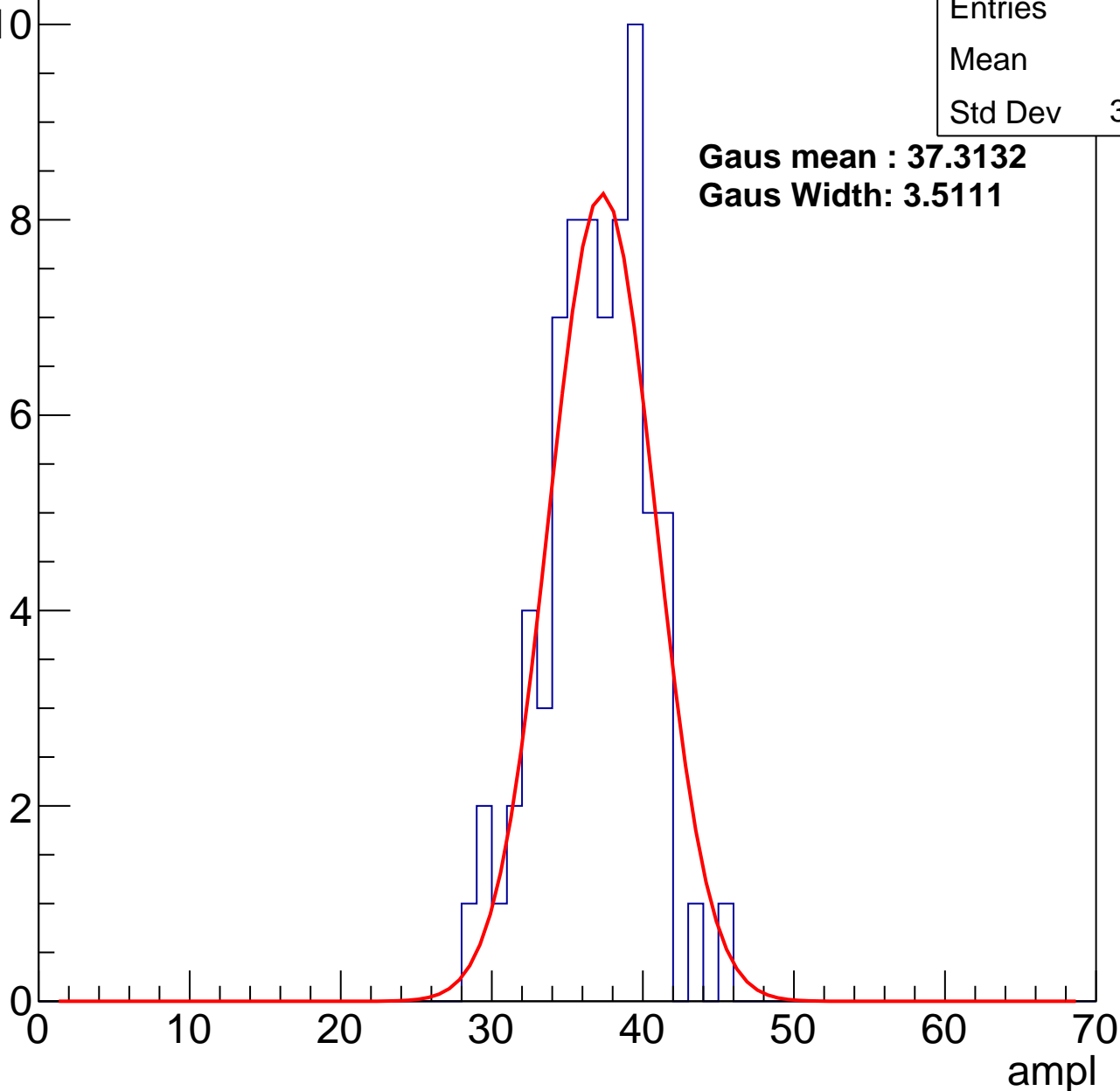
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.4
Std Dev	3.367

Gaus mean : 37.3132

Gaus Width: 3.5111



B1L102S, U26-ch55, adc2

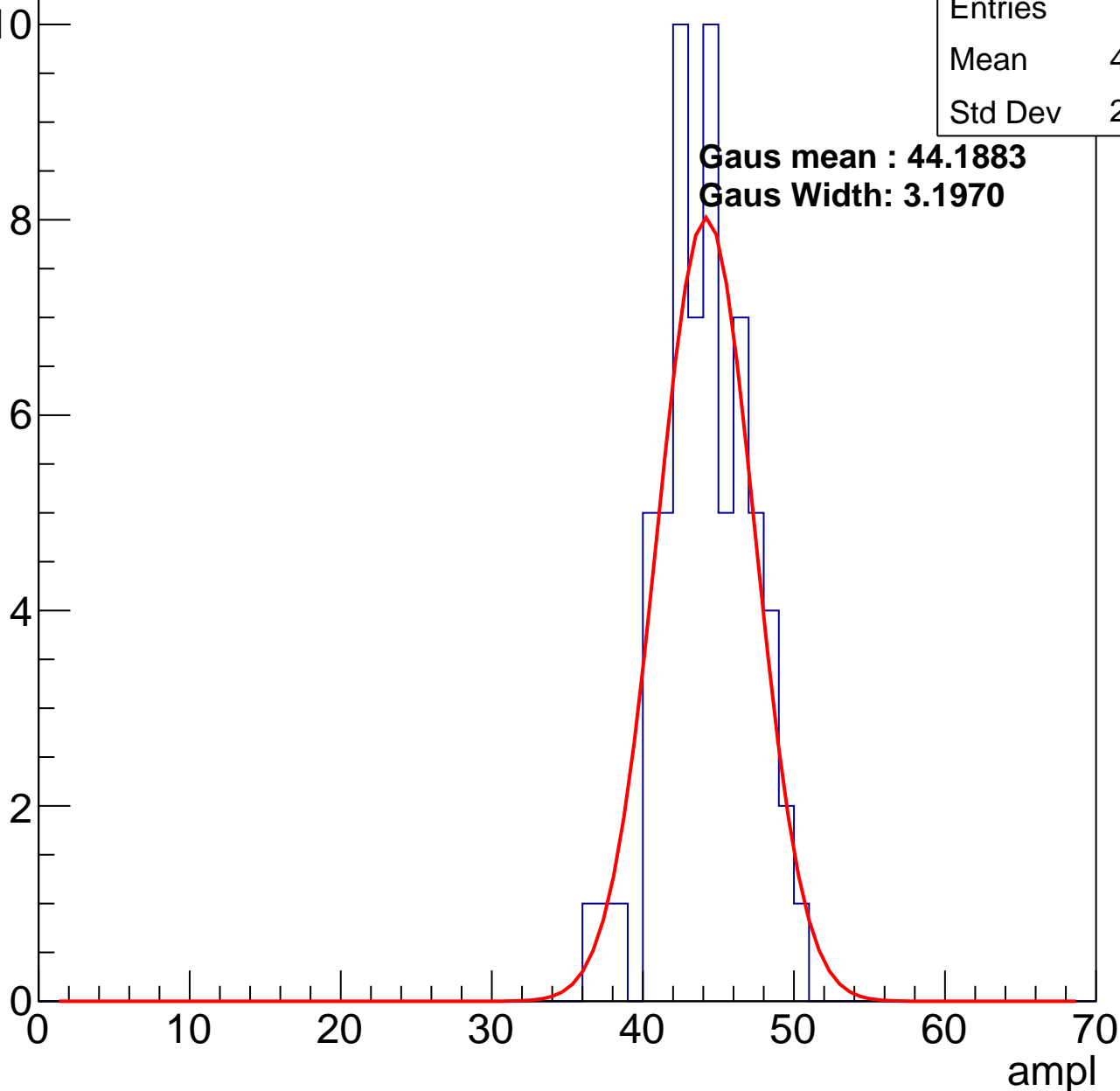
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.73
Std Dev	2.917

Gaus mean : 44.1883

Gaus Width: 3.1970

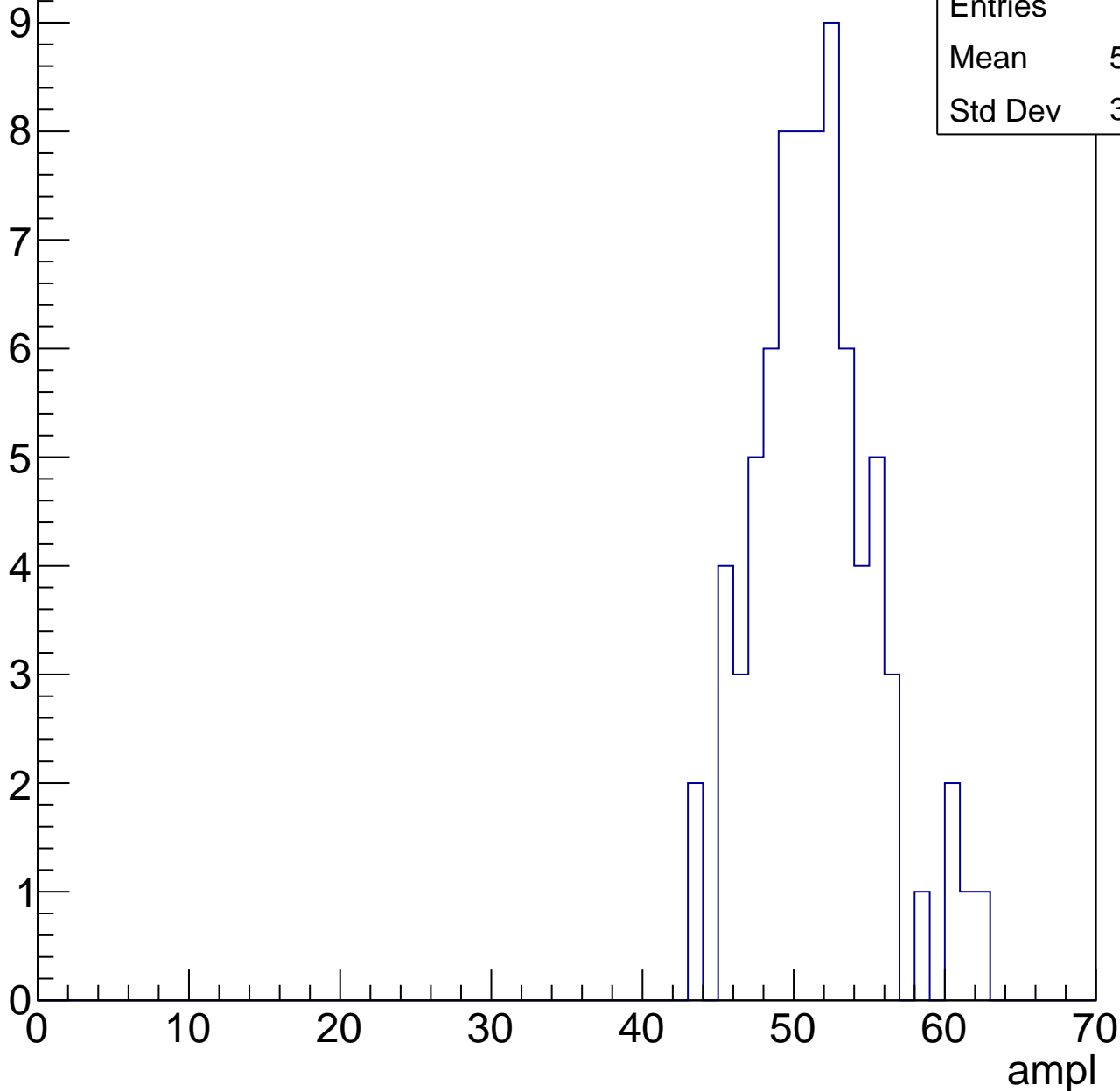


B1L102S, U26-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

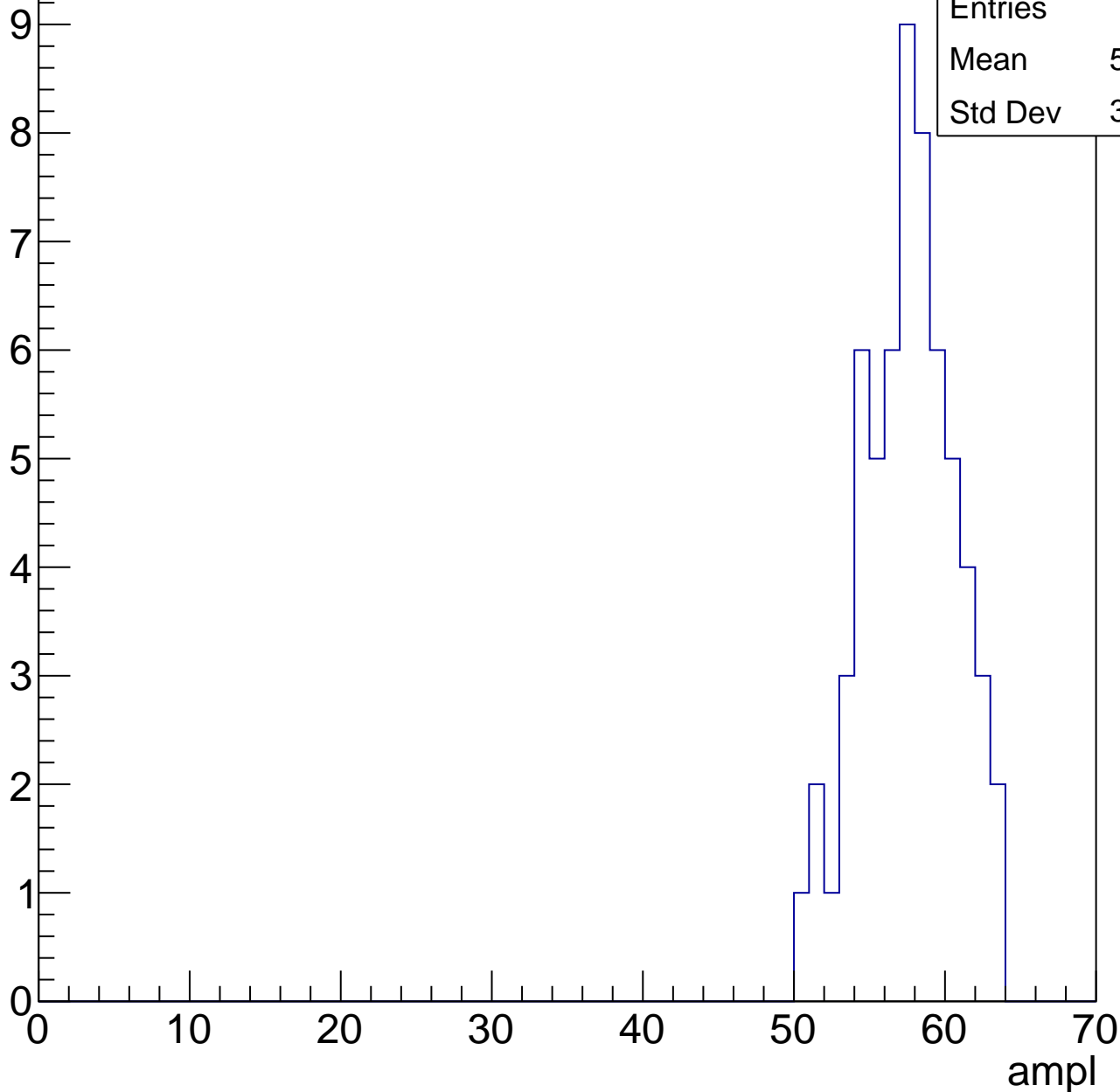
Entries	76
Mean	50.96
Std Dev	3.935



B1L102S, U26-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



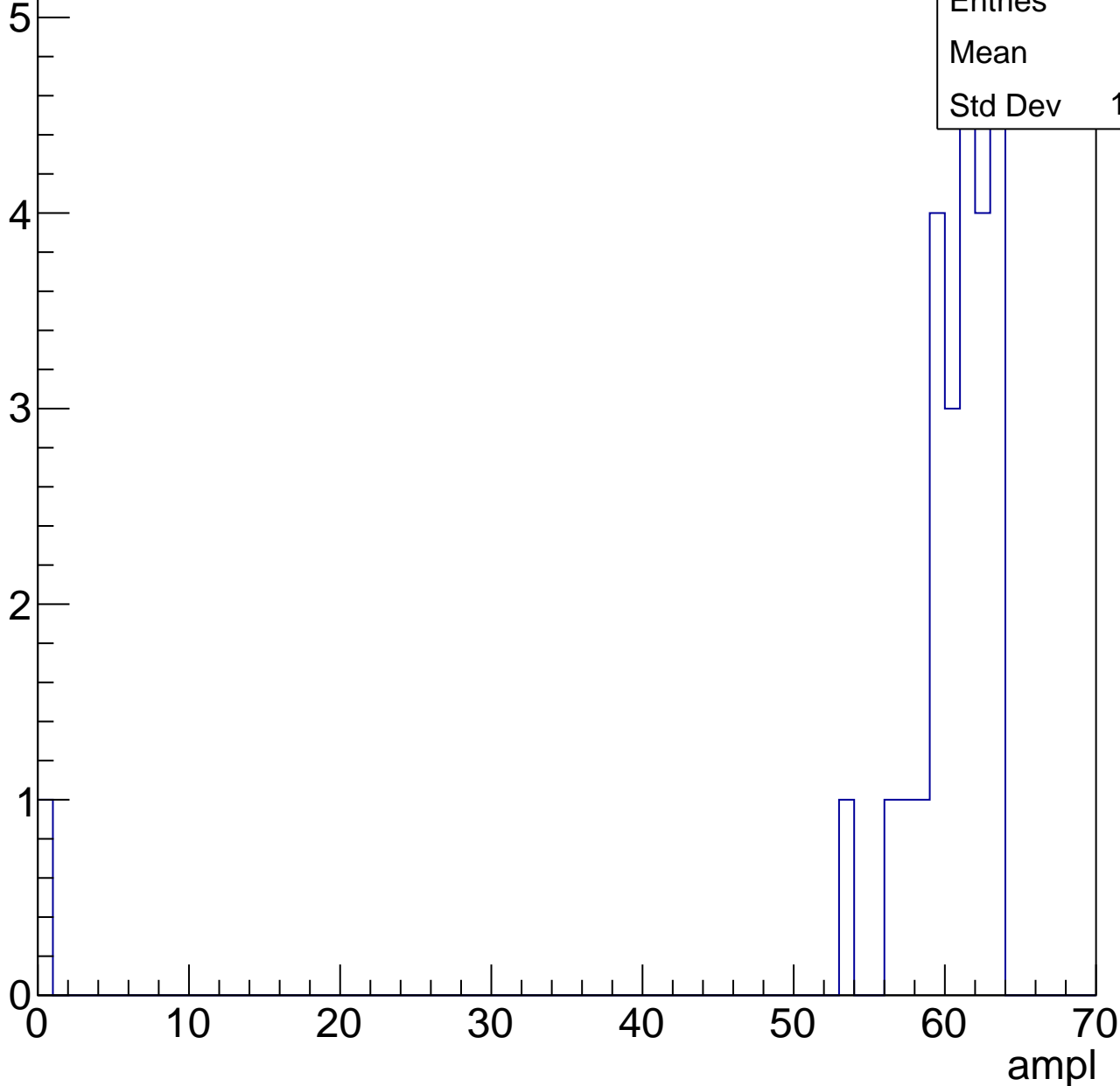
Entries	61
Mean	57.13
Std Dev	3.032

B1L102S, U26-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	58
Std Dev	11.84



B1L102S, U26-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

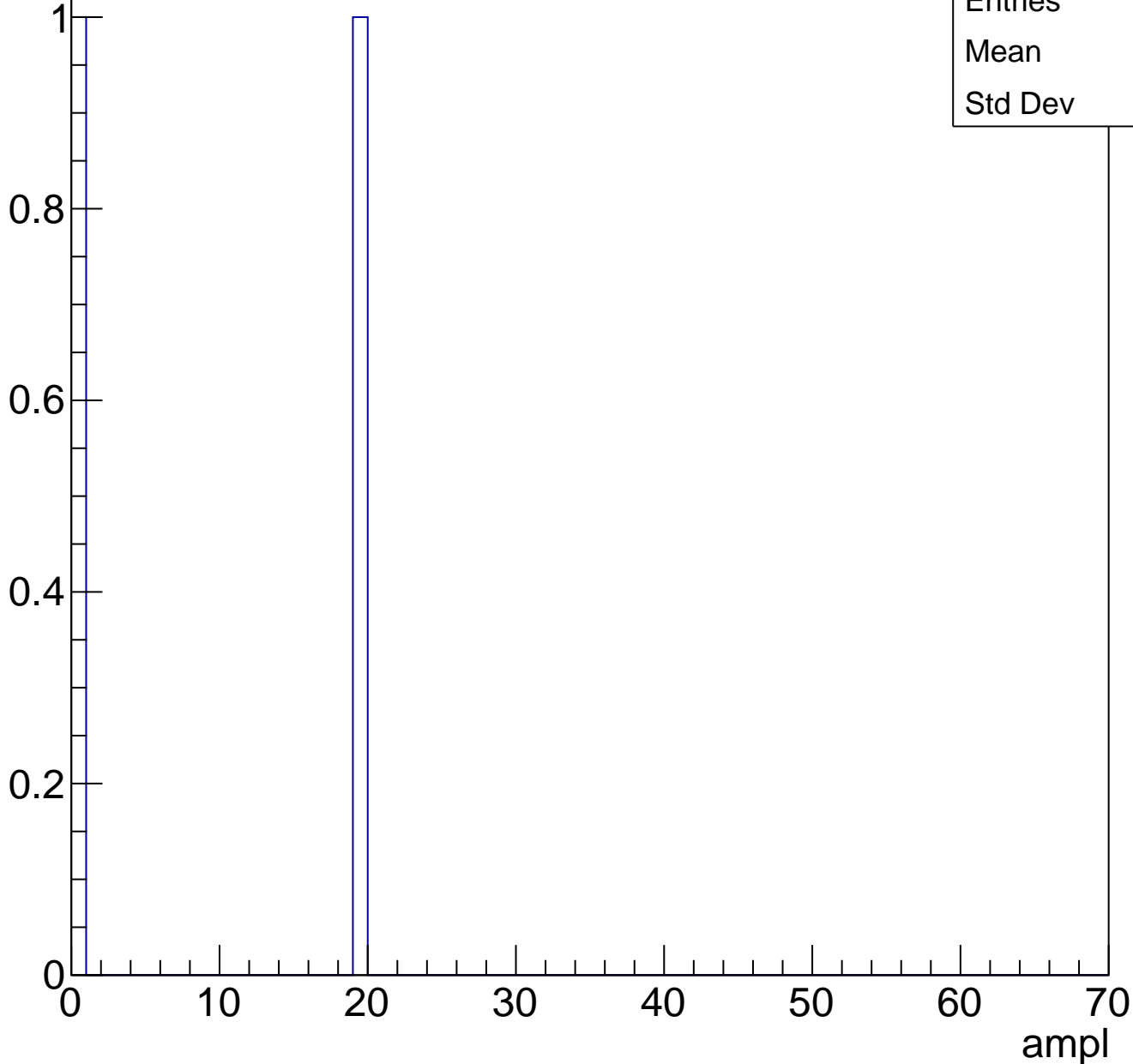
Entry



B1L102S, U26-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

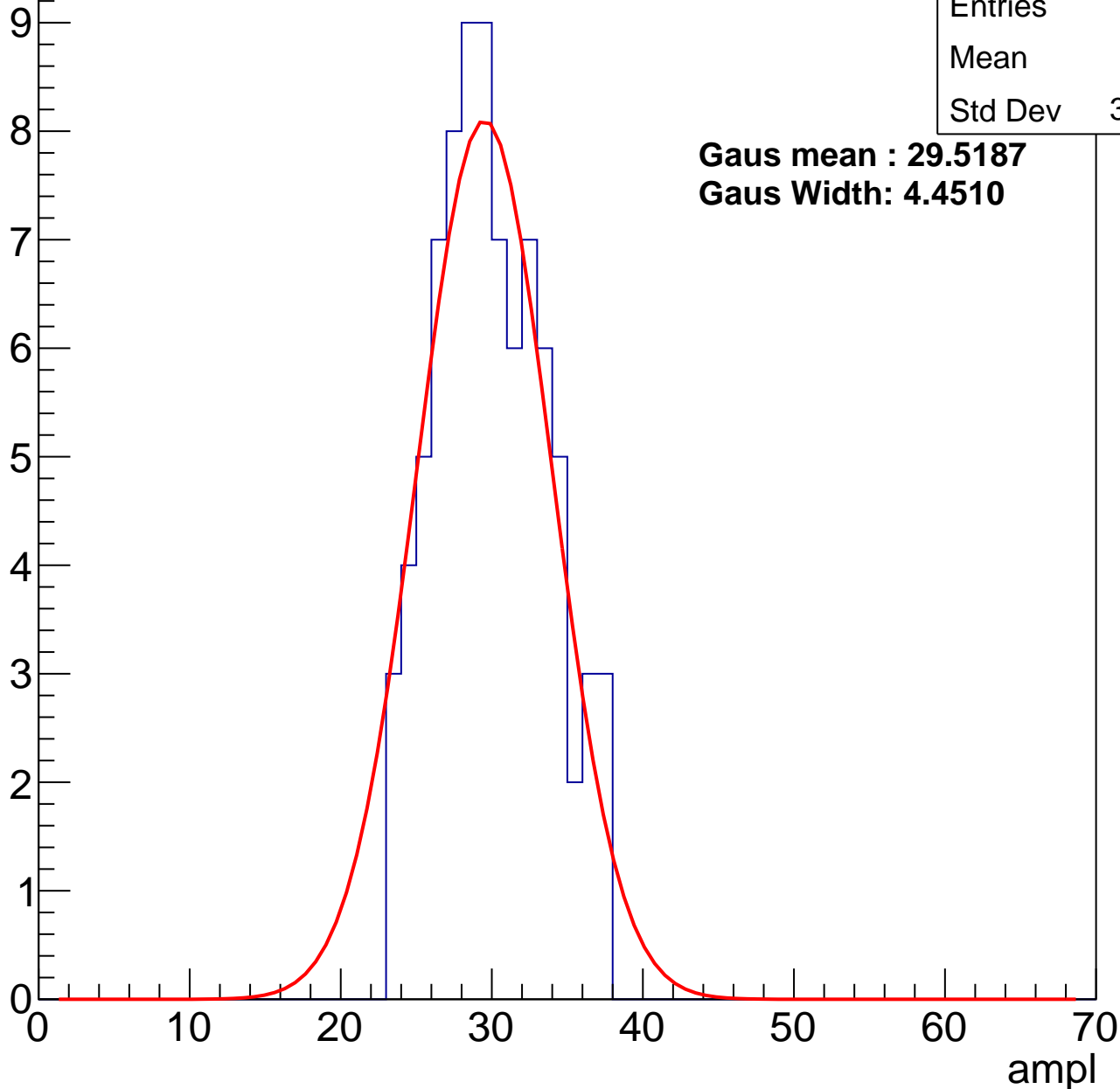


Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U26-ch56, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch56, adc1

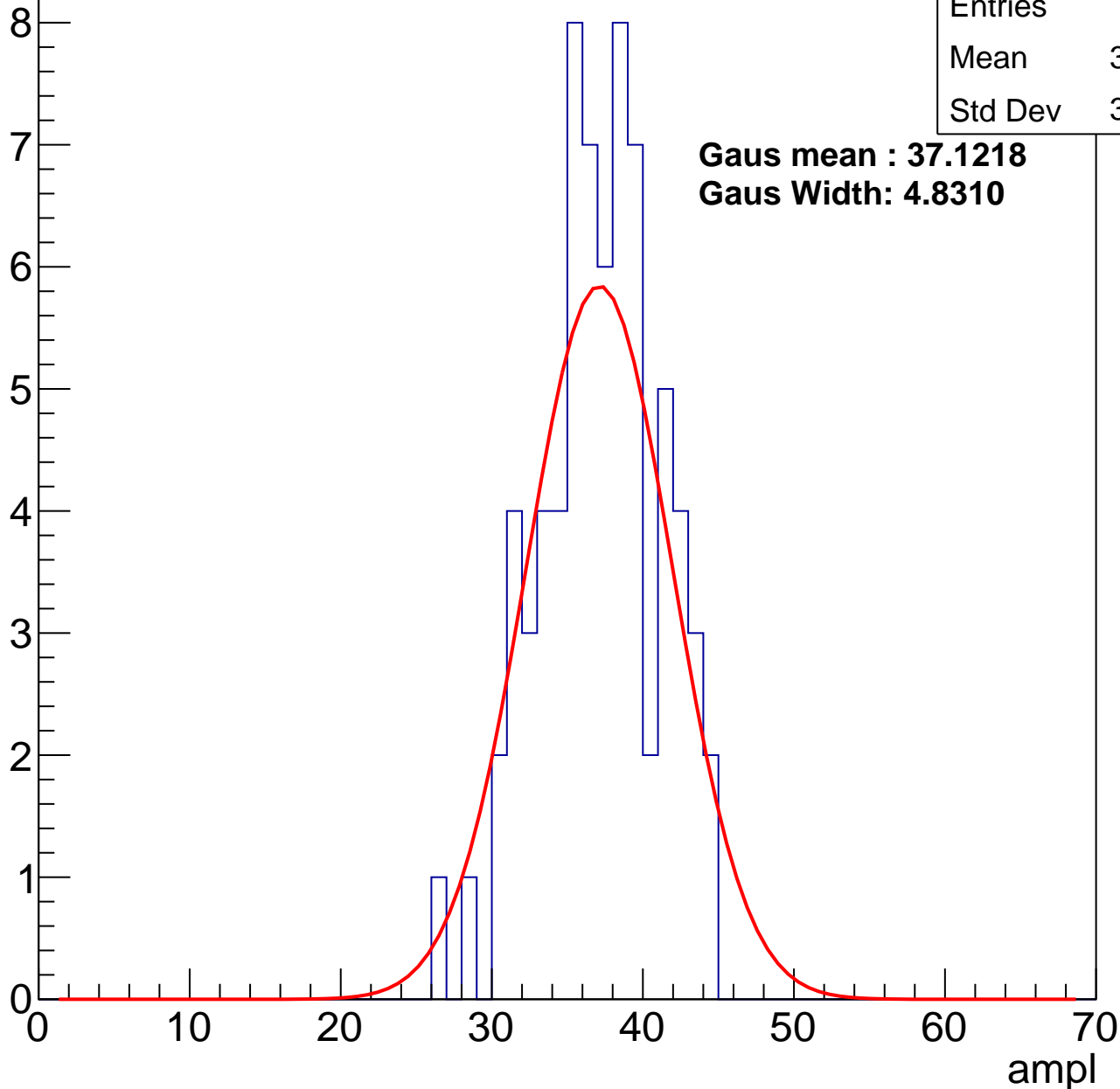
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.66
Std Dev	3.918

Gaus mean : 37.1218

Gaus Width: 4.8310



B1L102S, U26-ch56, adc2

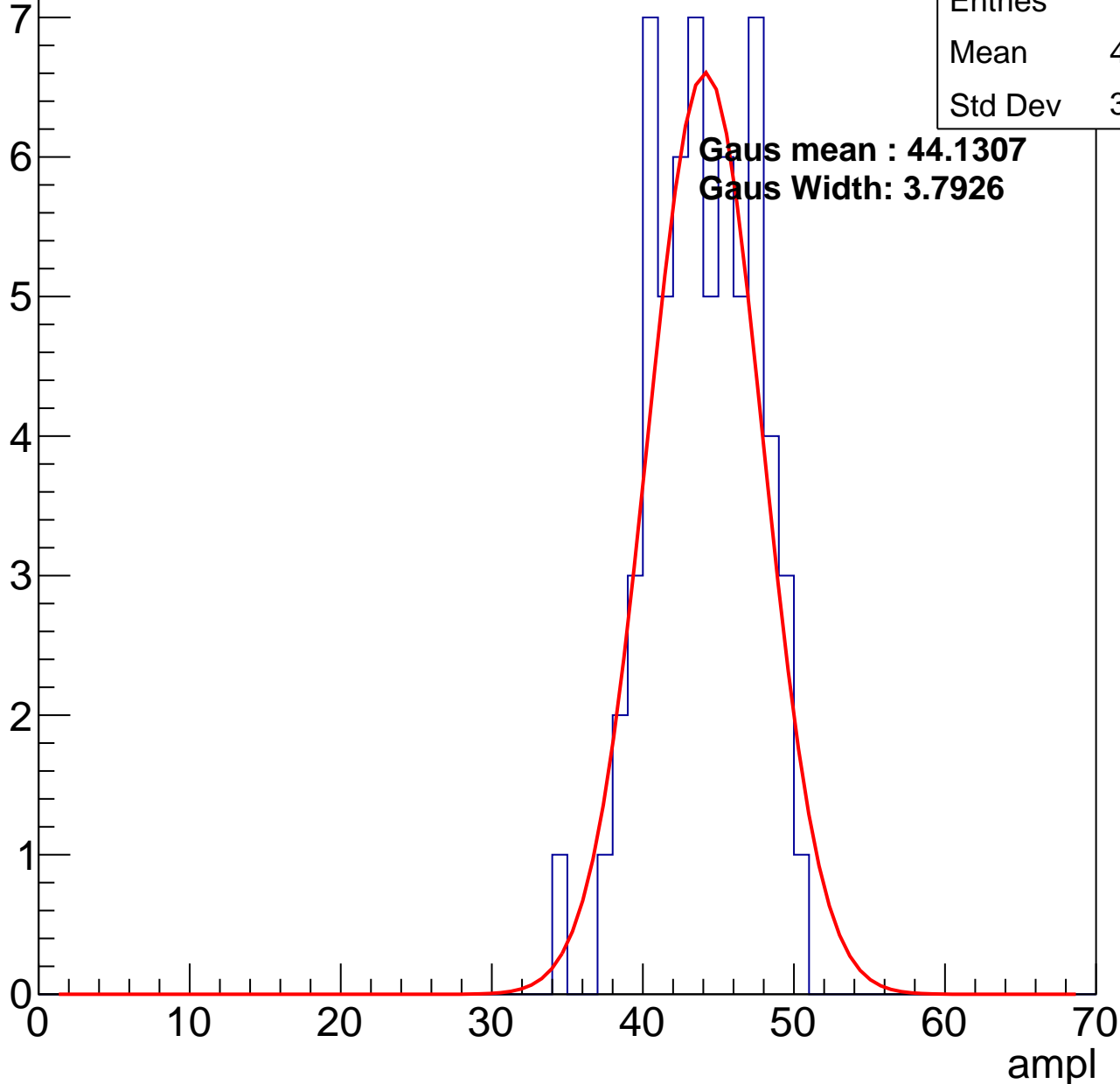
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.49
Std Dev	3.408

Gaus mean : 44.1307

Gaus Width: 3.7926

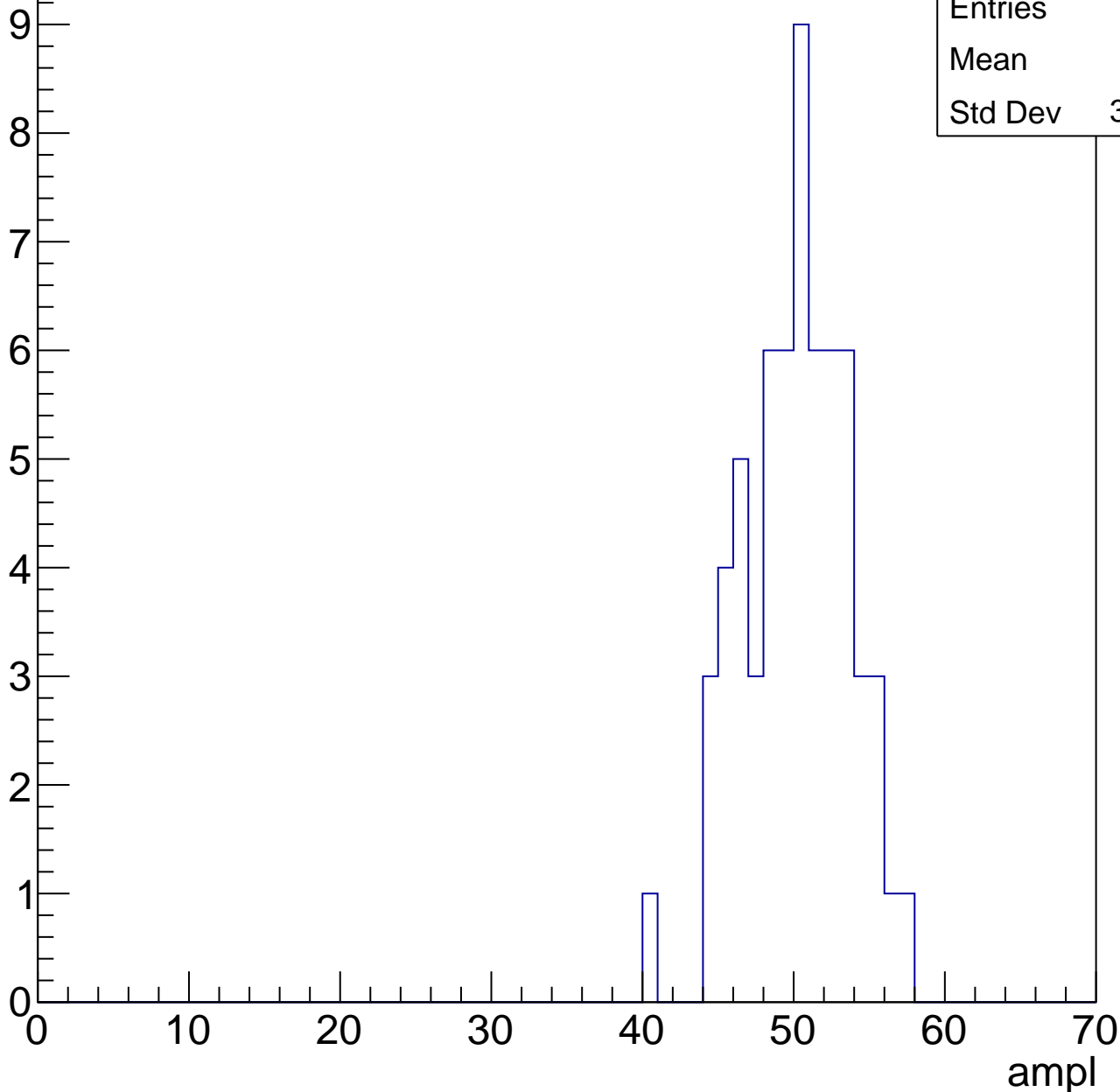


B1L102S, U26-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.7
Std Dev	3.407

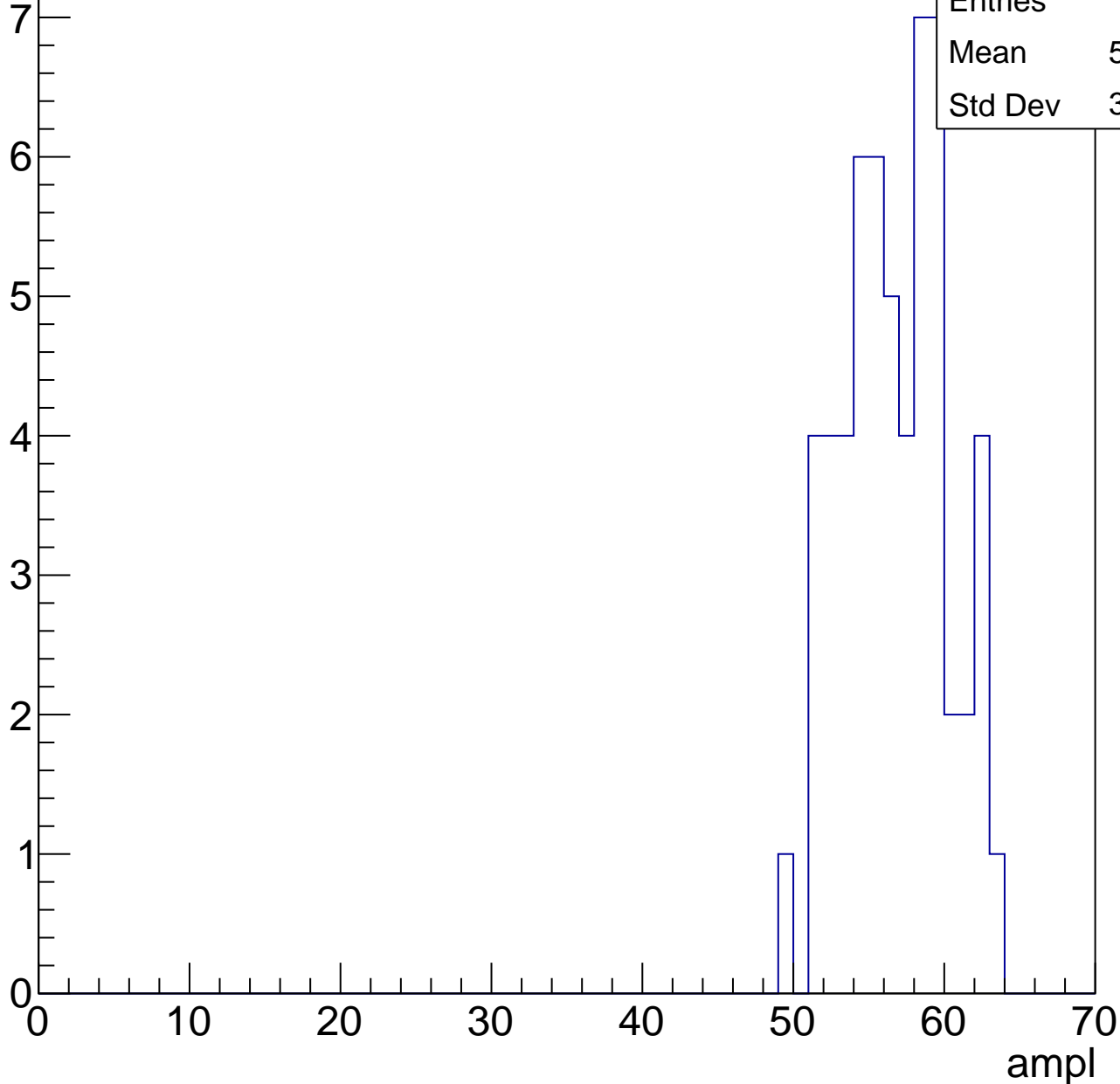


B1L102S, U26-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.26
Std Dev	3.364

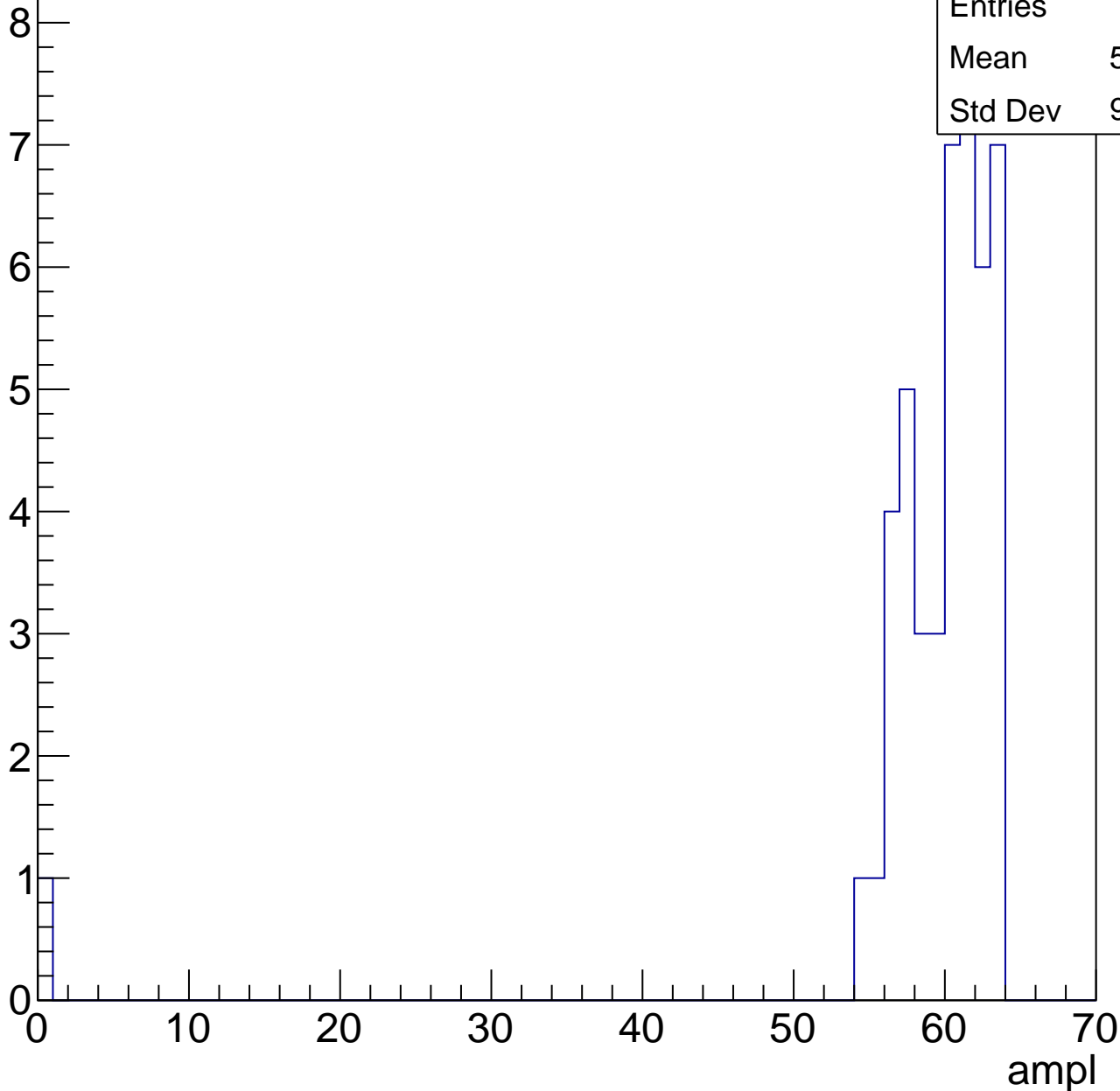


B1L102S, U26-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.48
Std Dev	9.057



B1L102S, U26-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L102S, U26-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch57, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	29.61
Std Dev	3.538

Gaus mean : 30.0851

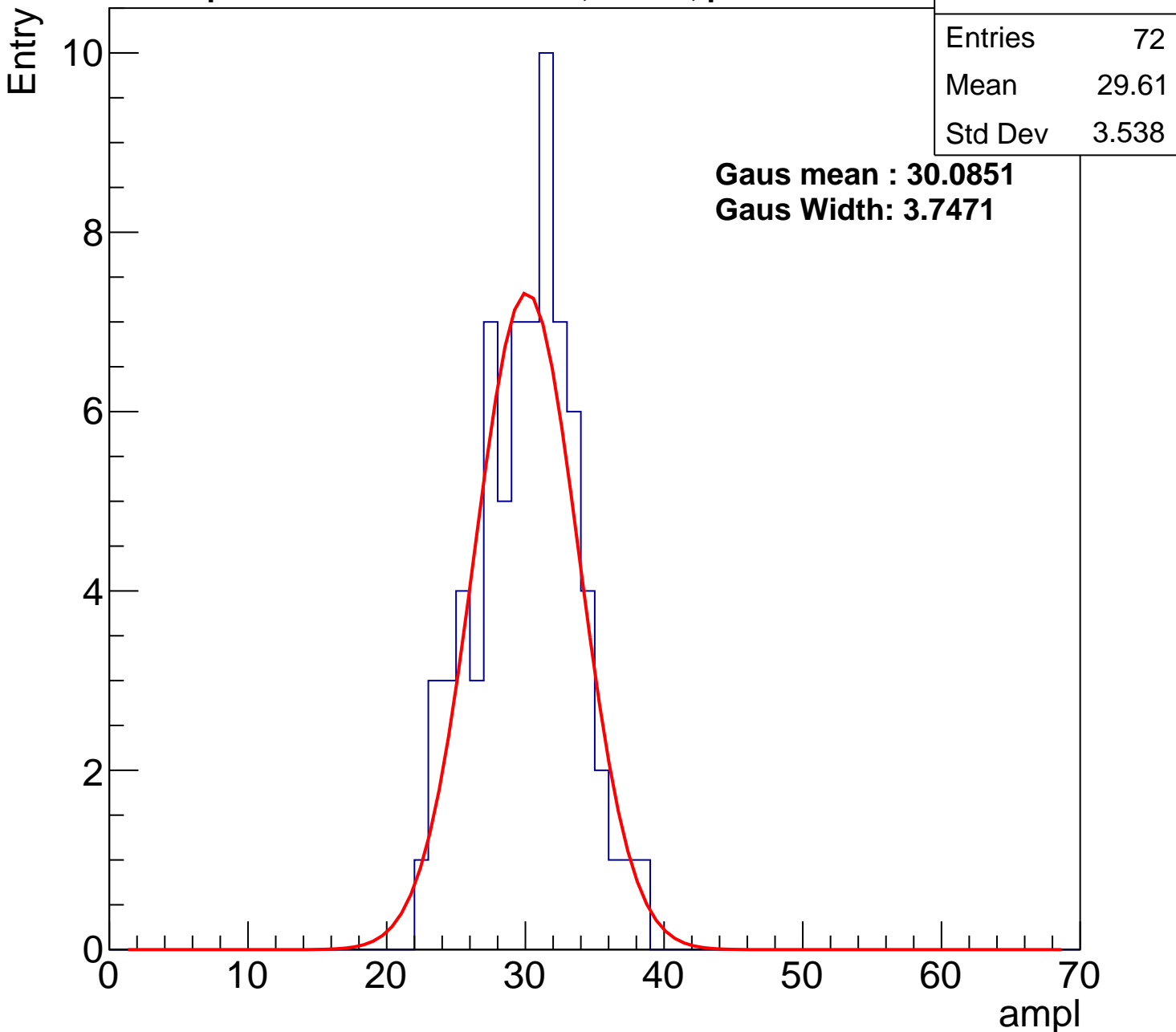
Gaus Width: 3.7471

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch57, adc1

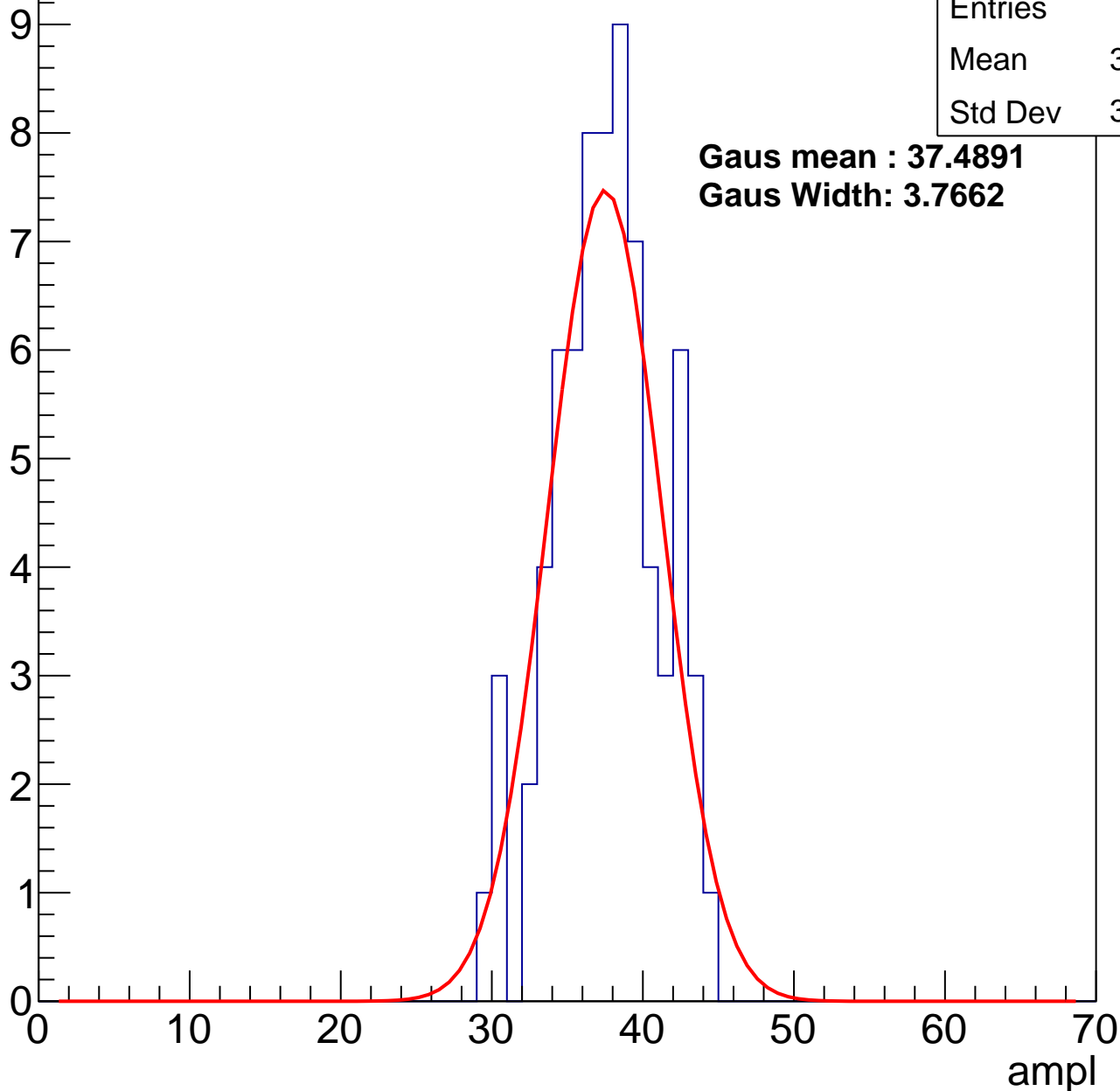
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	37.13
Std Dev	3.435

Gaus mean : 37.4891

Gaus Width: 3.7662



B1L102S, U26-ch57, adc2

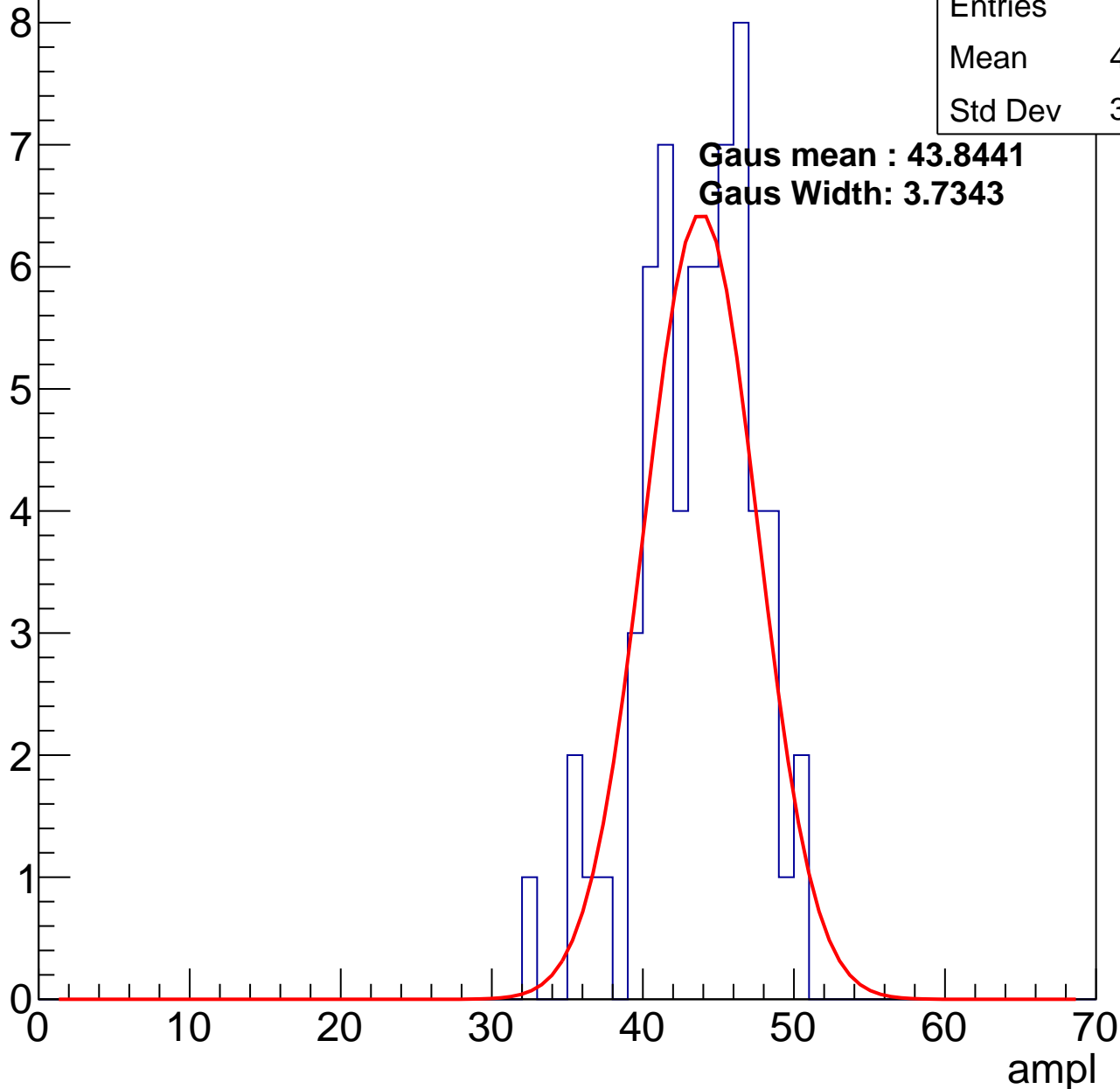
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.19
Std Dev	3.716

Gaus mean : 43.8441

Gaus Width: 3.7343

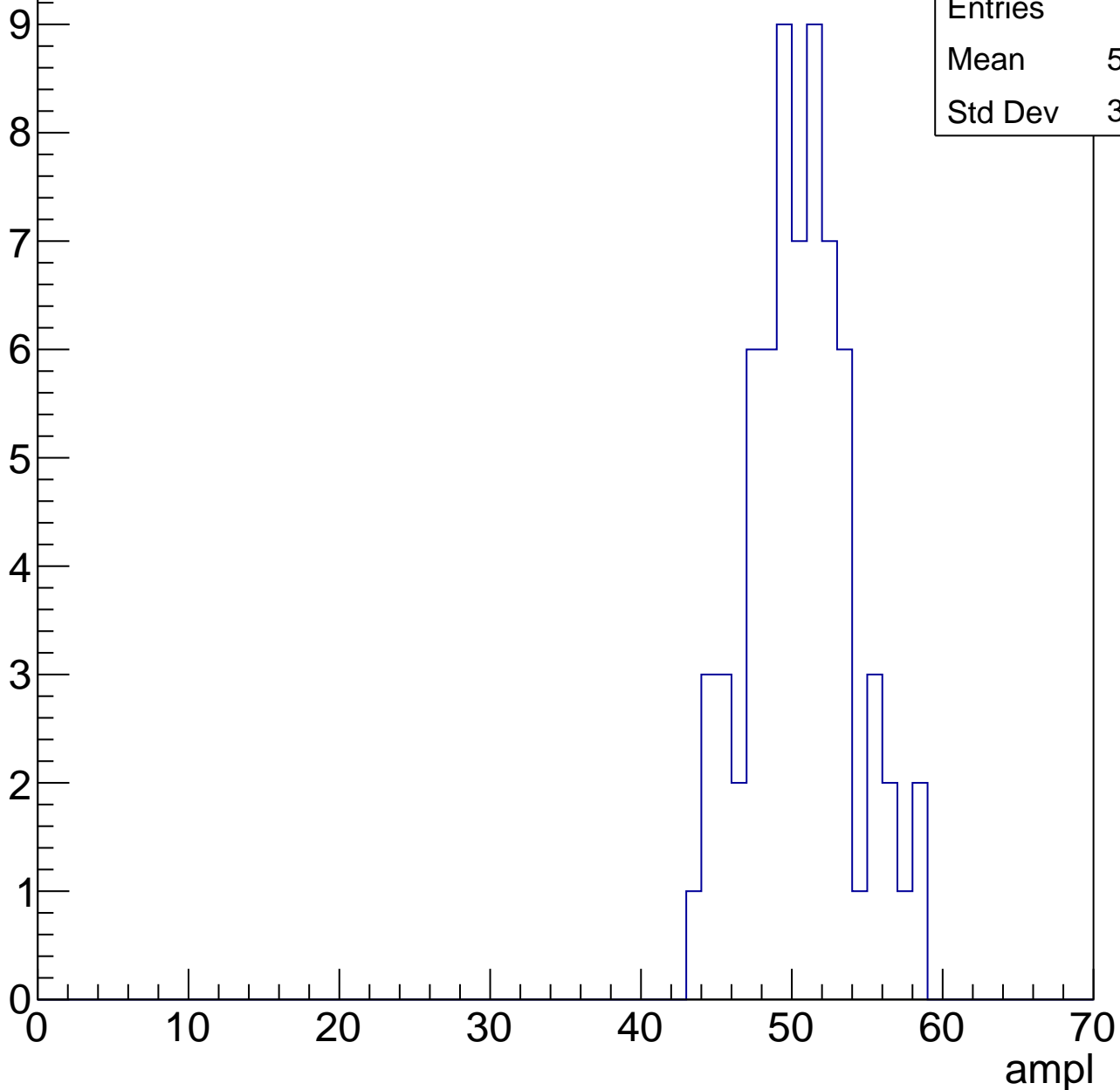


B1L102S, U26-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.12
Std Dev	3.389

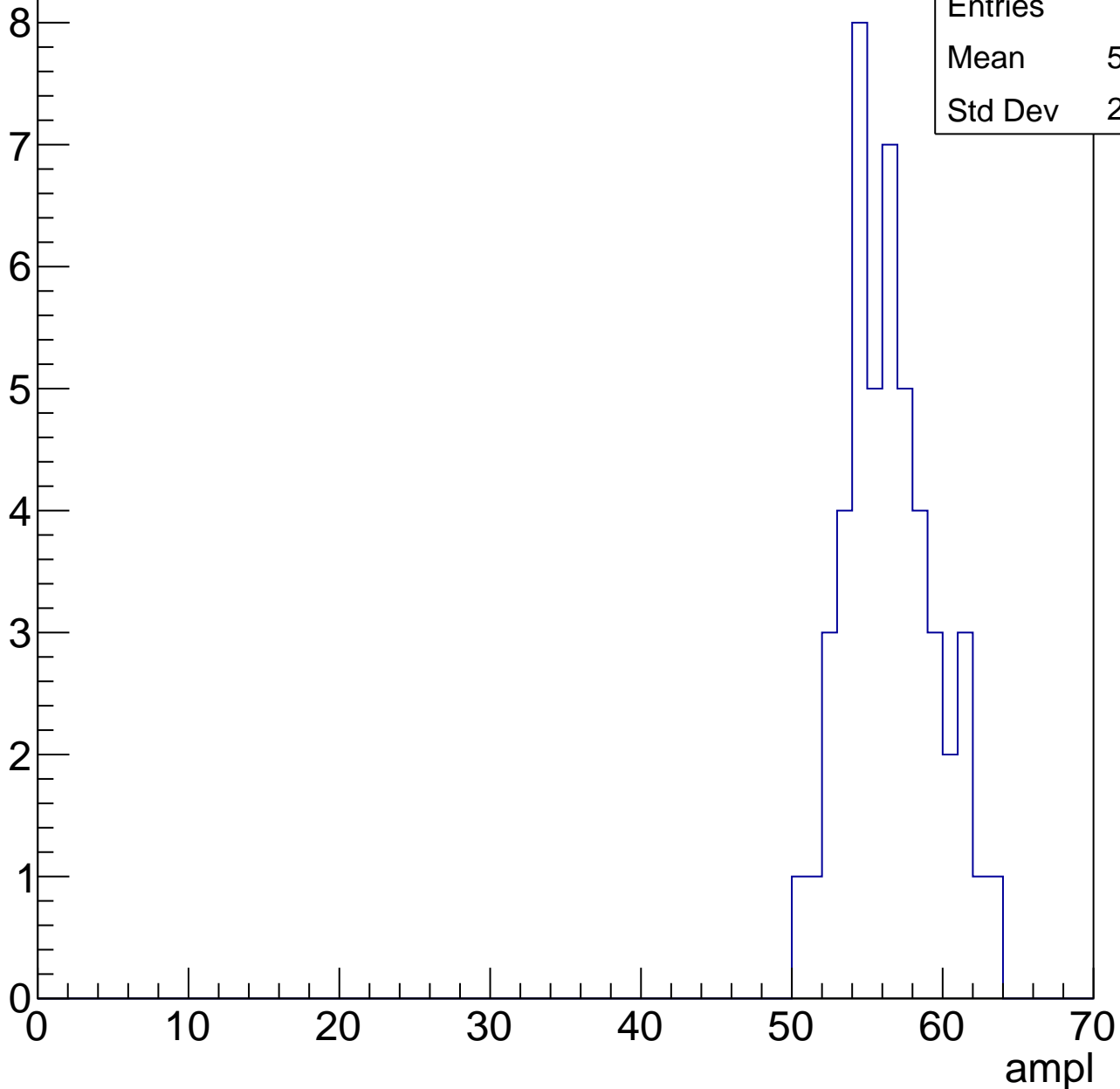


B1L102S, U26-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.04
Std Dev	2.965

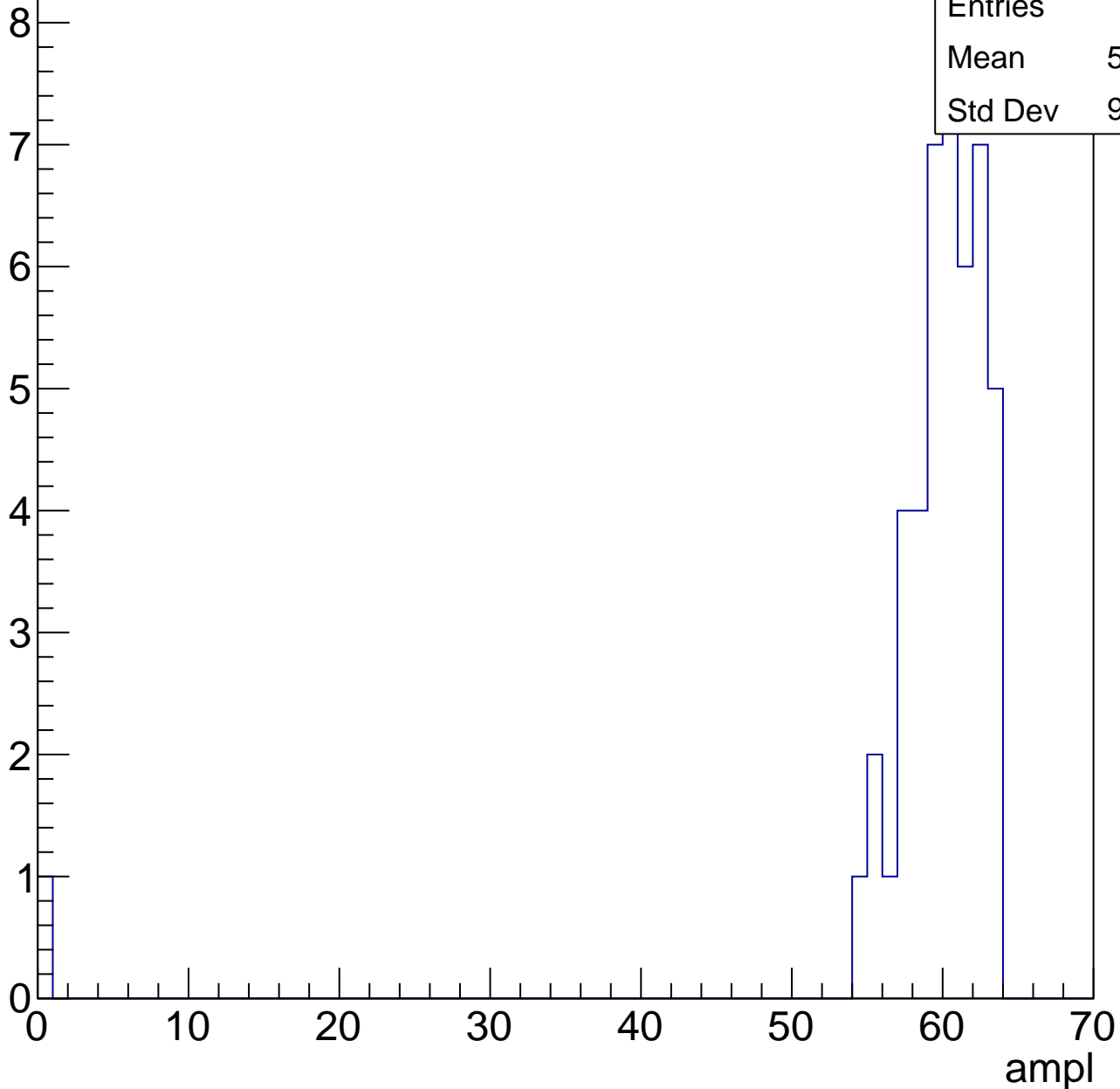


B1L102S, U26-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.43
Std Dev	9.002



B1L102S, U26-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch58, adc0

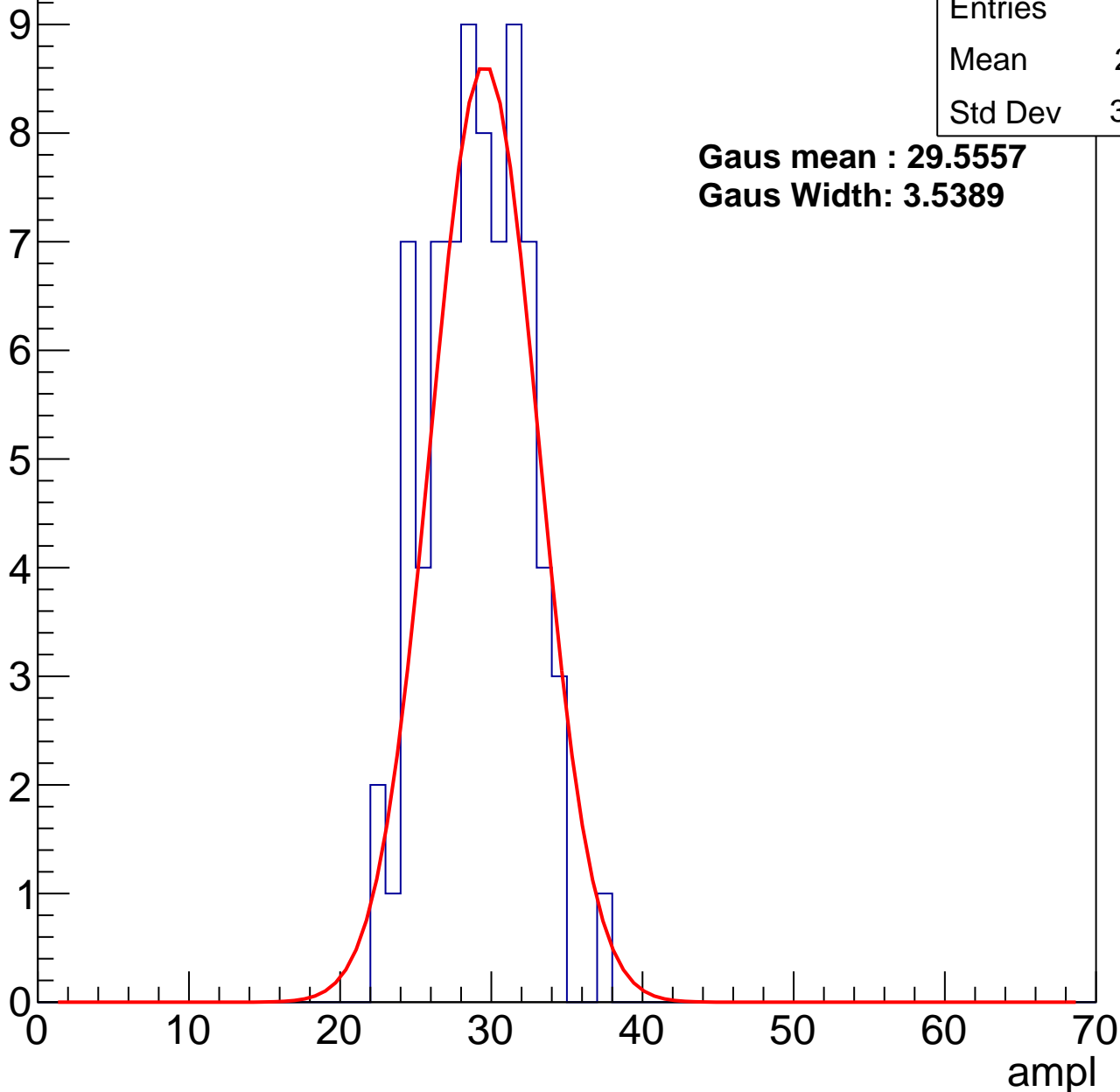
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.61
Std Dev	3.183

Gaus mean : 29.5557

Gaus Width: 3.5389



B1L102S, U26-ch58, adc1

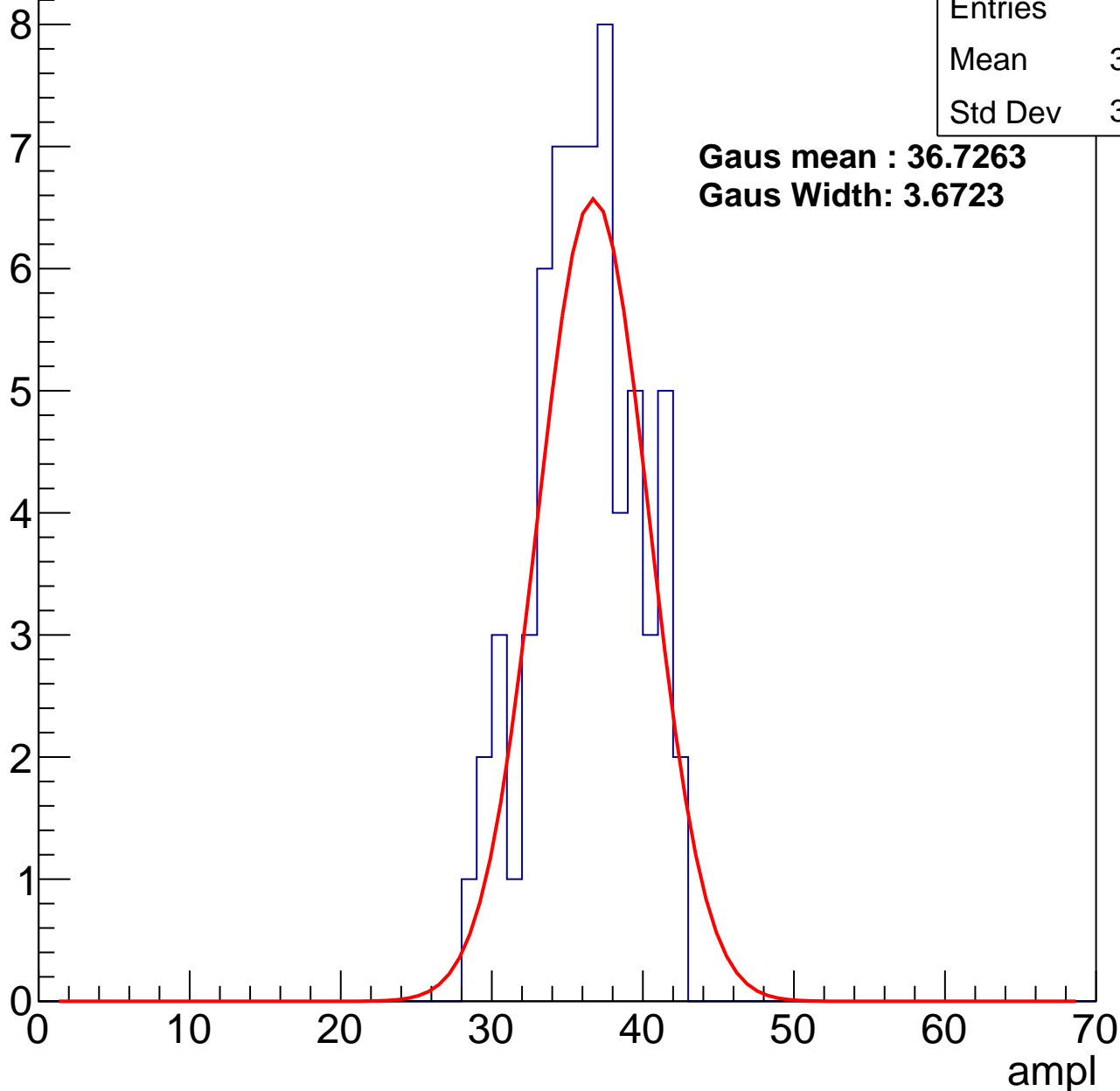
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.75
Std Dev	3.405

Gaus mean : 36.7263

Gaus Width: 3.6723



B1L102S, U26-ch58, adc2

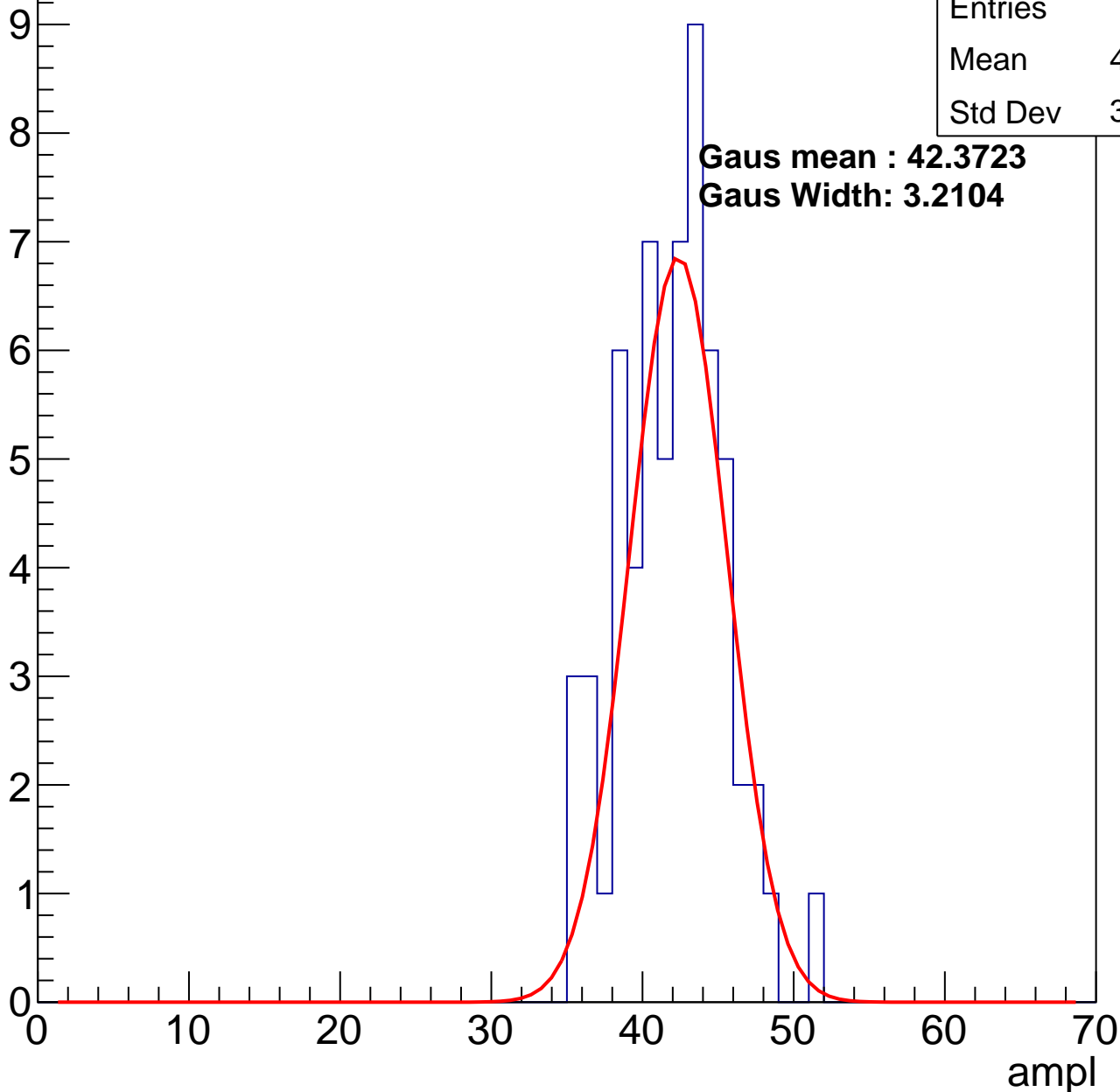
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.52
Std Dev	3.378

Gaus mean : 42.3723

Gaus Width: 3.2104

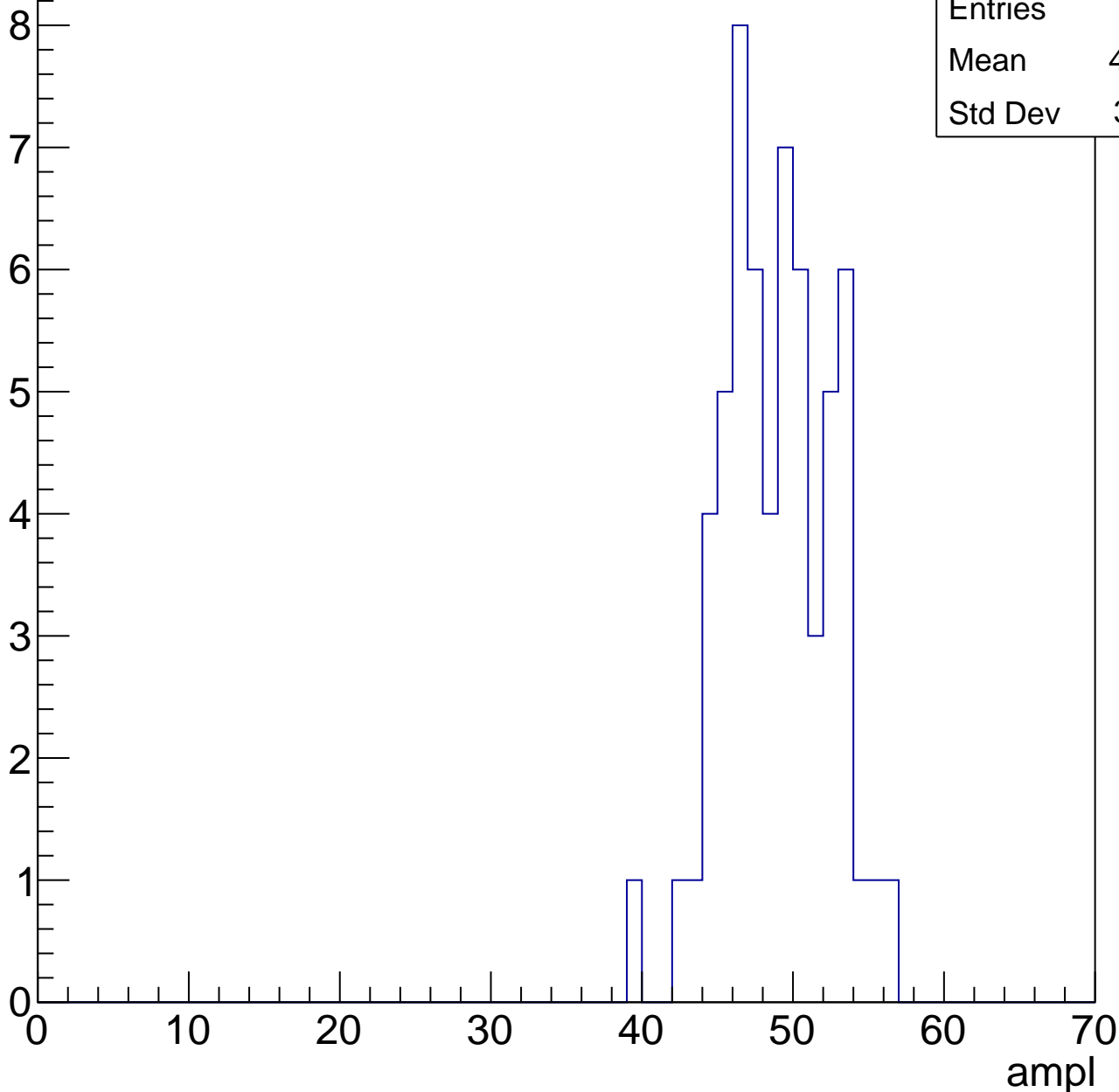


B1L102S, U26-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.43
Std Dev	3.451



B1L102S, U26-ch58, adc4

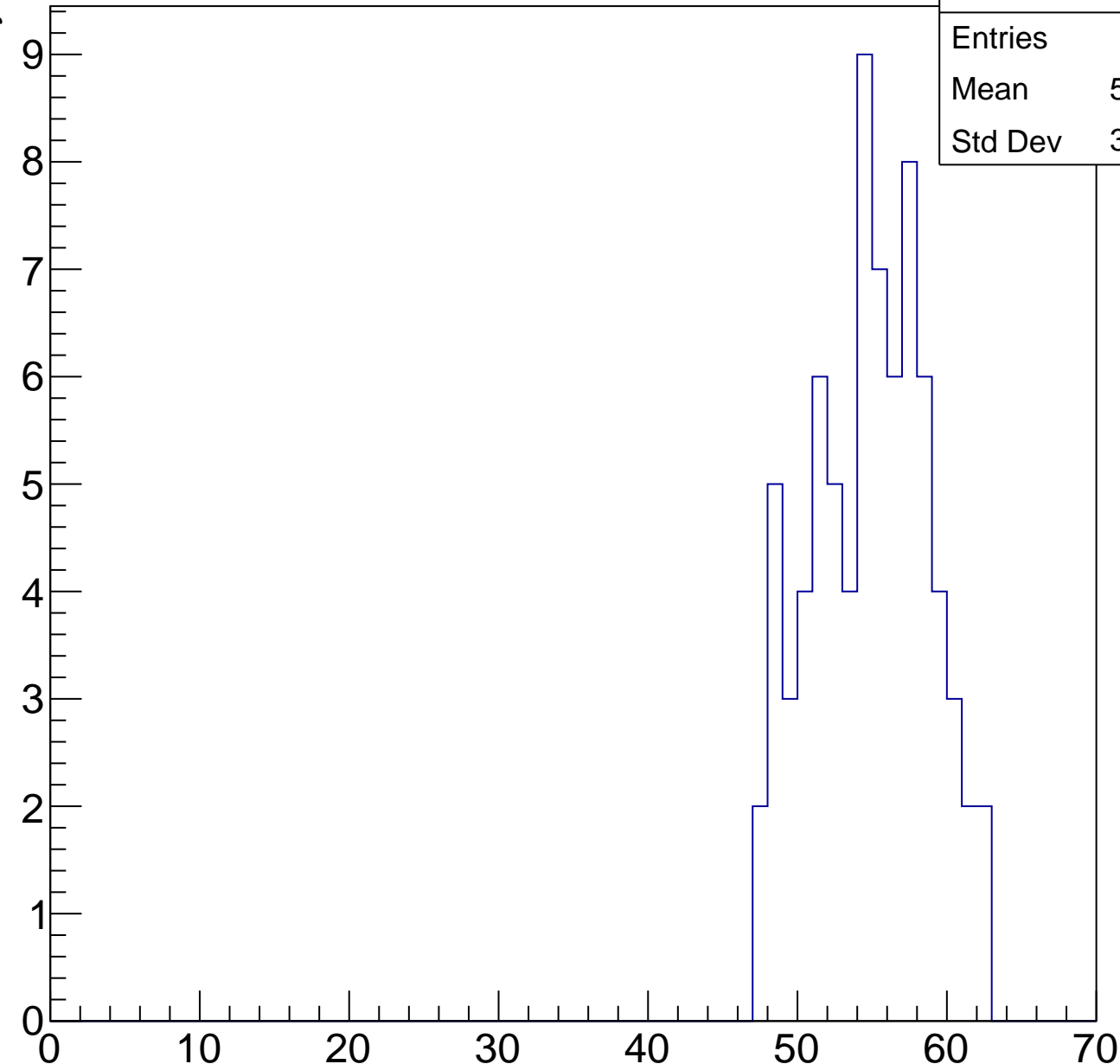
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	76
Mean	54.37
Std Dev	3.835

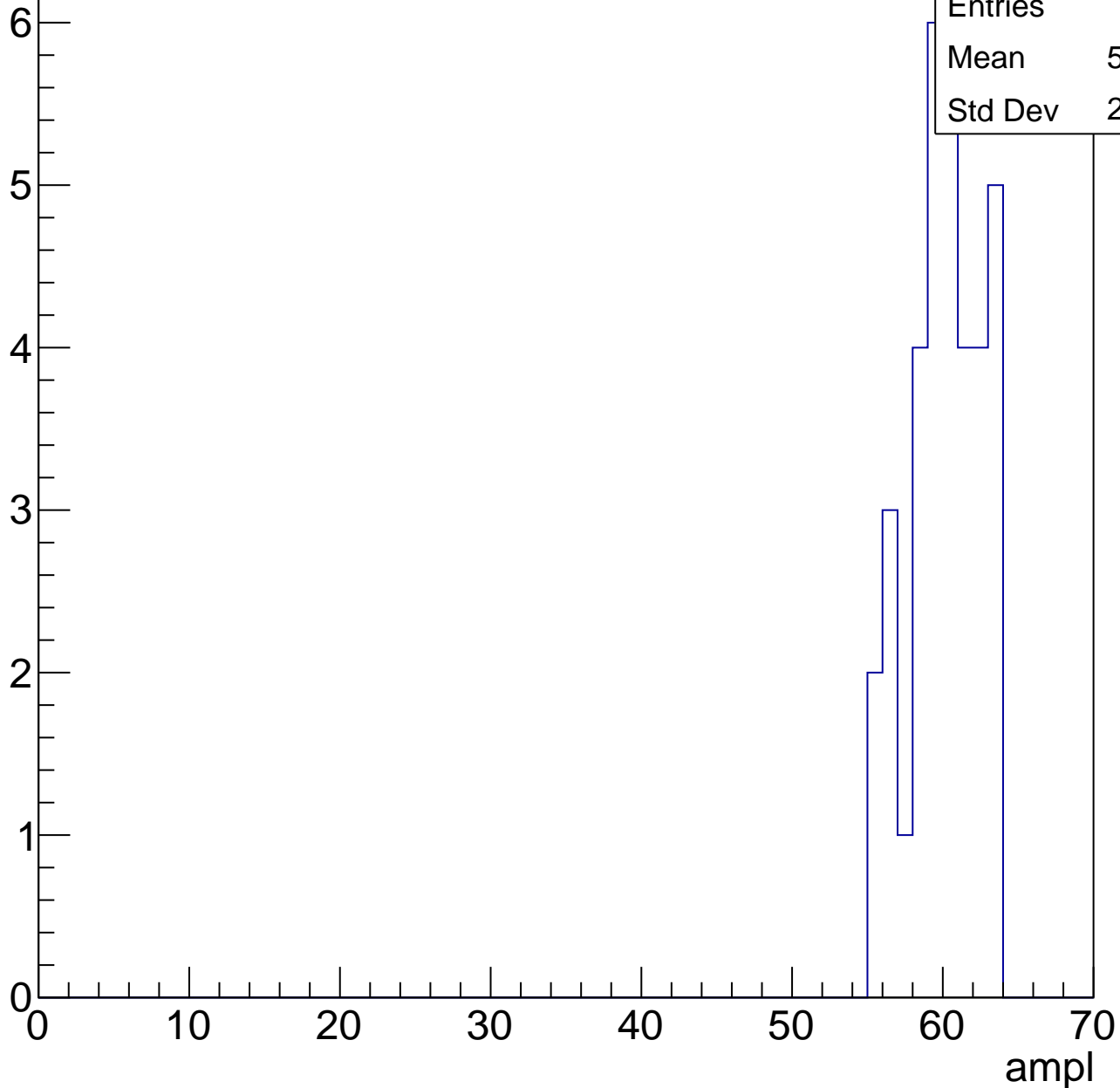
ampl



B1L102S, U26-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

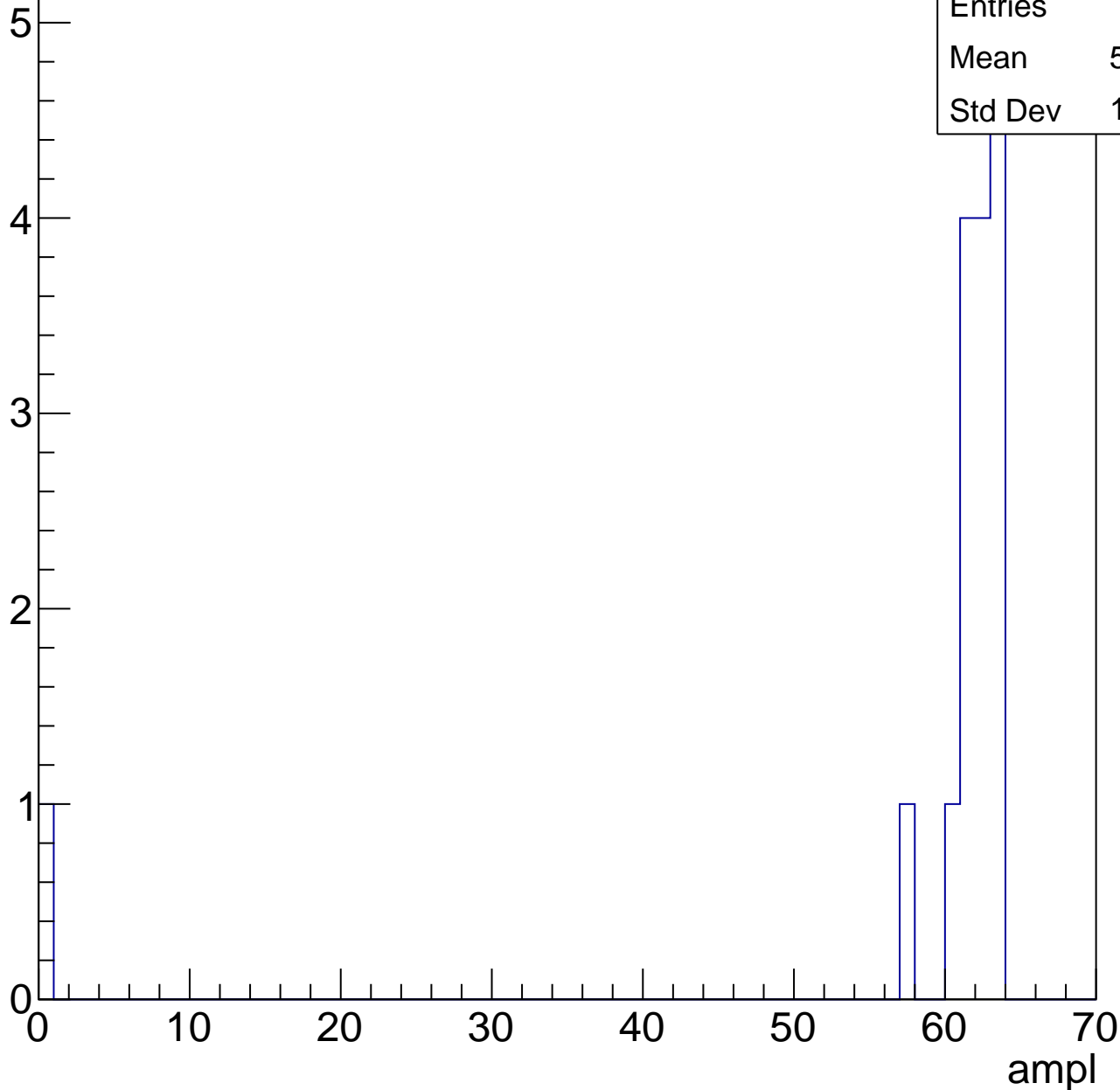


B1L102S, U26-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.75
Std Dev	14.99



B1L102S, U26-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch59, adc0

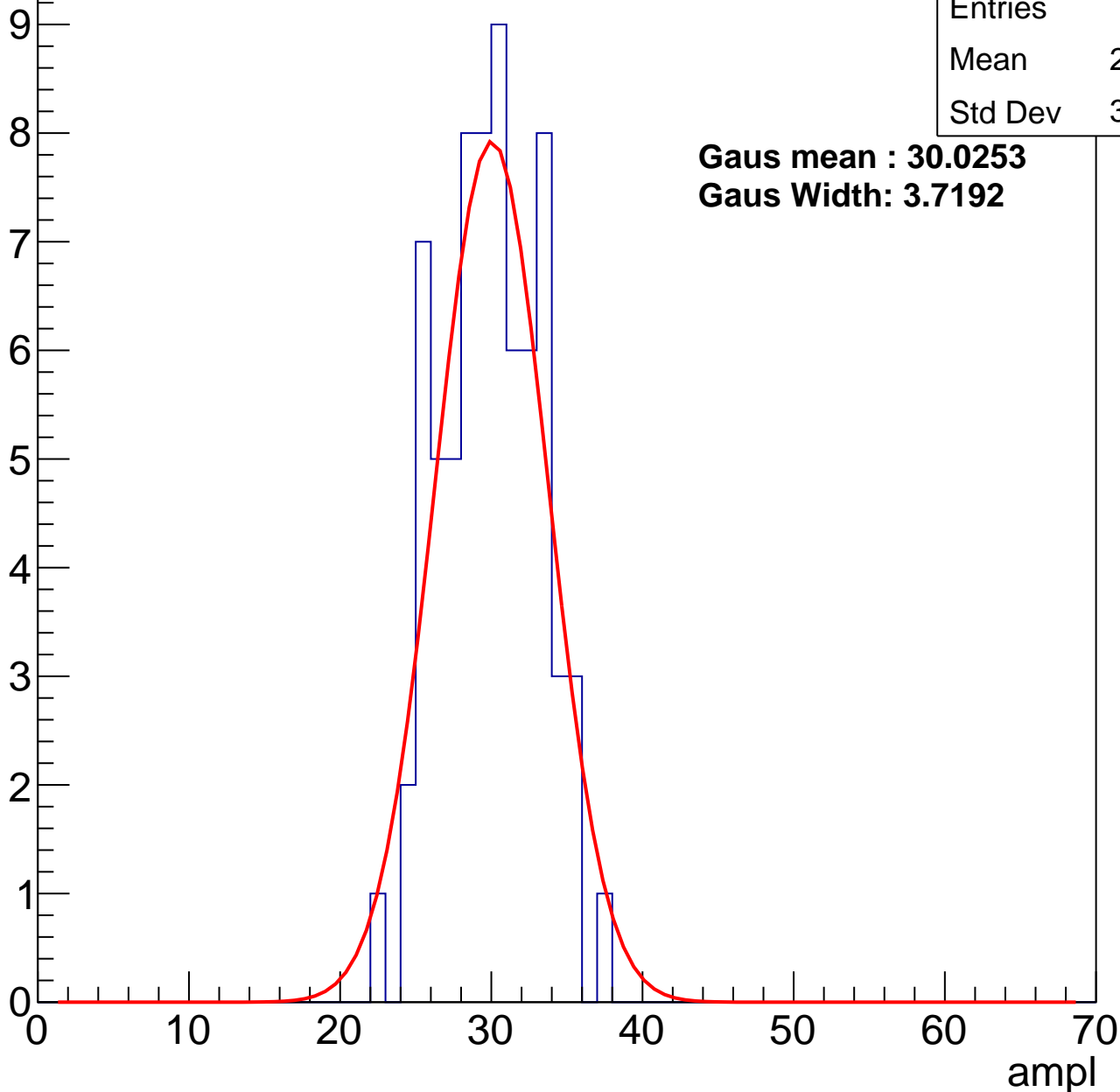
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.47
Std Dev	3.184

Gaus mean : 30.0253

Gaus Width: 3.7192

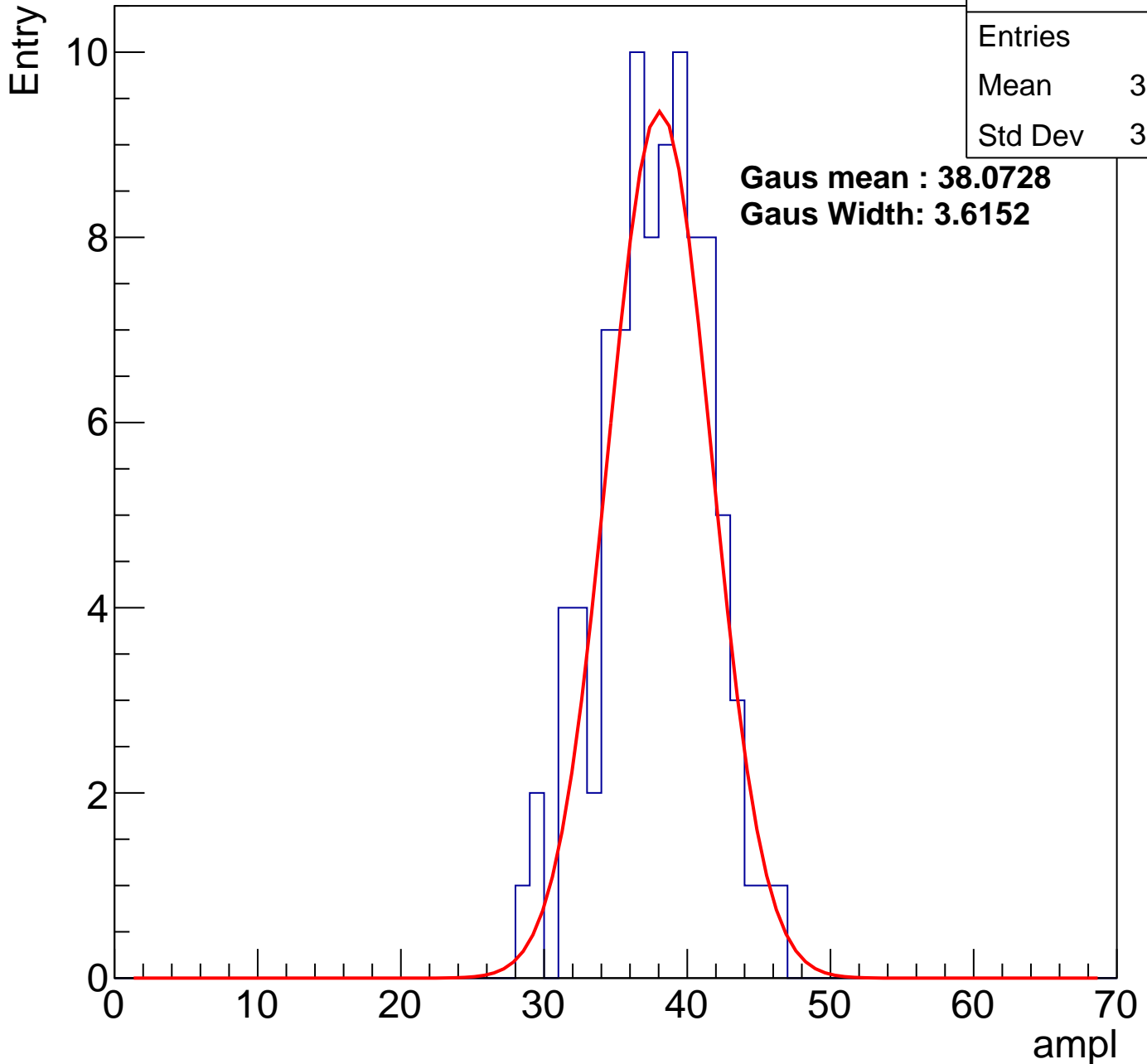


B1L102S, U26-ch59, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	37.33
Std Dev	3.709

Gaus mean : 38.0728
Gaus Width: 3.6152



B1L102S, U26-ch59, adc2

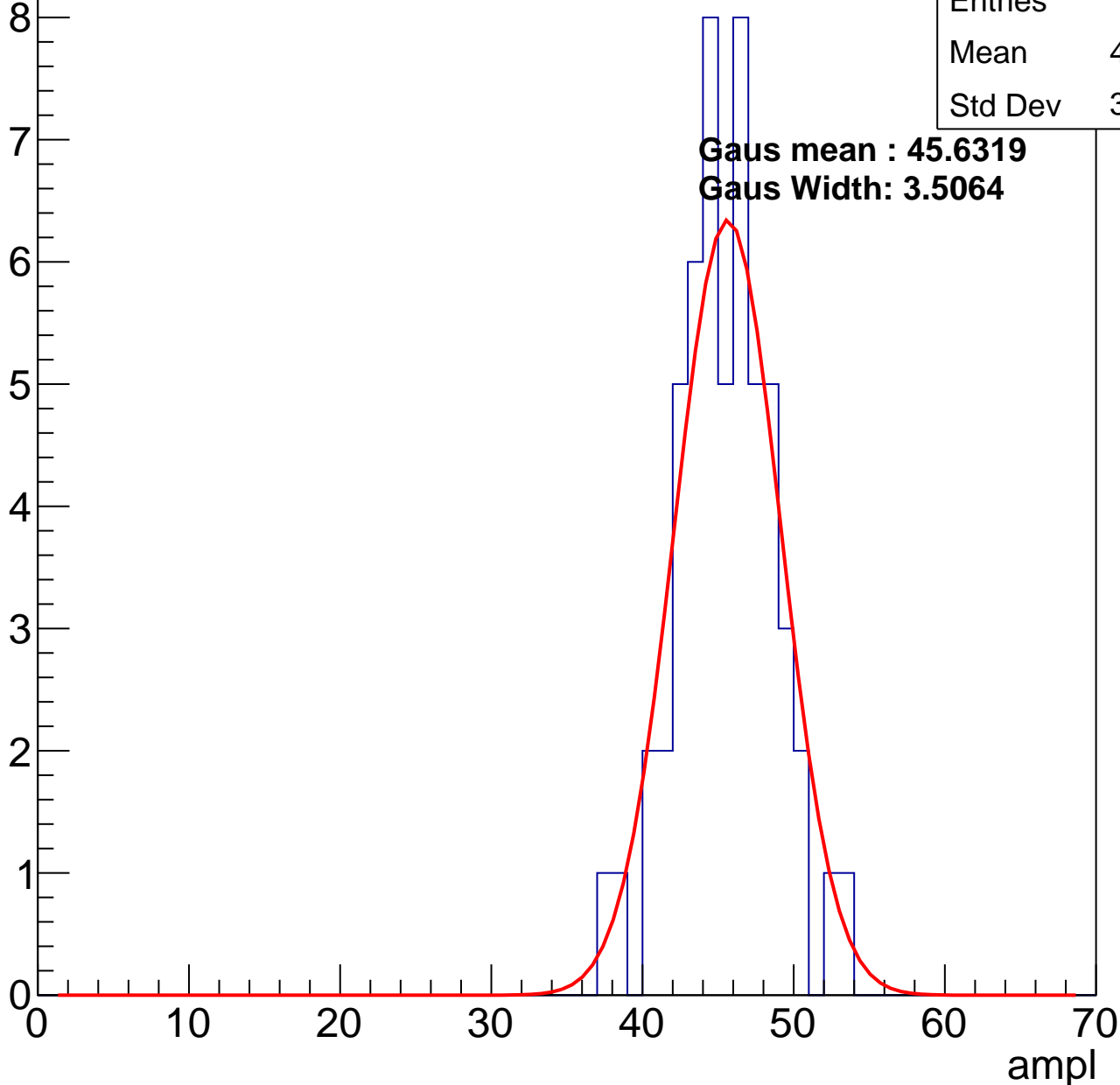
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	45.04
Std Dev	3.179

Gaus mean : 45.6319

Gaus Width: 3.5064

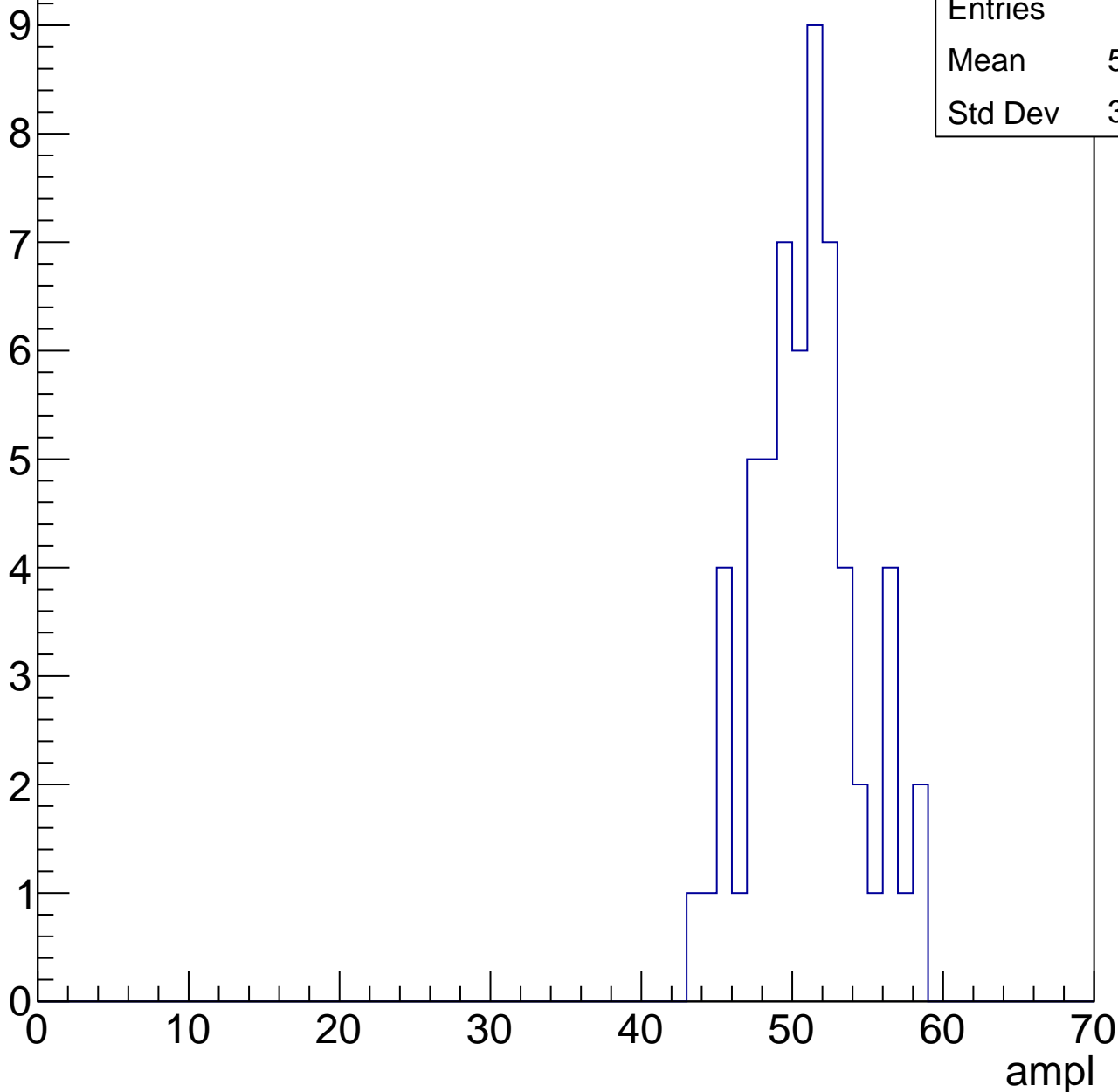


B1L102S, U26-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.43
Std Dev	3.447

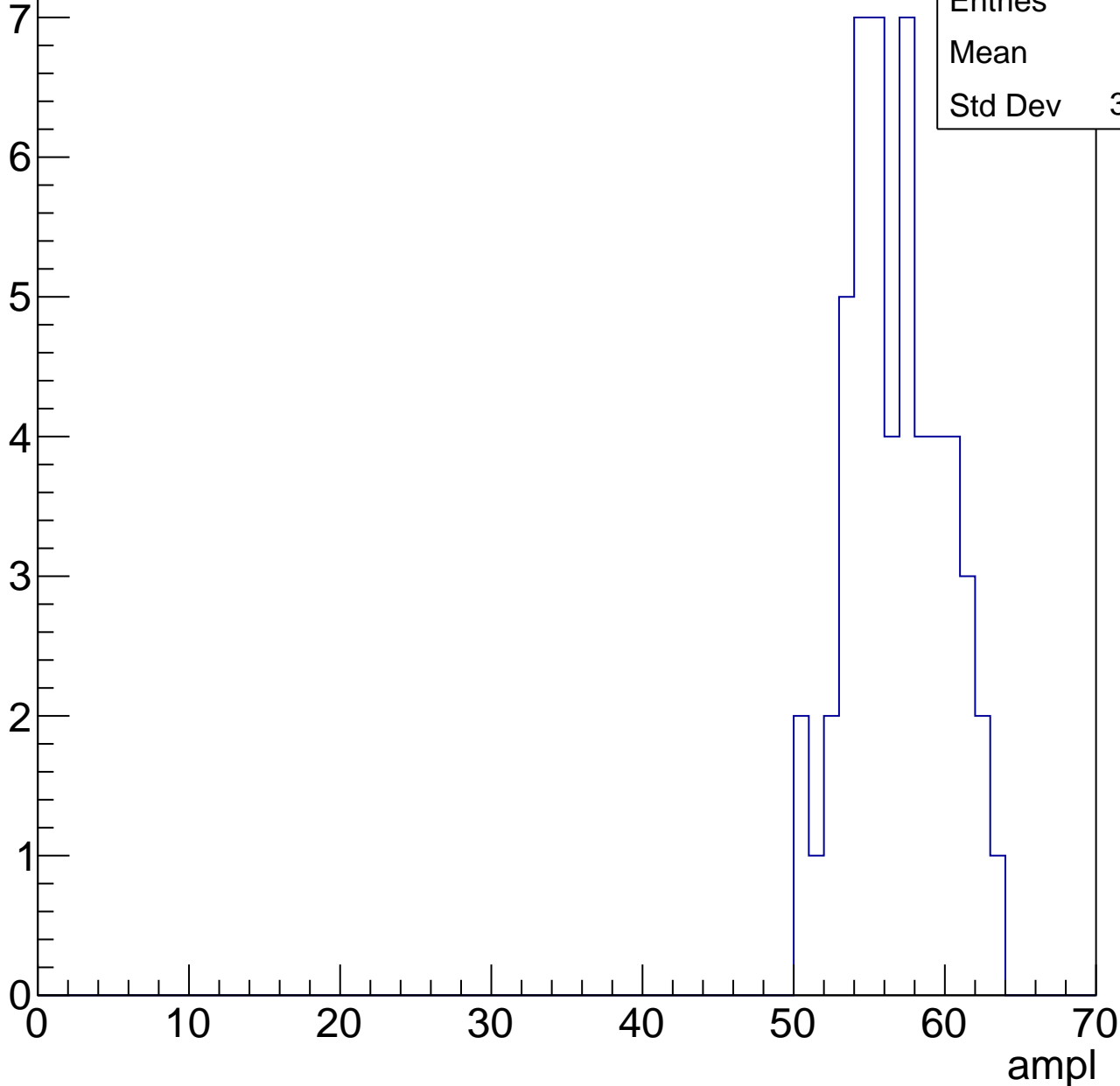


B1L102S, U26-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.3
Std Dev	3.142

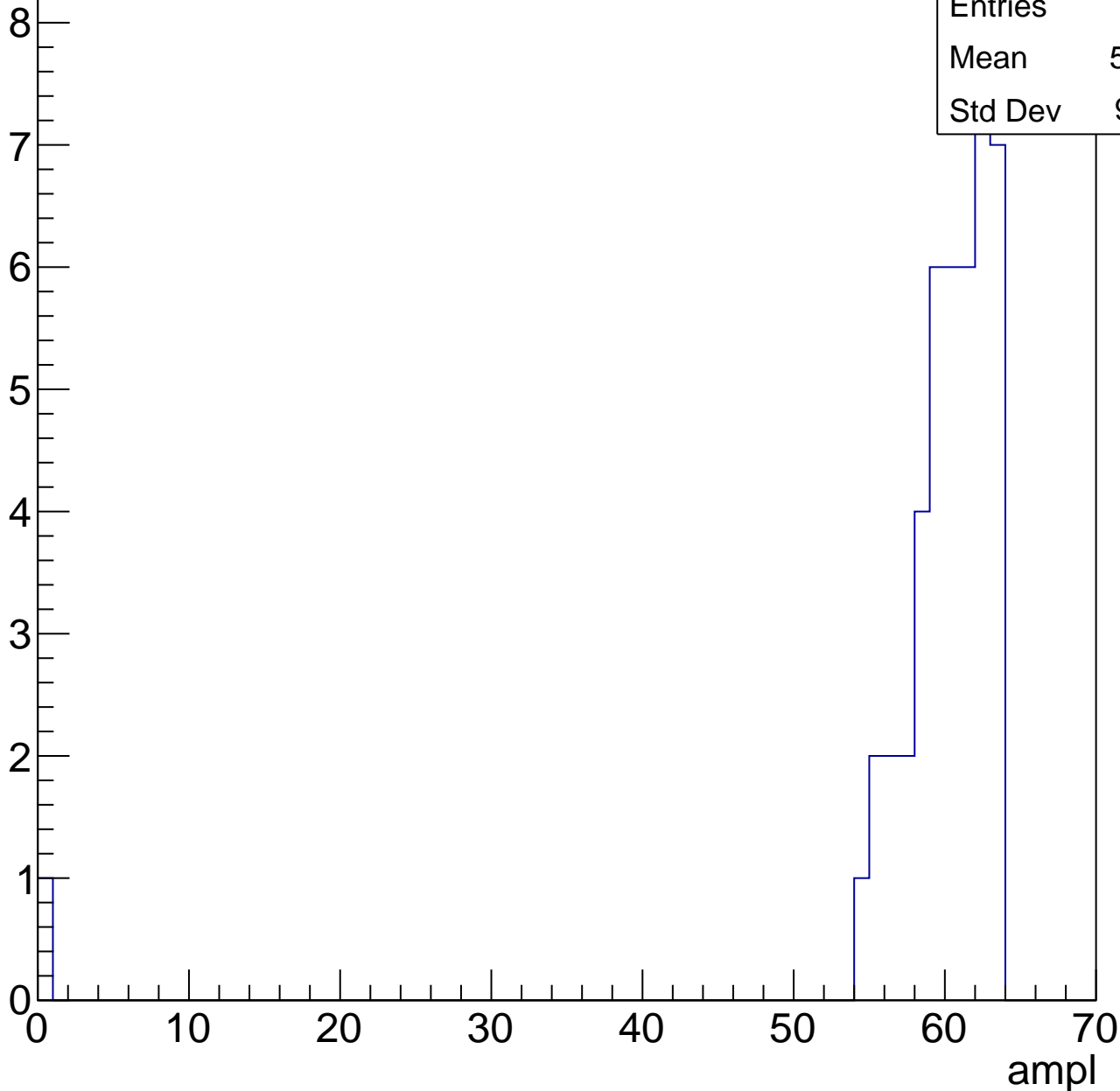


B1L102S, U26-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.64
Std Dev	9.161



B1L102S, U26-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch60, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	29.43
Std Dev	4.05

Gaus mean : 29.5582

Gaus Width: 3.8552

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L102S, U26-ch60, adc1

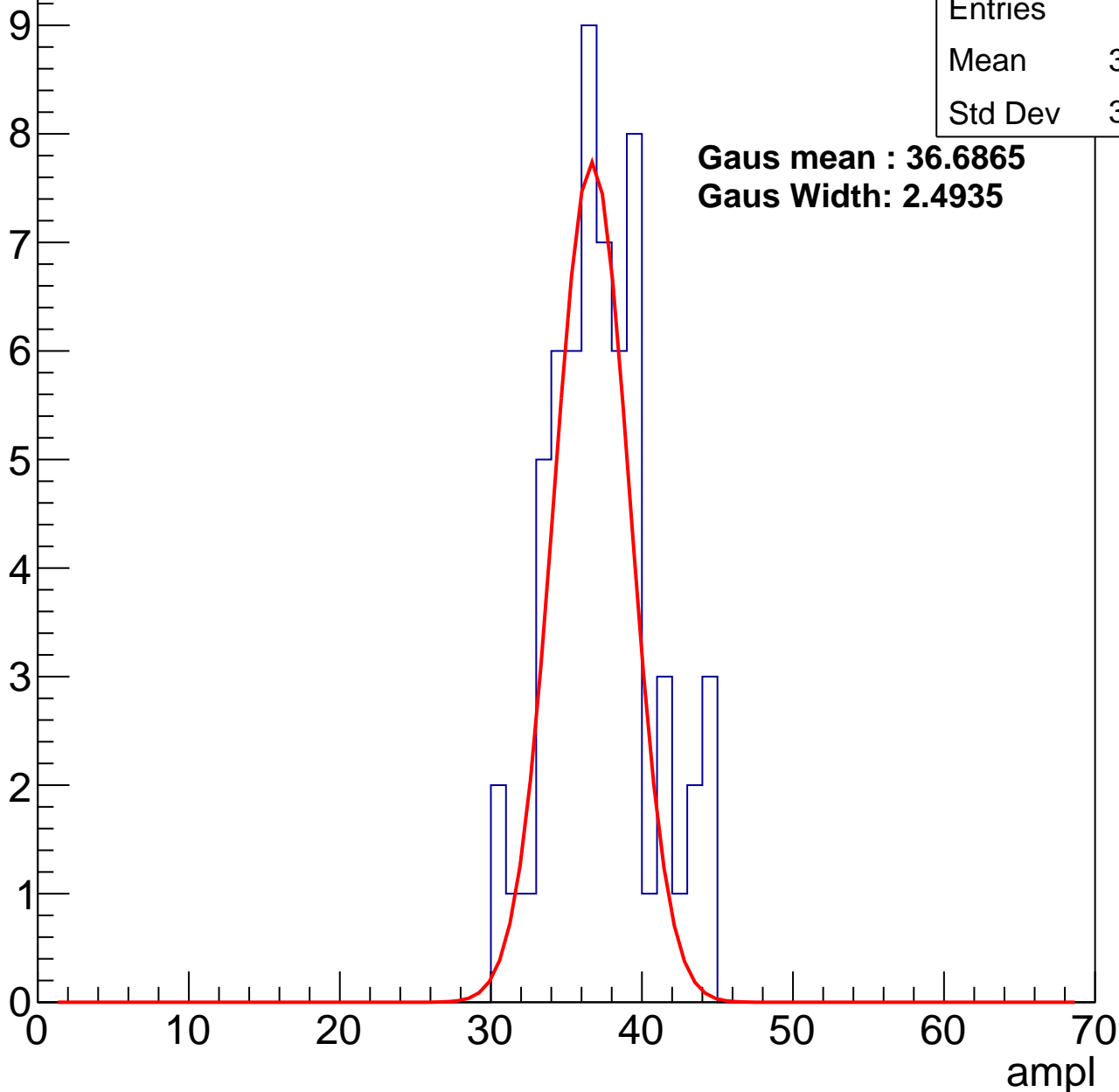
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.85
Std Dev	3.298

Gaus mean : 36.6865

Gaus Width: 2.4935



B1L102S, U26-ch60, adc2

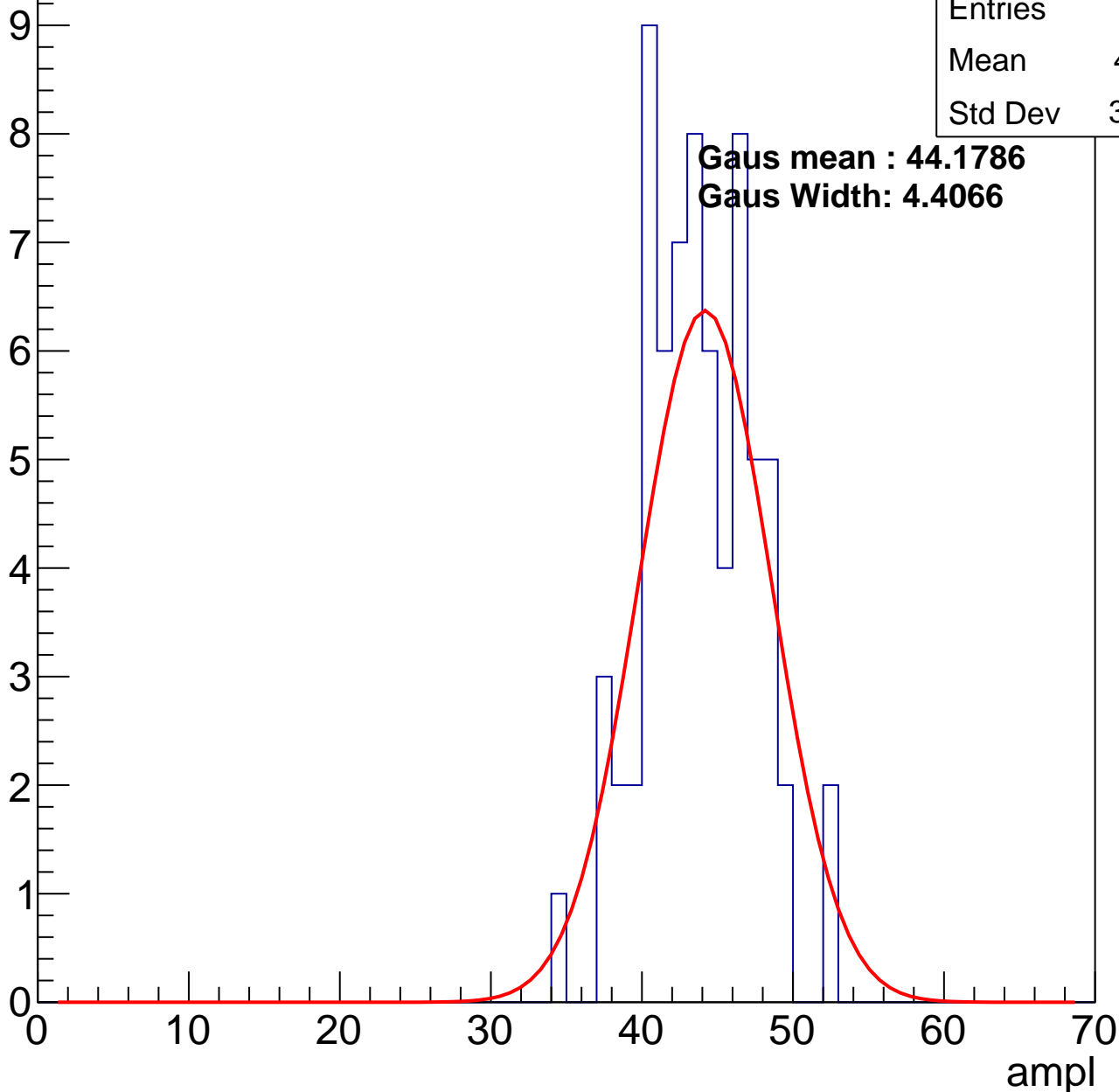
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.31
Std Dev	3.604

Gaus mean : 44.1786

Gaus Width: 4.4066

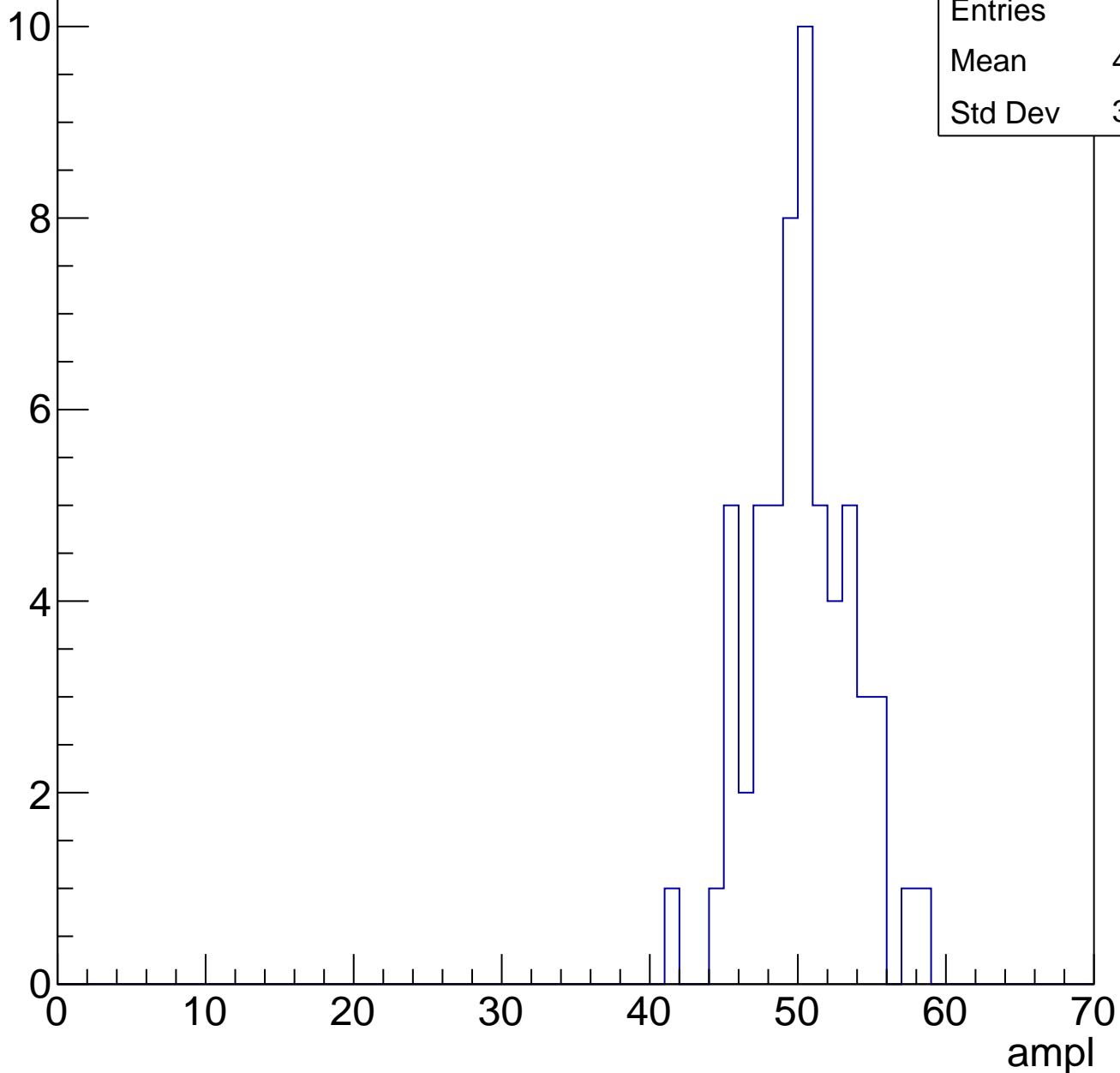


B1L102S, U26-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	49.81
Std Dev	3.311

Entry

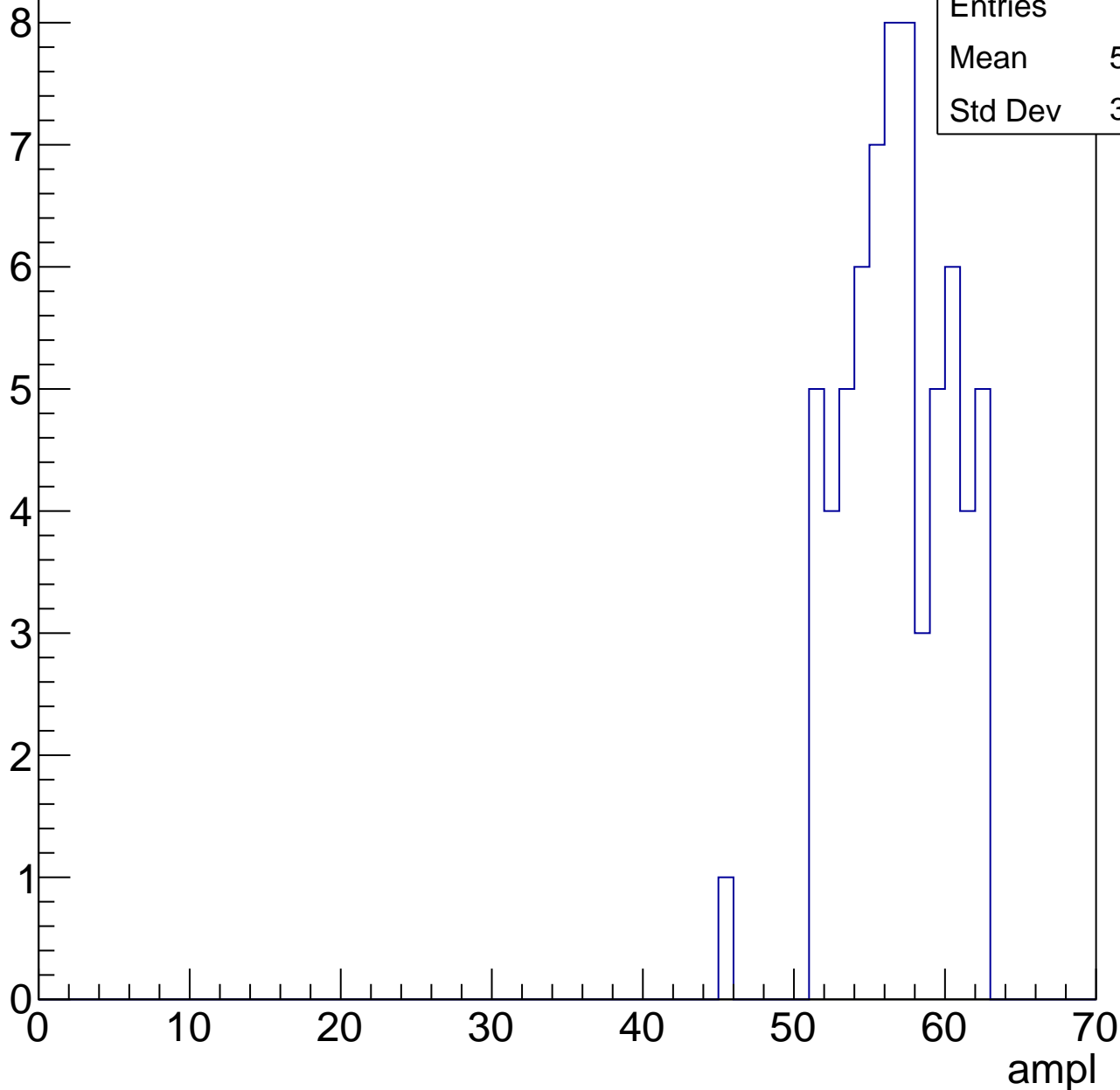


B1L102S, U26-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	56.25
Std Dev	3.504

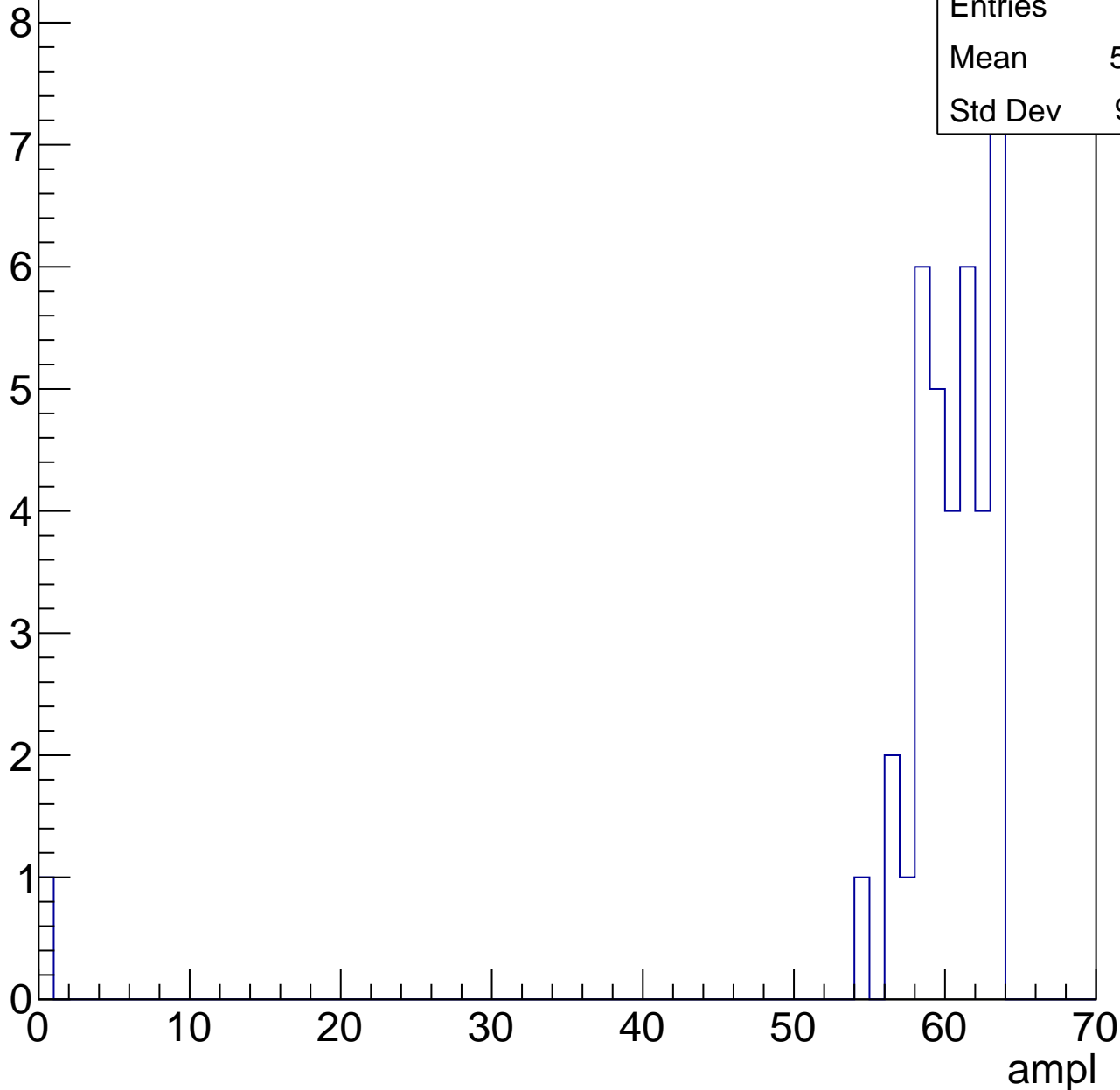


B1L102S, U26-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.53
Std Dev	9.891



B1L102S, U26-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch61, adc0

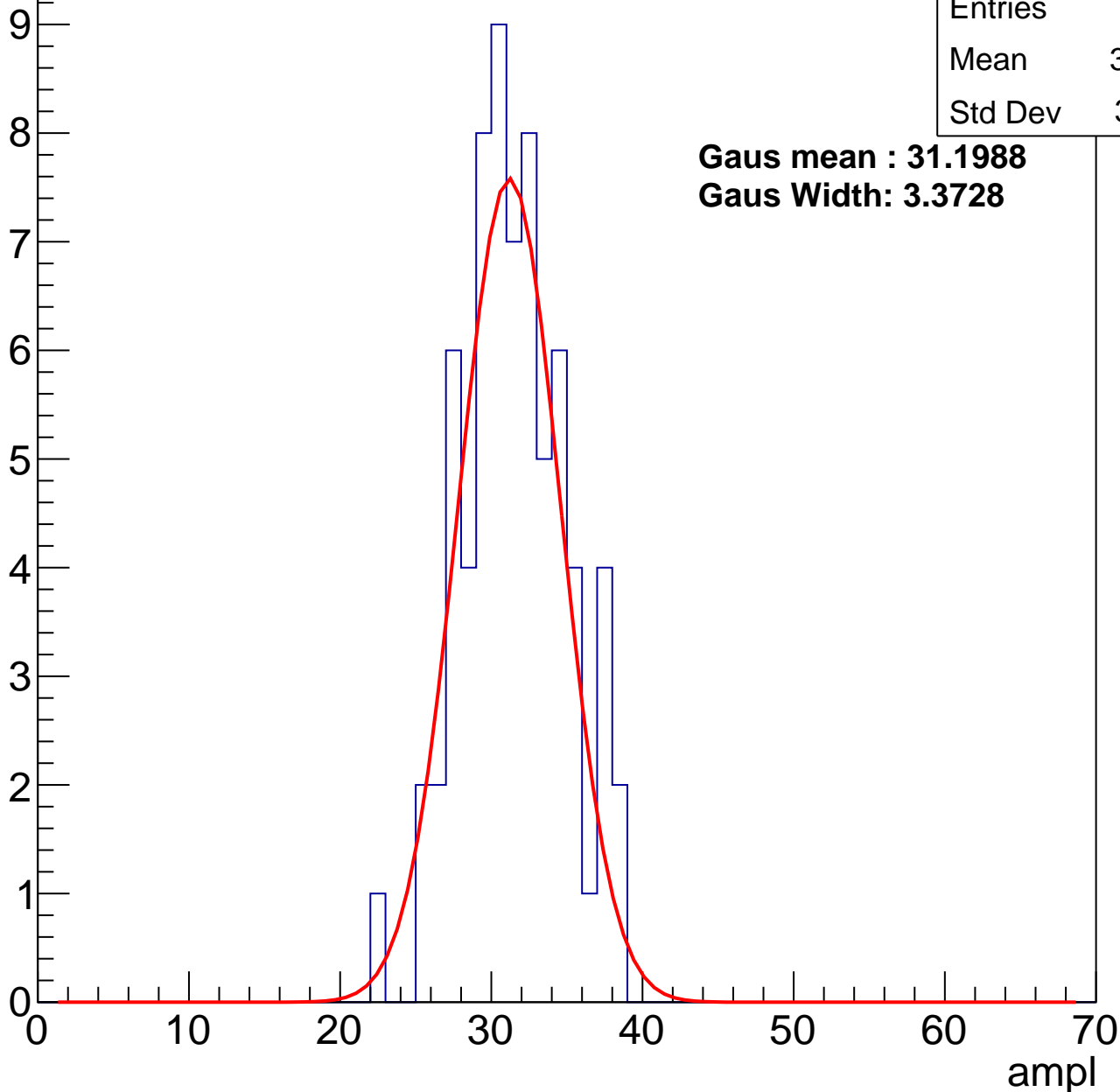
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	31.04
Std Dev	3.381

Gaus mean : 31.1988

Gaus Width: 3.3728



B1L102S, U26-ch61, adc1

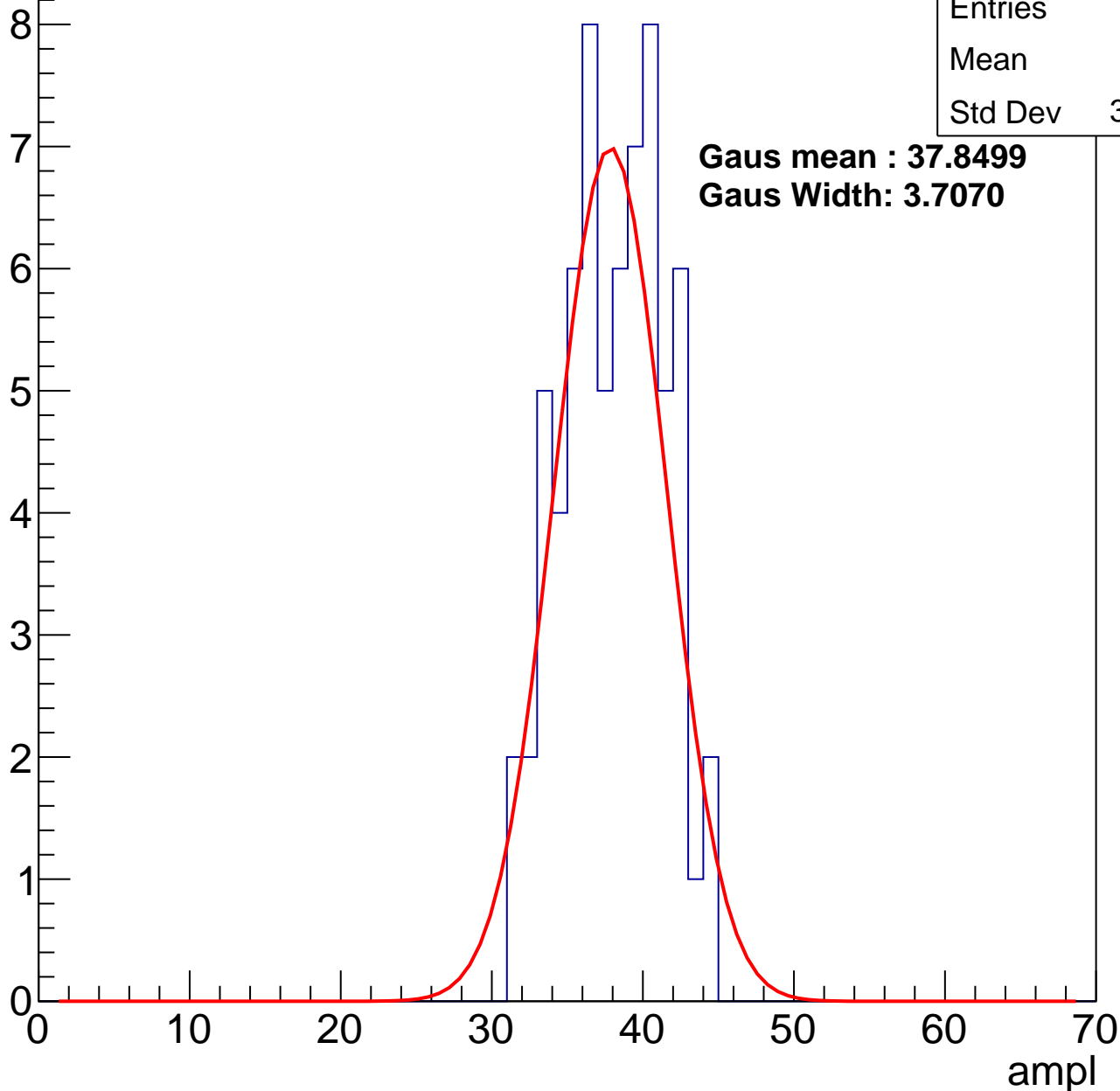
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.6
Std Dev	3.269

Gaus mean : 37.8499

Gaus Width: 3.7070



B1L102S, U26-ch61, adc2

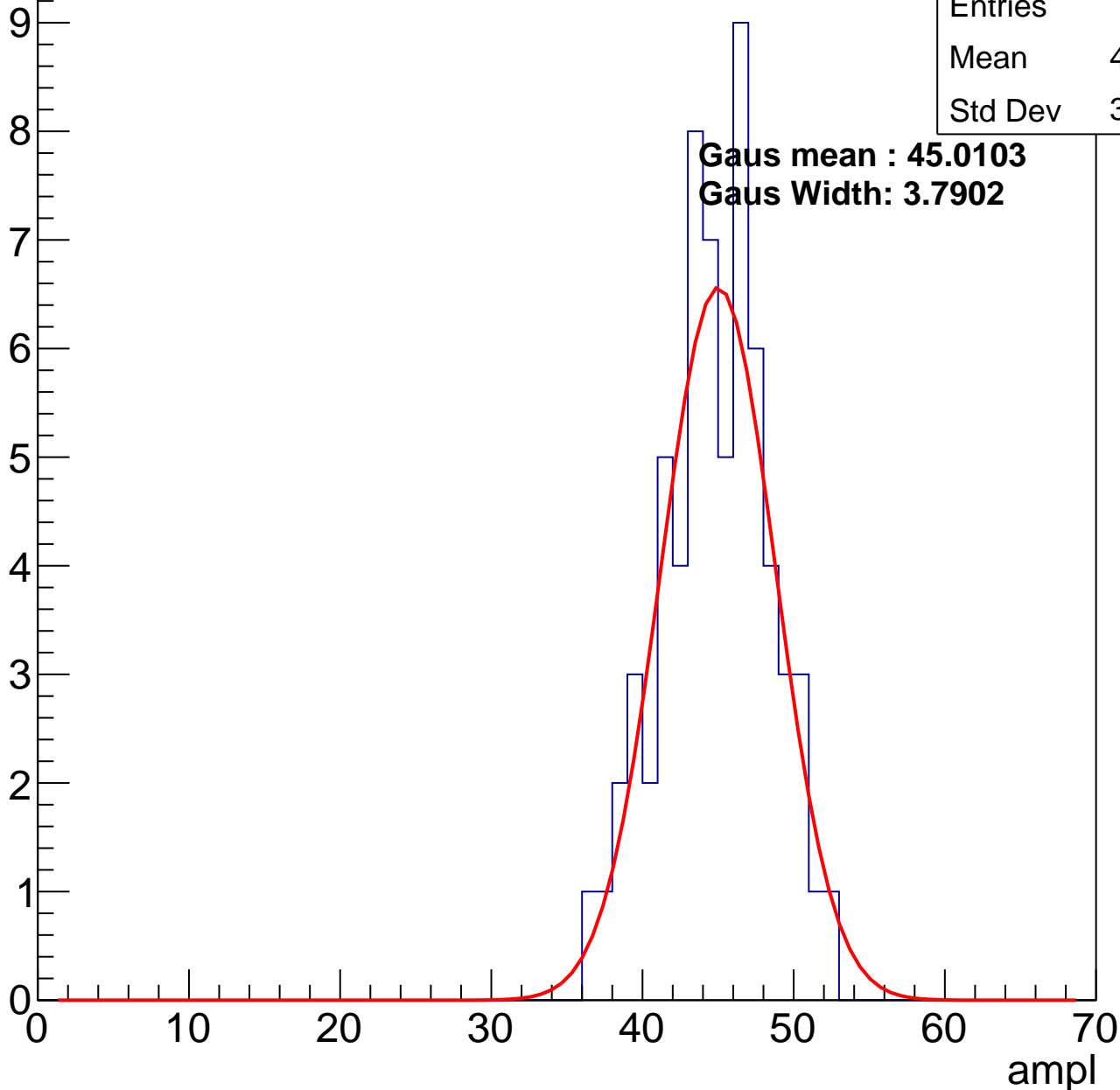
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.37
Std Dev	3.524

Gaus mean : 45.0103

Gaus Width: 3.7902

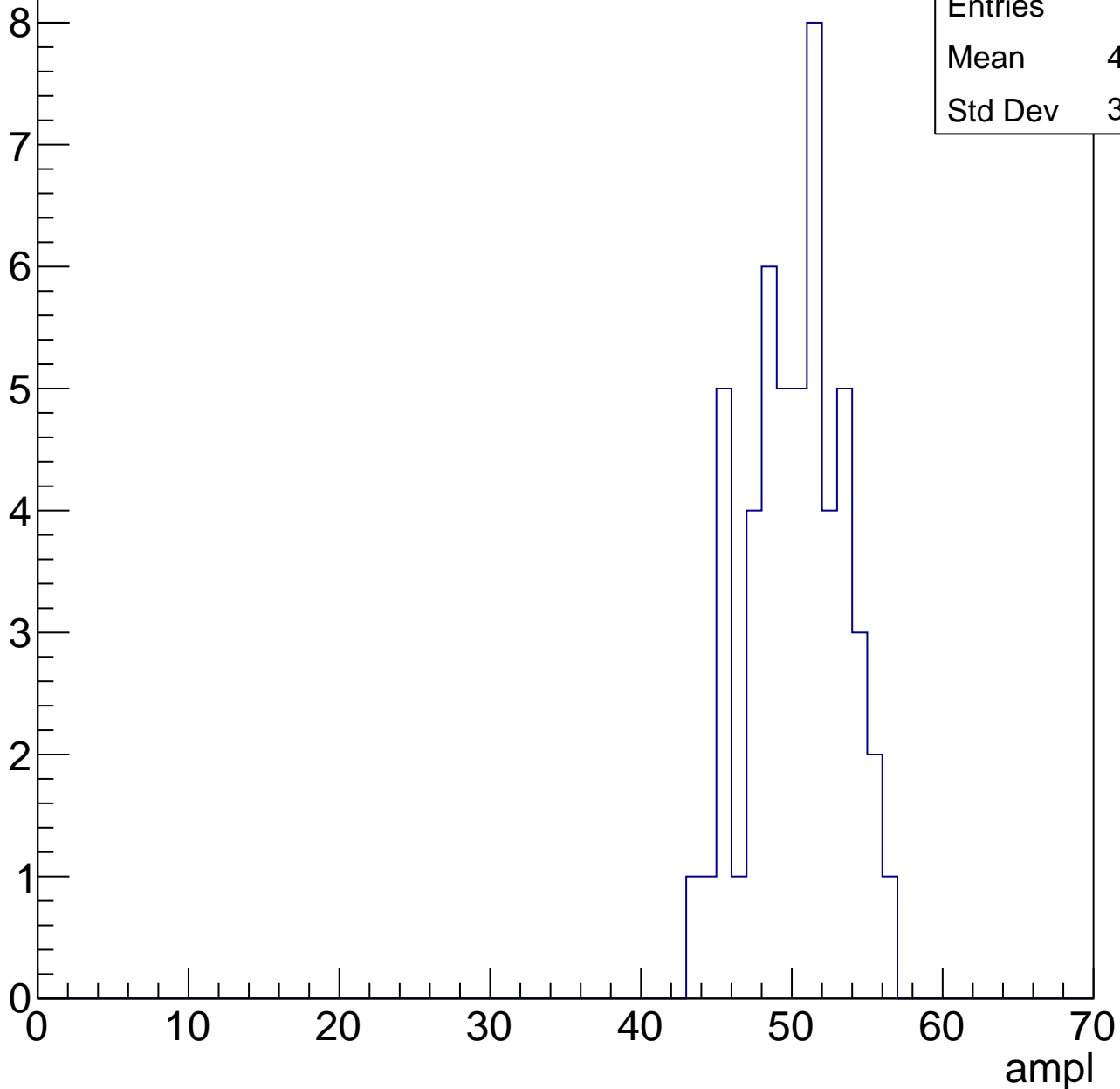


B1L102S, U26-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.76
Std Dev	3.103



B1L102S, U26-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	55.68
Std Dev	7.178

Entry

10

8

6

4

2

0

0

10

20

30

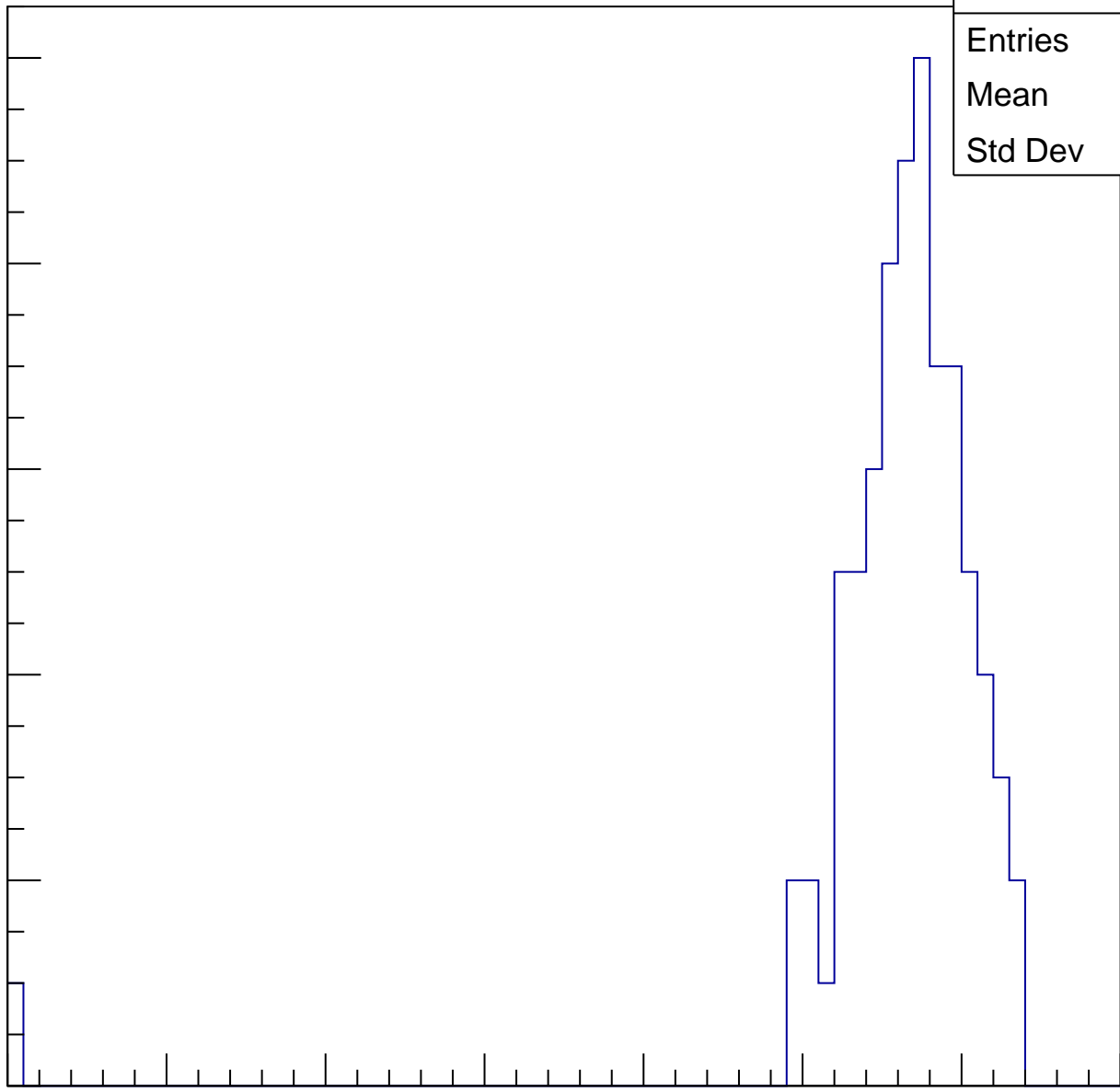
40

50

60

70

ampl

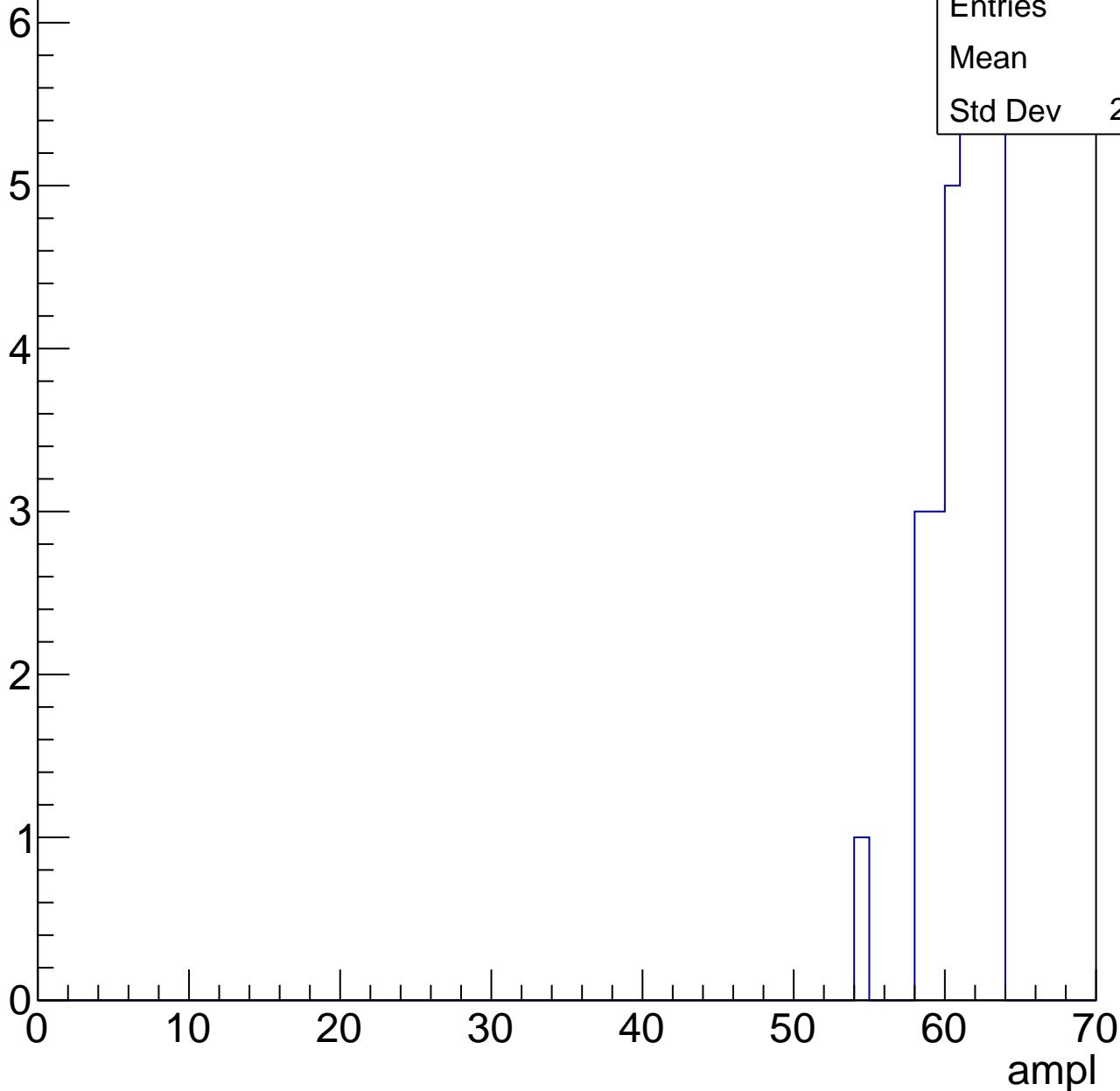


B1L102S, U26-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	60.7
Std Dev	2.002



B1L102S, U26-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch62, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	97
Mean	27.11
Std Dev	6.384

Gaus mean : 28.7481

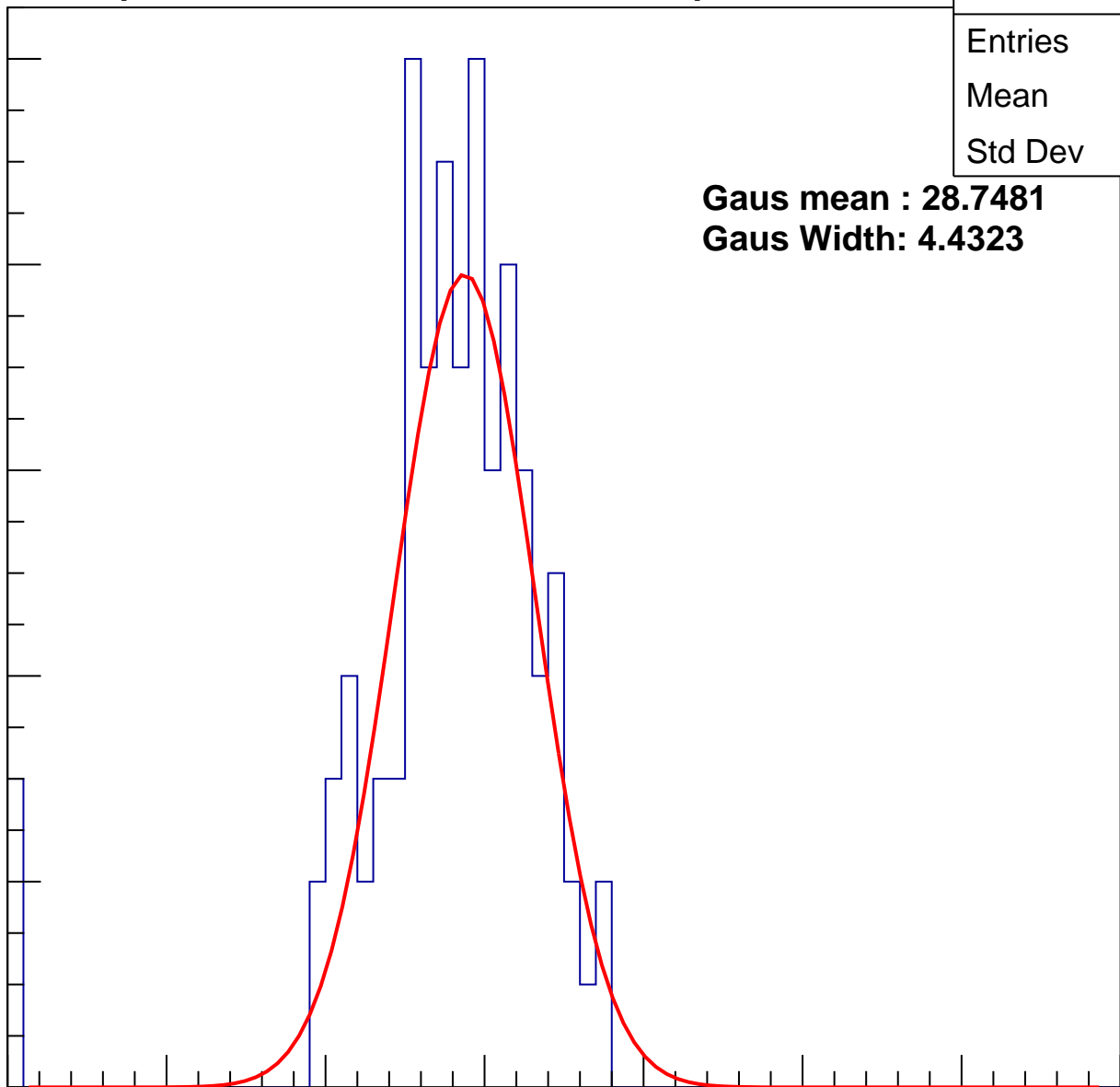
Gaus Width: 4.4323

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch62, adc1

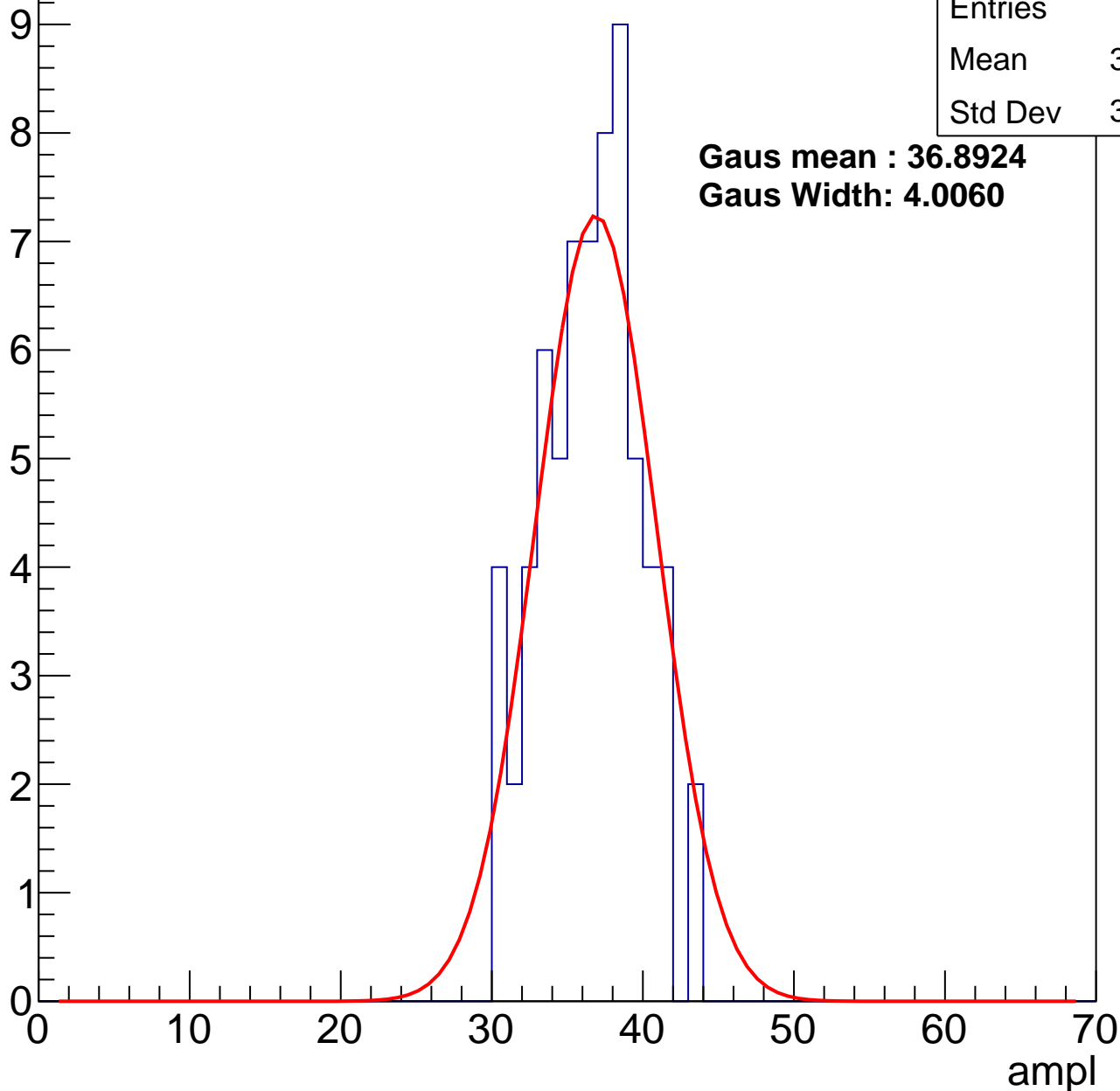
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.09
Std Dev	3.208

Gaus mean : 36.8924

Gaus Width: 4.0060



B1L102S, U26-ch62, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	43.16
Std Dev	2.98

Gaus mean : 43.4773

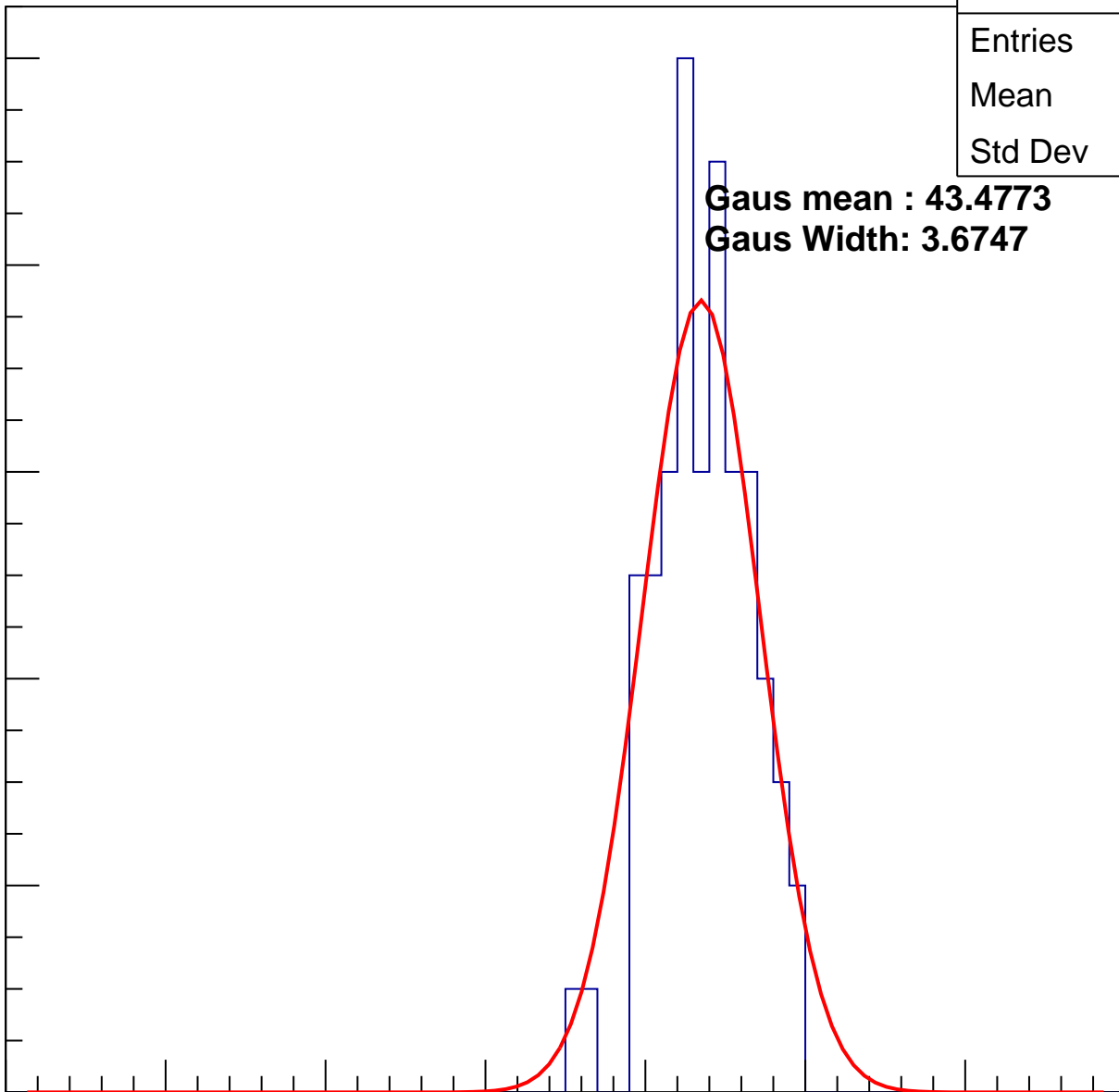
Gaus Width: 3.6747

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

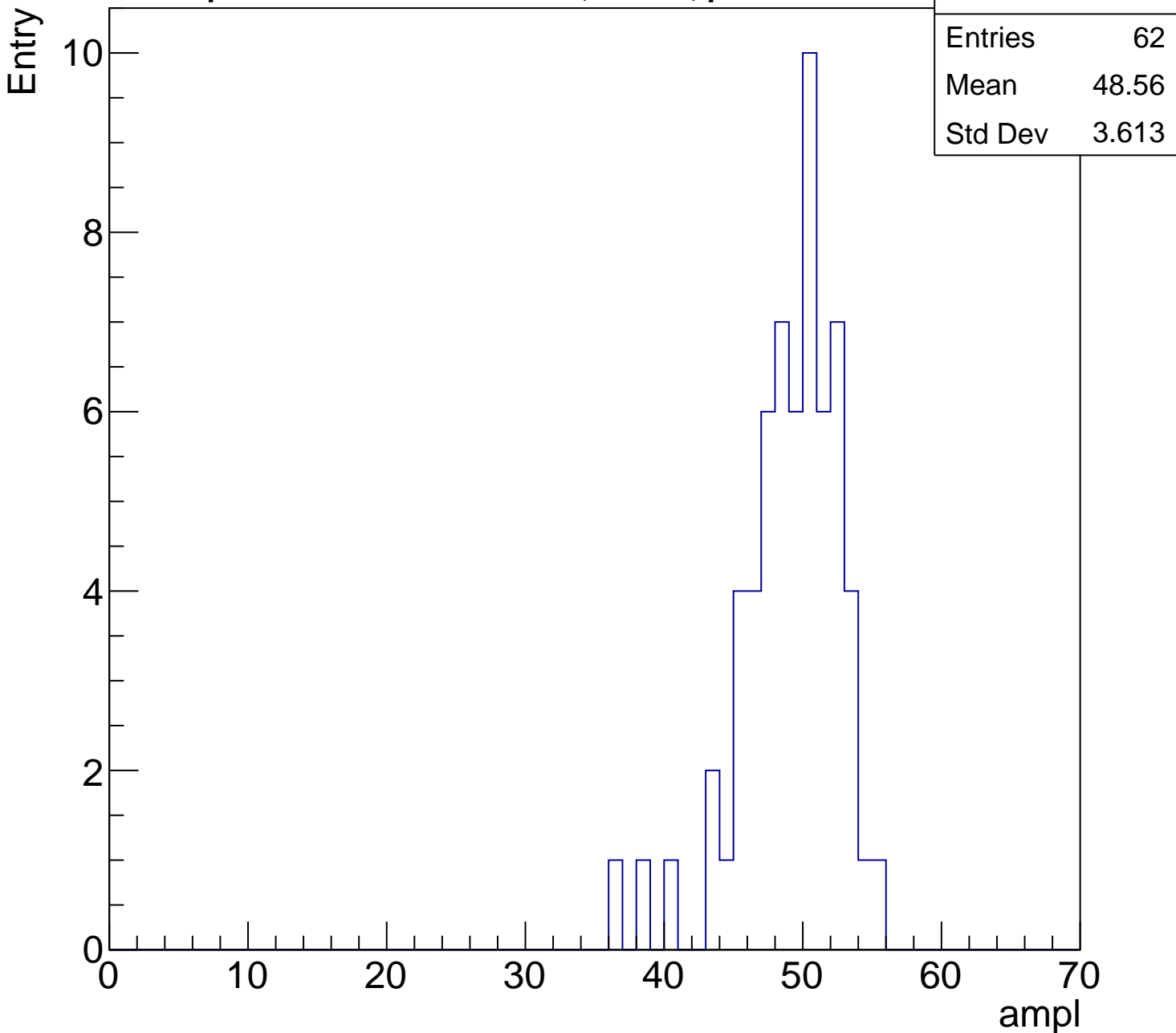
Entries	62
Mean	48.56
Std Dev	3.613

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

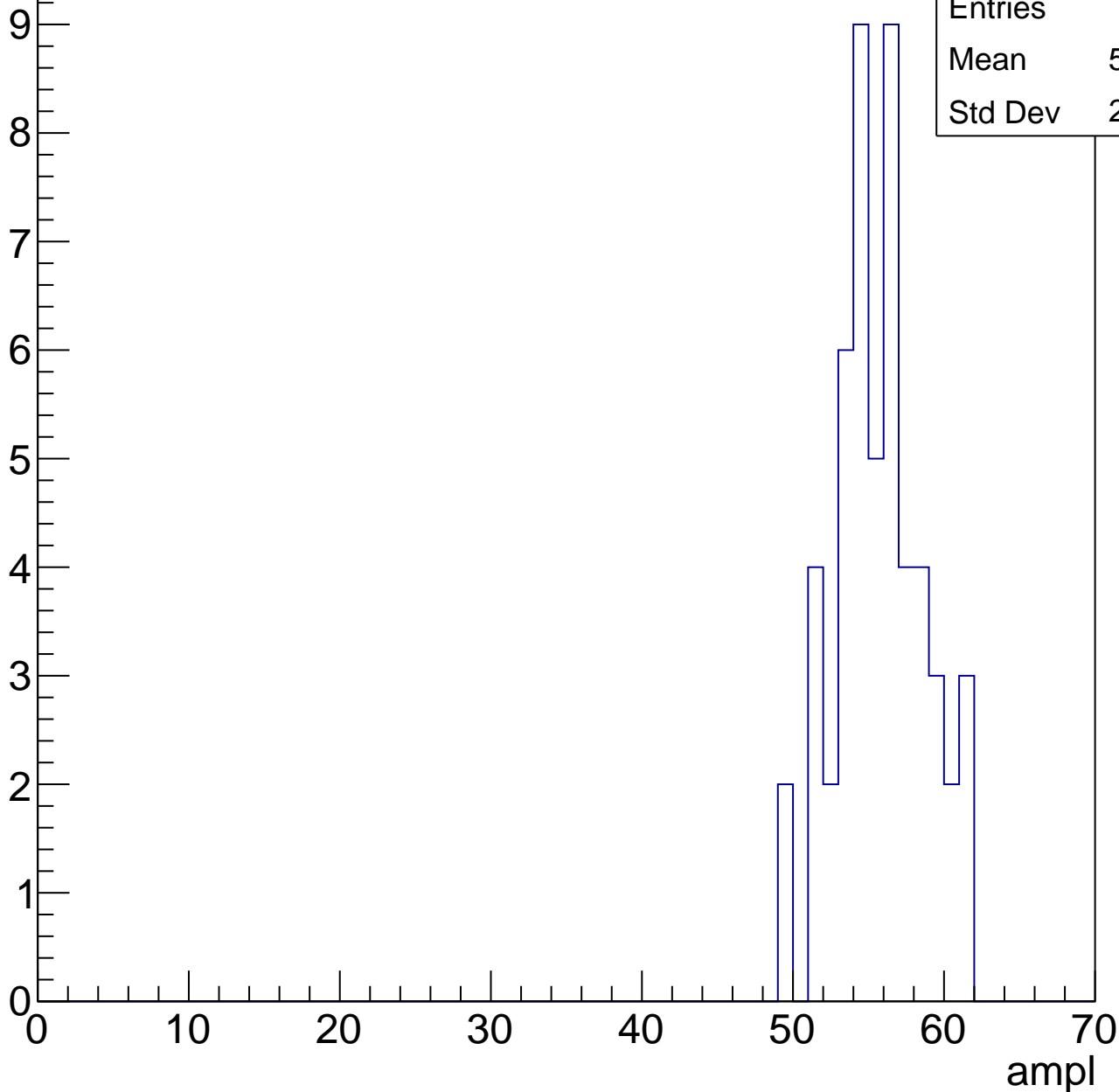


B1L102S, U26-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.26
Std Dev	2.915

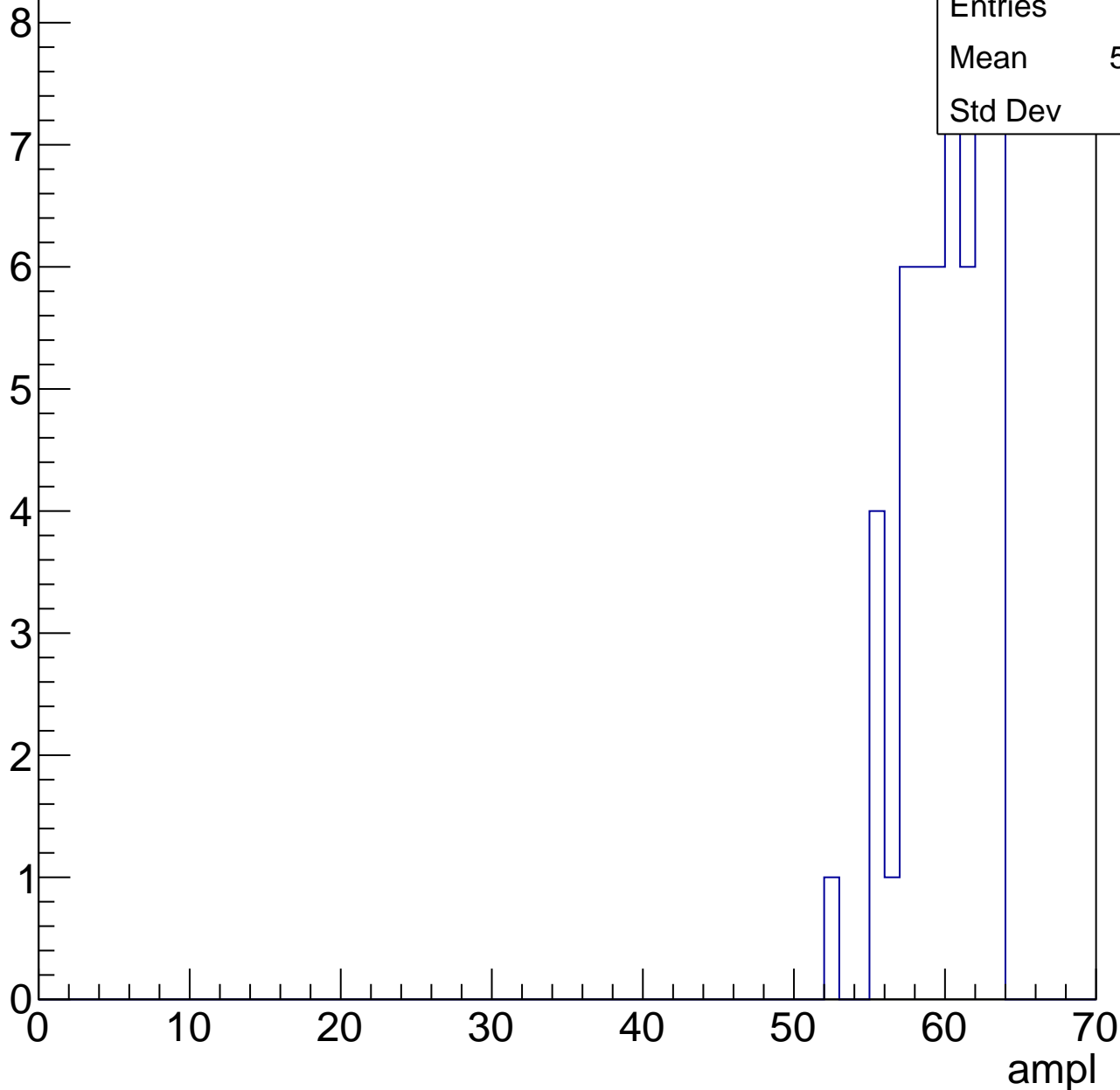


B1L102S, U26-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

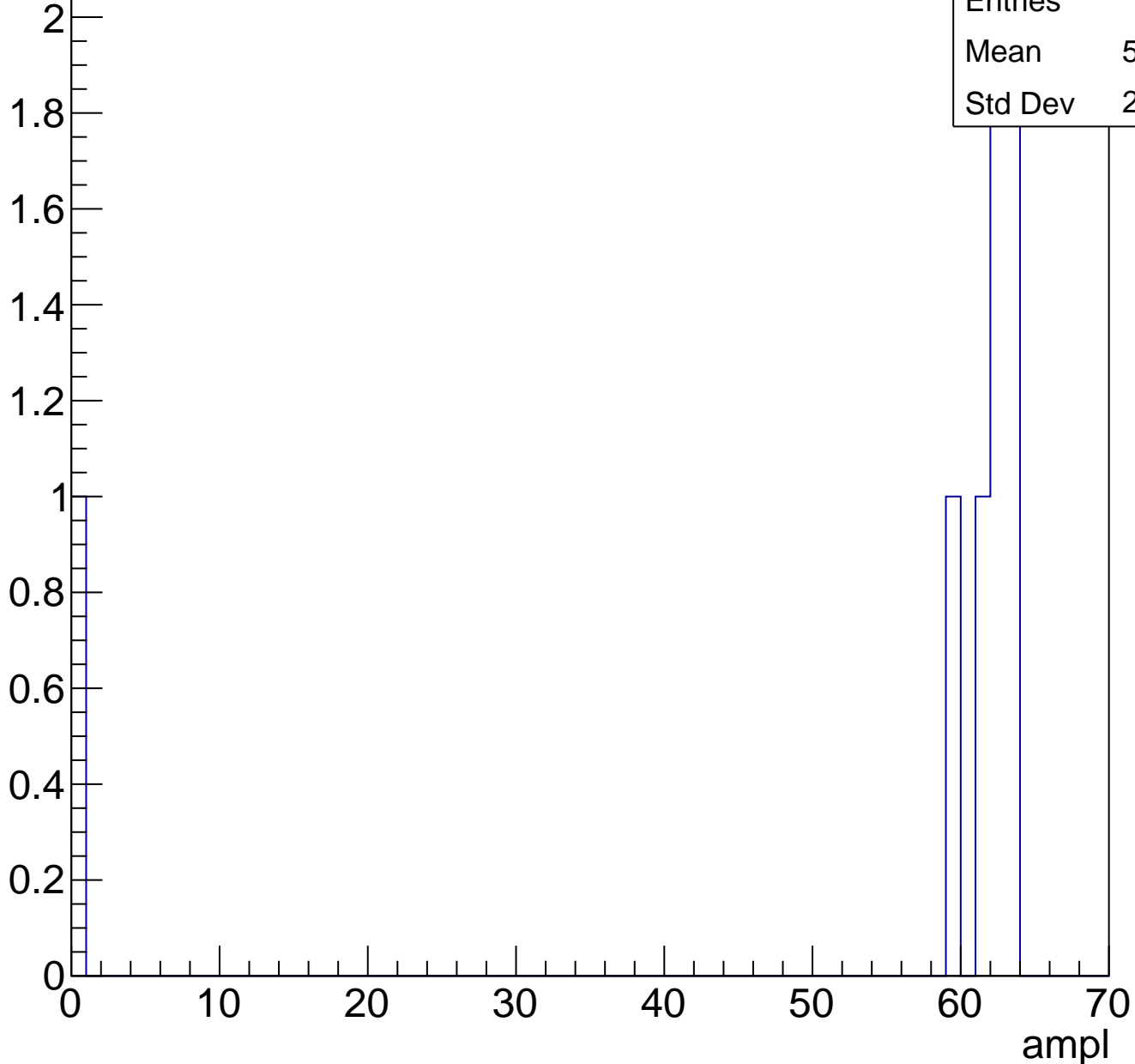
Entries	54
Mean	59.59
Std Dev	2.6



B1L102S, U26-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch63, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	30.7
Std Dev	4.84

Gaus mean : 31.1365

Gaus Width: 3.7424

Entry

10
8
6
4
2
0

ampl

0

10

20

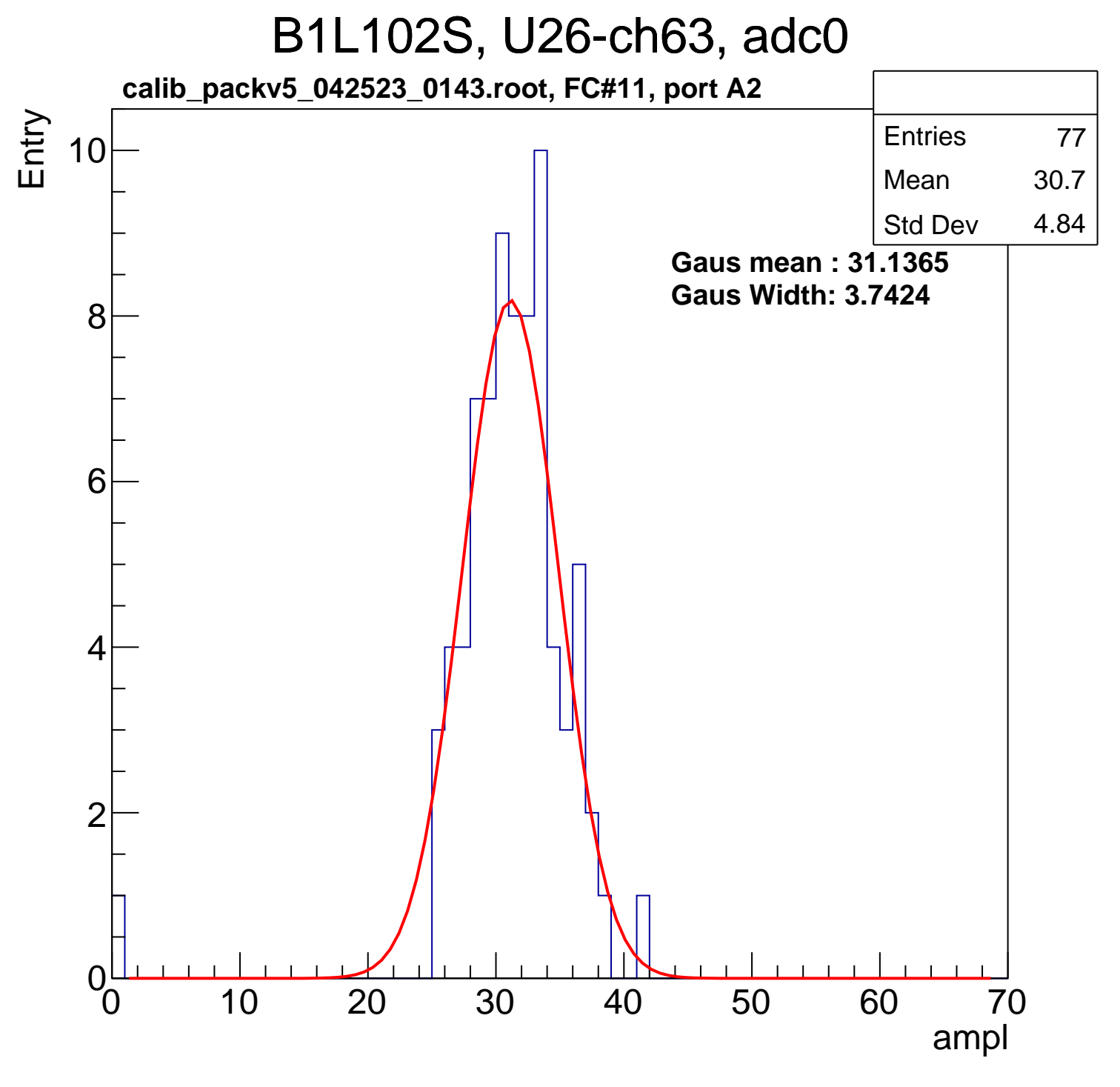
30

40

50

60

70



B1L102S, U26-ch63, adc1

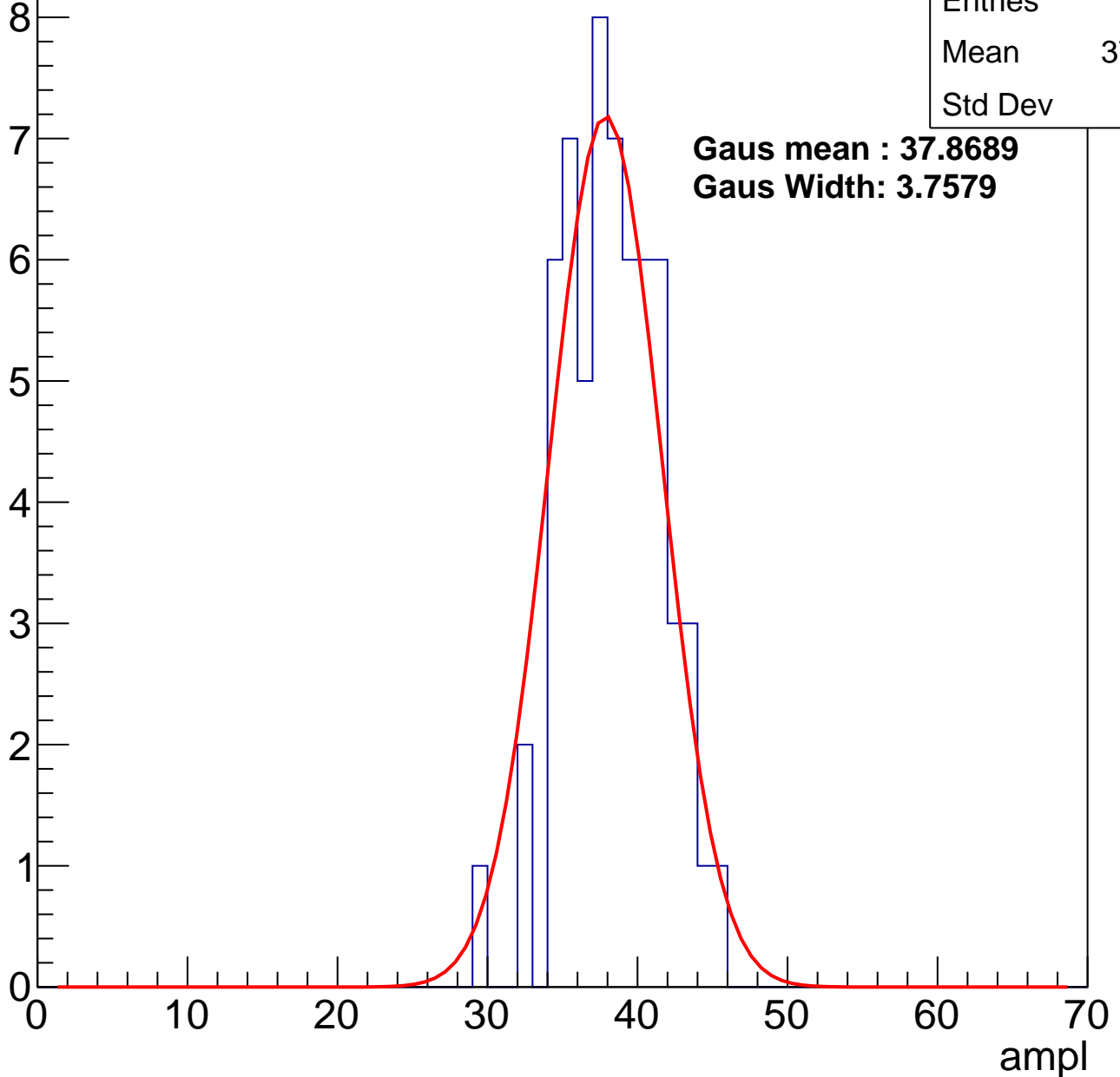
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.87
Std Dev	3.19

Gaus mean : 37.8689

Gaus Width: 3.7579



B1L102S, U26-ch63, adc2

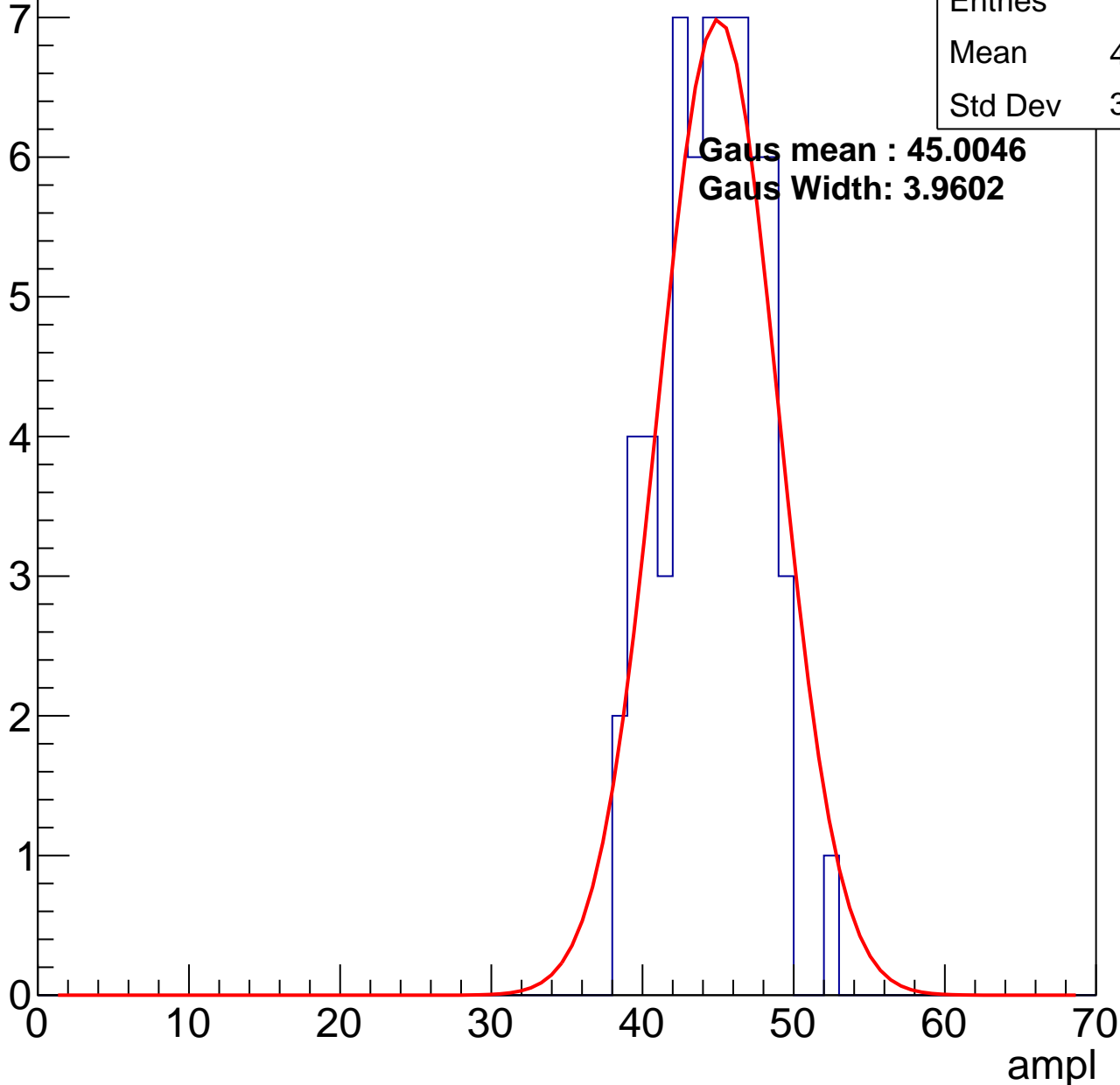
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	44.14
Std Dev	3.136

Gaus mean : 45.0046

Gaus Width: 3.9602

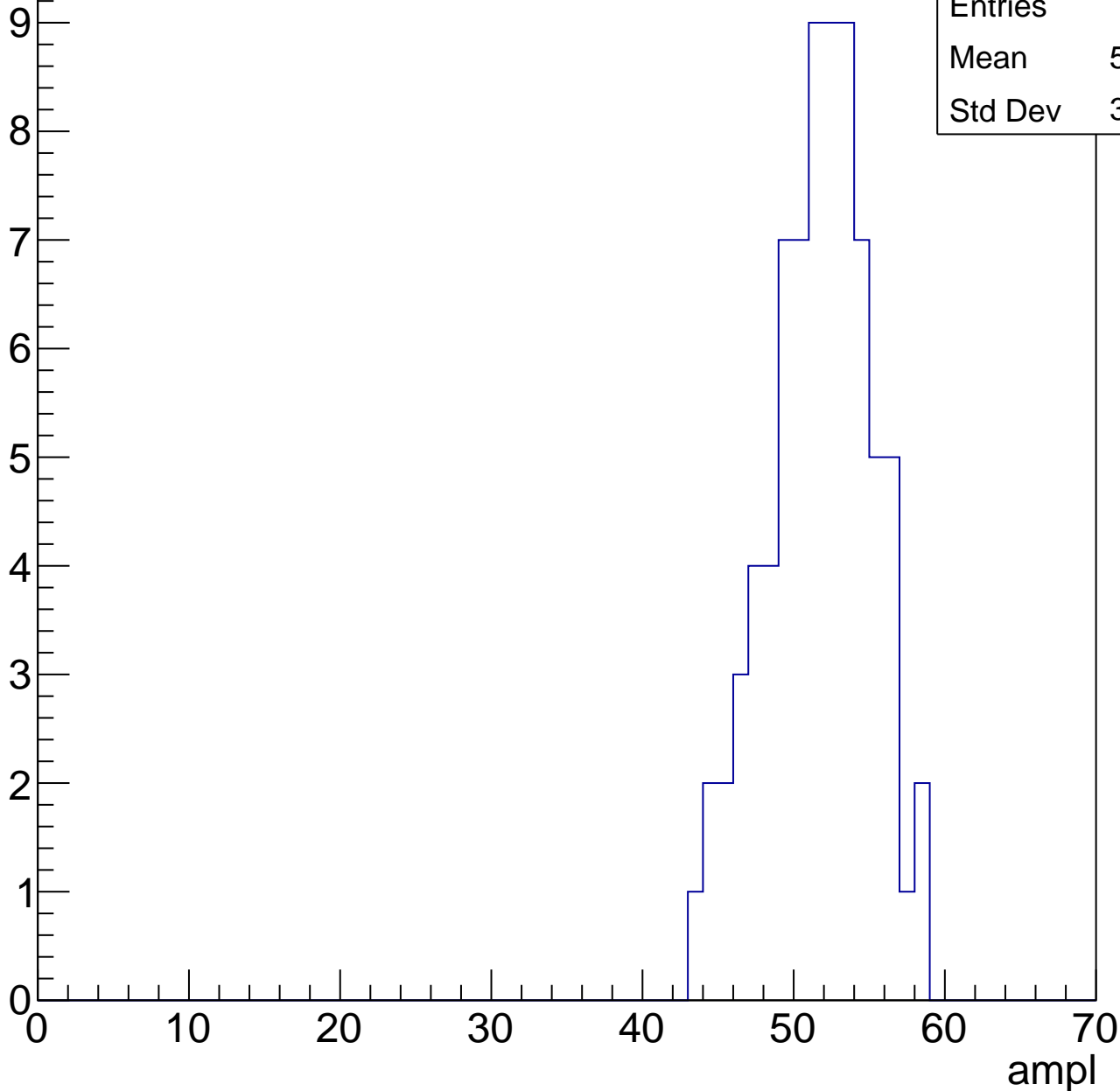


B1L102S, U26-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	51.19
Std Dev	3.396

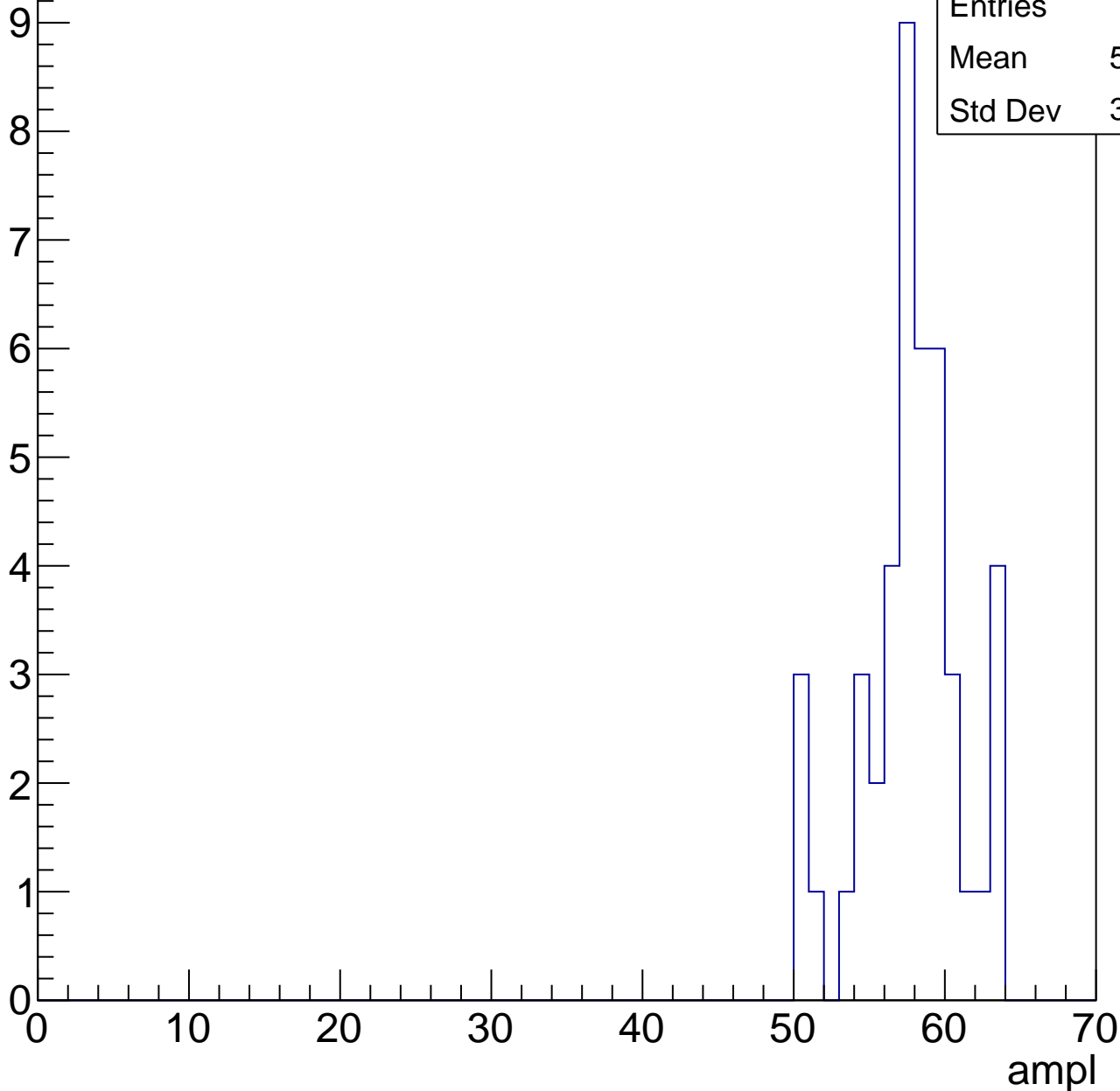


B1L102S, U26-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.27
Std Dev	3.292

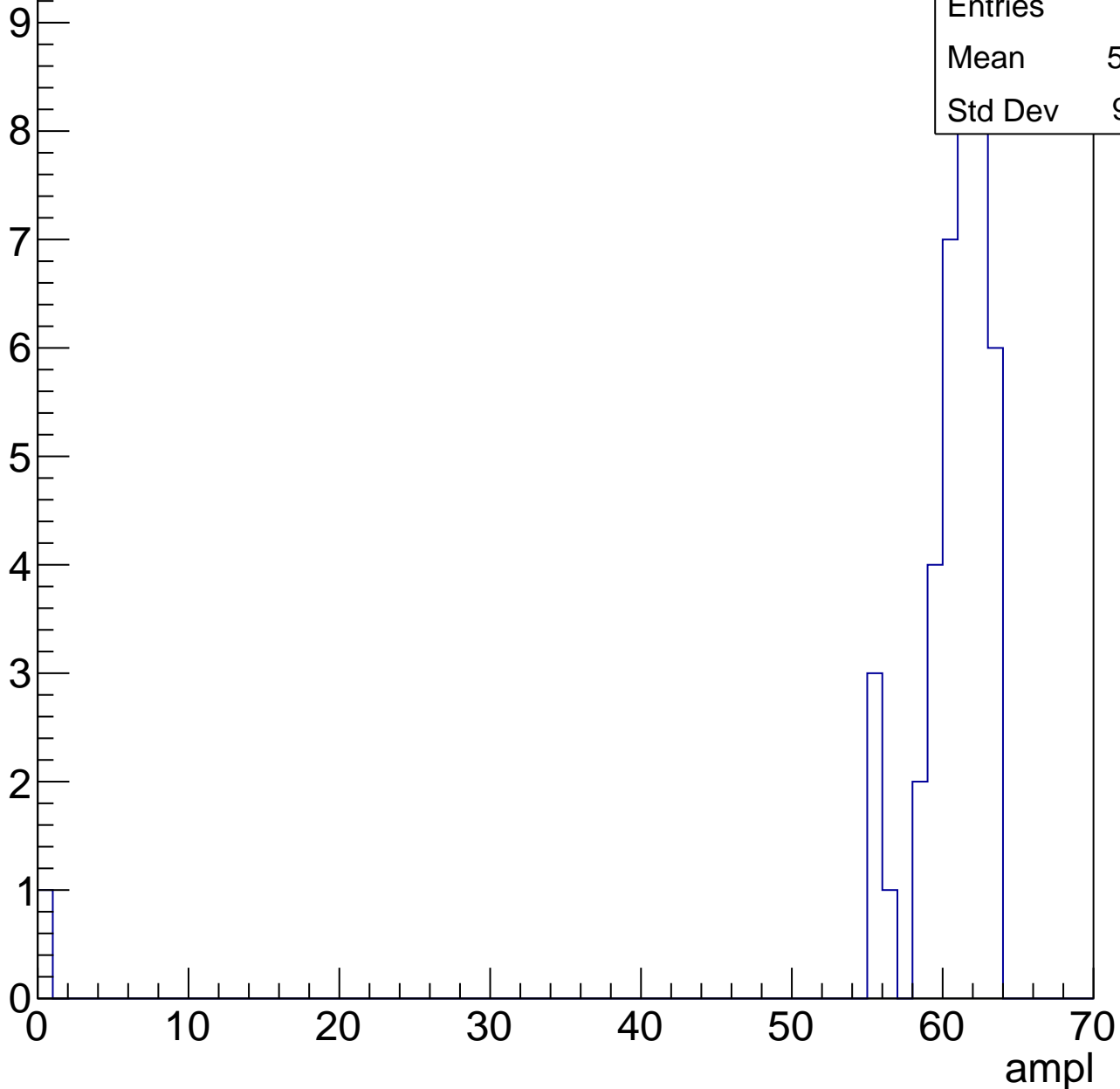


B1L102S, U26-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.95
Std Dev	9.571



B1L102S, U26-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch64, adc0

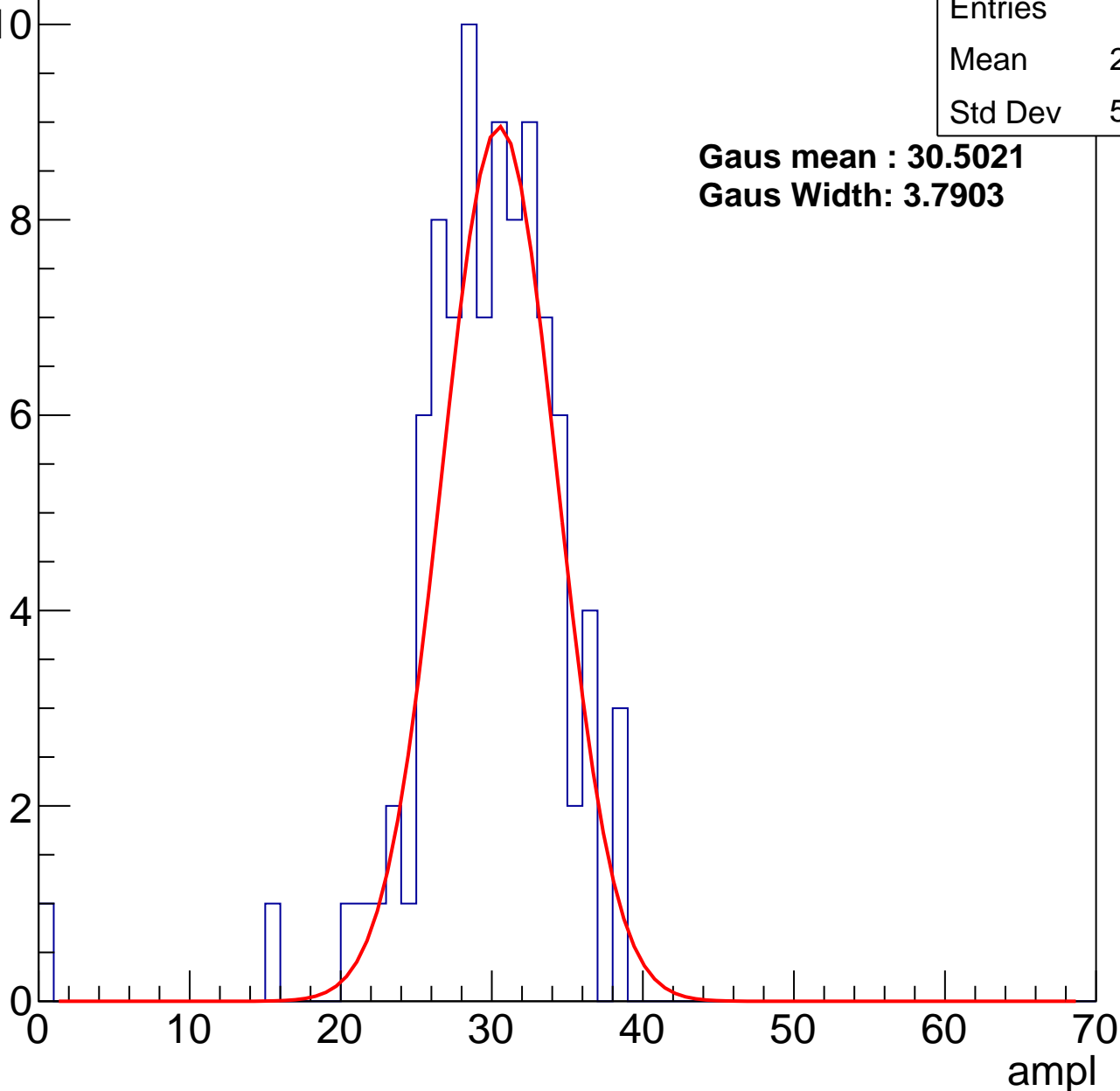
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	29.22
Std Dev	5.078

Gaus mean : 30.5021

Gaus Width: 3.7903



B1L102S, U26-ch64, adc1

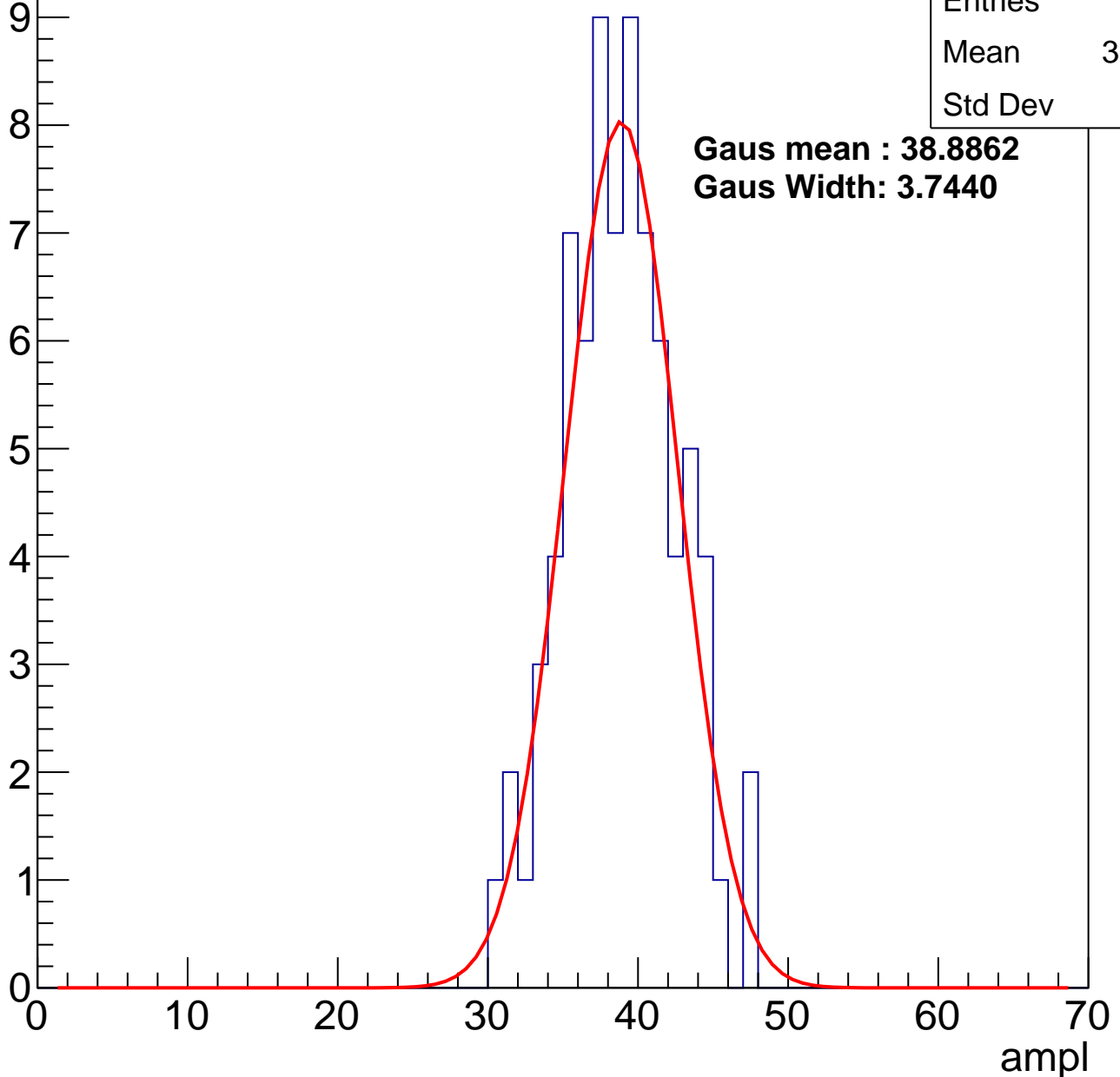
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	38.38
Std Dev	3.68

Gaus mean : 38.8862

Gaus Width: 3.7440



B1L102S, U26-ch64, adc2

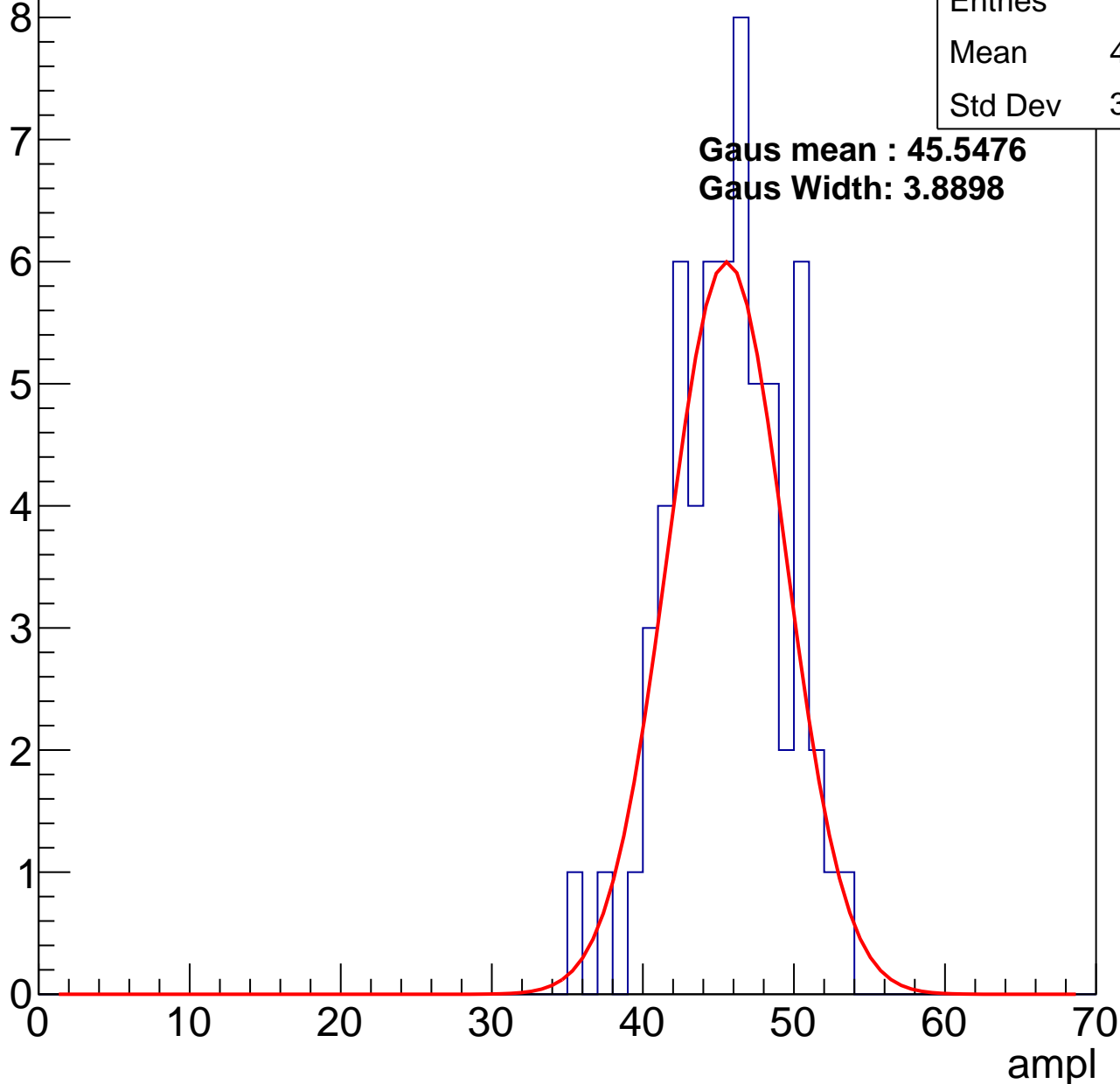
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	45.18
Std Dev	3.709

Gaus mean : 45.5476

Gaus Width: 3.8898

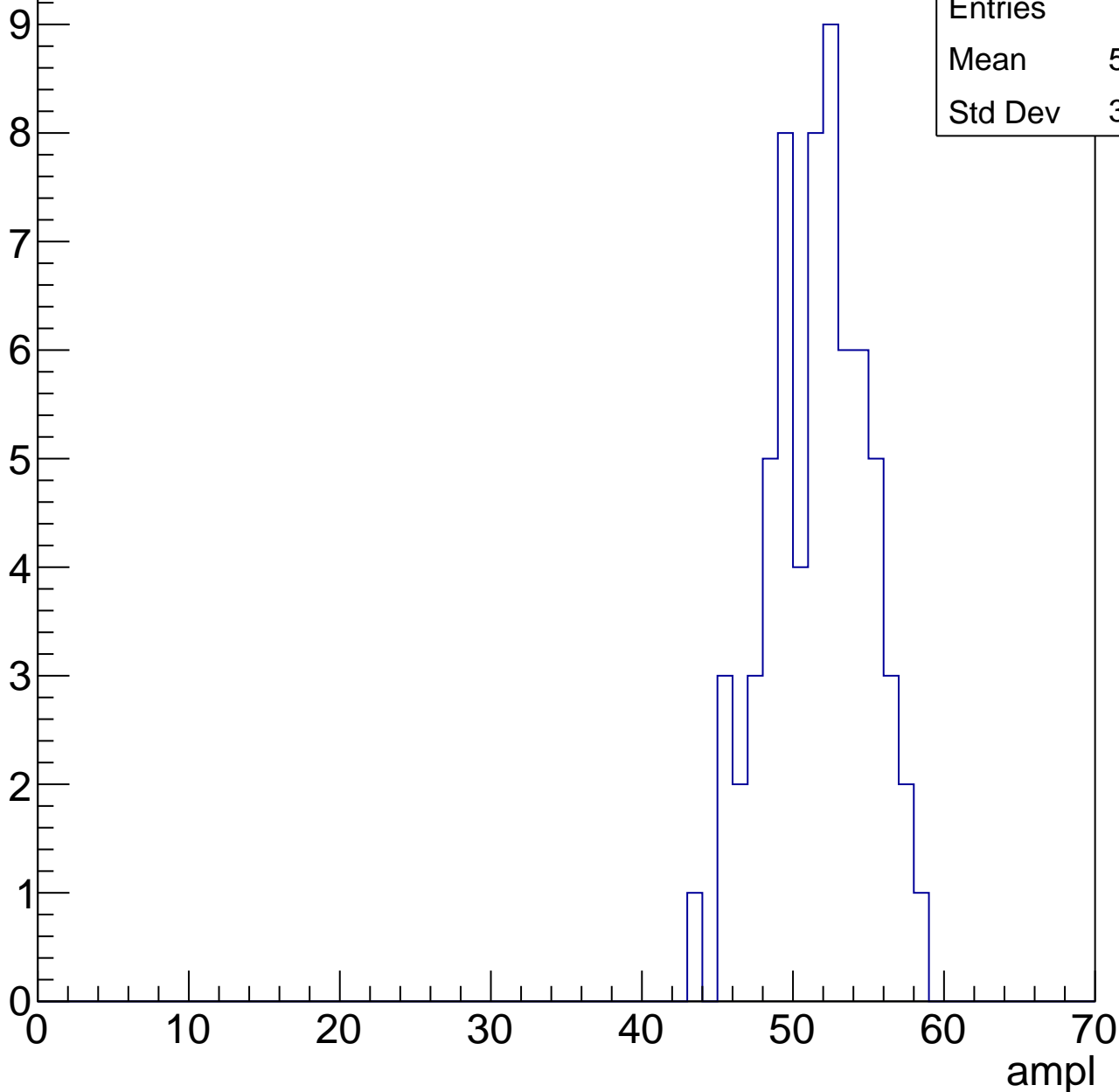


B1L102S, U26-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	51.15
Std Dev	3.286

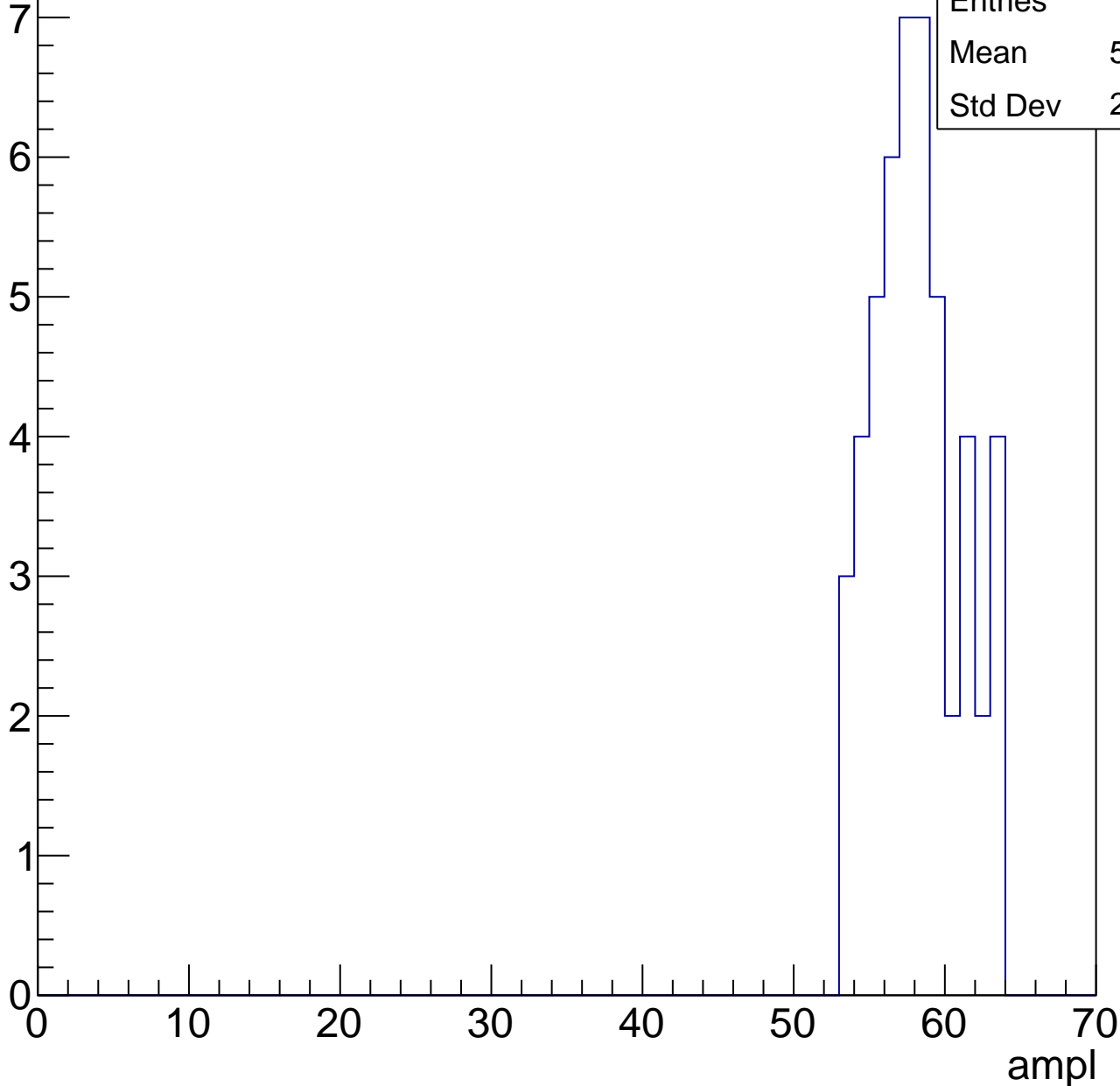


B1L102S, U26-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.67
Std Dev	2.824

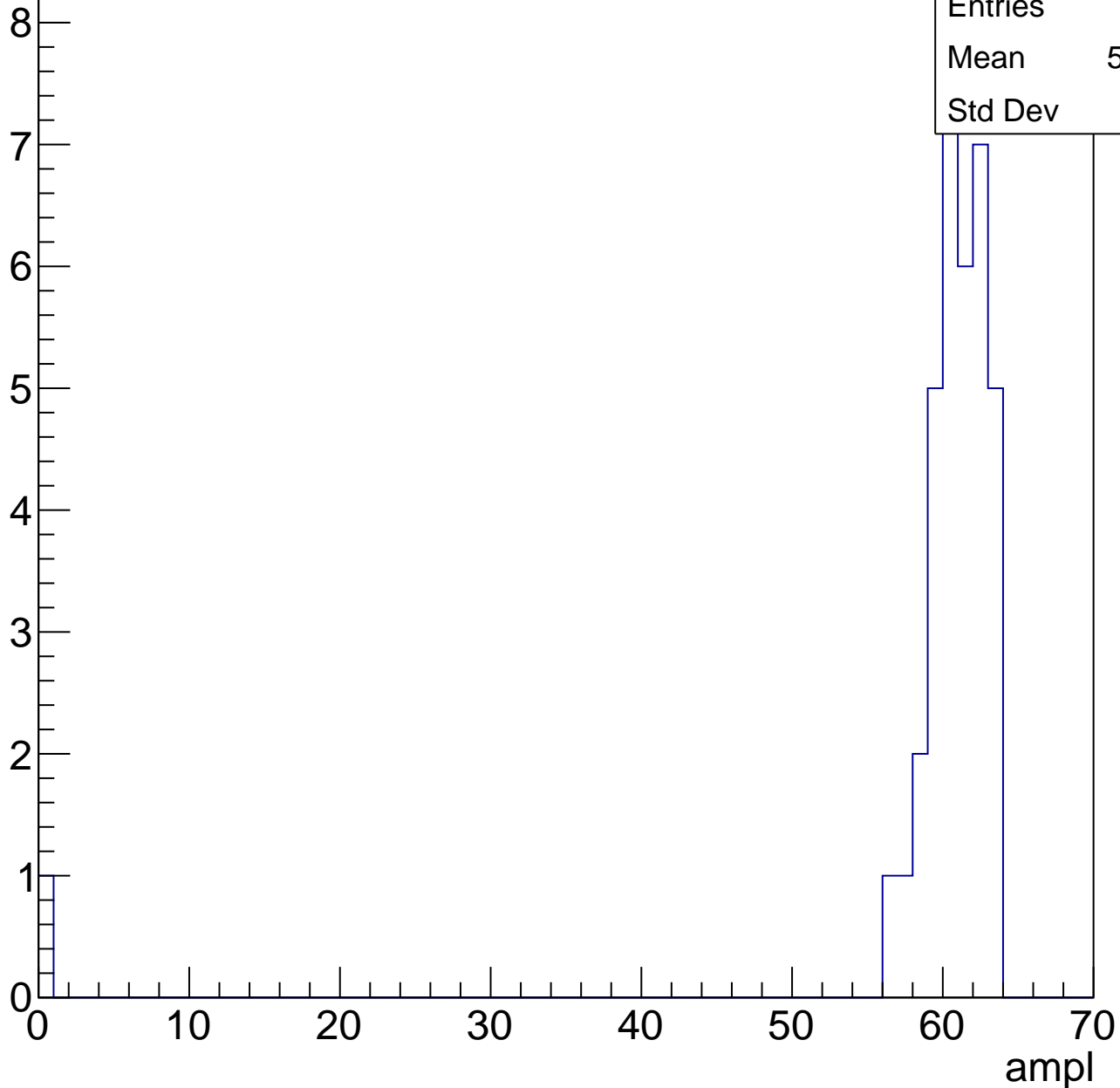


B1L102S, U26-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.86
Std Dev	10.1



B1L102S, U26-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch65, adc0

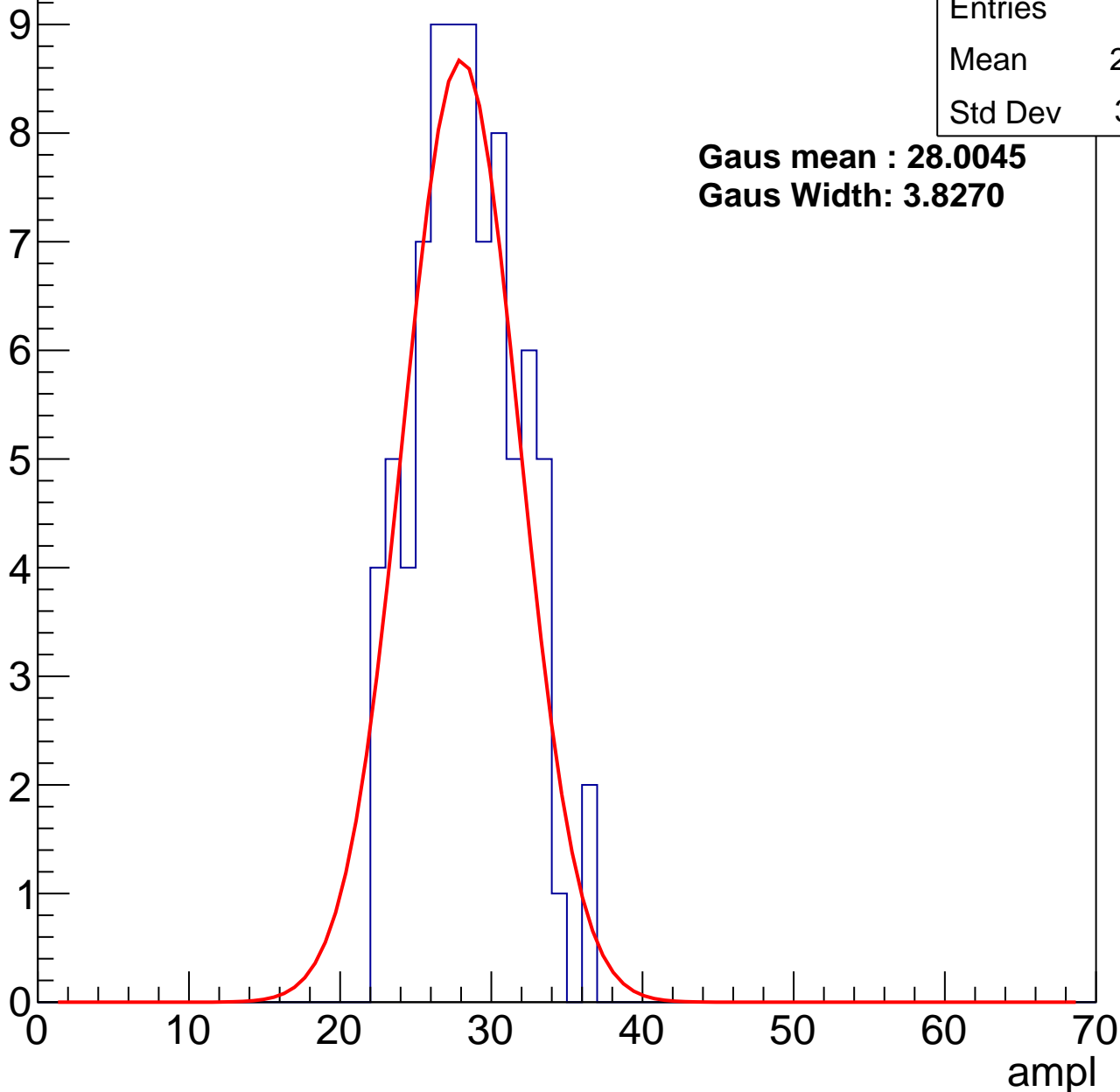
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.95
Std Dev	3.351

Gaus mean : 28.0045

Gaus Width: 3.8270



B1L102S, U26-ch65, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	35.86
Std Dev	3.81

Gaus mean : 35.9169

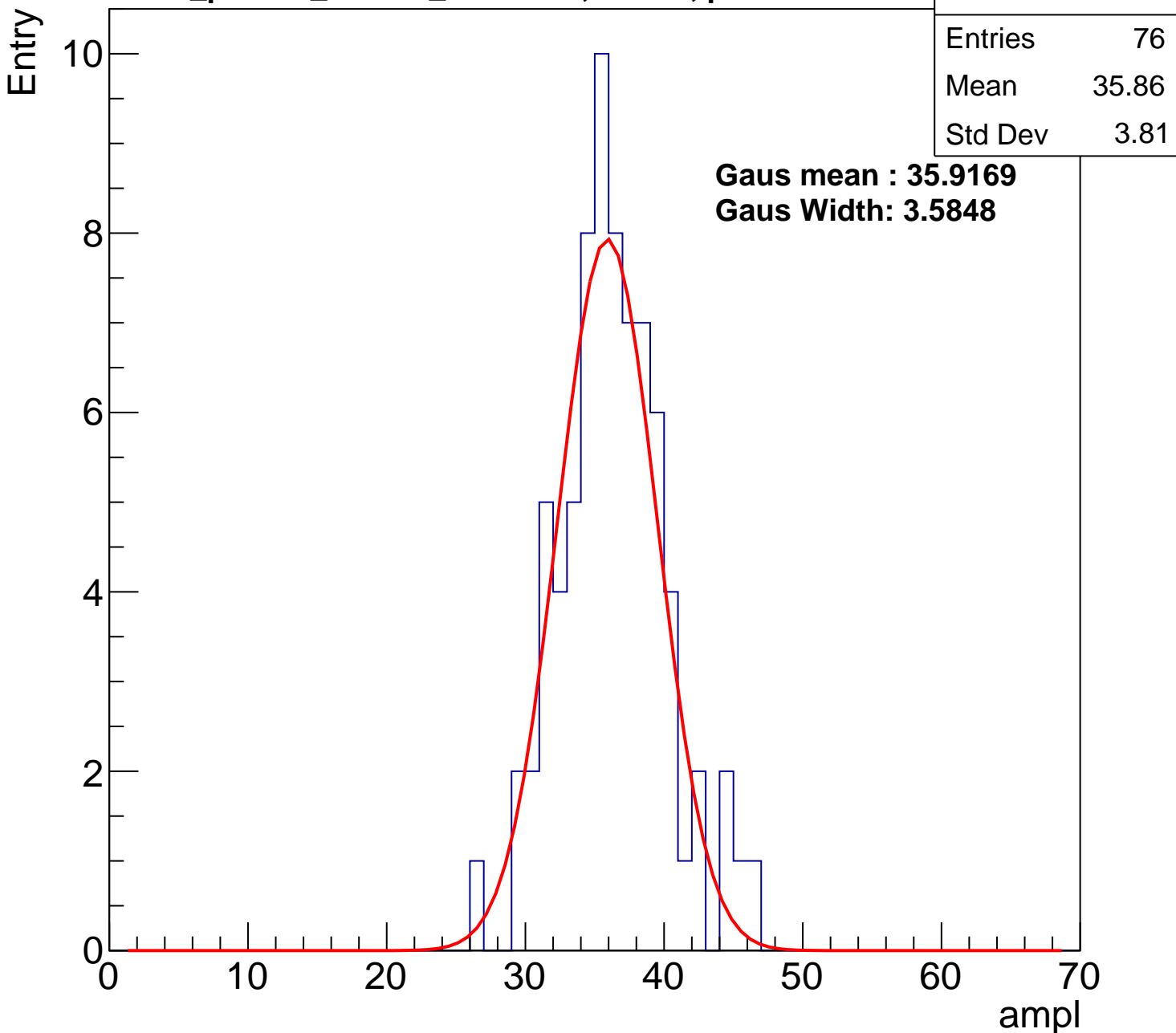
Gaus Width: 3.5848

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch65, adc2

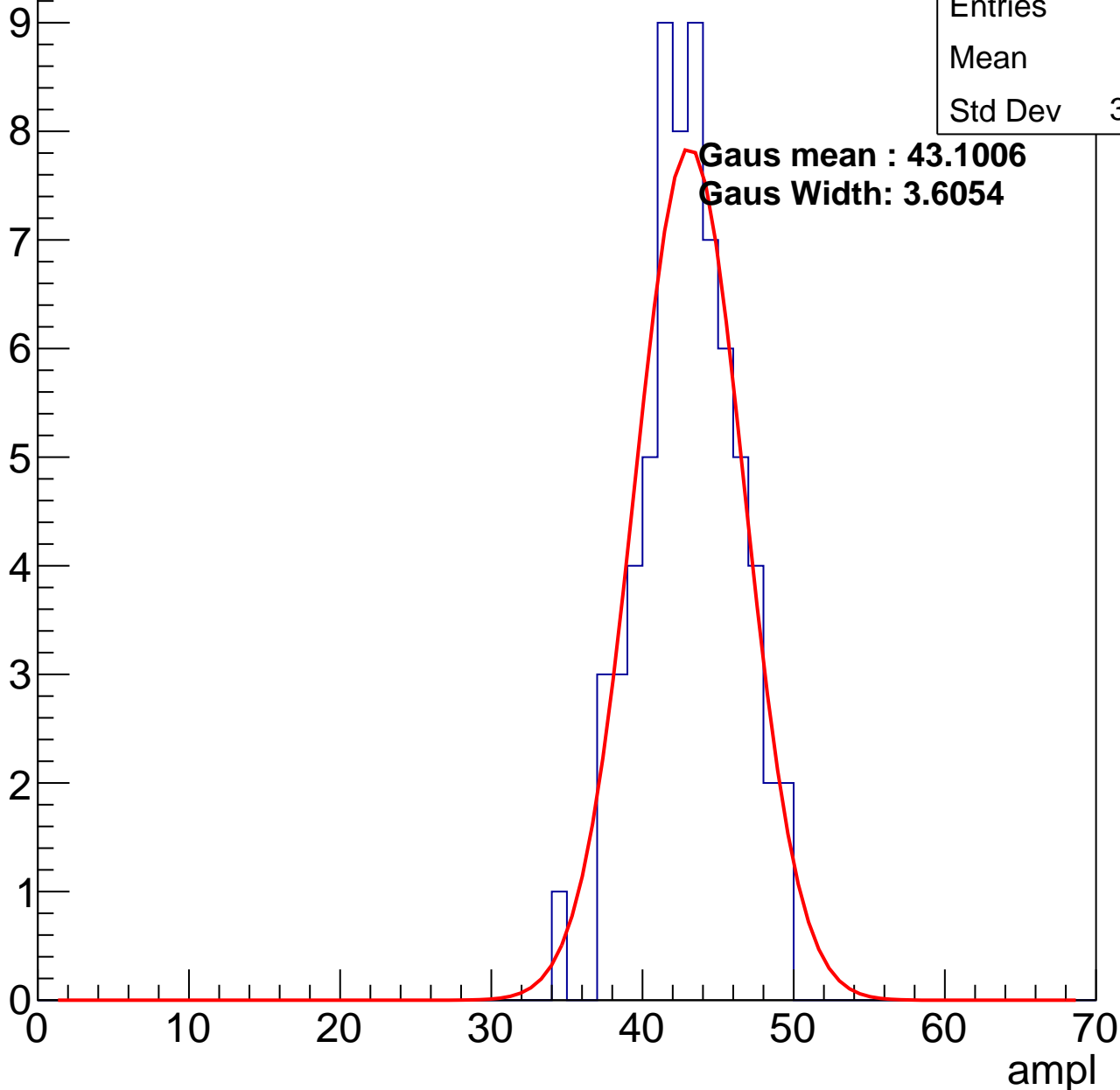
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.6
Std Dev	3.135

Gaus mean : 43.1006

Gaus Width: 3.6054

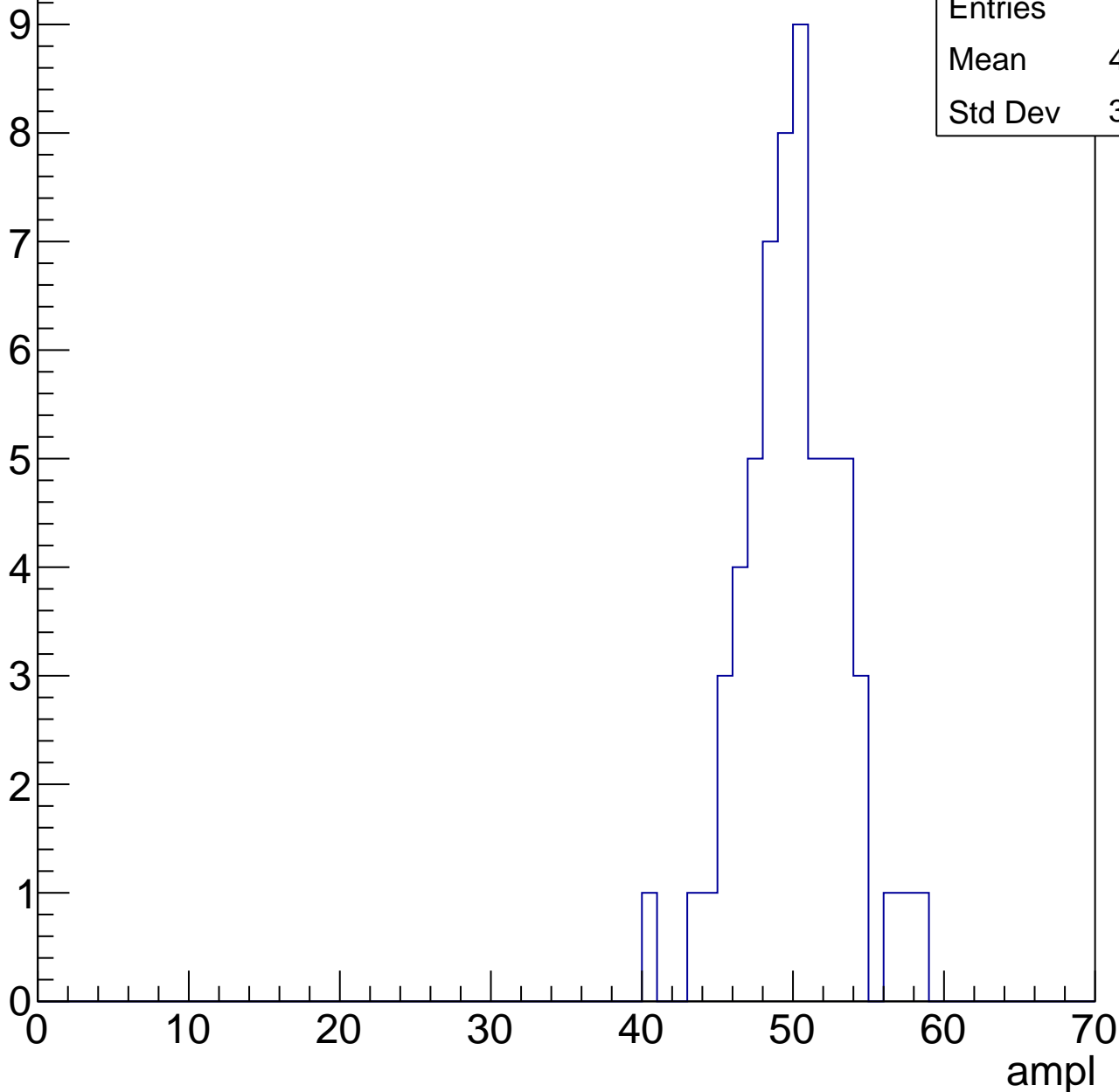


B1L102S, U26-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.53
Std Dev	3.314

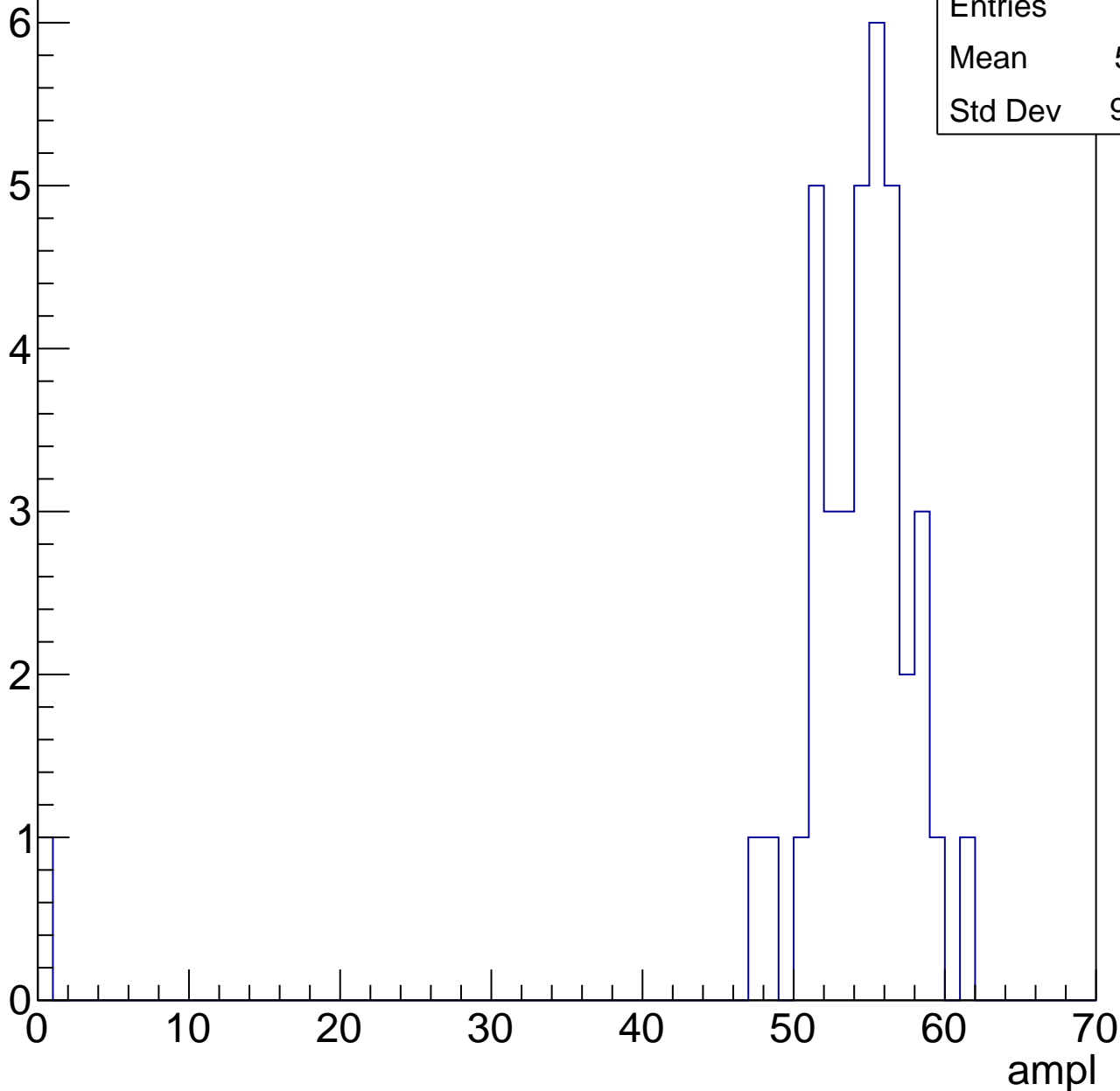


B1L102S, U26-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	52.71
Std Dev	9.148



B1L102S, U26-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

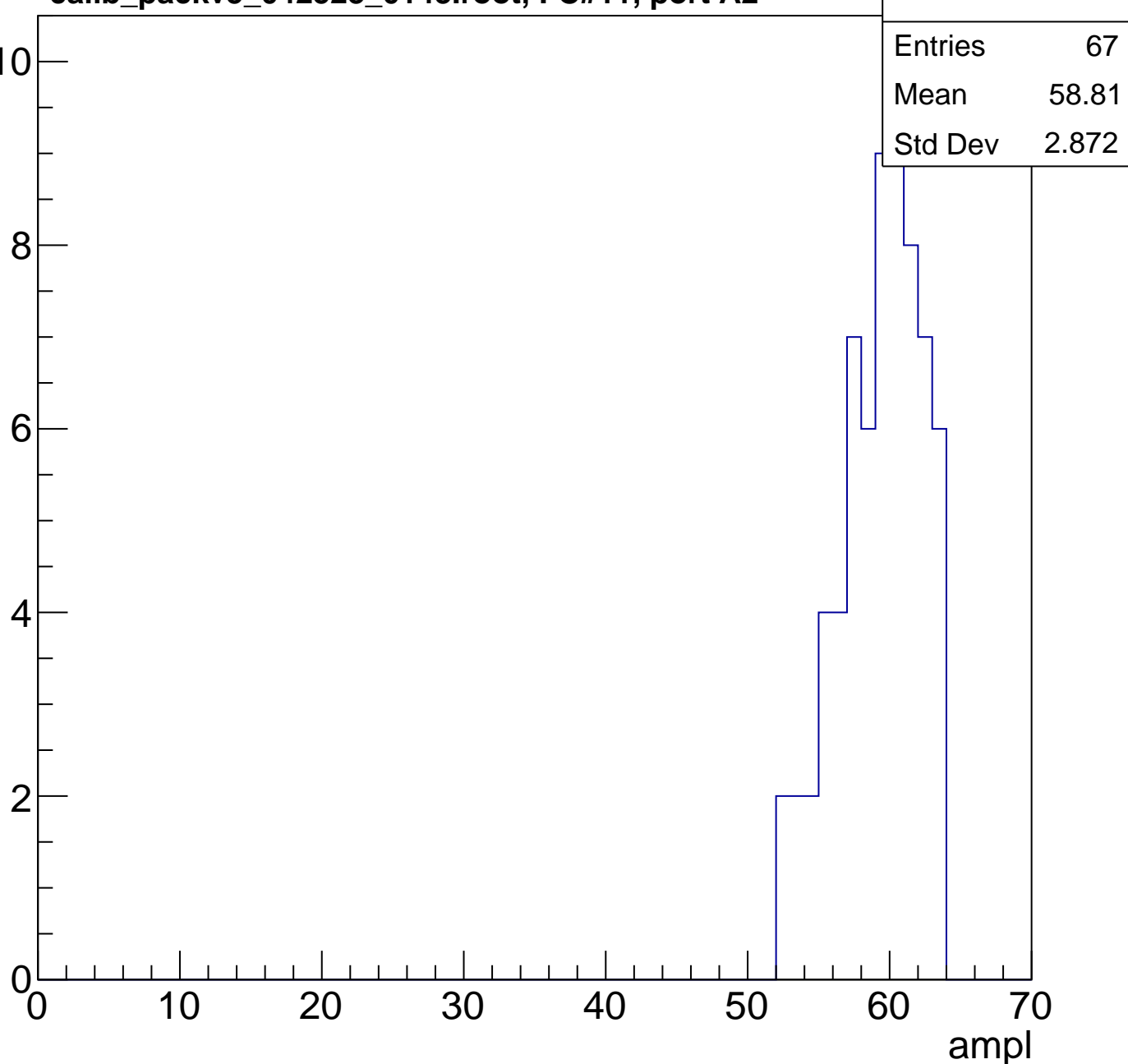
67

Mean

58.81

Std Dev

2.872



B1L102S, U26-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch66, adc0

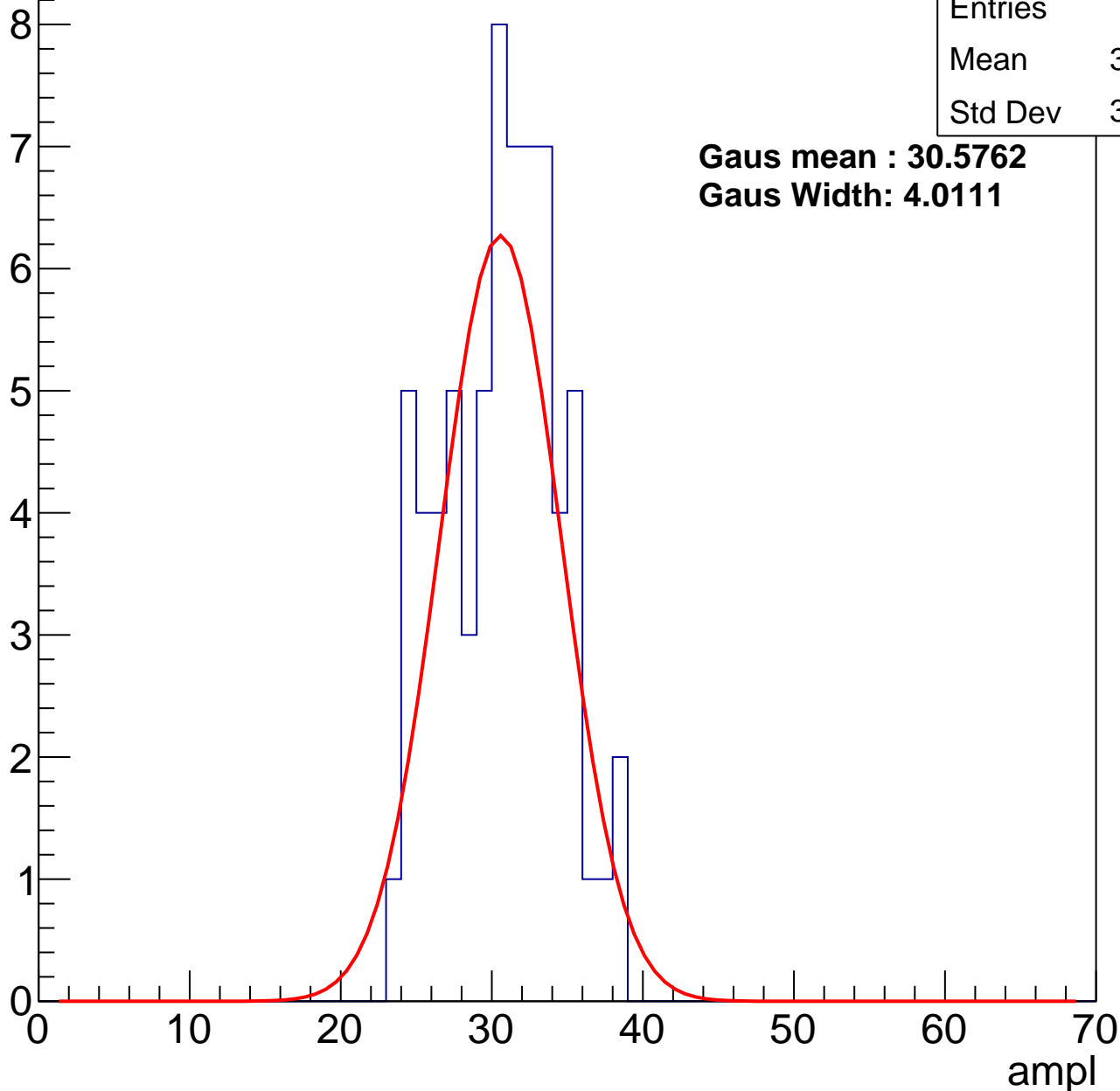
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.19
Std Dev	3.716

Gaus mean : 30.5762

Gaus Width: 4.0111



B1L102S, U26-ch66, adc1

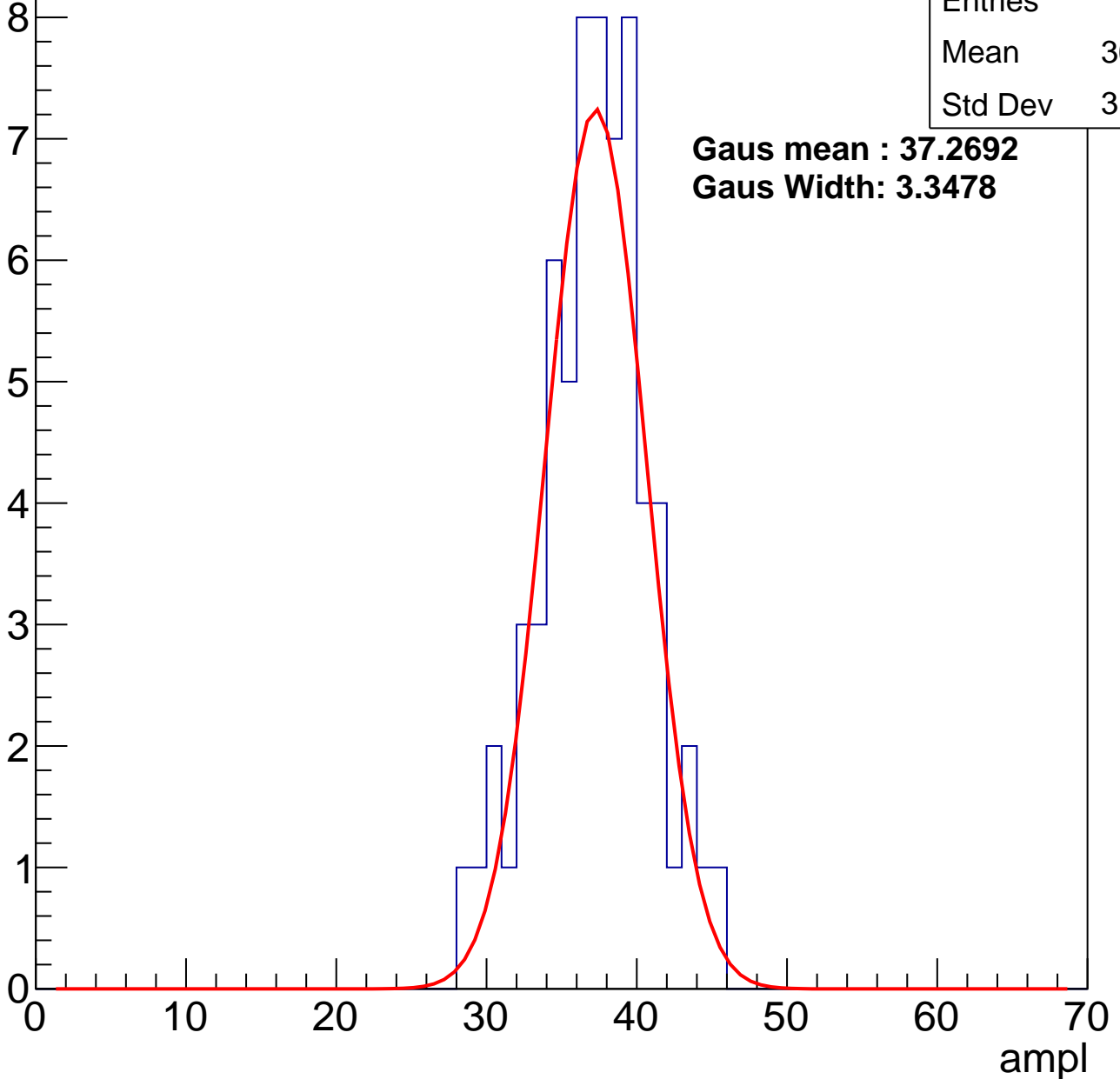
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.74
Std Dev	3.543

Gaus mean : 37.2692

Gaus Width: 3.3478



B1L102S, U26-ch66, adc2

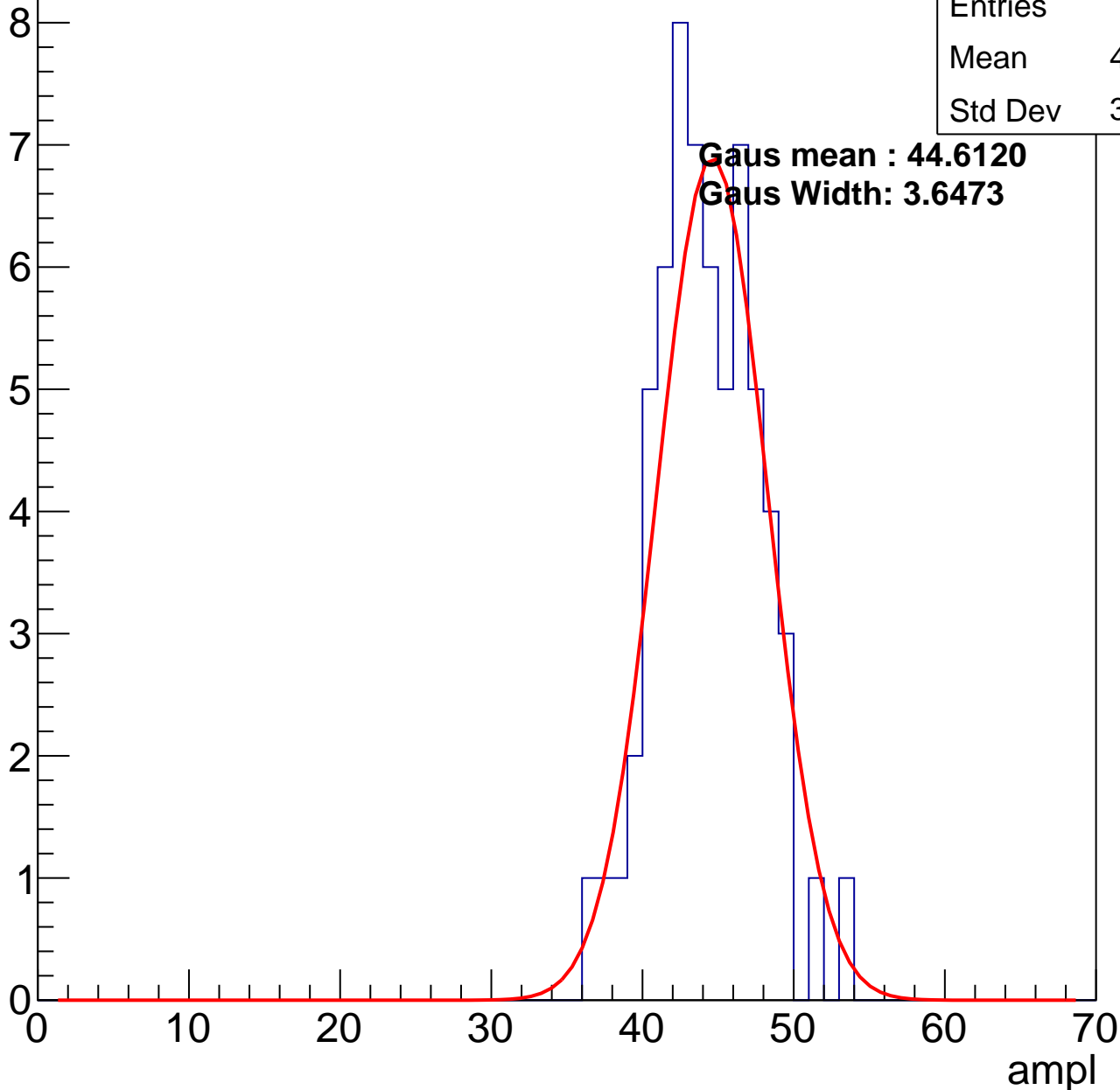
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.83
Std Dev	3.374

Gaus mean : 44.6120

Gaus Width: 3.6473

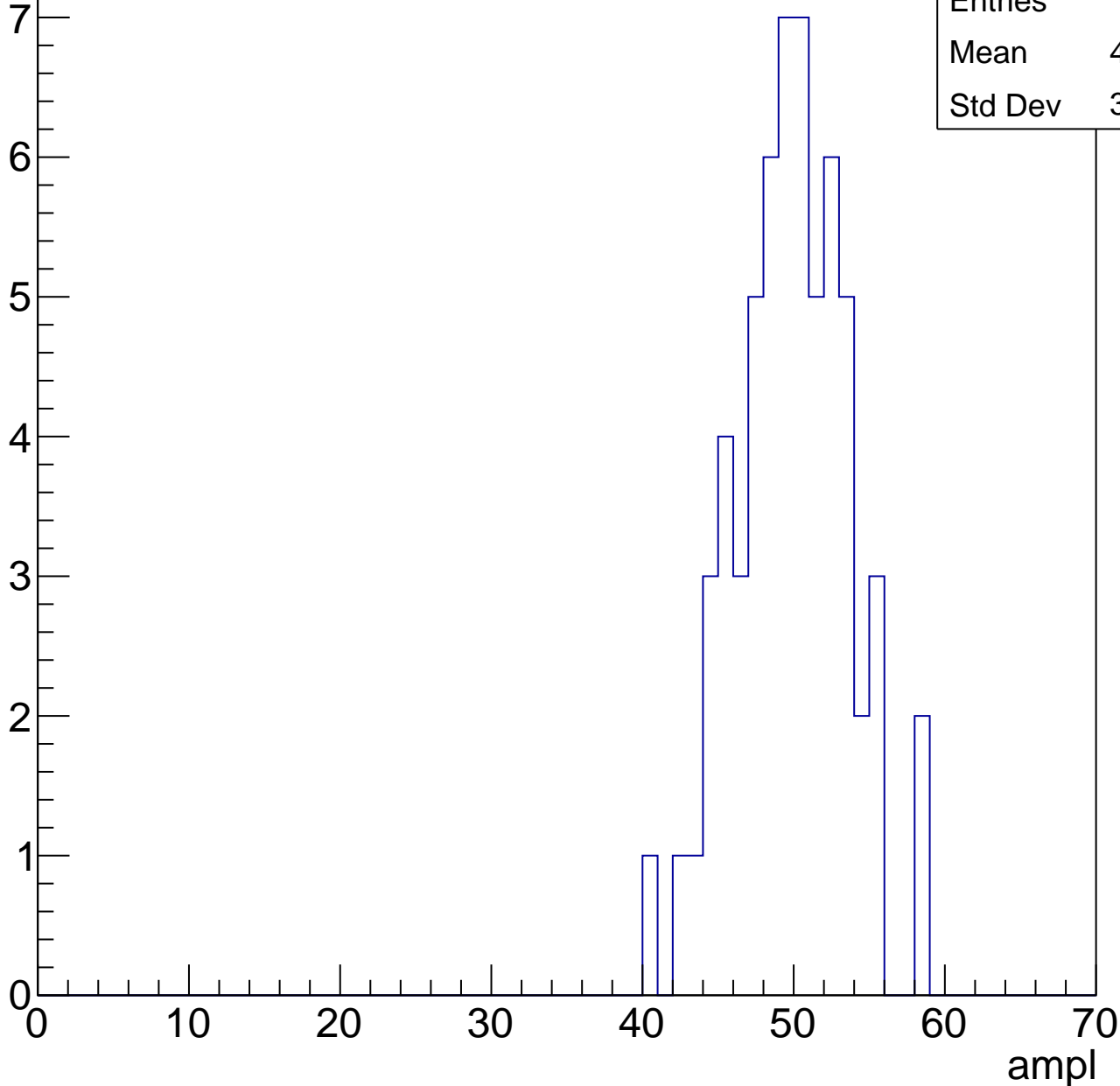


B1L102S, U26-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.38
Std Dev	3.689

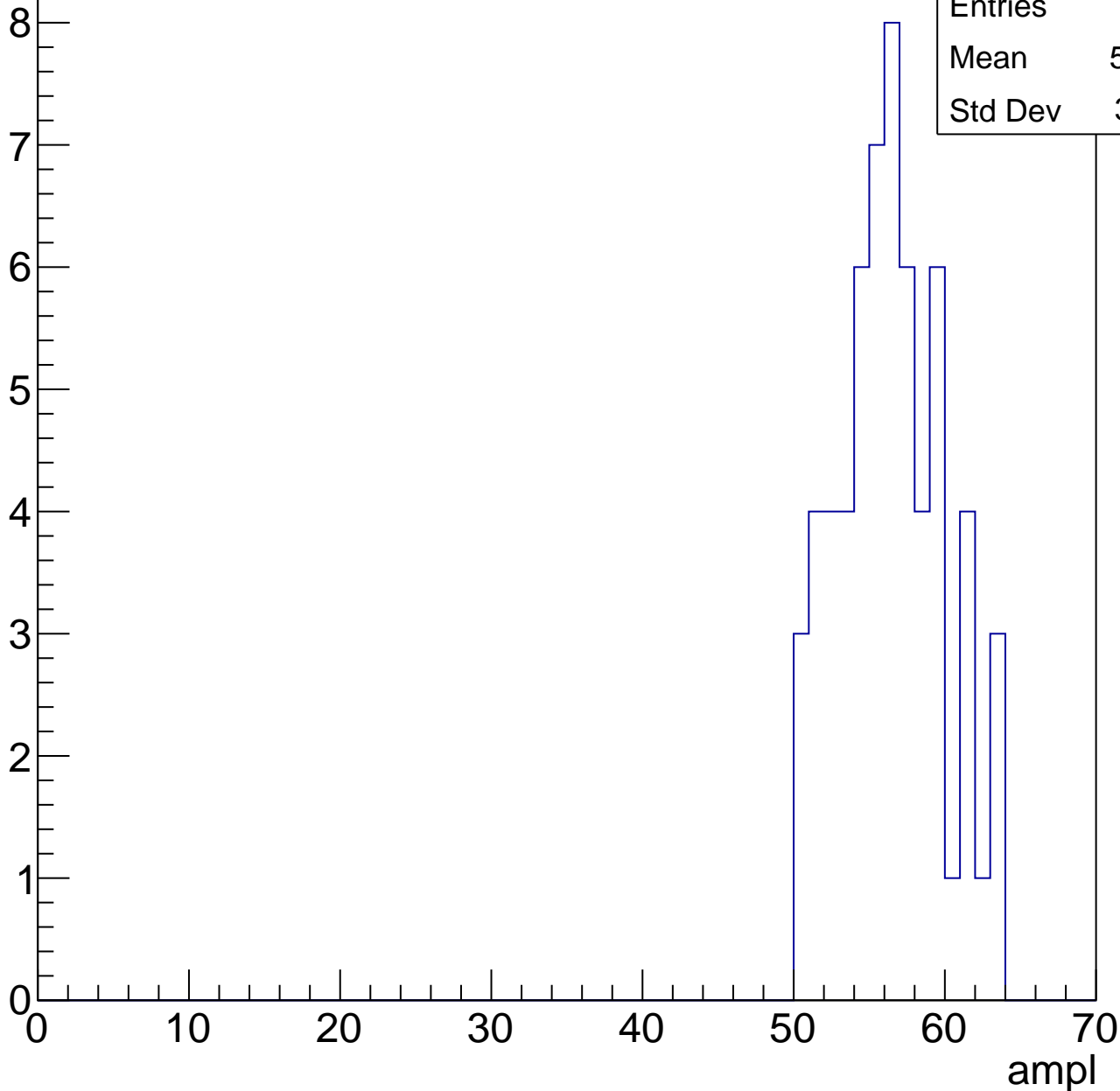


B1L102S, U26-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.97
Std Dev	3.421



B1L102S, U26-ch66, adc5

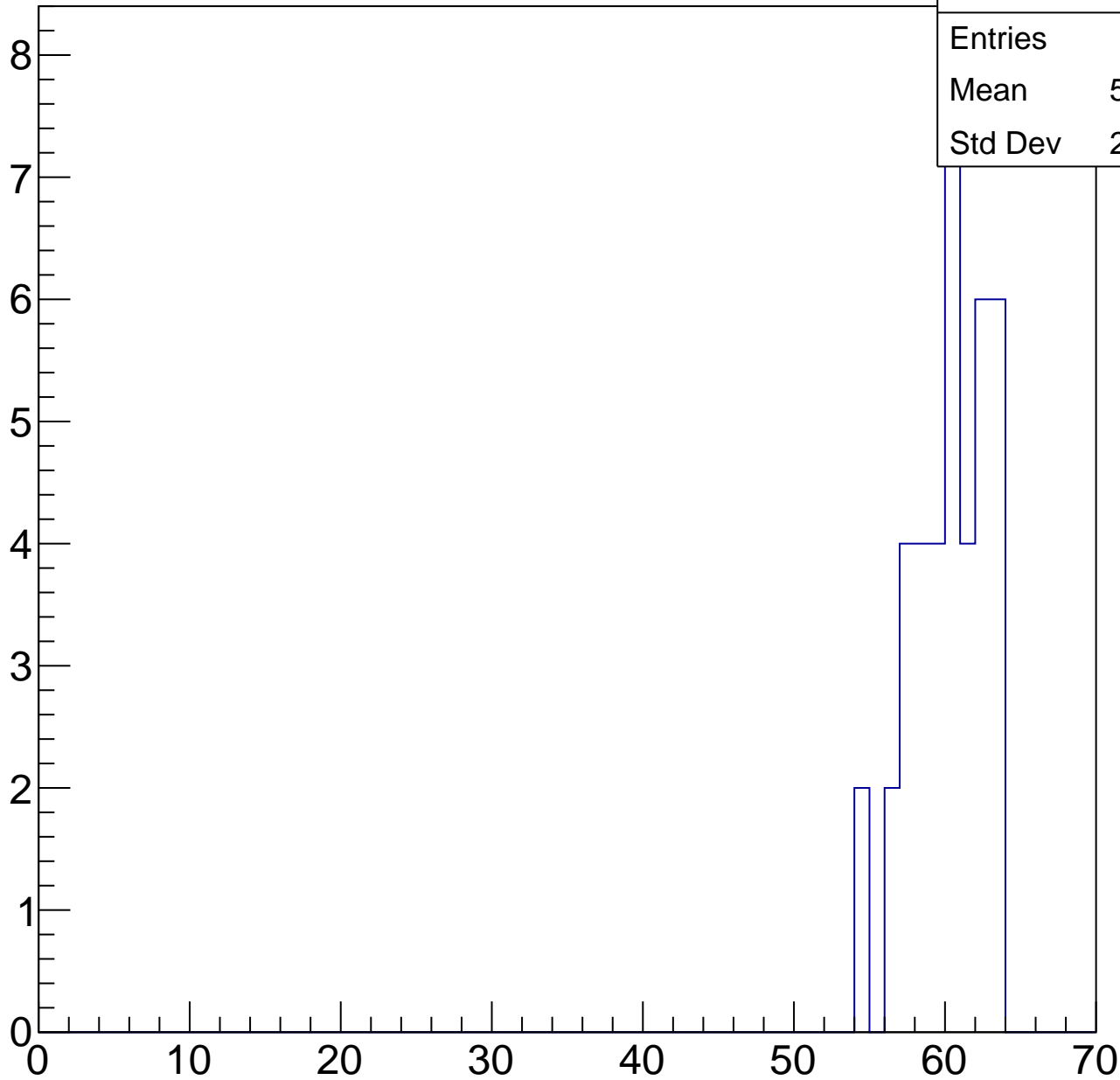
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.75
Std Dev	2.447

ampl



B1L102S, U26-ch66, adc6

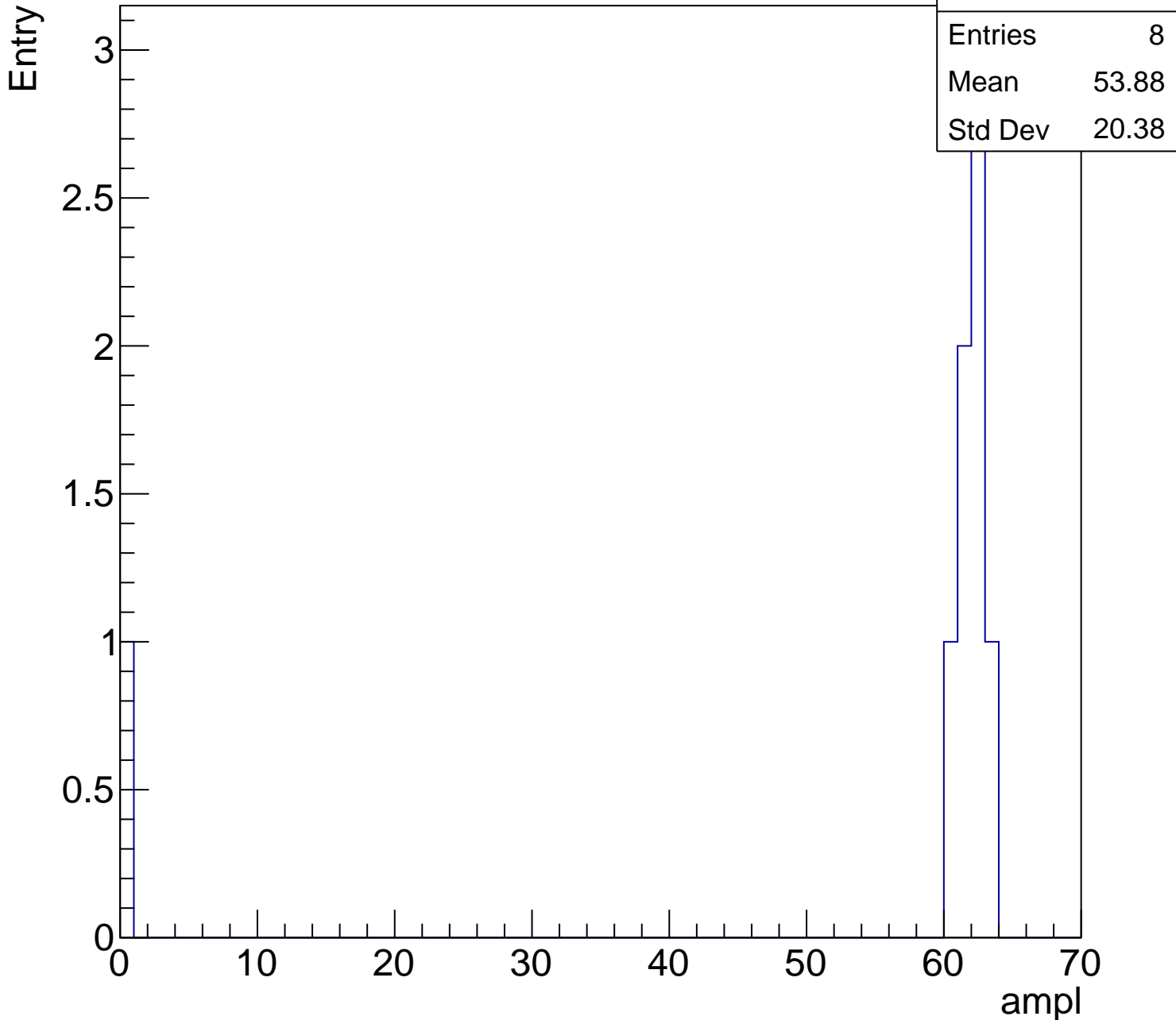
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	53.88
Std Dev	20.38

ampl



B1L102S, U26-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch67, adc0

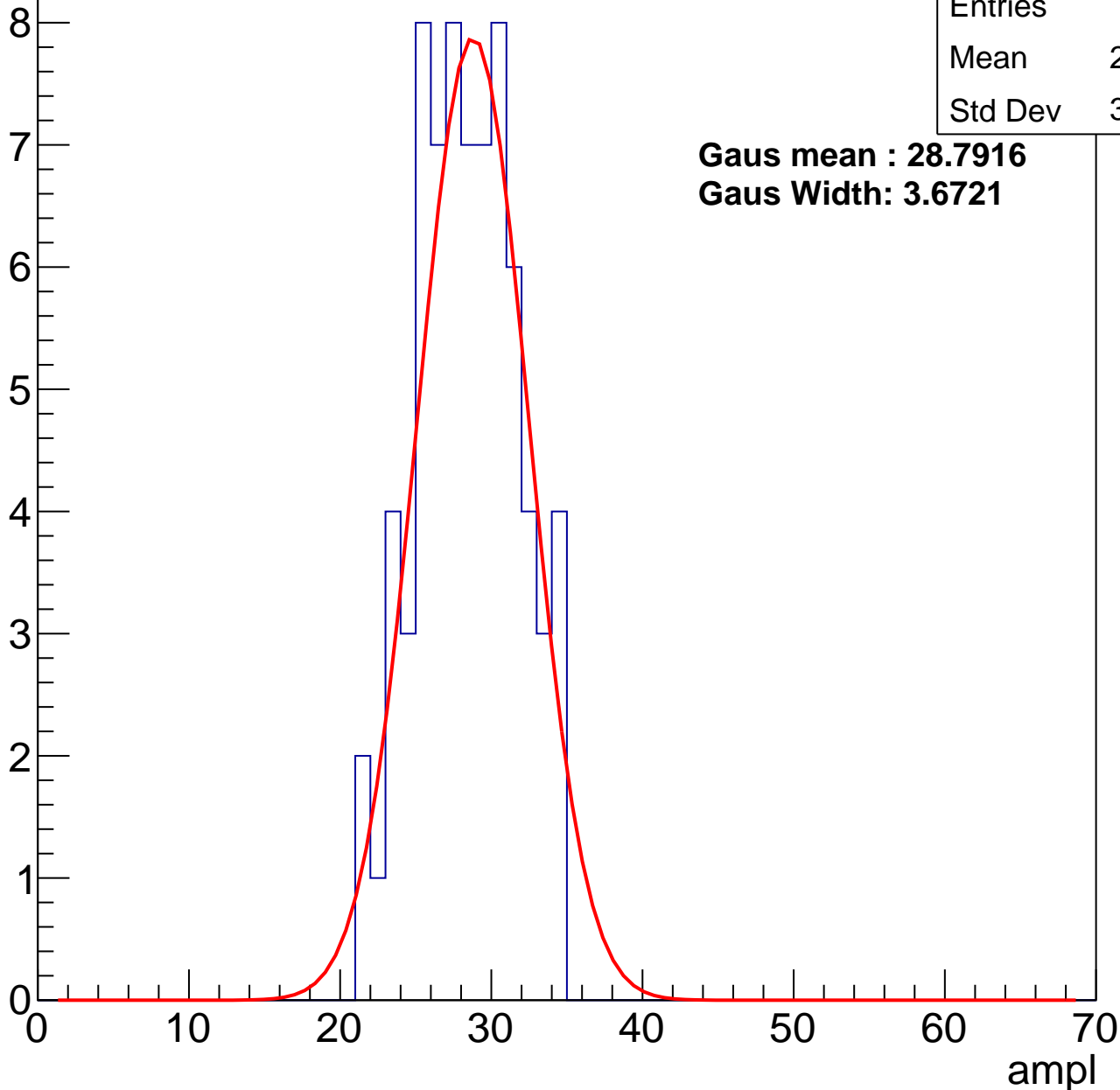
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.97
Std Dev	3.262

Gaus mean : 28.7916

Gaus Width: 3.6721



B1L102S, U26-ch67, adc1

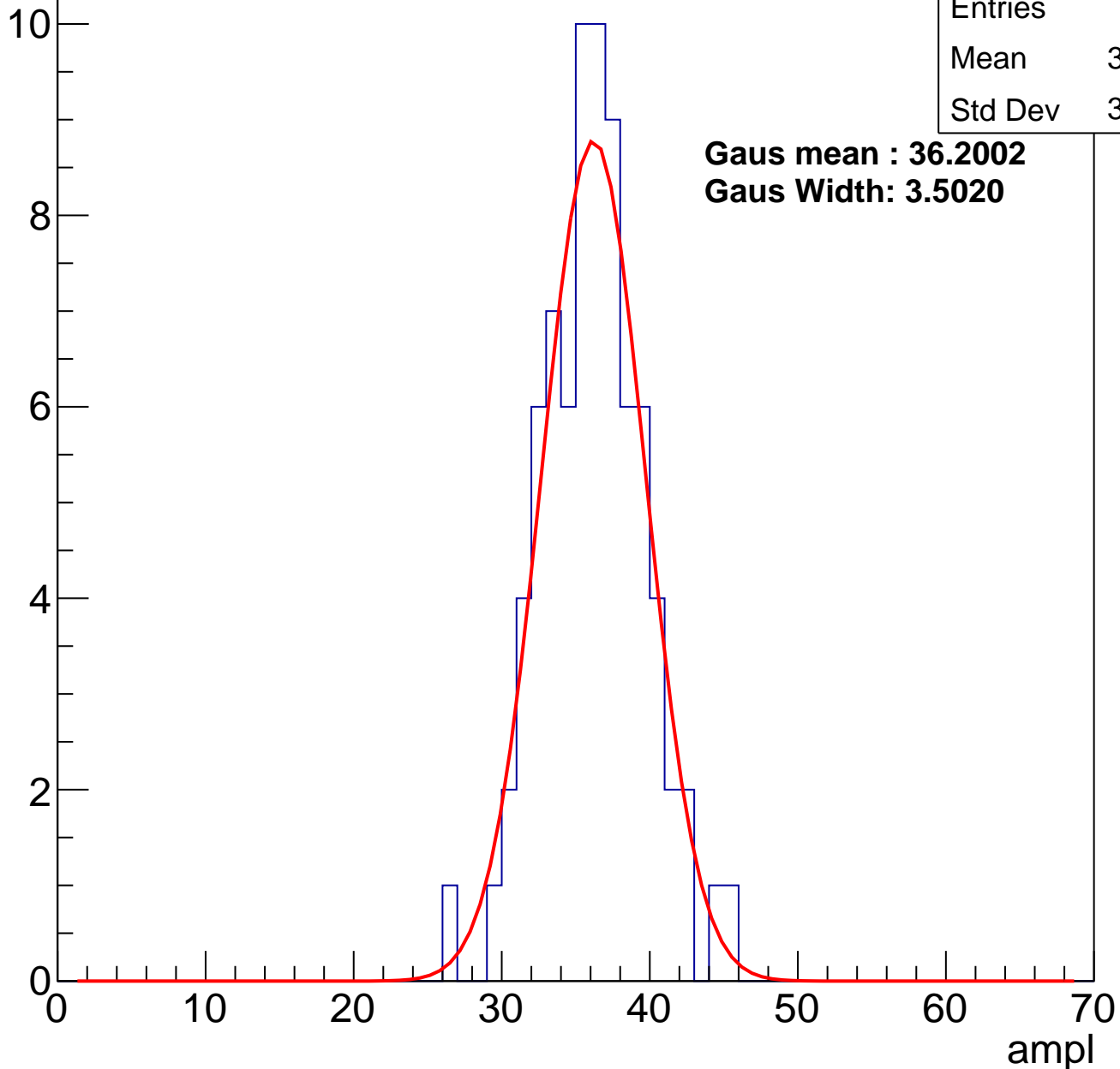
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	35.72
Std Dev	3.445

Gaus mean : 36.2002

Gaus Width: 3.5020

Entry



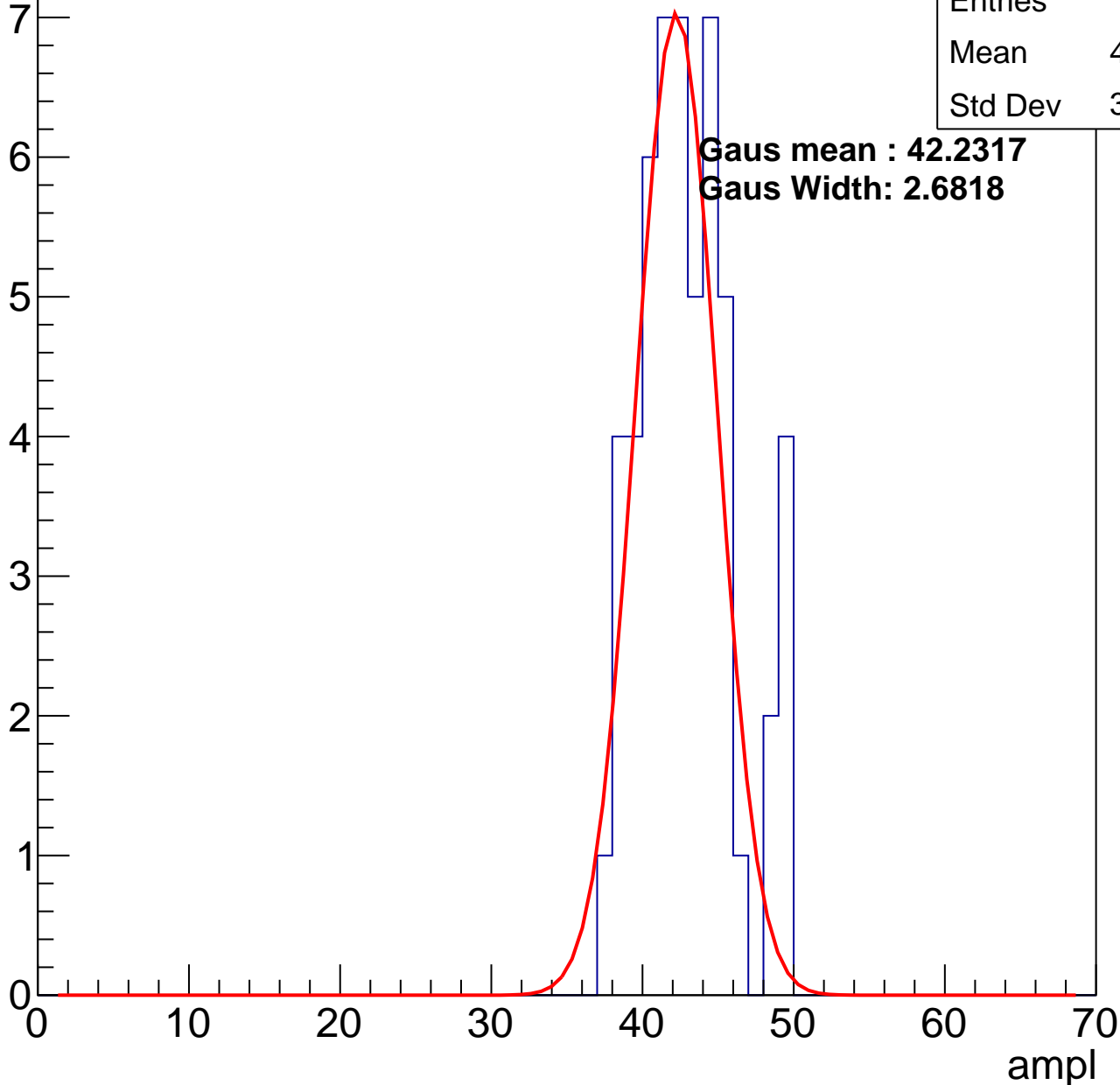
B1L102S, U26-ch67, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.49
Std Dev	3.088

Gaus mean : 42.2317
Gaus Width: 2.6818



B1L102S, U26-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

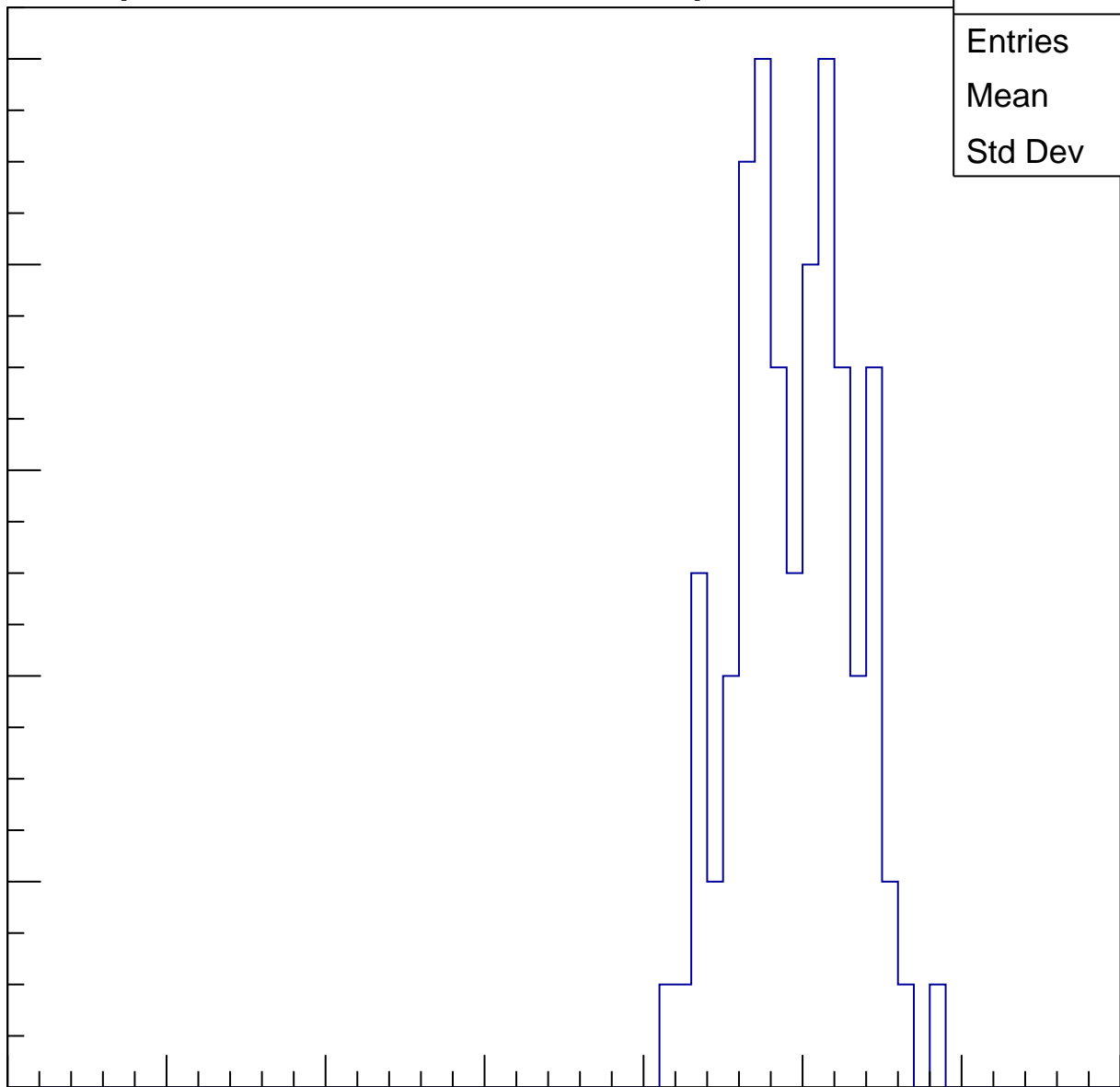
Entries	84
Mean	49.04
Std Dev	3.597

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

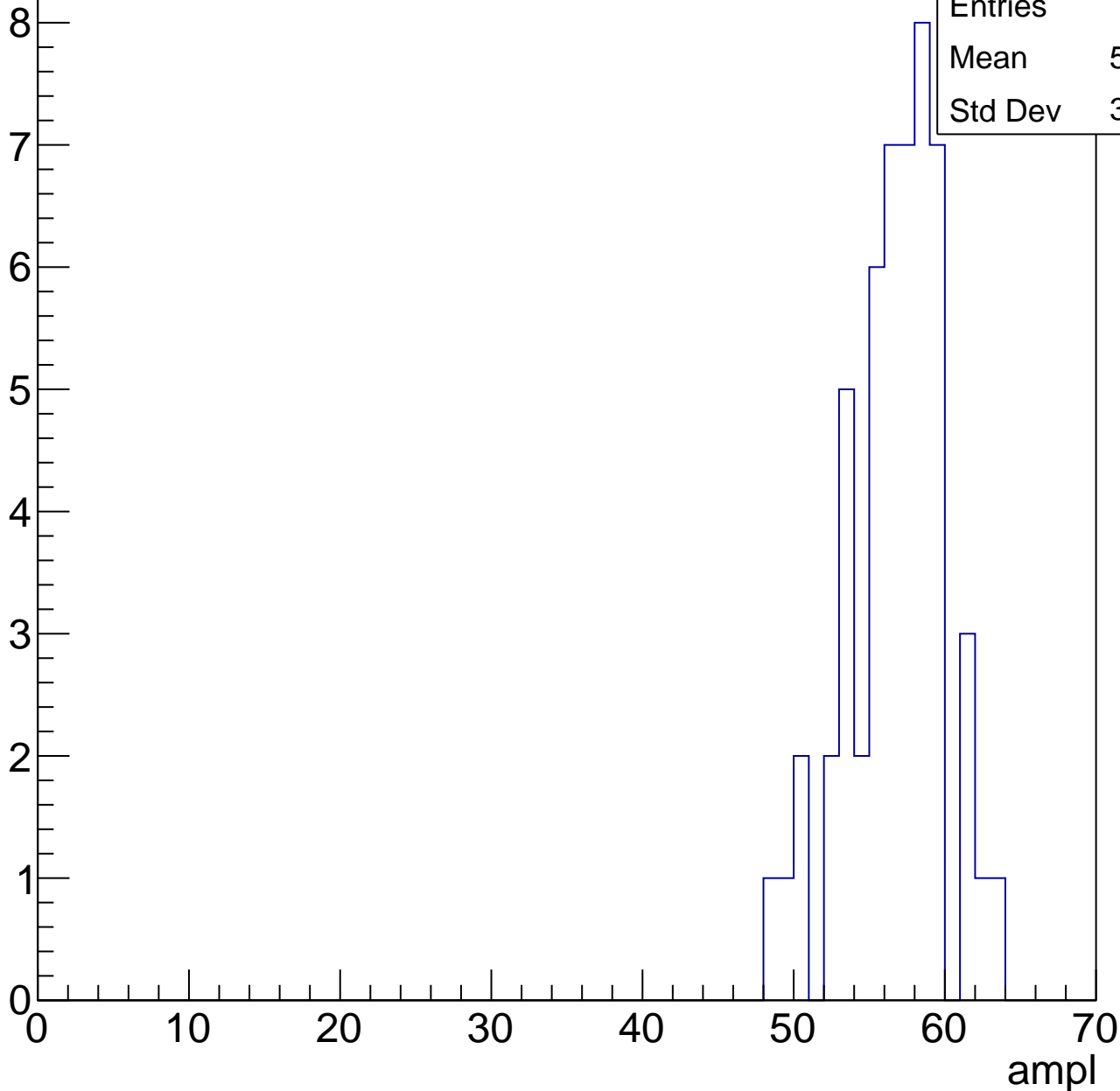


B1L102S, U26-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.23
Std Dev	3.178

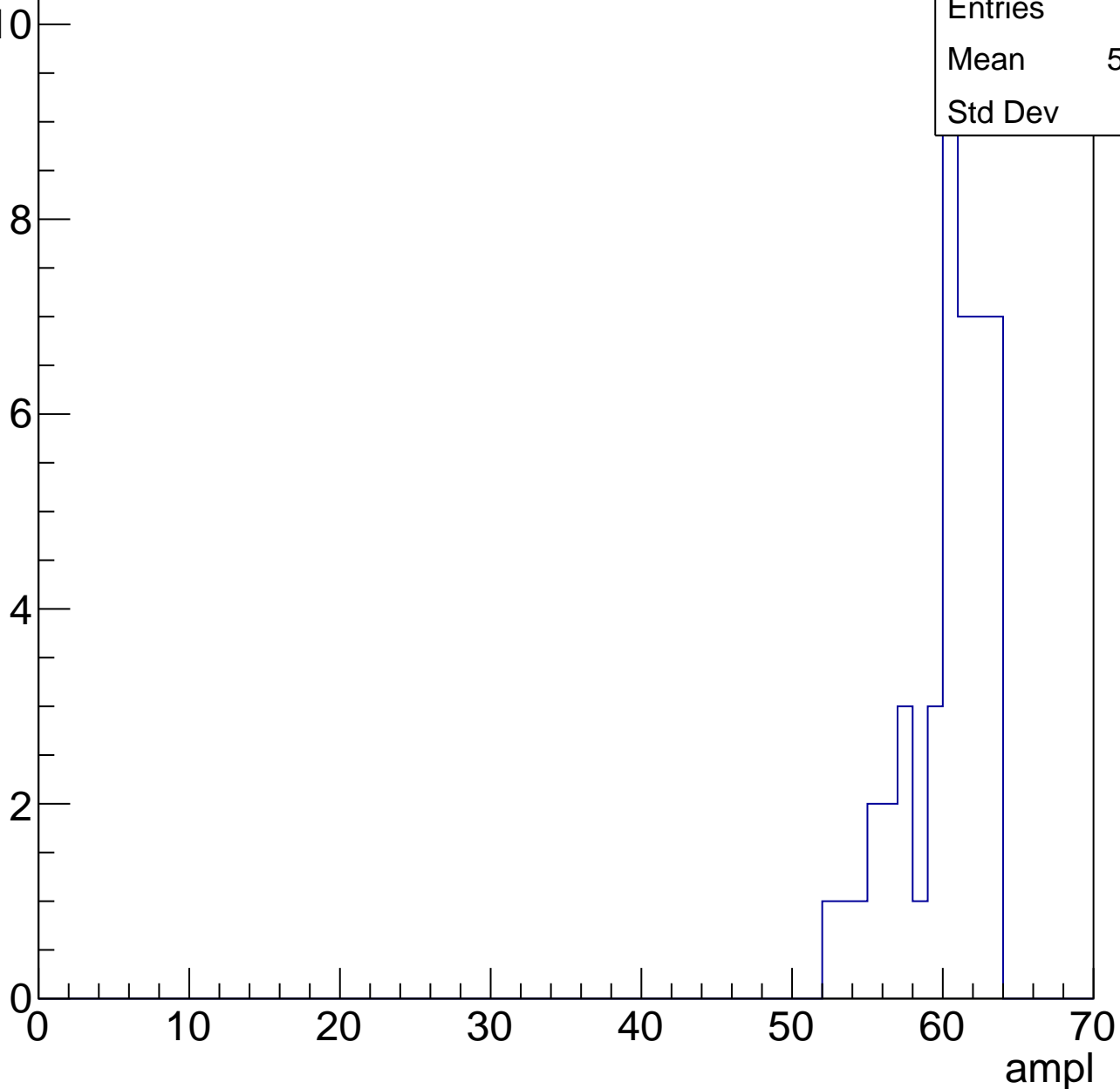


B1L102S, U26-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.76
Std Dev	2.83



B1L102S, U26-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch68, adc0

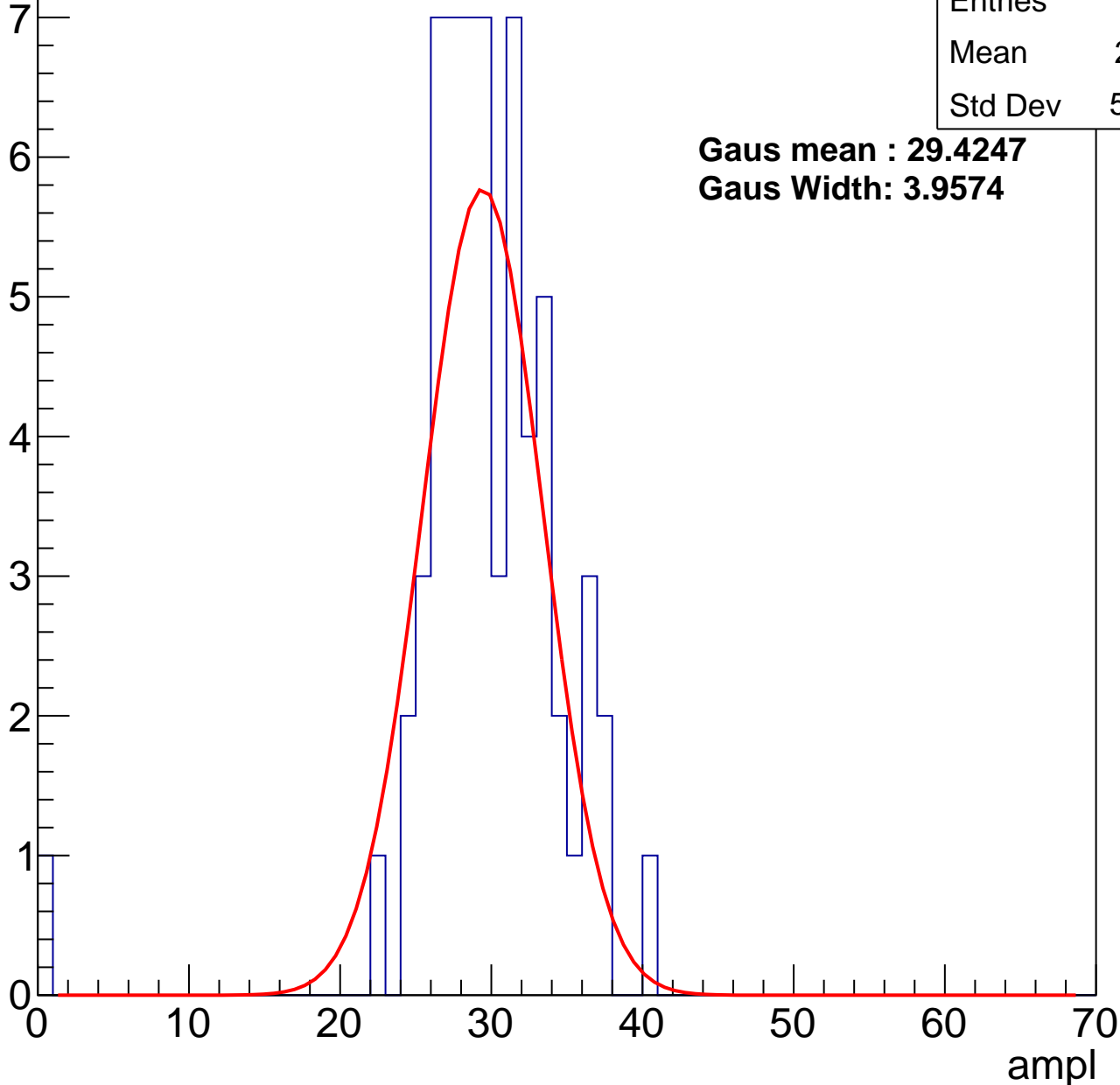
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.21
Std Dev	5.213

Gaus mean : 29.4247

Gaus Width: 3.9574



B1L102S, U26-ch68, adc1

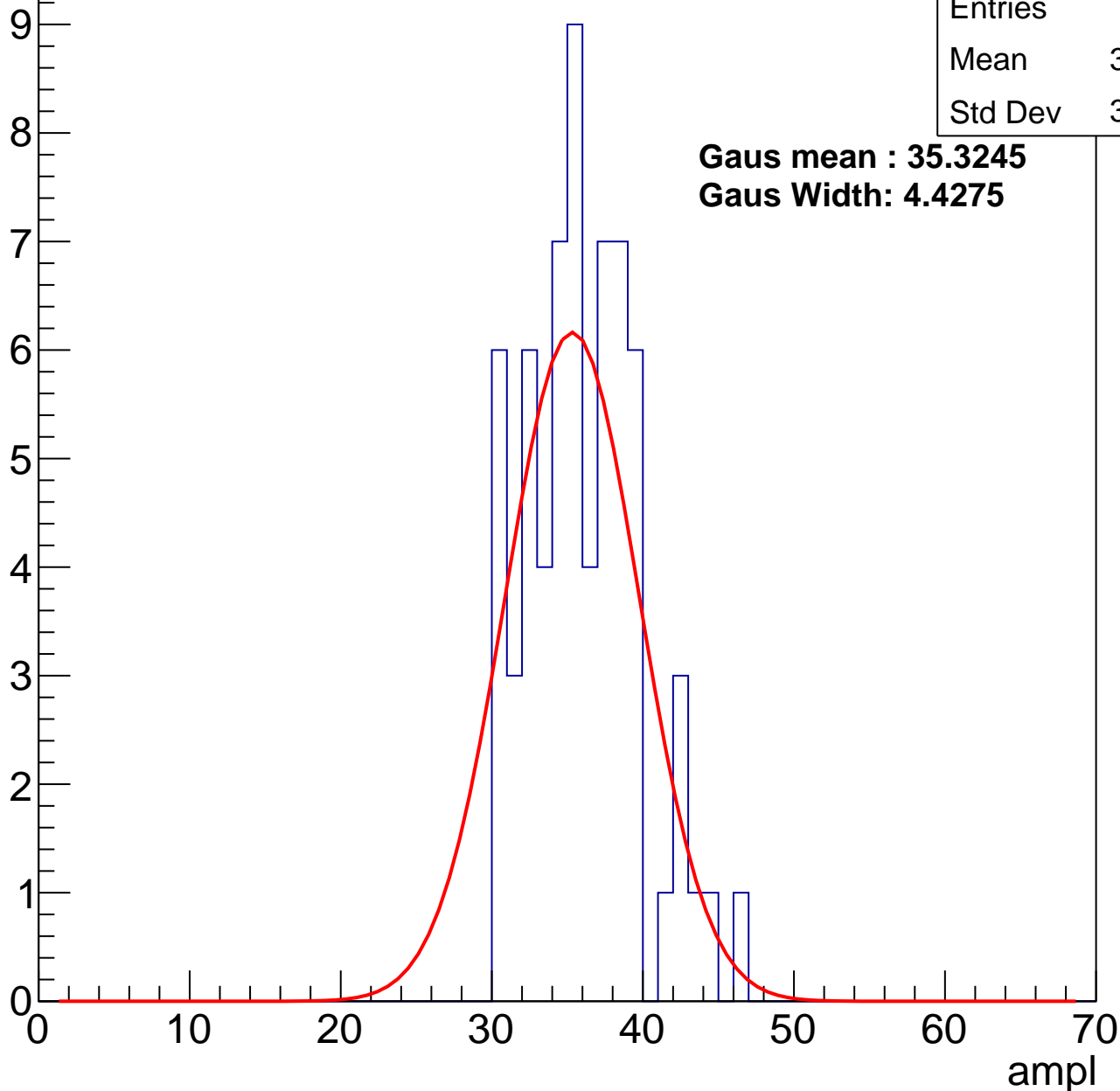
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.65
Std Dev	3.666

Gaus mean : 35.3245

Gaus Width: 4.4275



B1L102S, U26-ch68, adc2

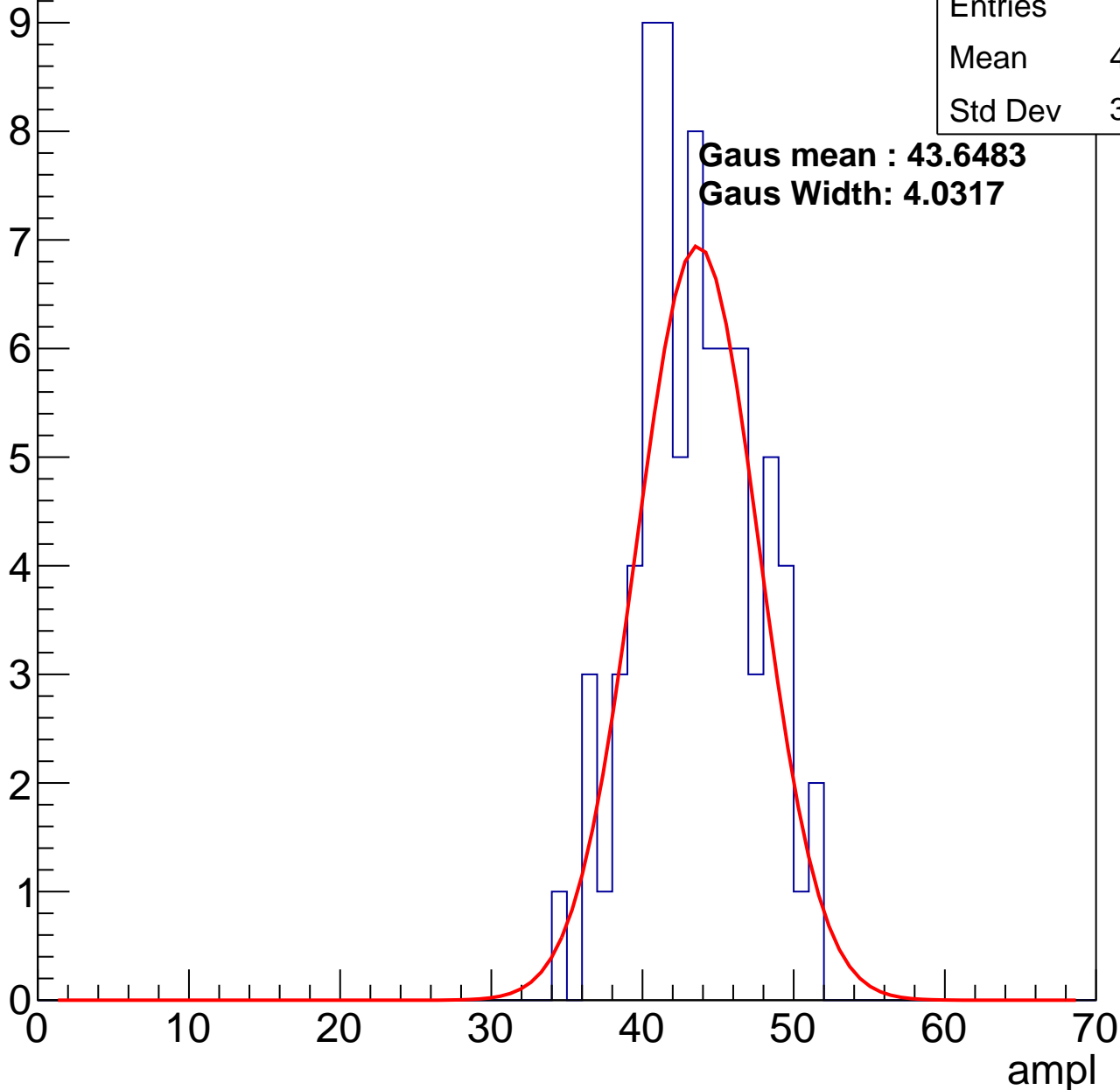
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.04
Std Dev	3.809

Gaus mean : 43.6483

Gaus Width: 4.0317

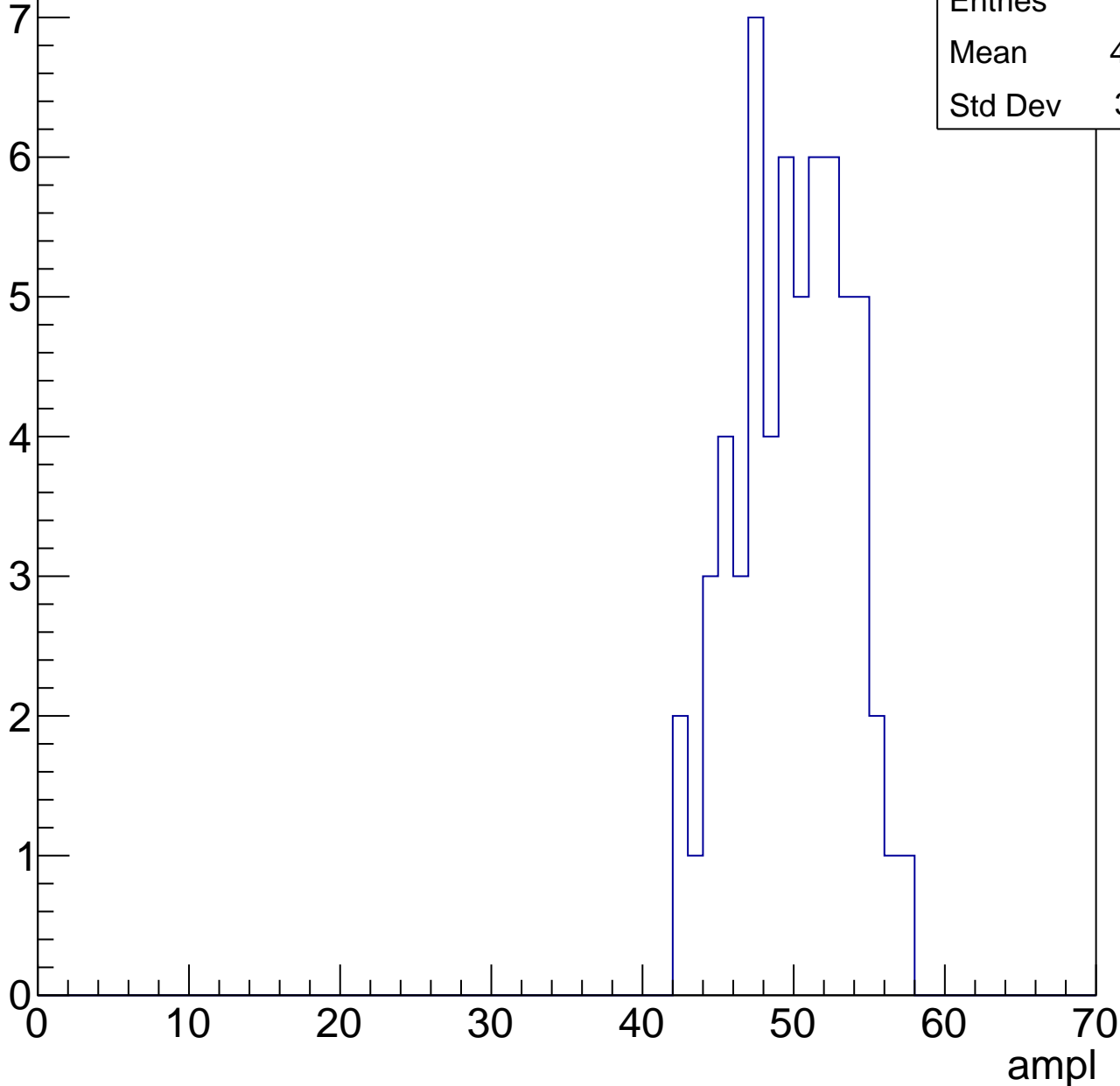


B1L102S, U26-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.48
Std Dev	3.601

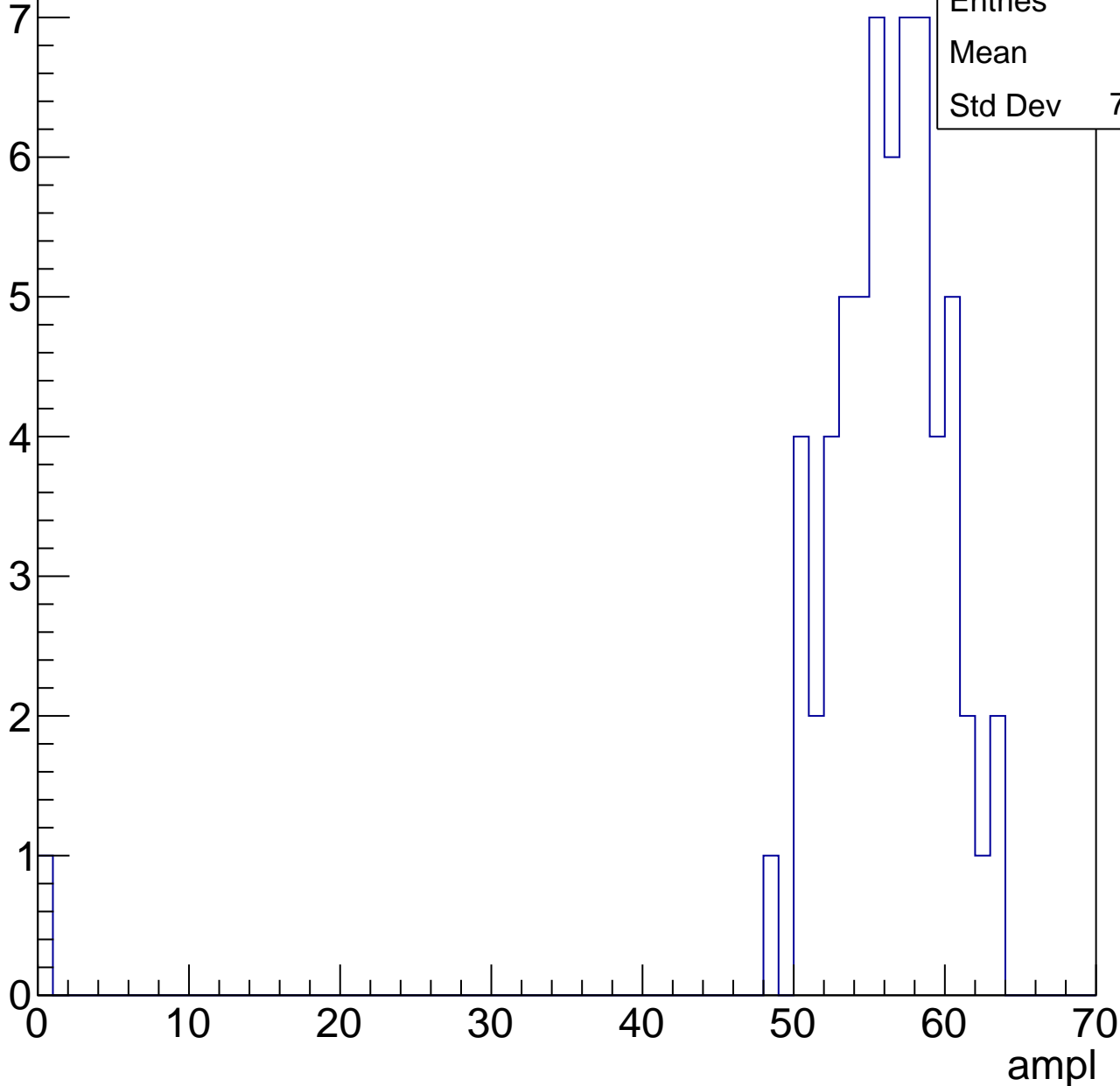


B1L102S, U26-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55
Std Dev	7.768

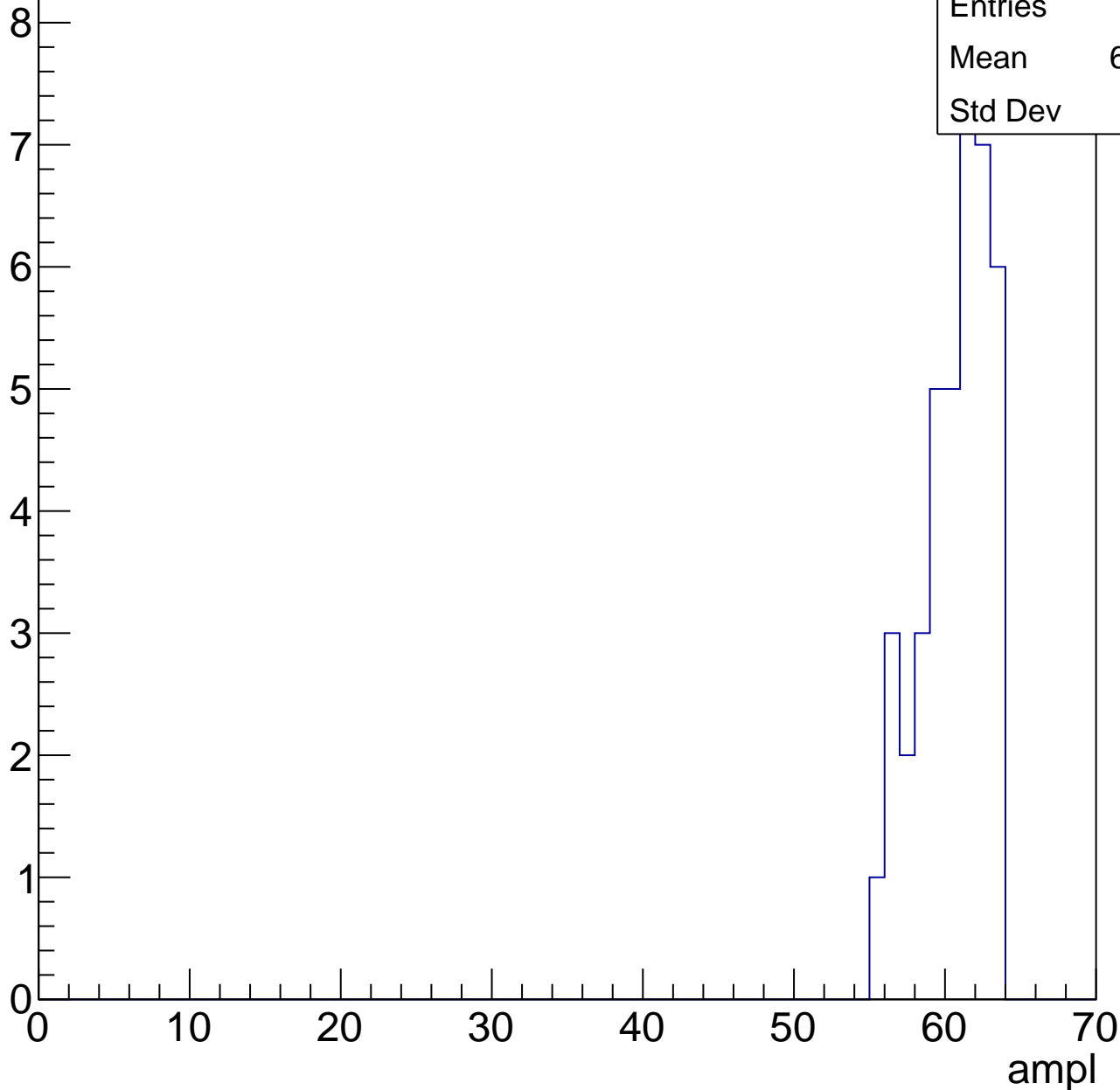


B1L102S, U26-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	60.15
Std Dev	2.22



B1L102S, U26-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

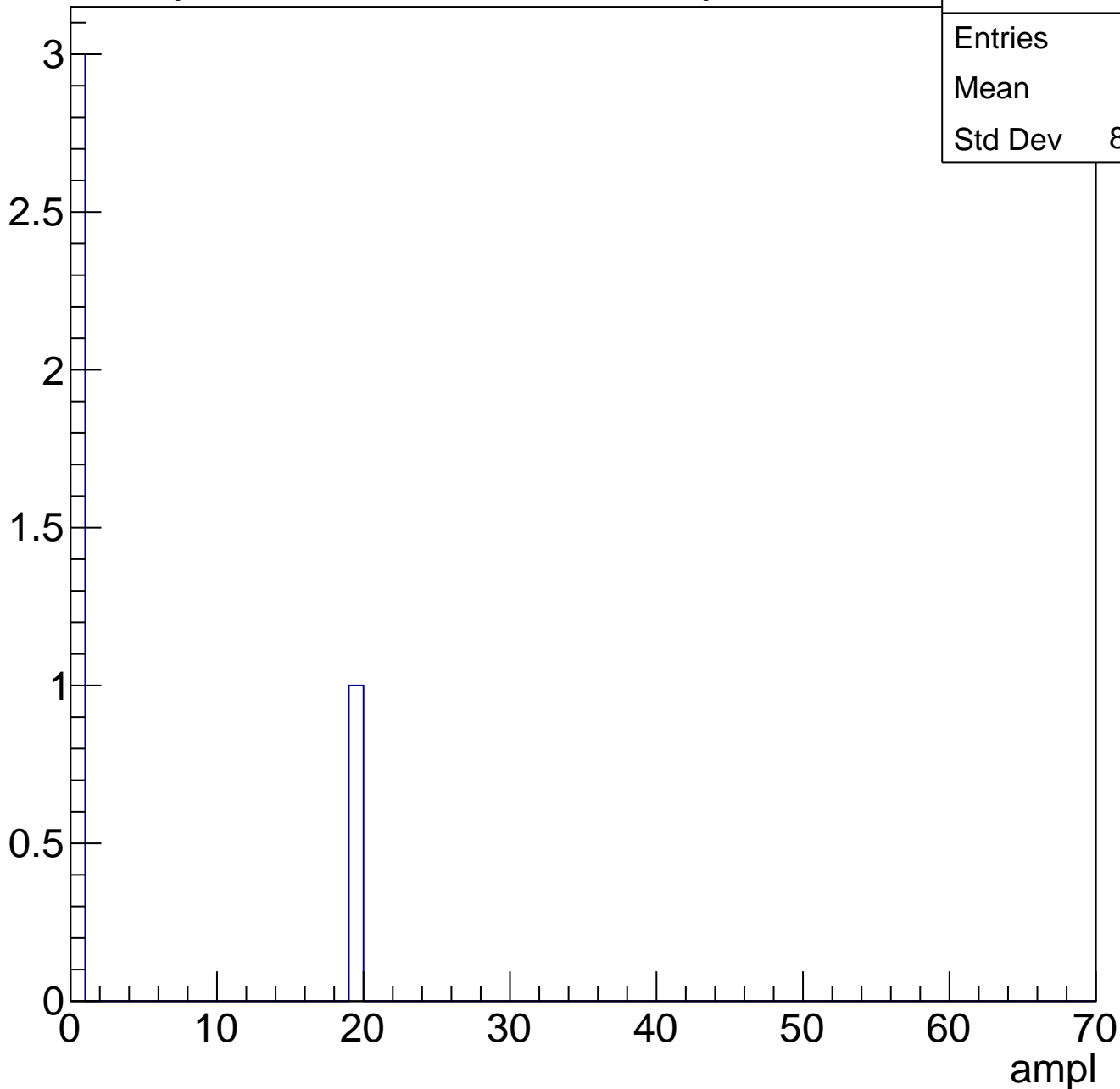
Entry



B1L102S, U26-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	4.75
Std Dev	8.227

B1L102S, U26-ch69, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	28.94
Std Dev	5.865

Gaus mean : 30.2699

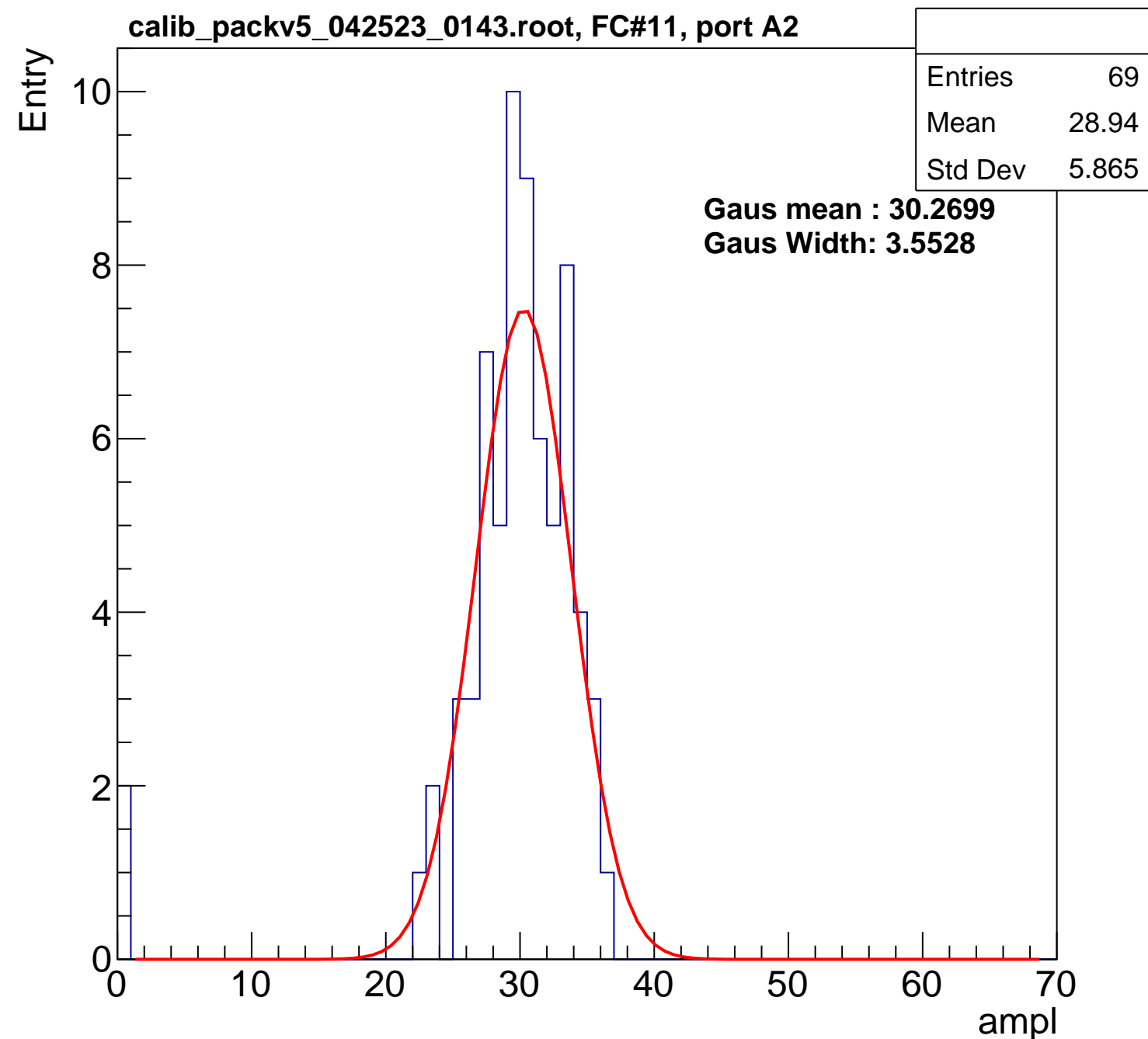
Gaus Width: 3.5528

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



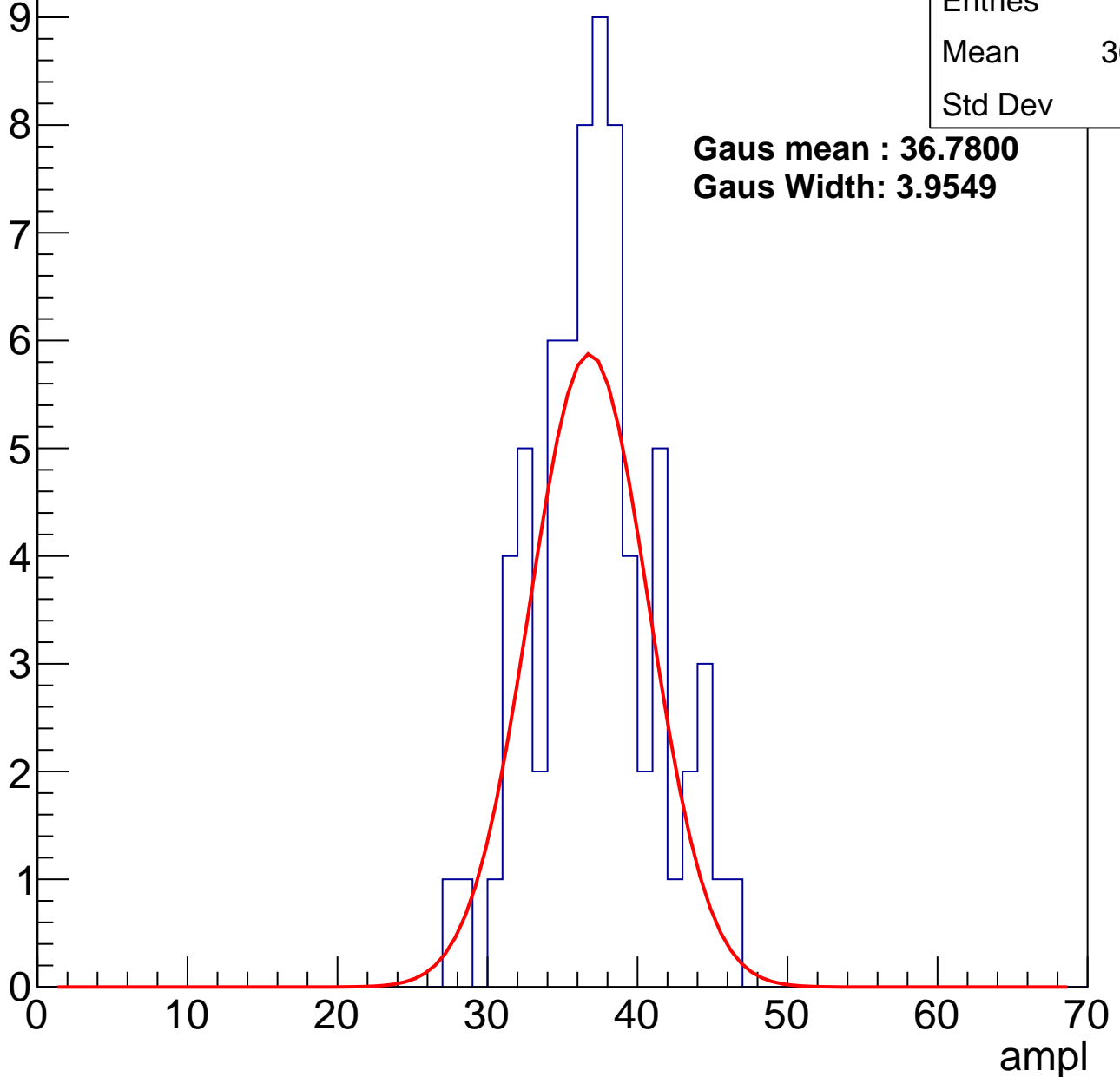
B1L102S, U26-ch69, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.66
Std Dev	4

Gaus mean : 36.7800
Gaus Width: 3.9549



B1L102S, U26-ch69, adc2

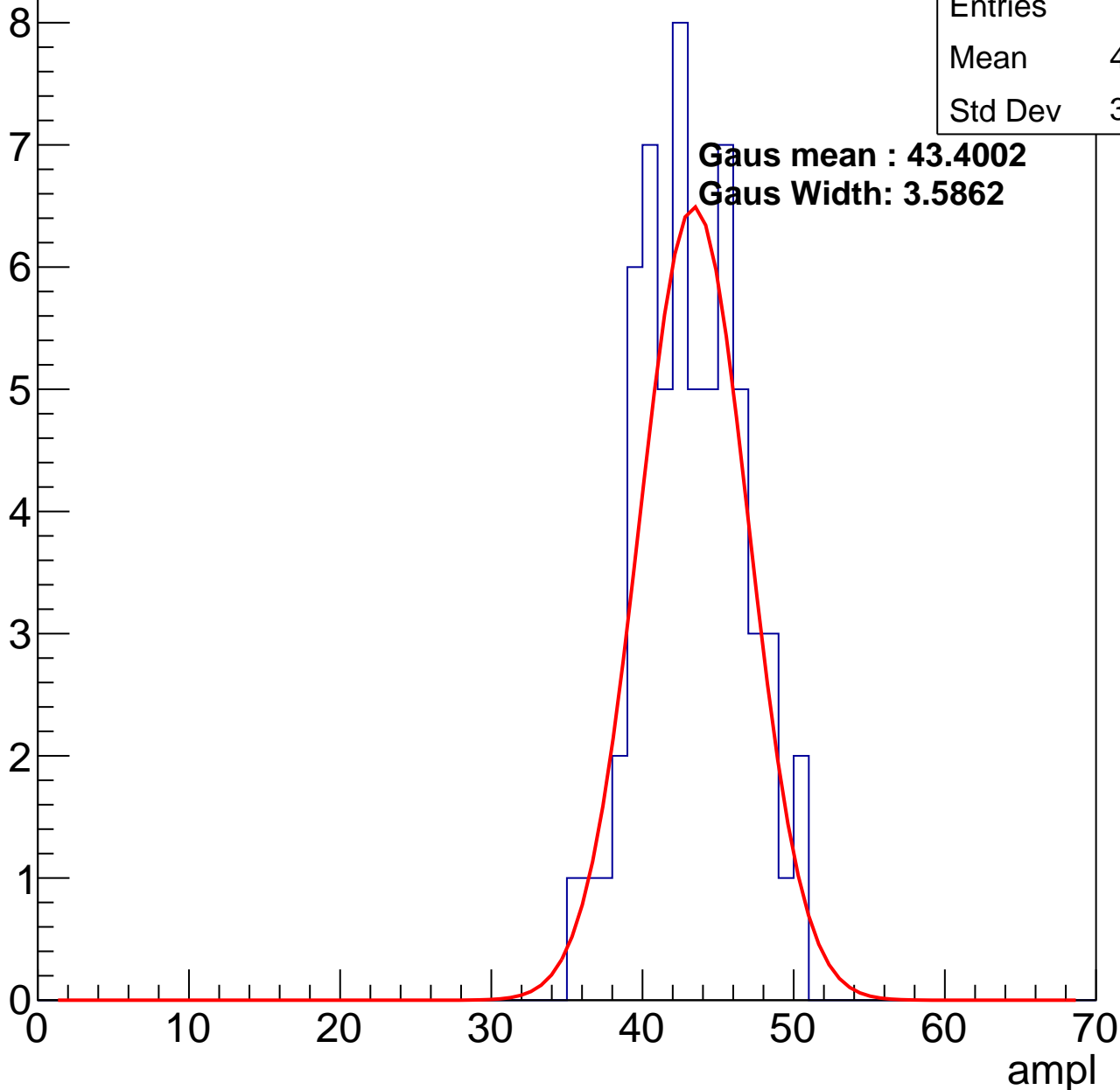
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.79
Std Dev	3.404

Gaus mean : 43.4002

Gaus Width: 3.5862

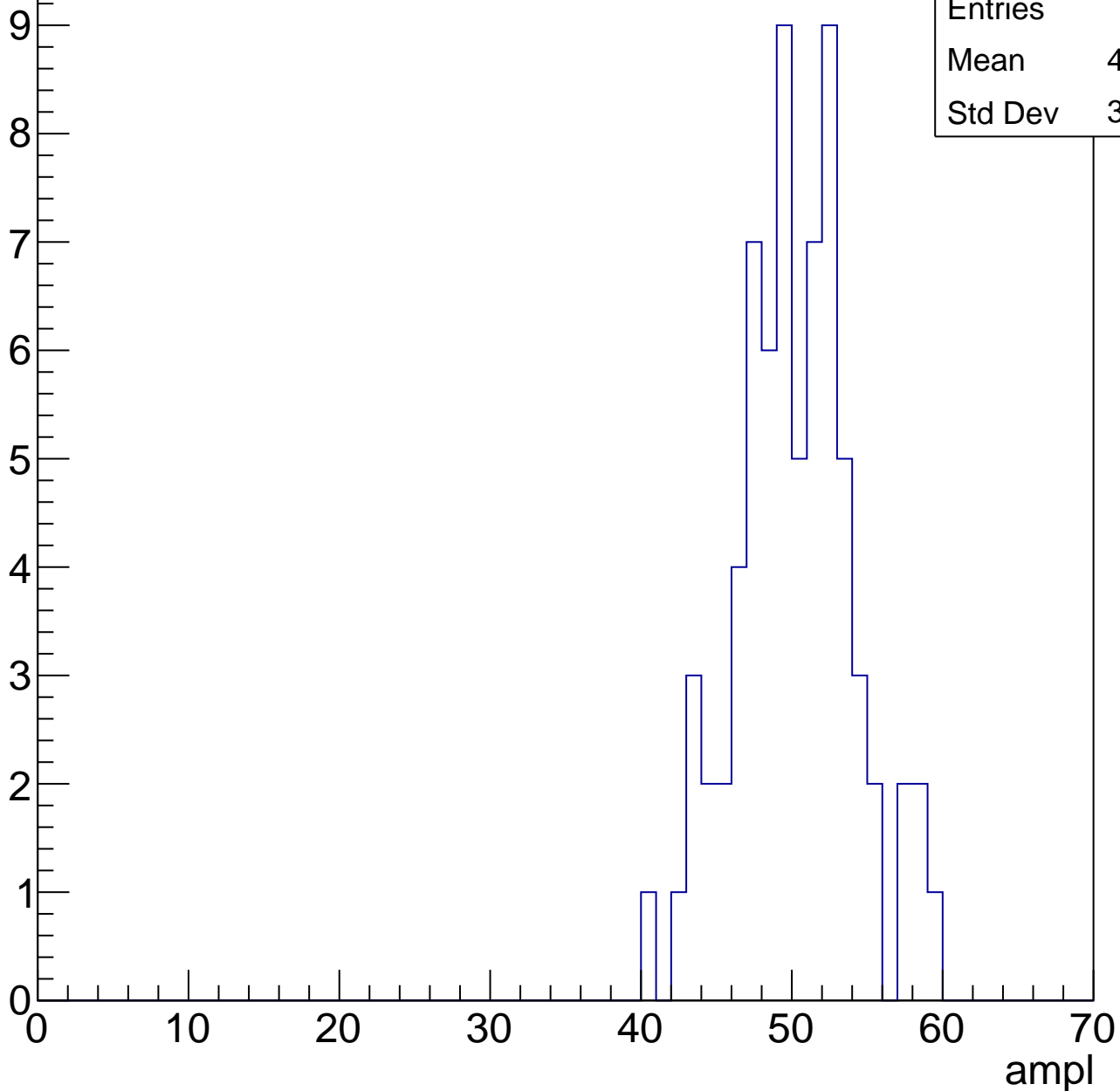


B1L102S, U26-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.75
Std Dev	3.903

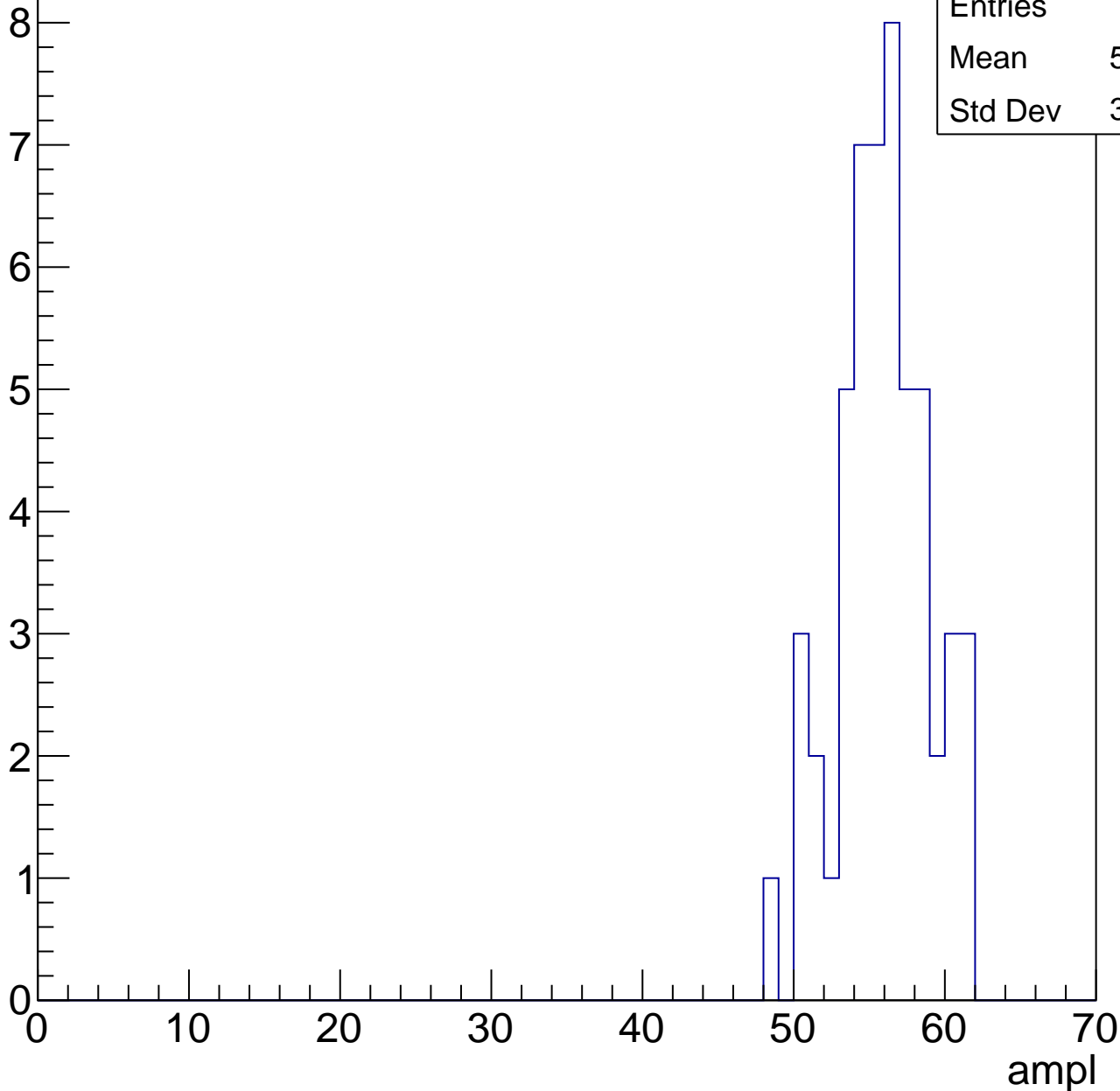


B1L102S, U26-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.46
Std Dev	3.003

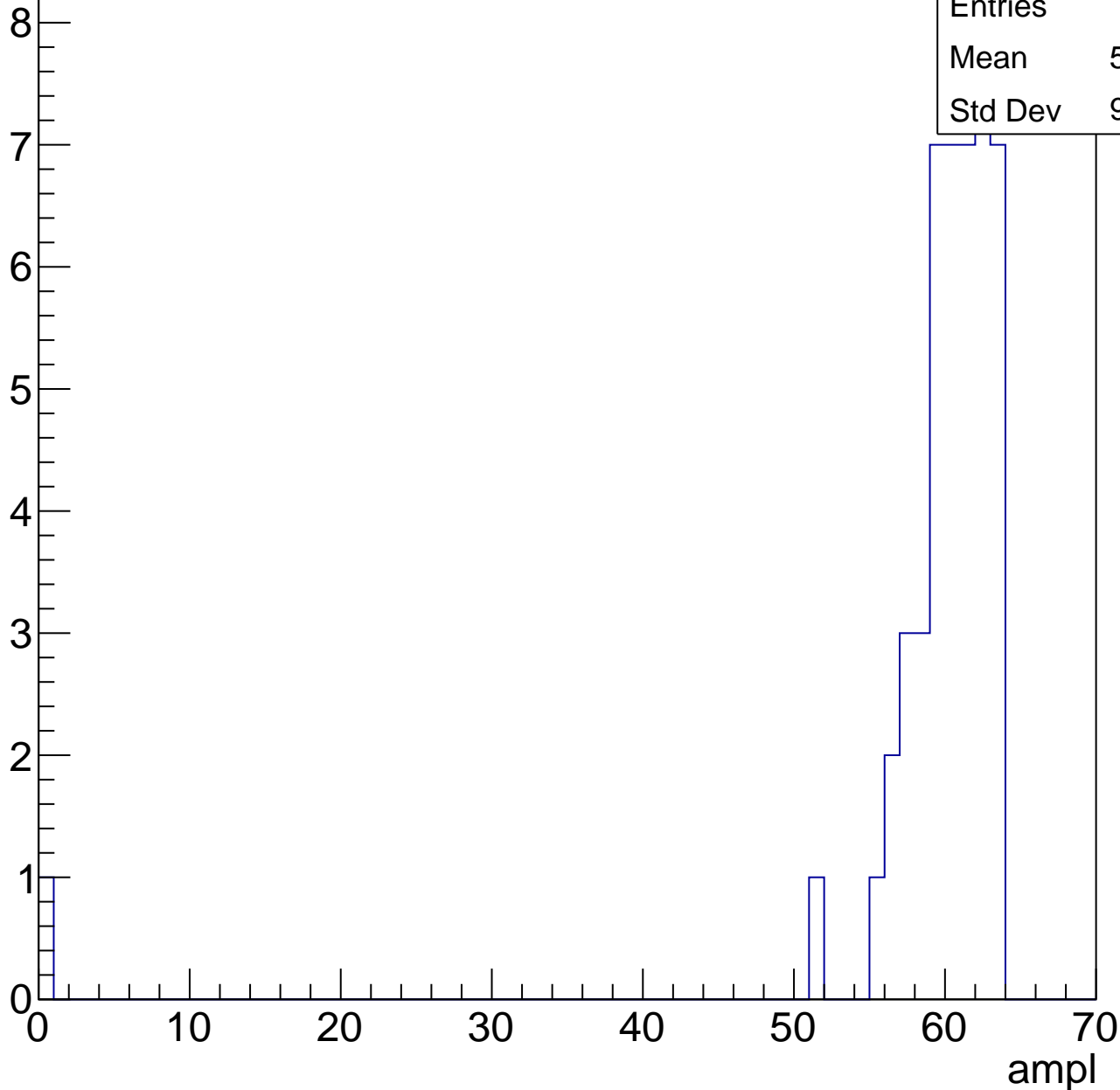


B1L102S, U26-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.72
Std Dev	9.003



B1L102S, U26-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch70, adc0

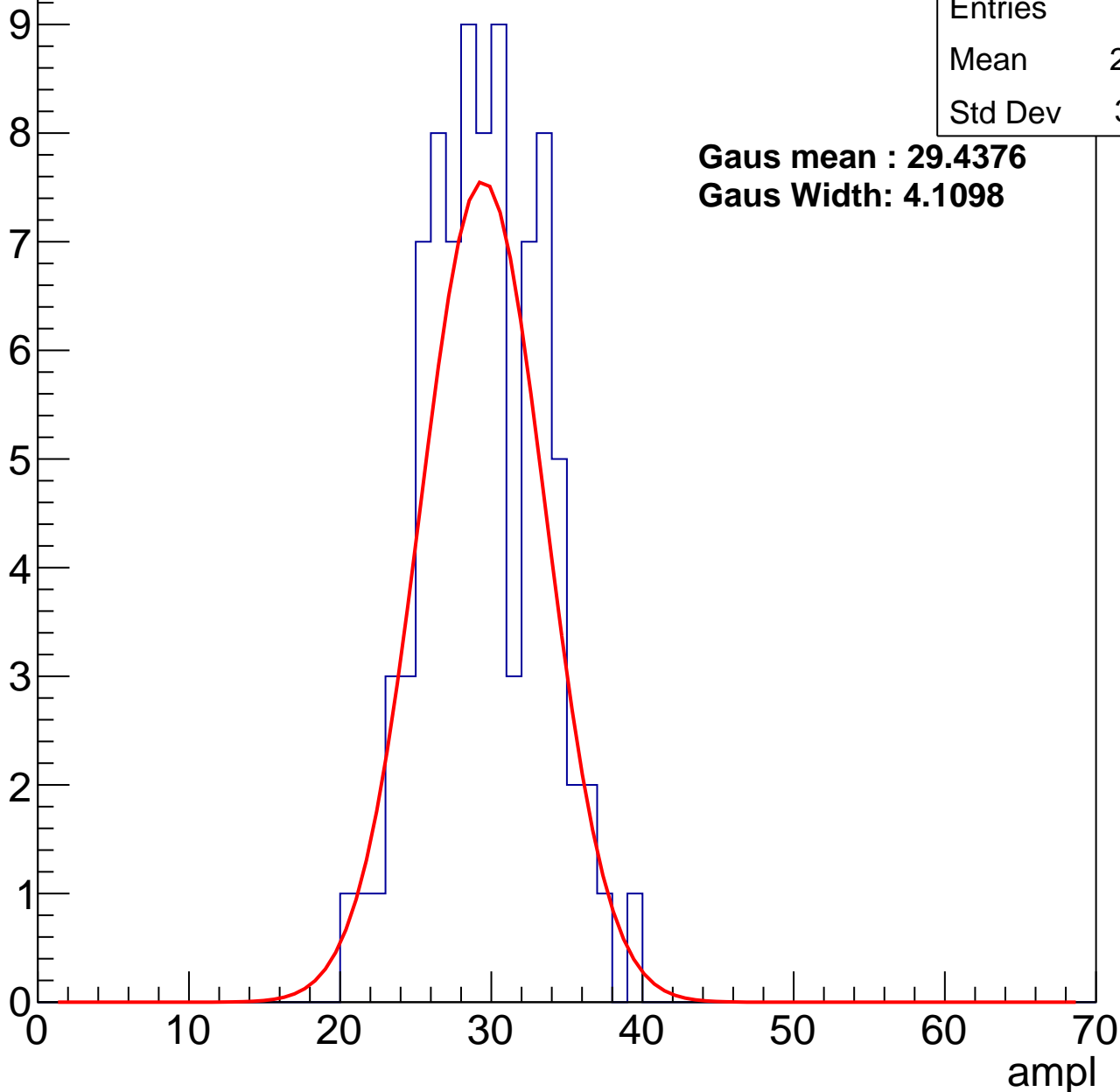
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	29.06
Std Dev	3.841

Gaus mean : 29.4376

Gaus Width: 4.1098



B1L102S, U26-ch70, adc1

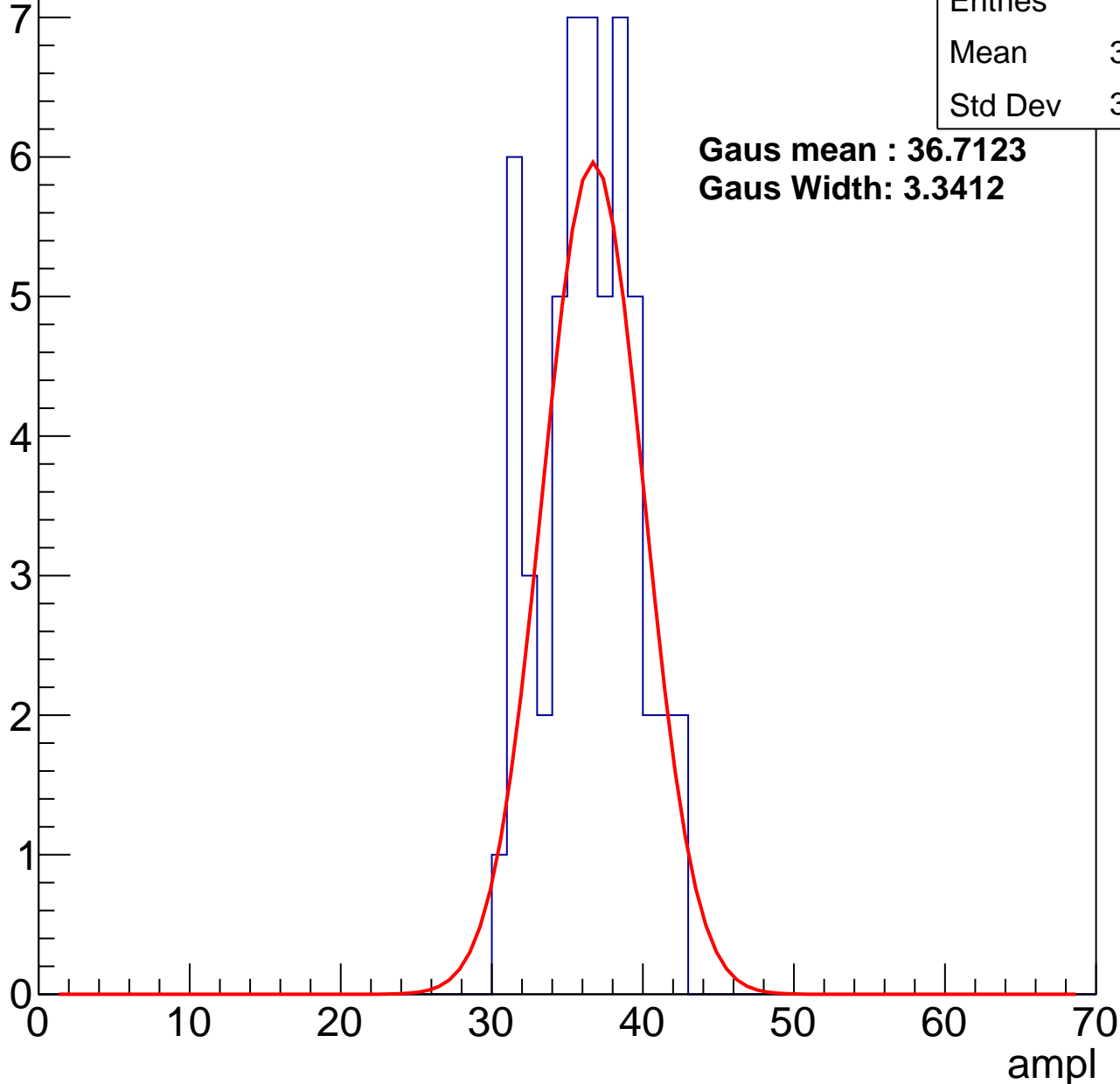
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.87
Std Dev	3.073

Gaus mean : 36.7123

Gaus Width: 3.3412



B1L102S, U26-ch70, adc2

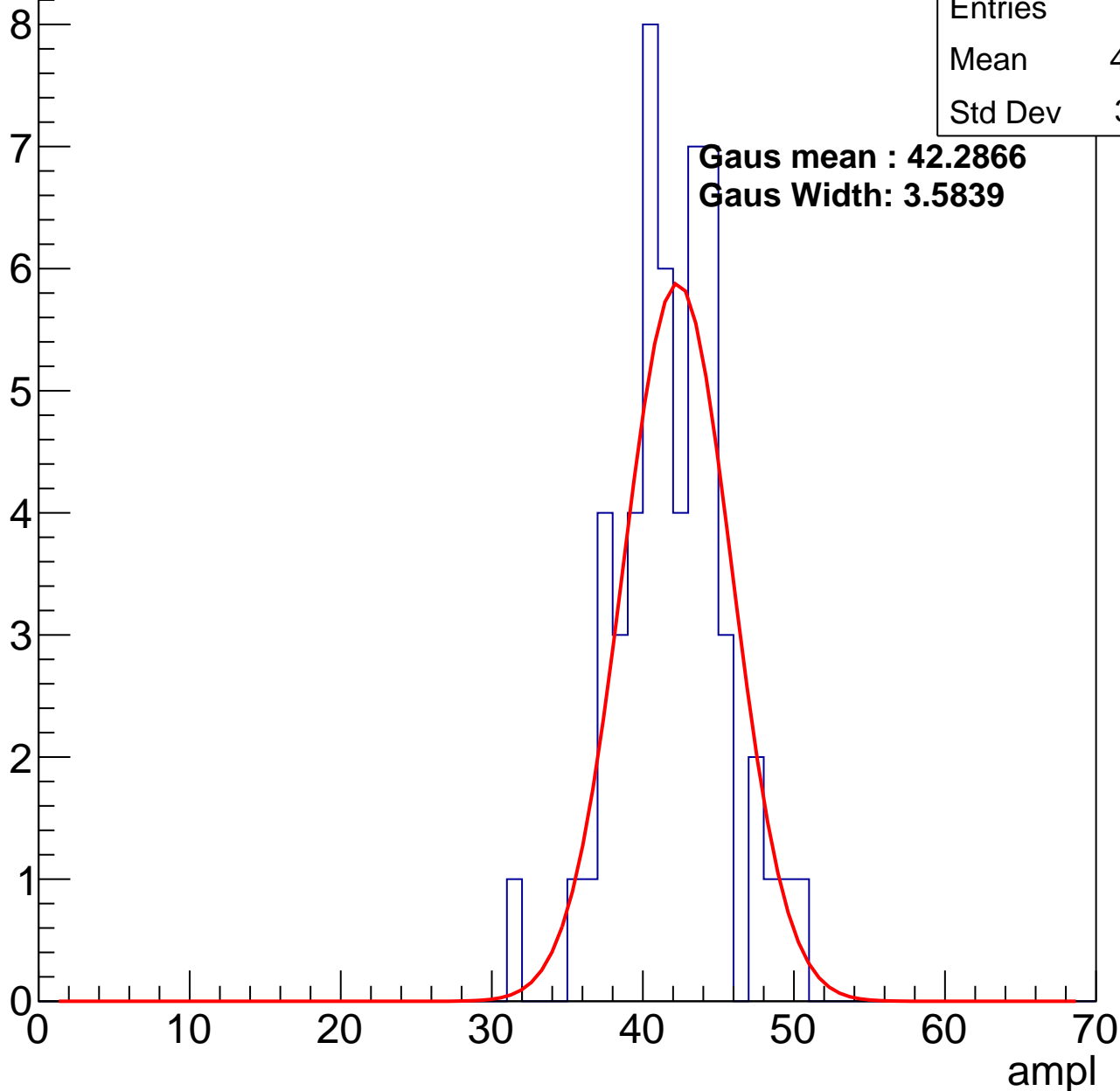
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.46
Std Dev	3.531

Gaus mean : 42.2866

Gaus Width: 3.5839

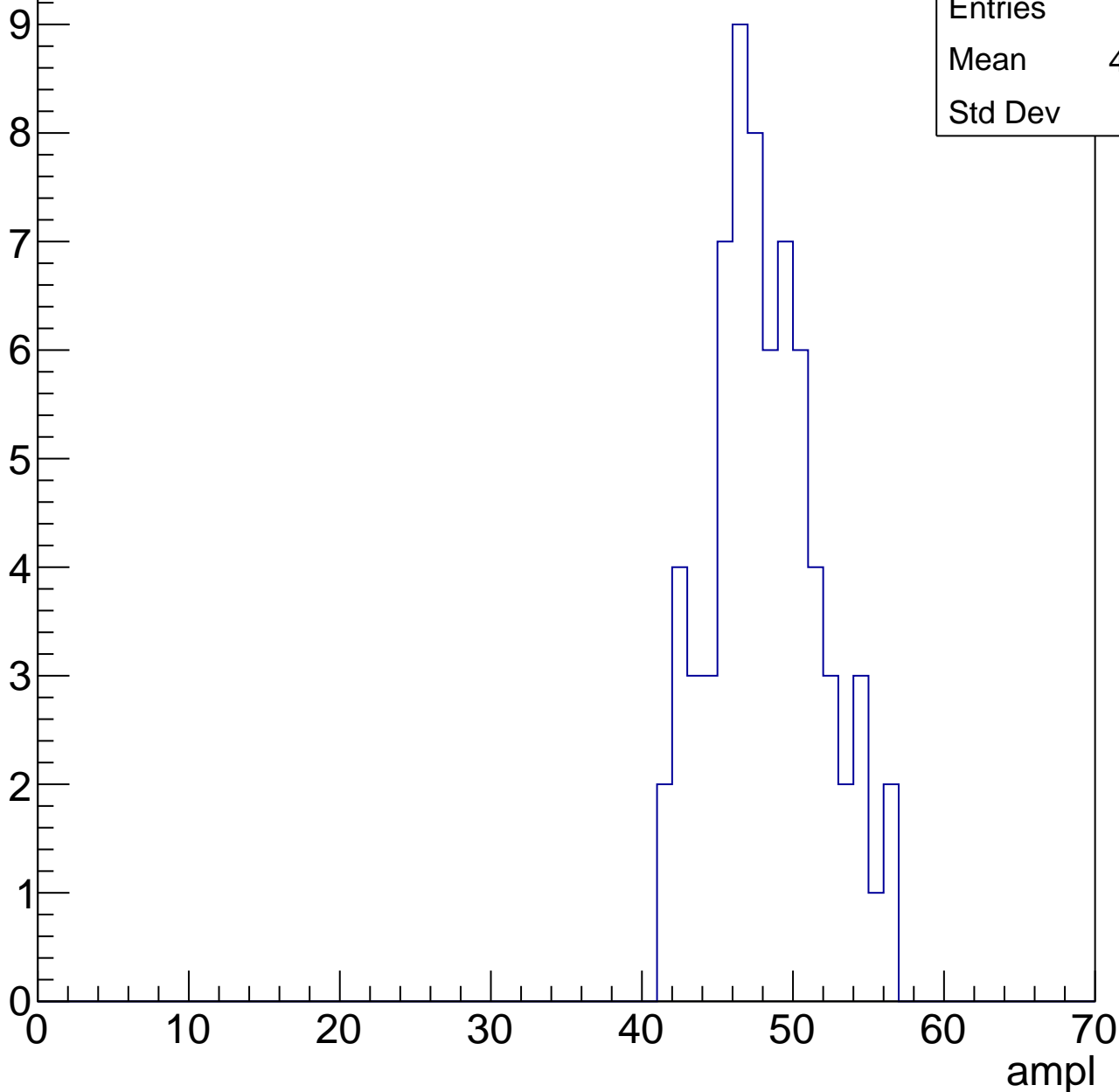


B1L102S, U26-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.74
Std Dev	3.62

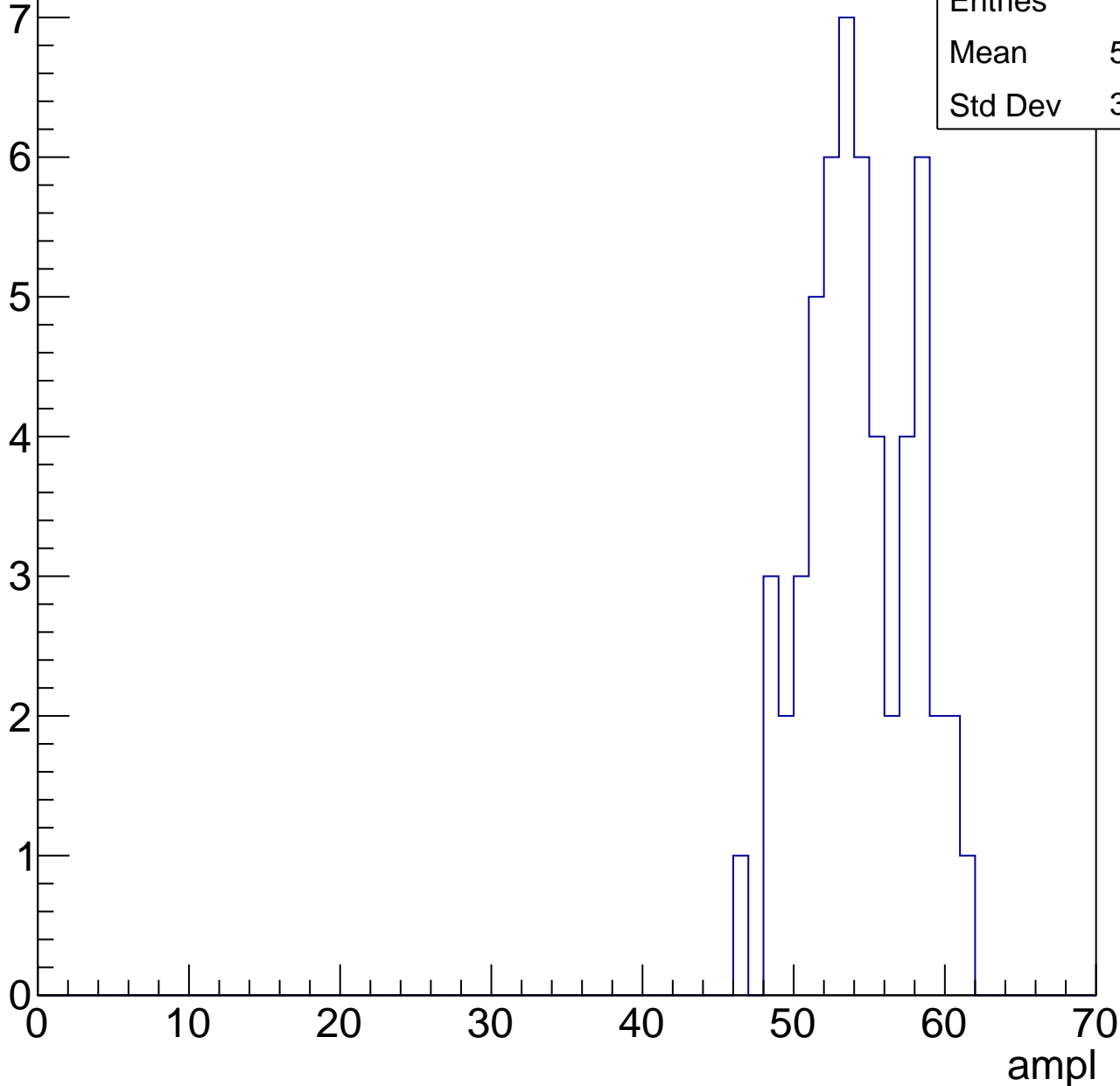


B1L102S, U26-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.83
Std Dev	3.495

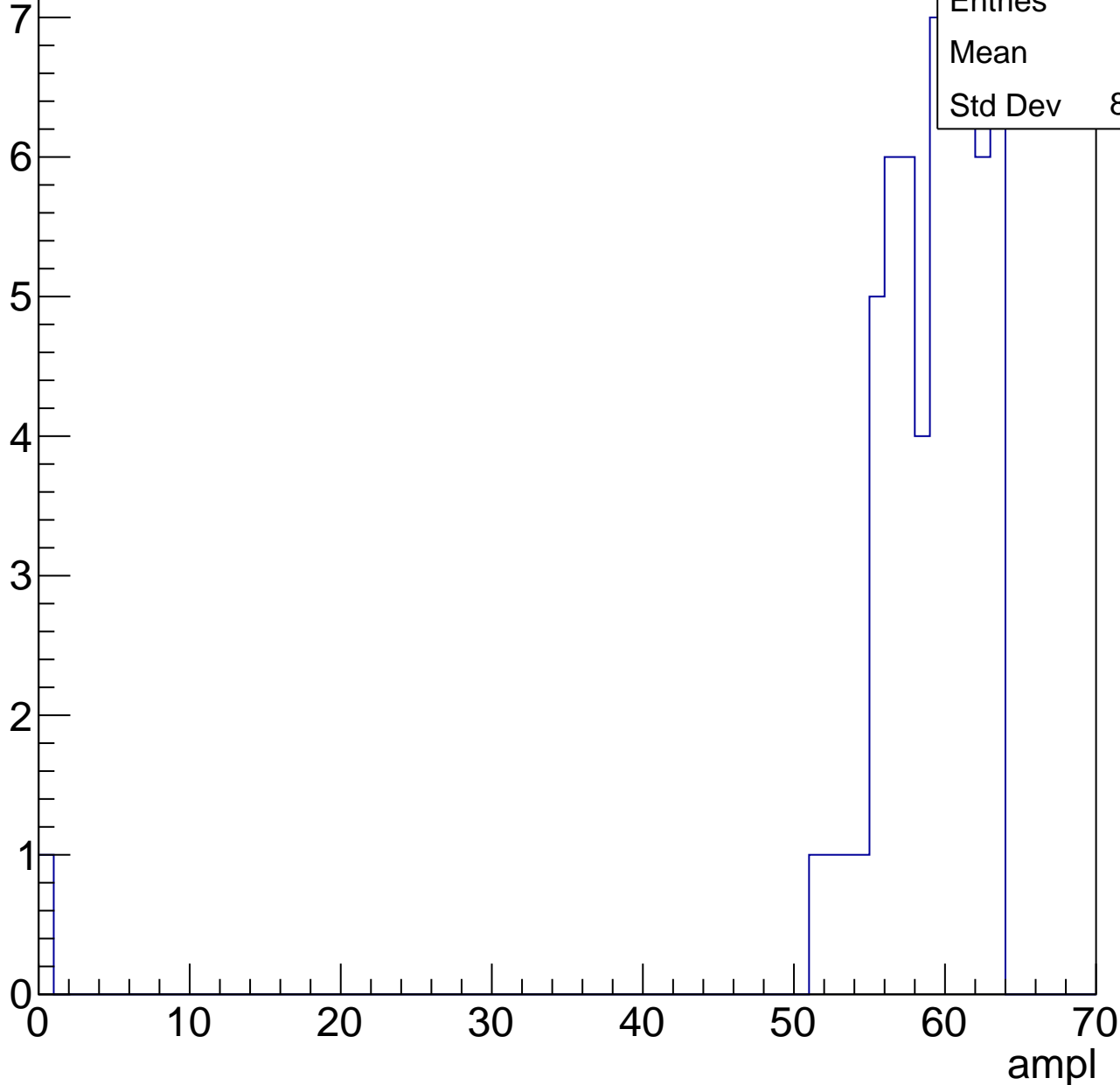


B1L102S, U26-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	57.8
Std Dev	8.095



B1L102S, U26-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.6
Std Dev	1.281

B1L102S, U26-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch71, adc0

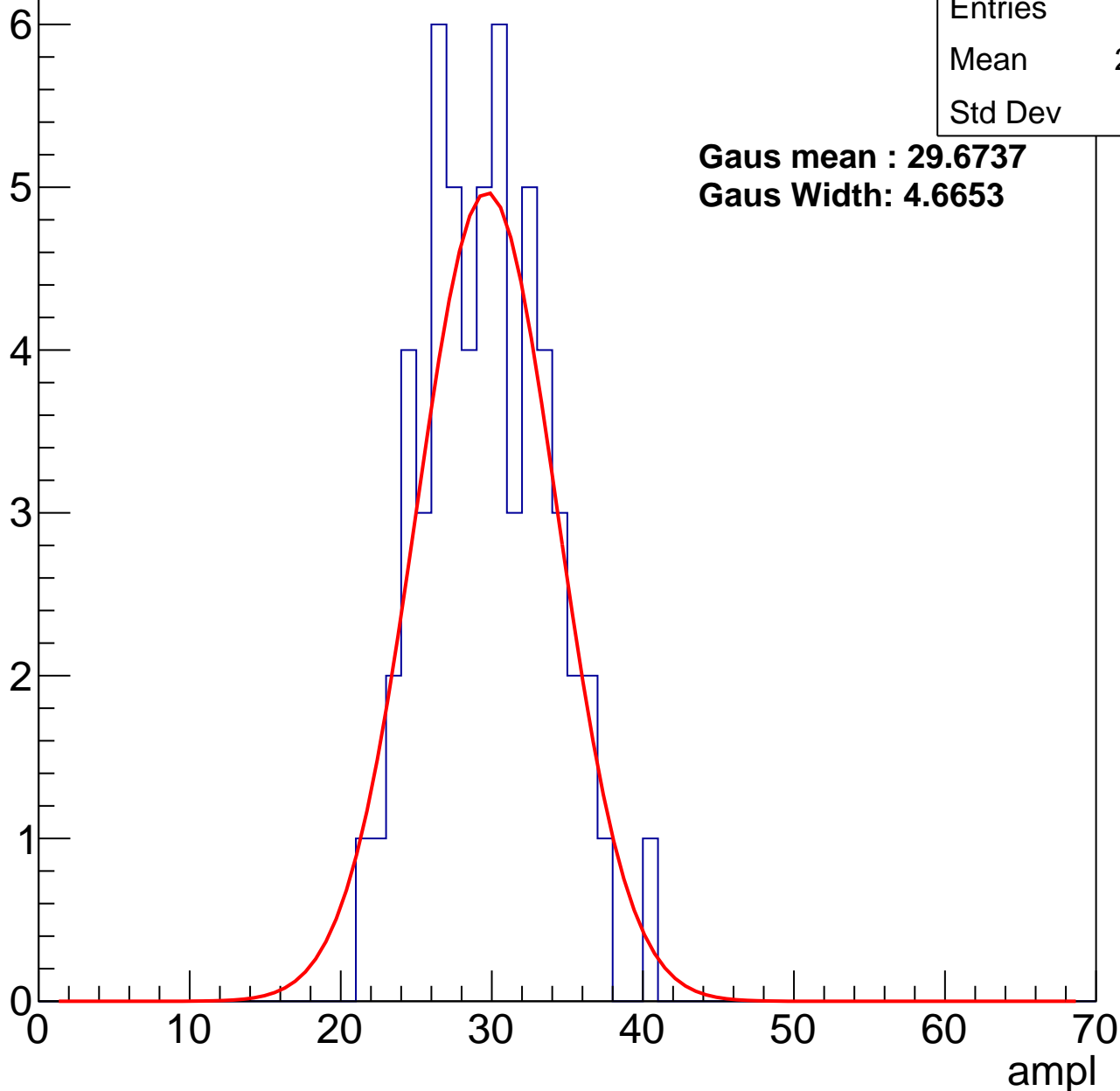
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.21
Std Dev	4.08

Gaus mean : 29.6737

Gaus Width: 4.6653



B1L102S, U26-ch71, adc1

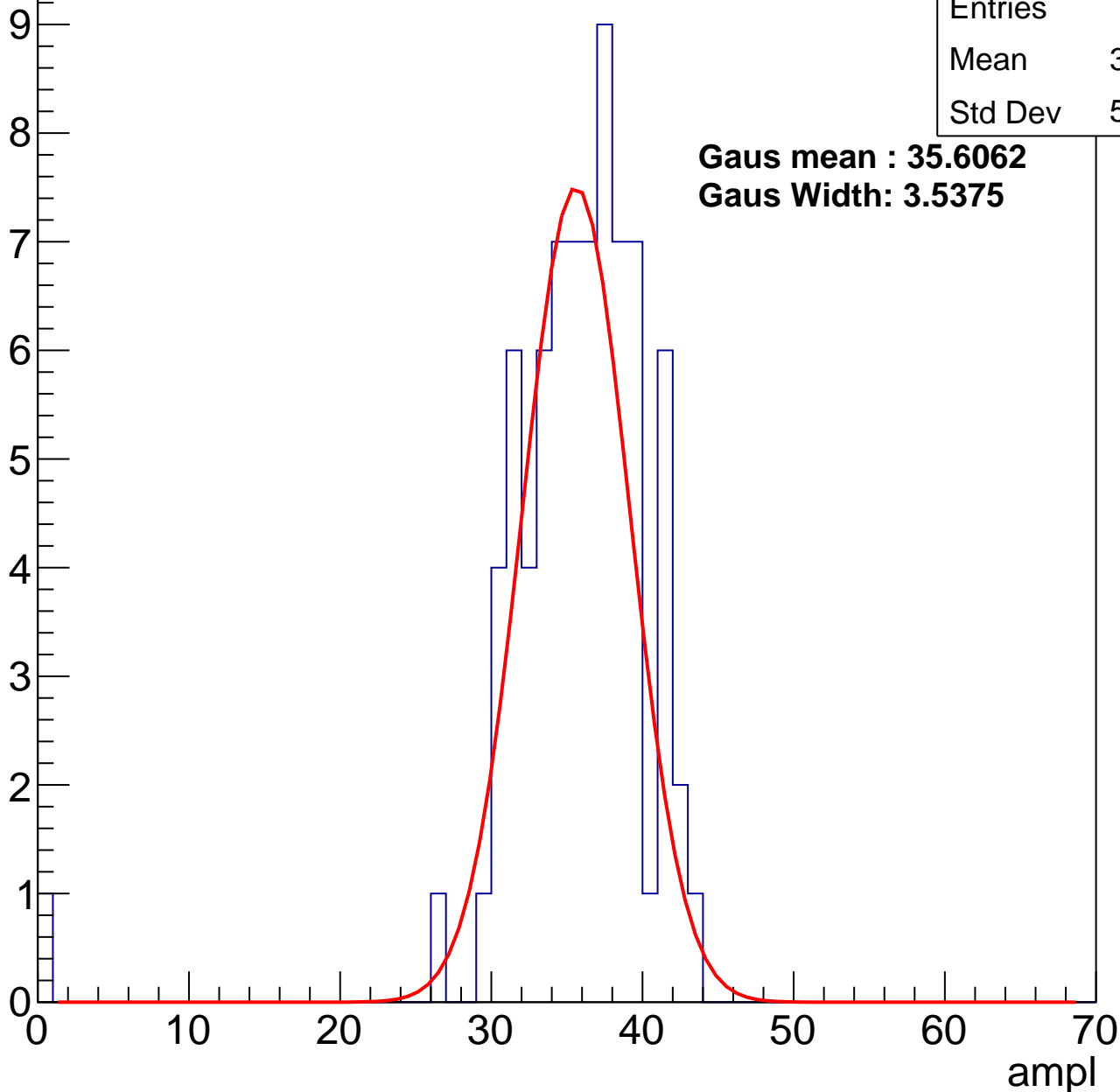
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.16
Std Dev	5.374

Gaus mean : 35.6062

Gaus Width: 3.5375



B1L102S, U26-ch71, adc2

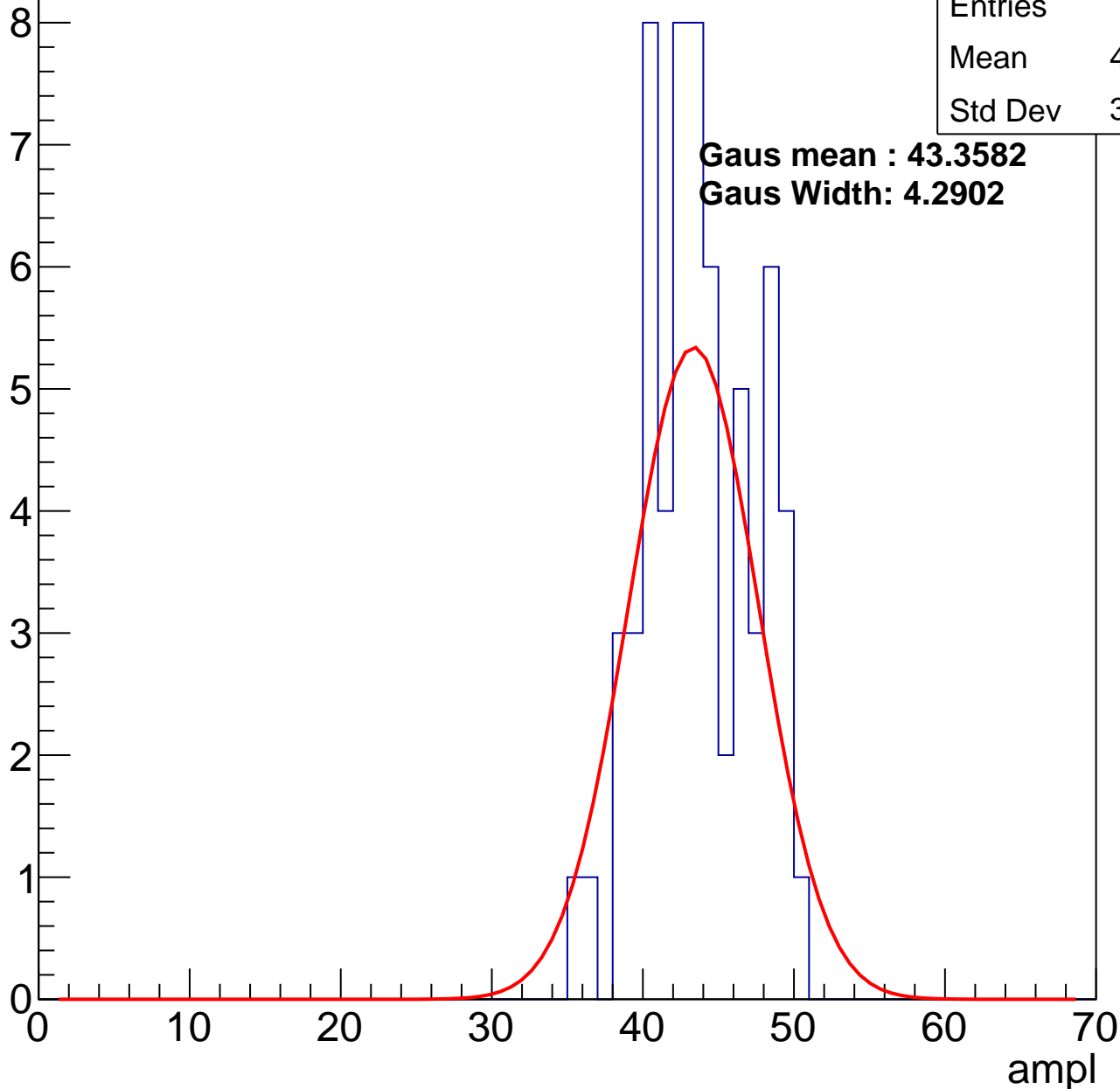
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.25
Std Dev	3.514

Gaus mean : 43.3582

Gaus Width: 4.2902

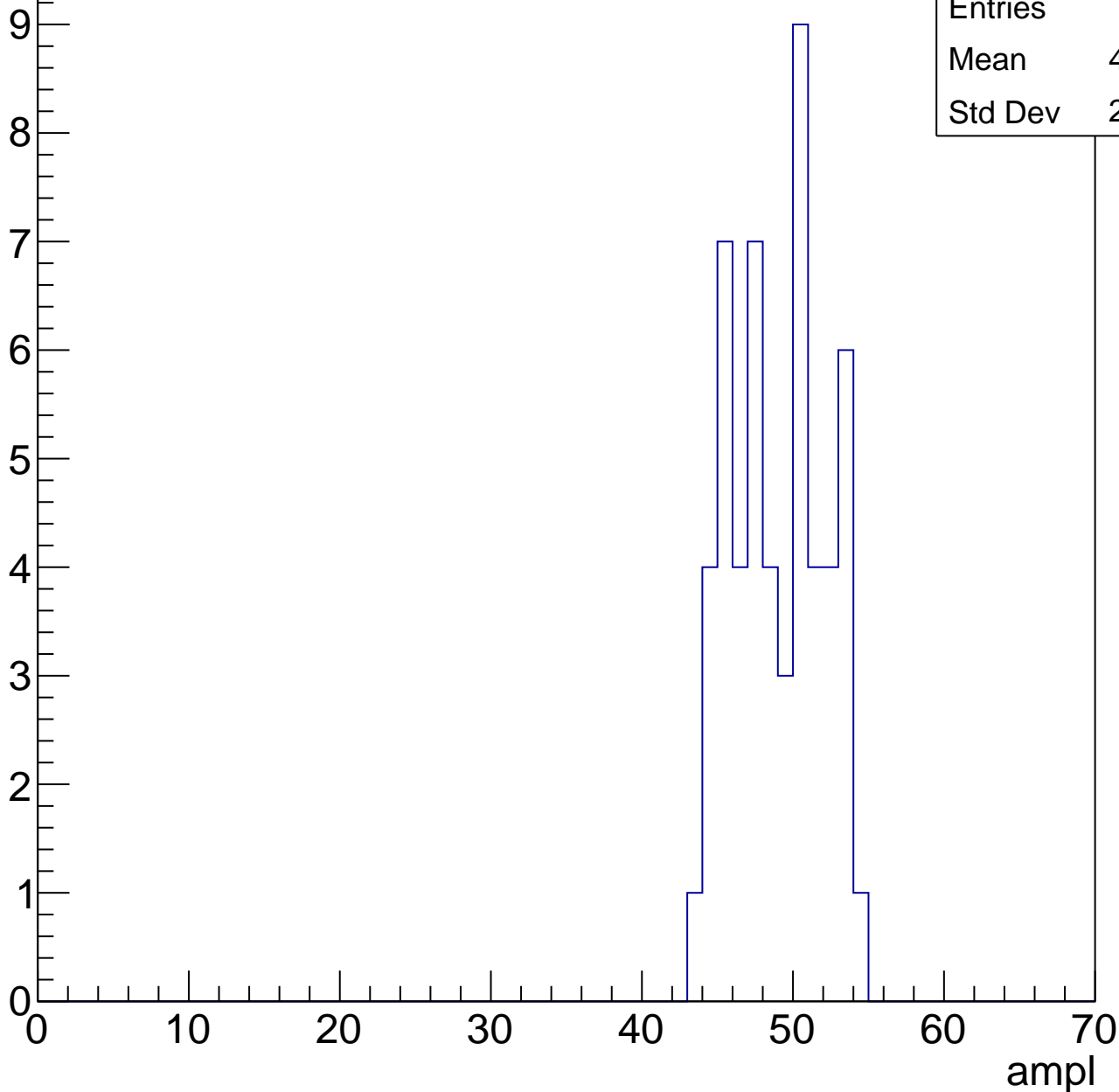


B1L102S, U26-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.52
Std Dev	2.998



B1L102S, U26-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	55.06
Std Dev	7.111

Entry

10

8

6

4

2

0

0

10

20

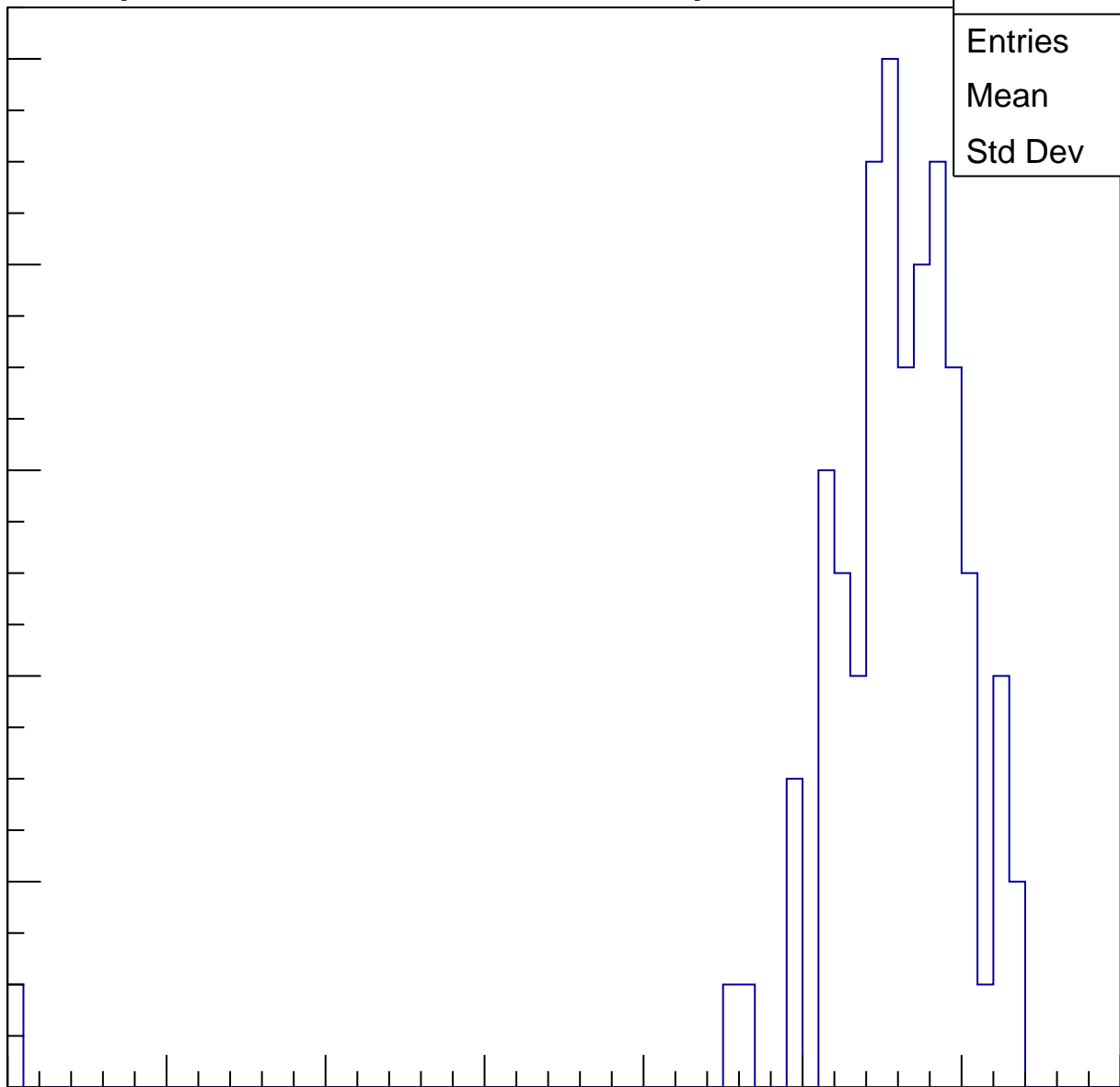
30

40

50

60

ampl

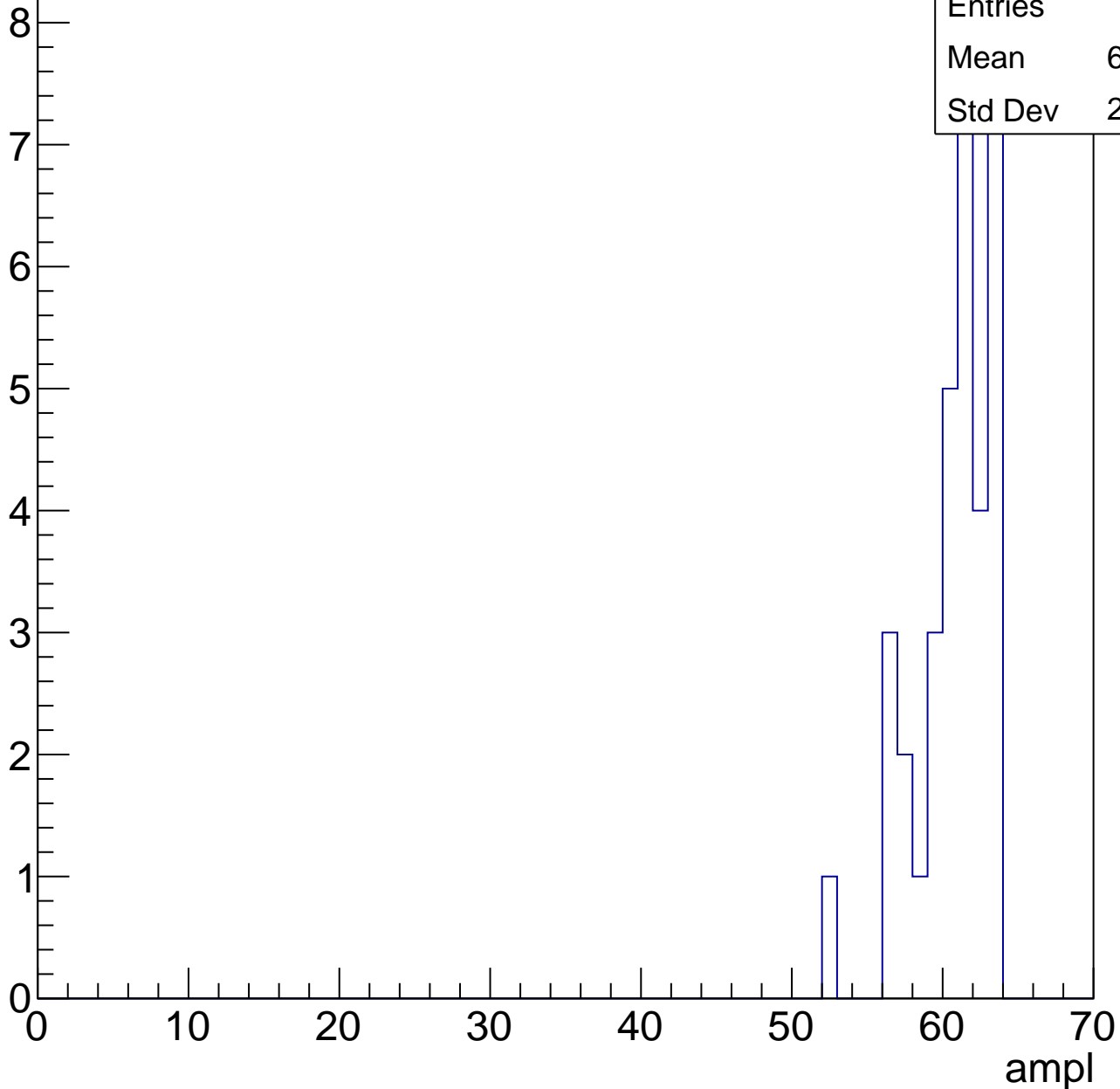


B1L102S, U26-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.26
Std Dev	2.567



B1L102S, U26-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch72, adc0

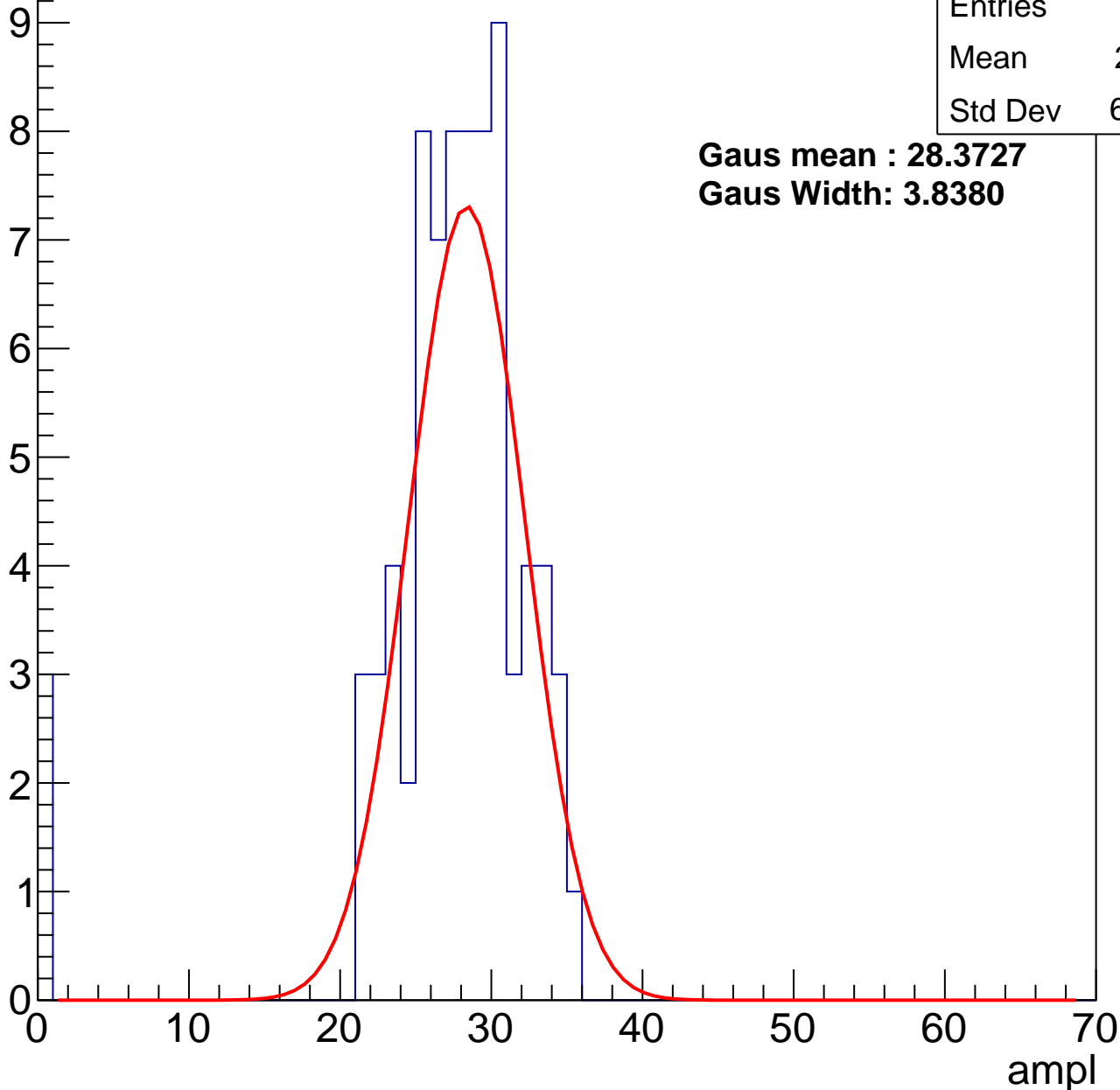
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	26.71
Std Dev	6.313

Gaus mean : 28.3727

Gaus Width: 3.8380



B1L102S, U26-ch72, adc1

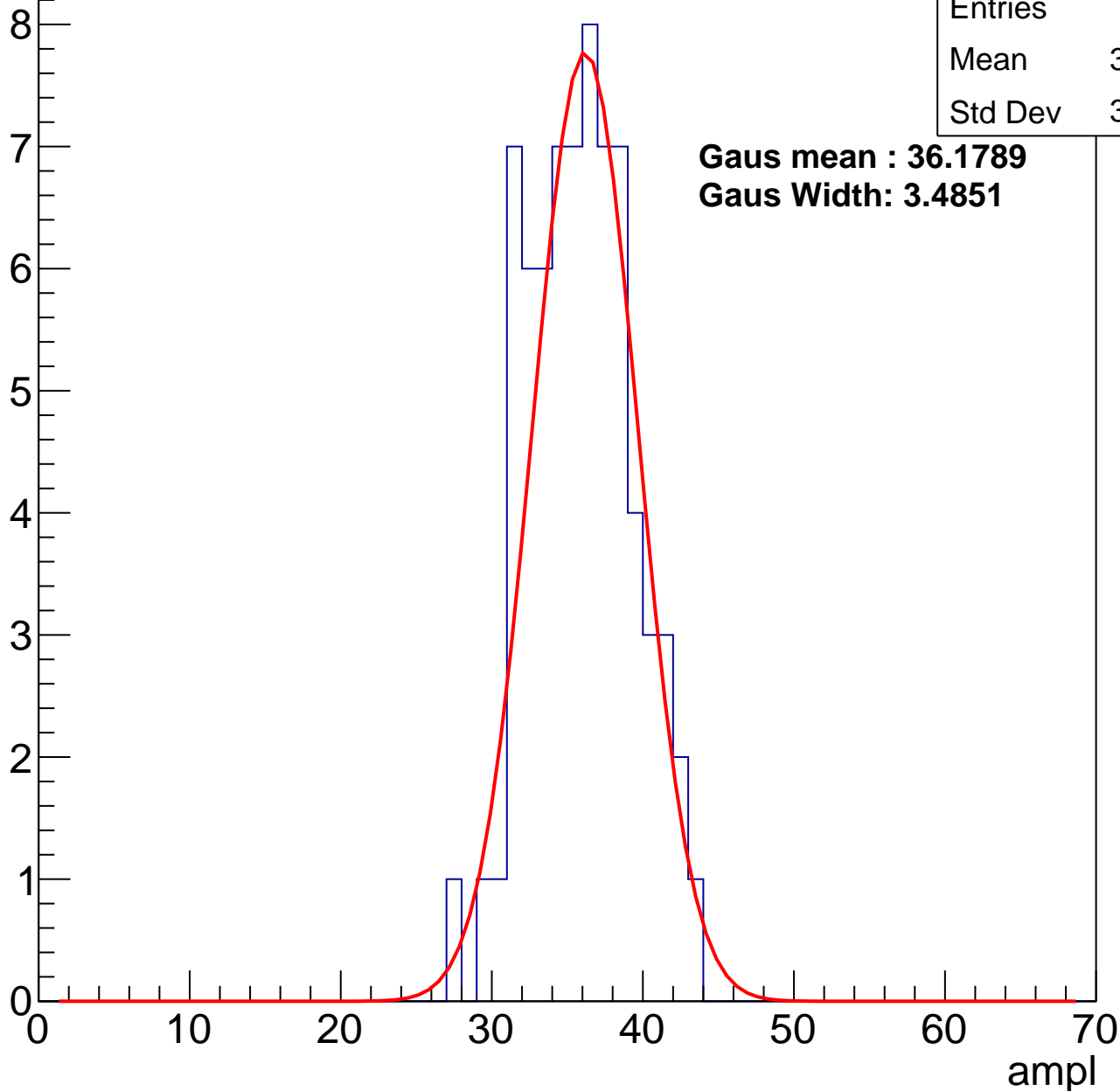
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.42
Std Dev	3.393

Gaus mean : 36.1789

Gaus Width: 3.4851



B1L102S, U26-ch72, adc2

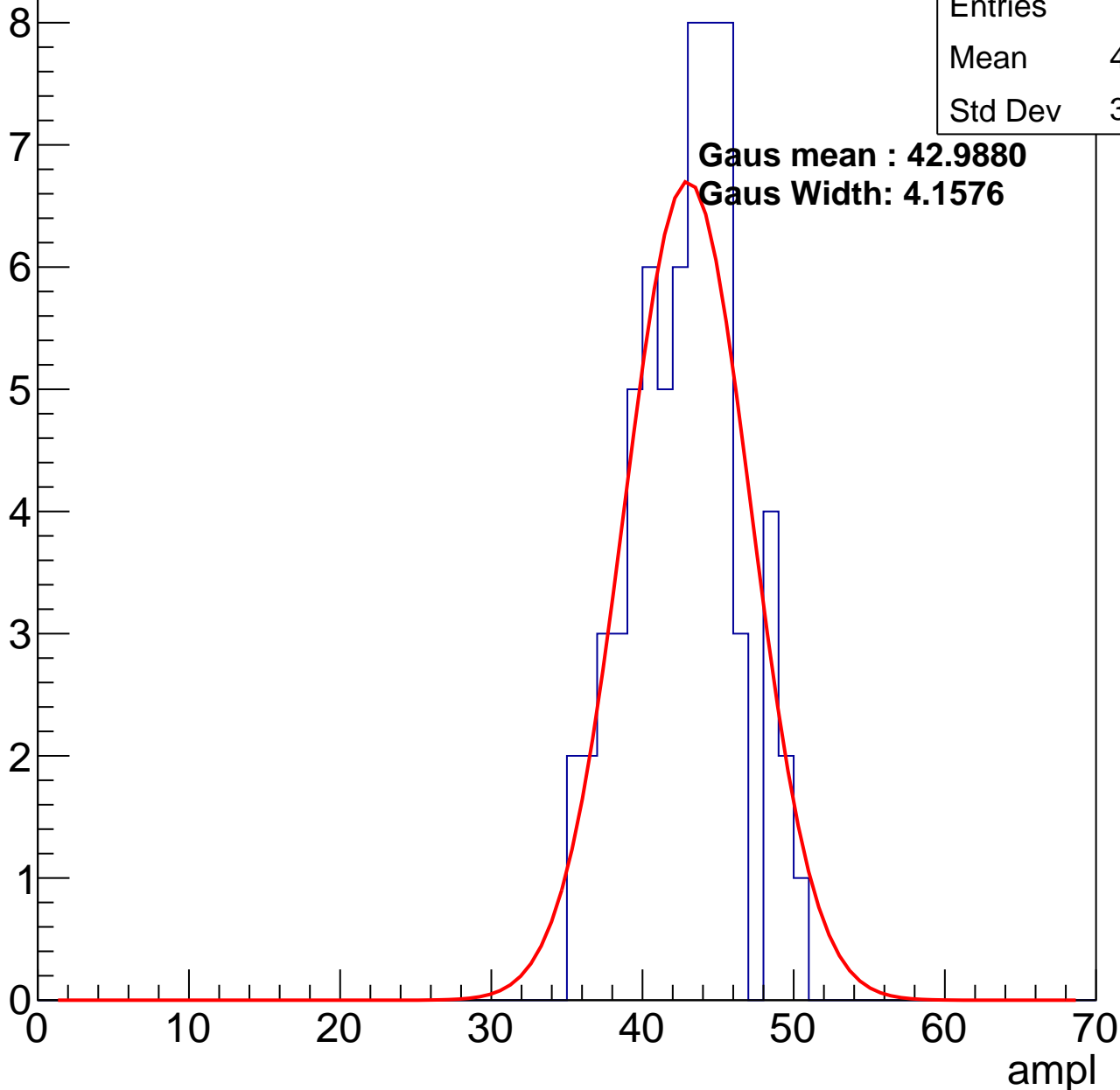
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.32
Std Dev	3.538

Gaus mean : 42.9880

Gaus Width: 4.1576



B1L102S, U26-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

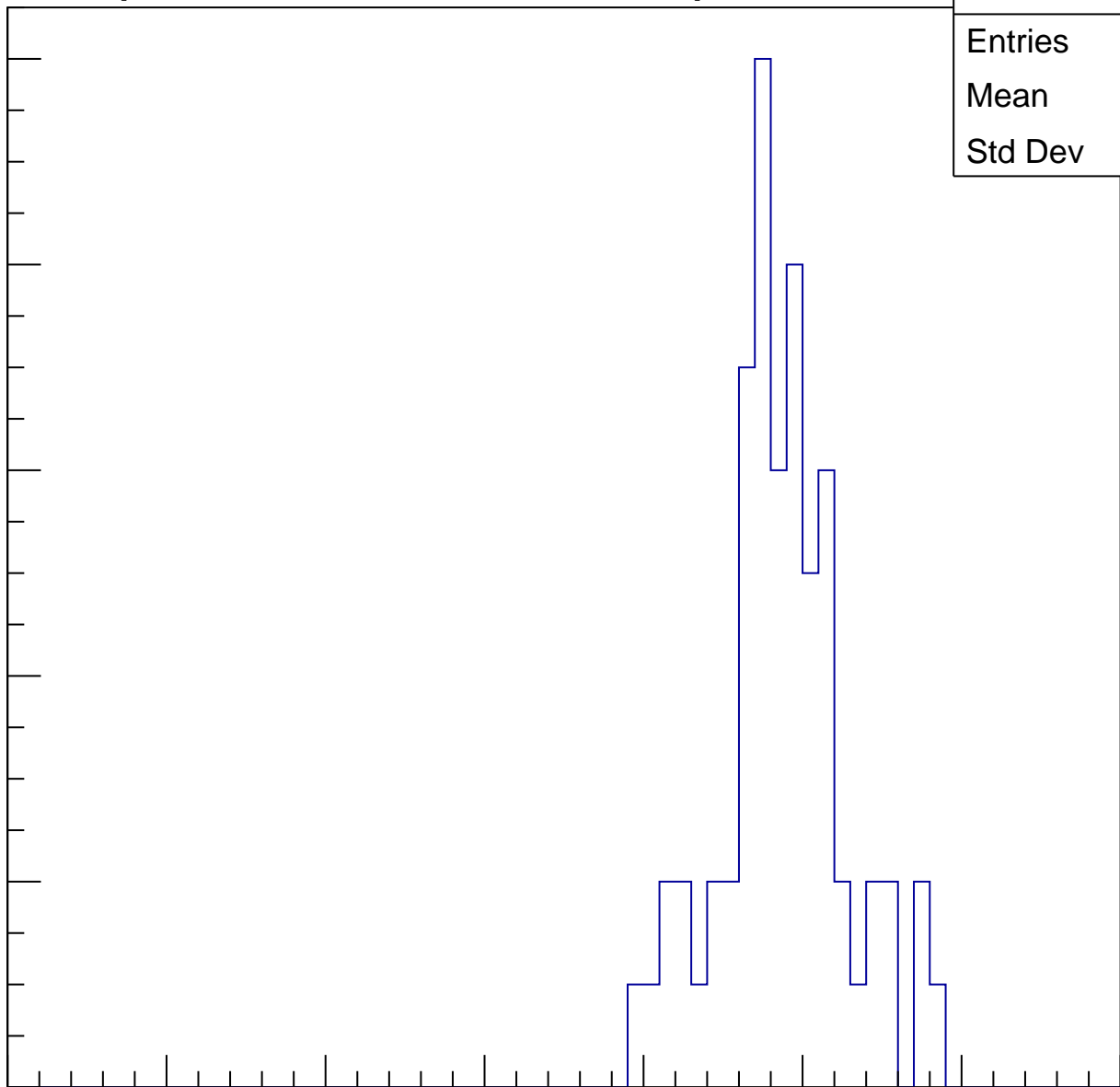
Entries	63
Mean	48.27
Std Dev	3.973

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

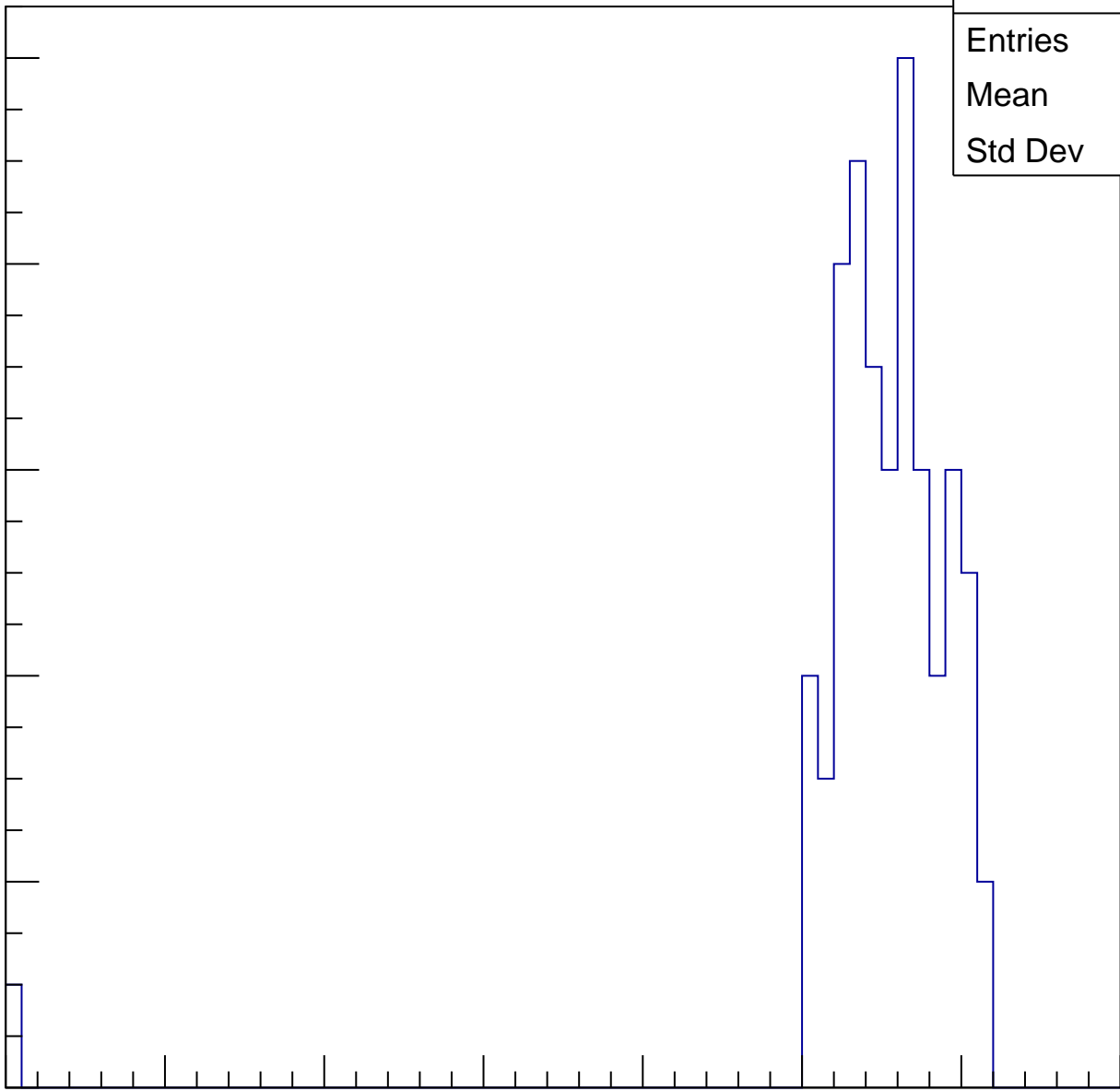
Entries	71
Mean	54.42
Std Dev	7.148

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

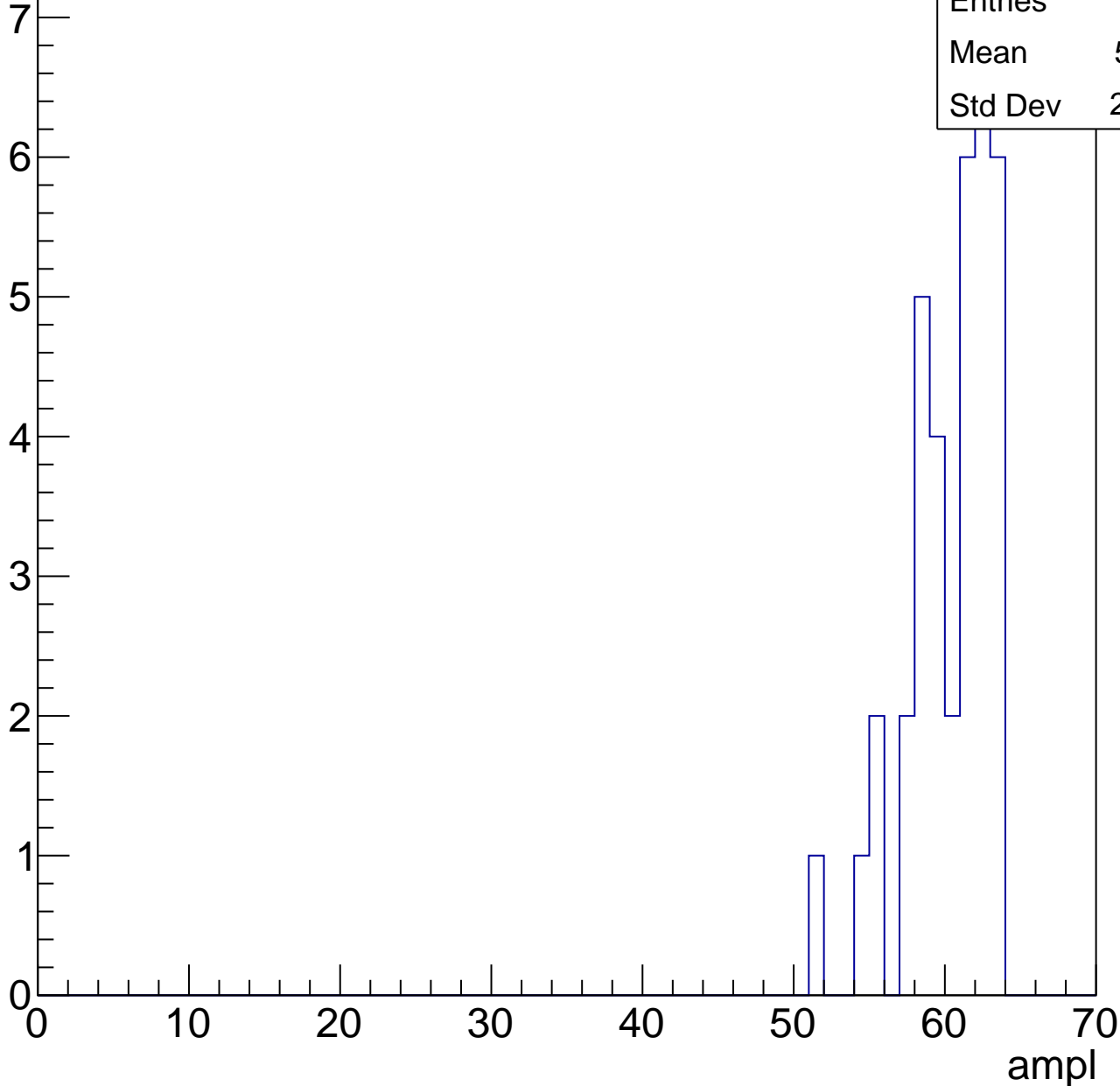


B1L102S, U26-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

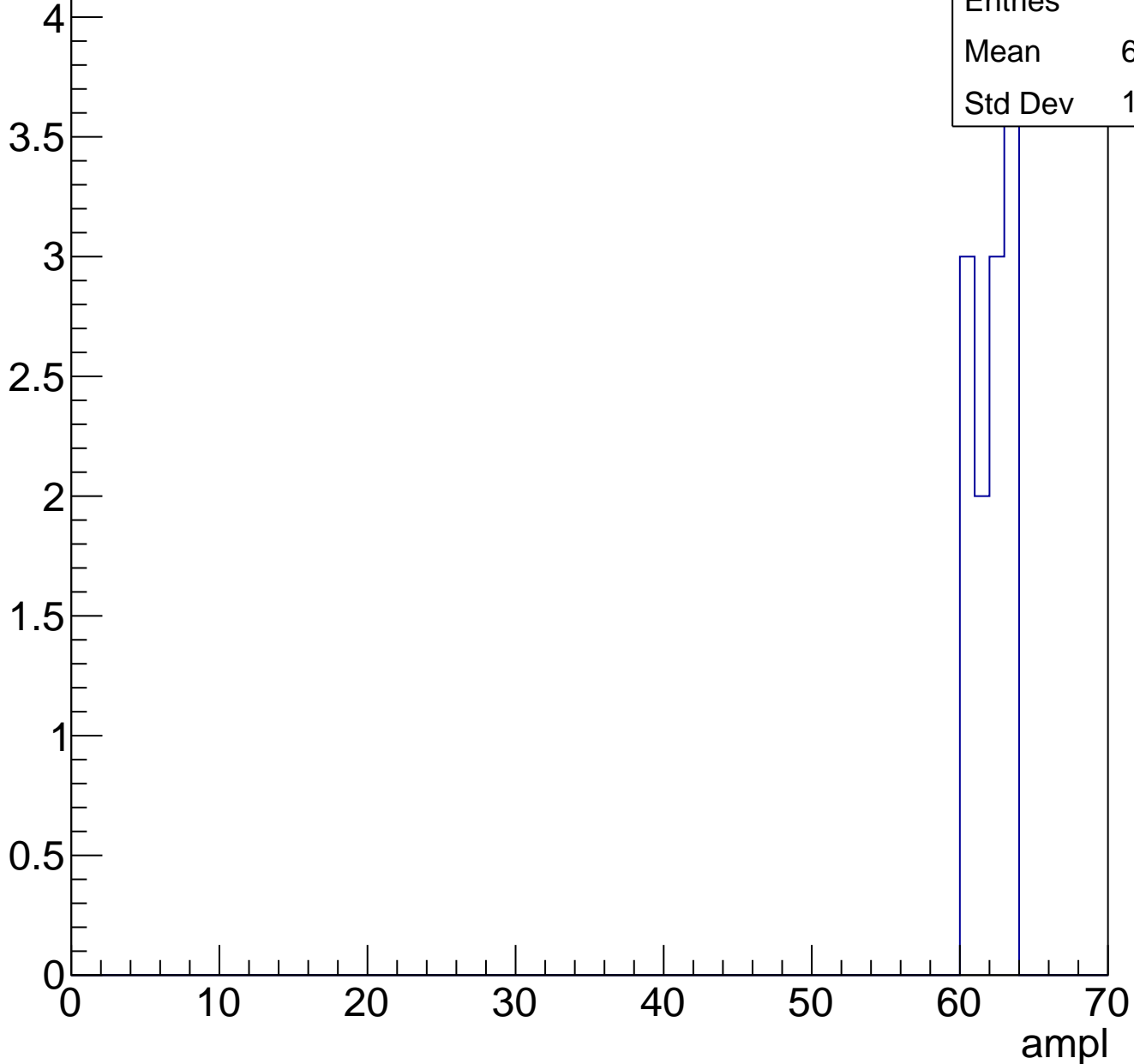
Entries	36
Mean	59.81
Std Dev	2.866



B1L102S, U26-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch73, adc0

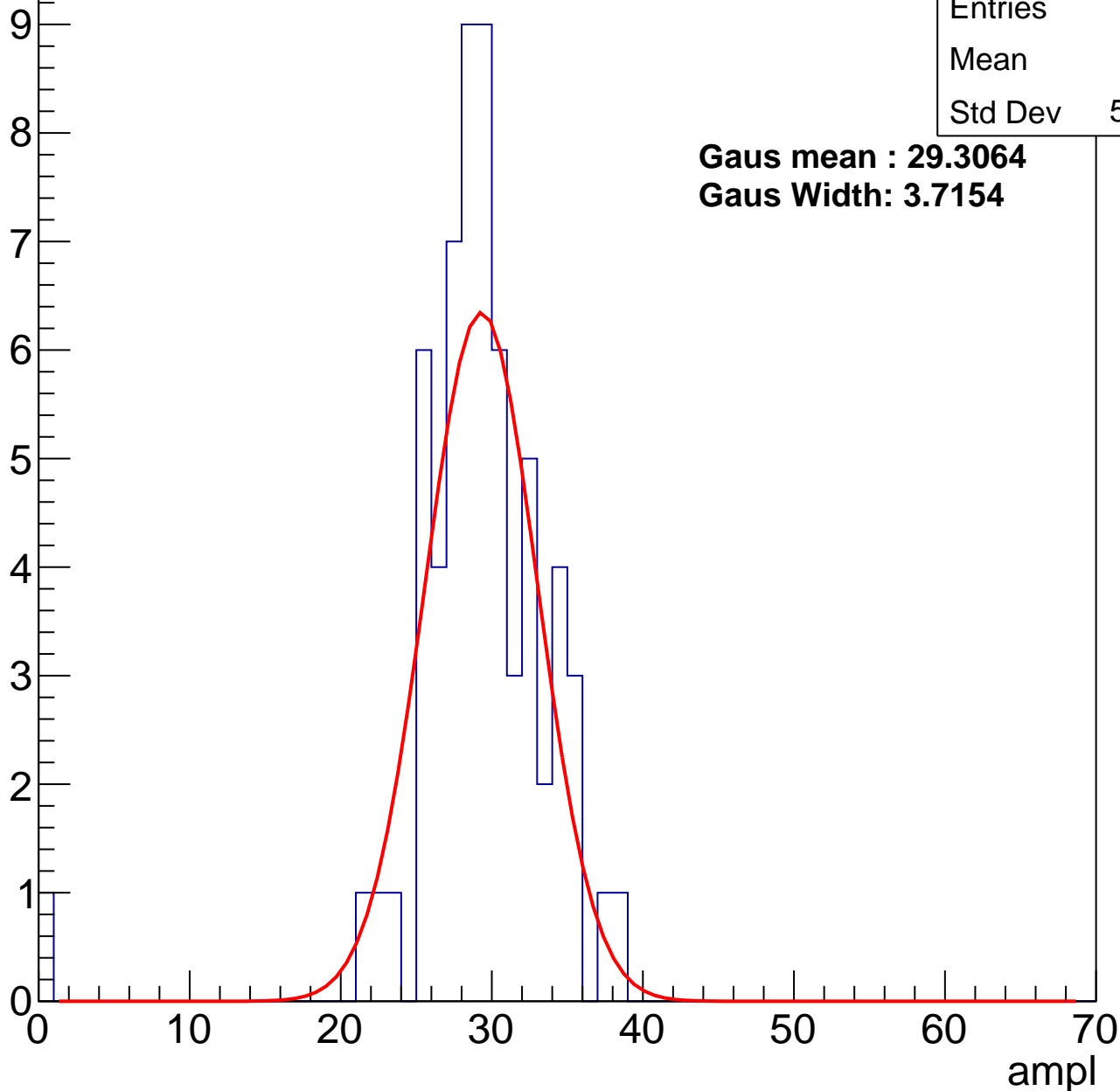
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.7
Std Dev	5.002

Gaus mean : 29.3064

Gaus Width: 3.7154



B1L102S, U26-ch73, adc1

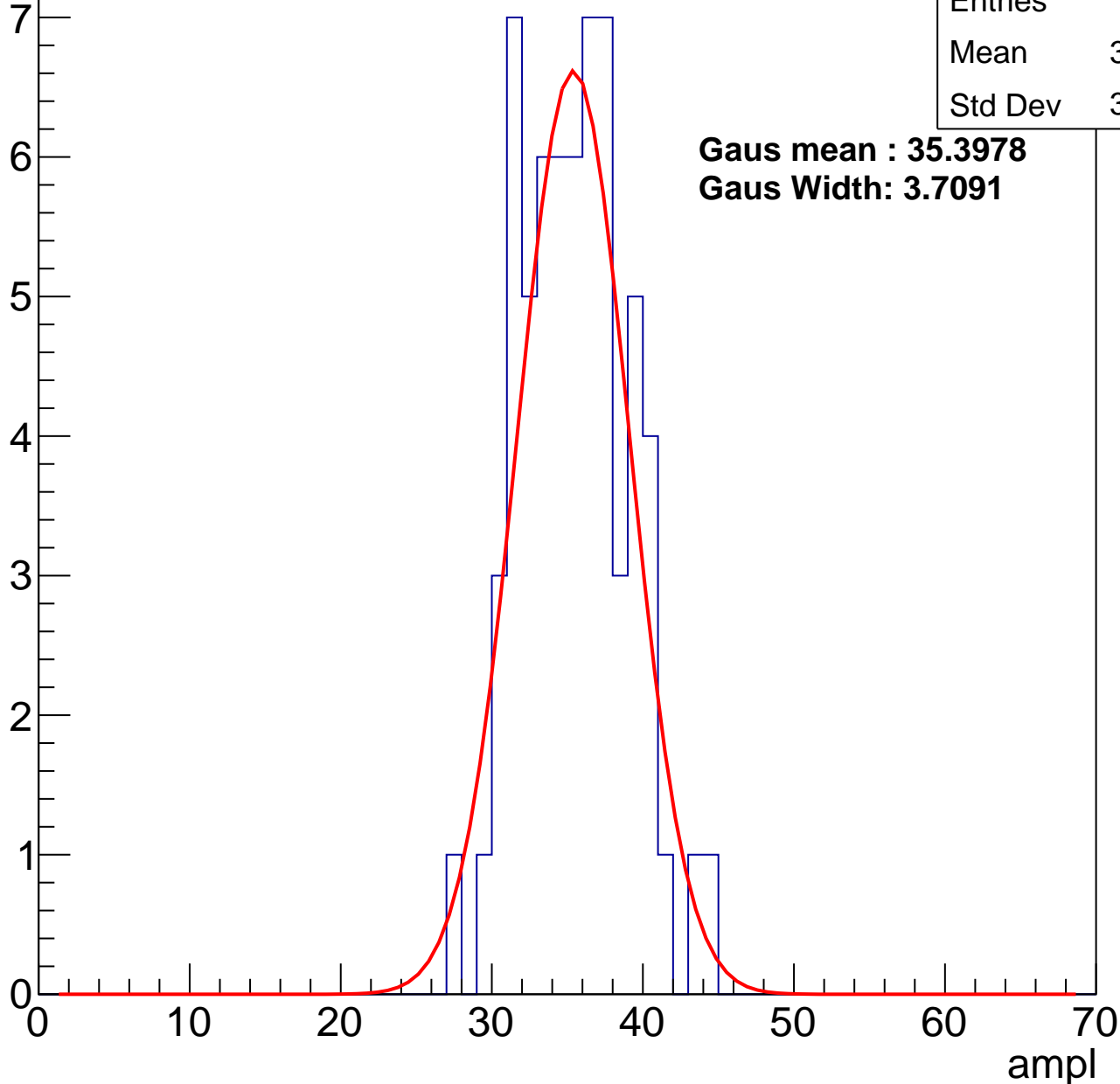
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.05
Std Dev	3.502

Gaus mean : 35.3978

Gaus Width: 3.7091



B1L102S, U26-ch73, adc2

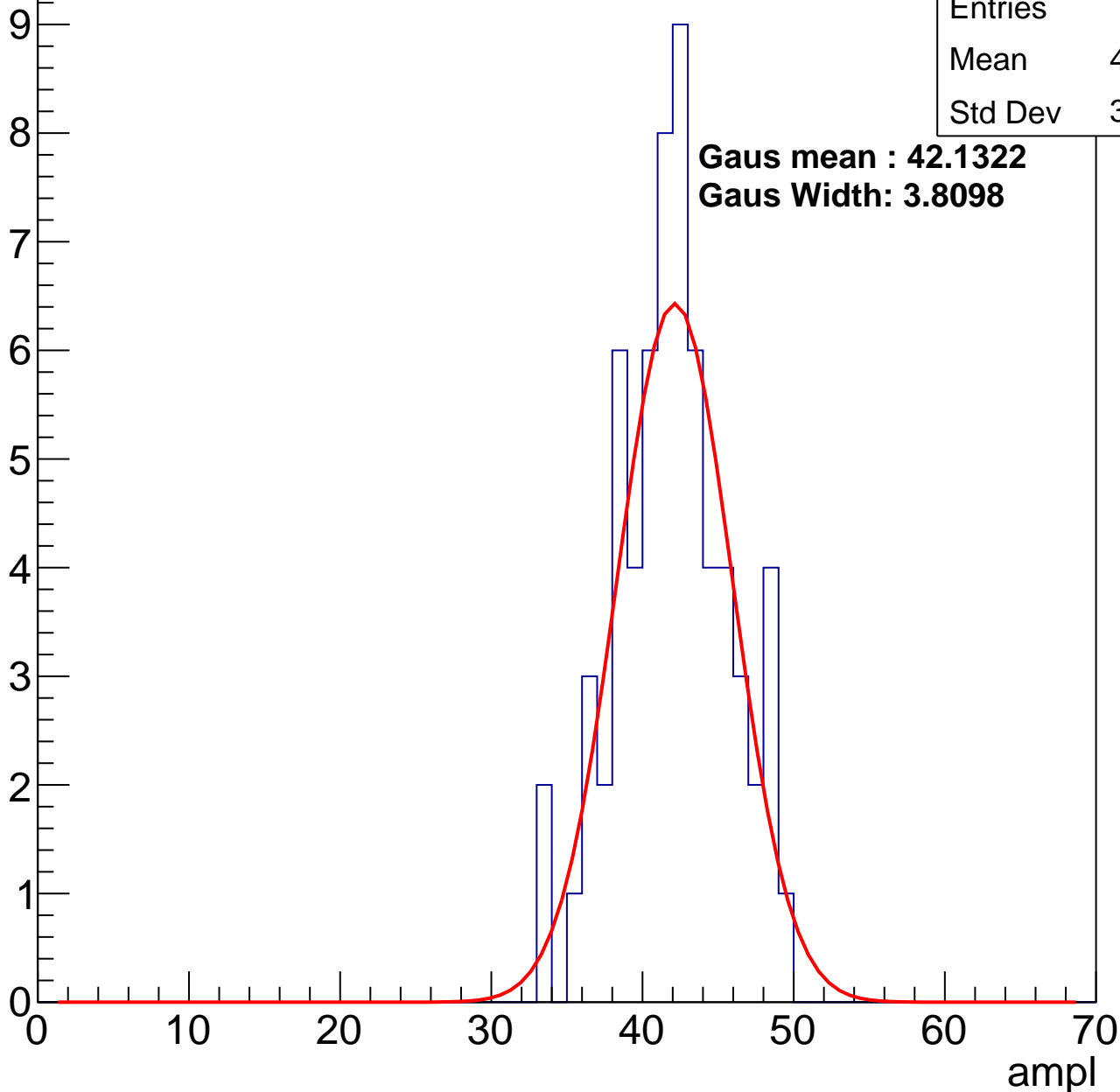
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.54
Std Dev	3.655

Gaus mean : 42.1322

Gaus Width: 3.8098

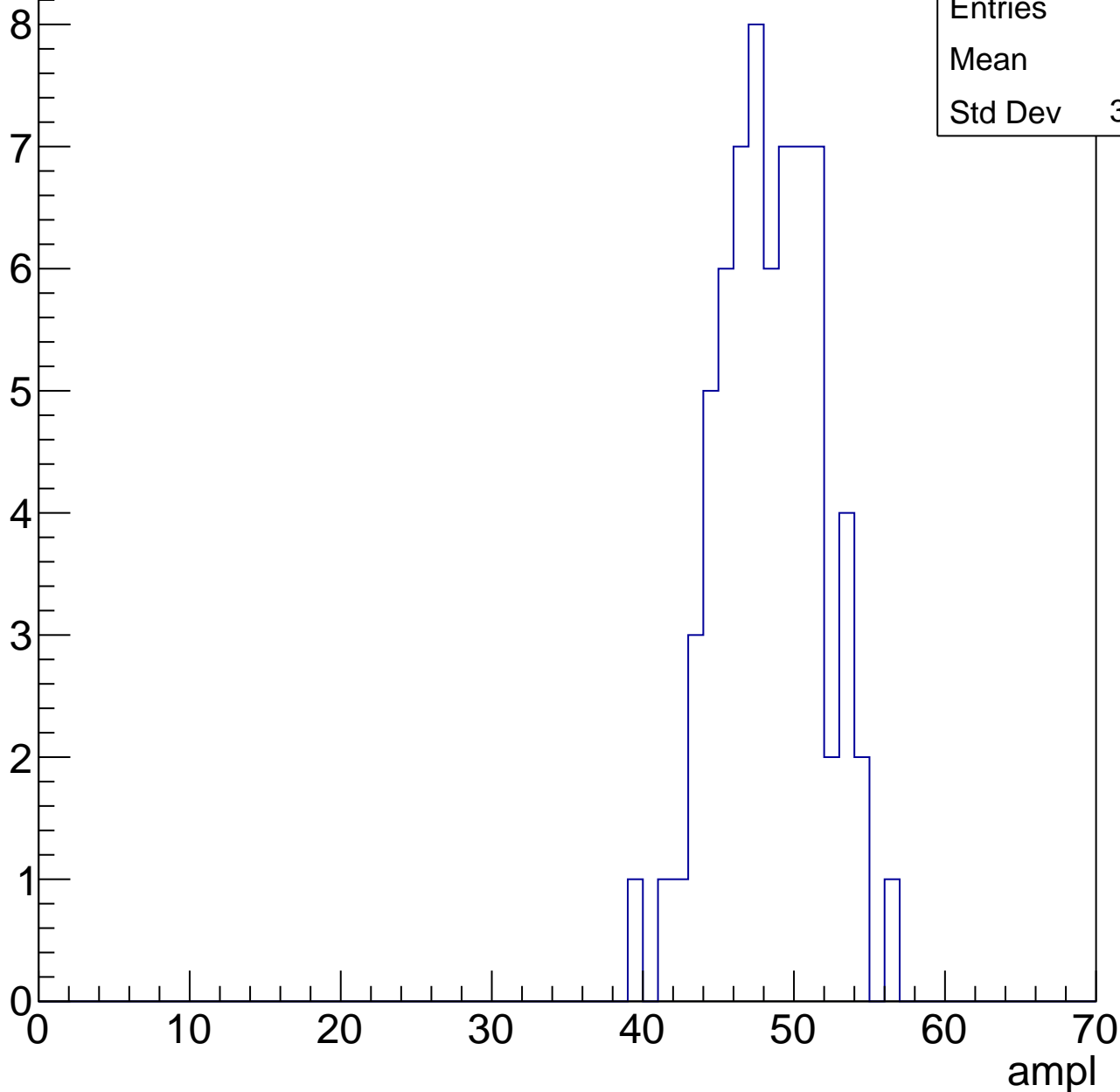


B1L102S, U26-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

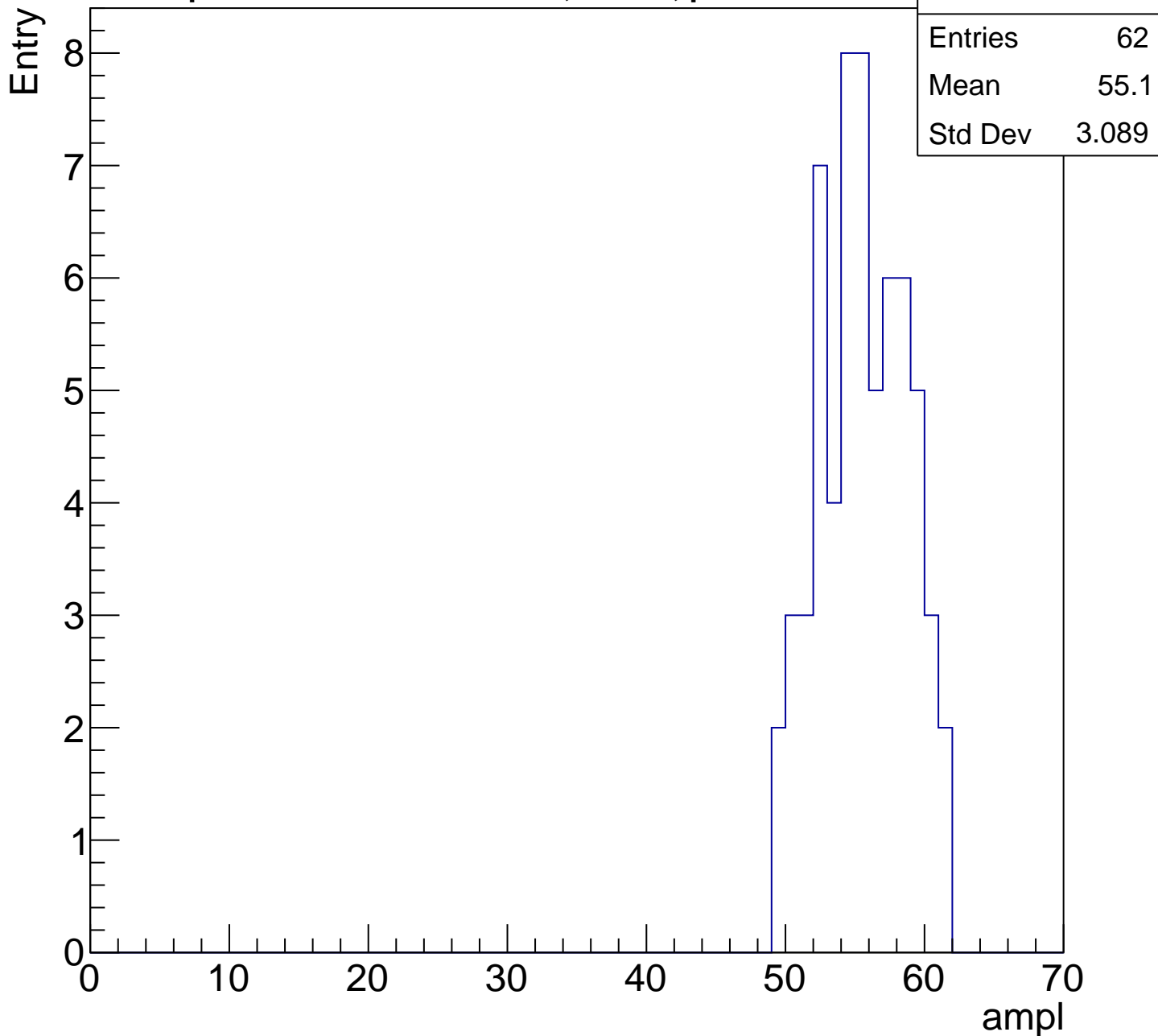
Entry

Entries	68
Mean	47.9
Std Dev	3.379



B1L102S, U26-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

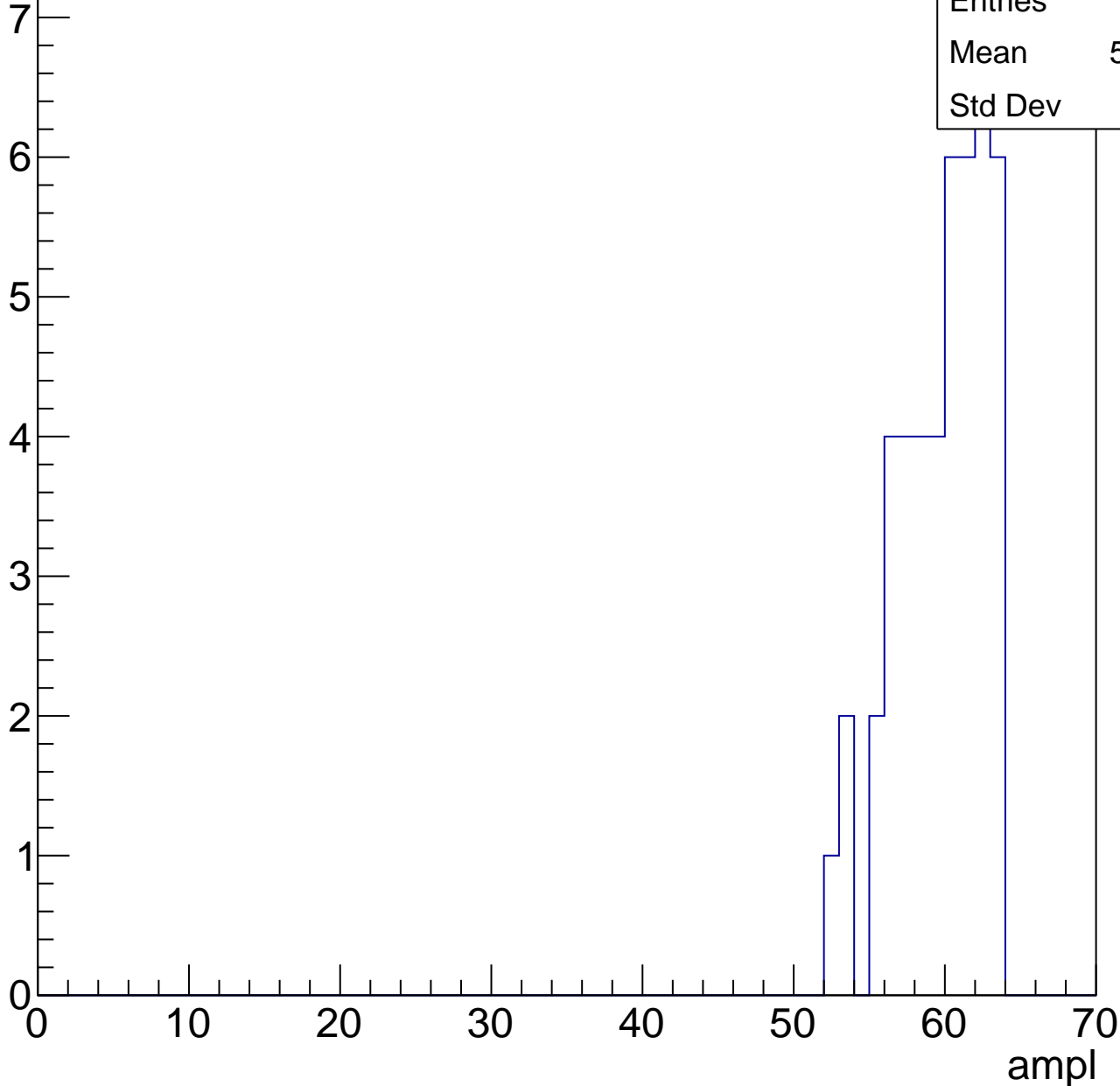


B1L102S, U26-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

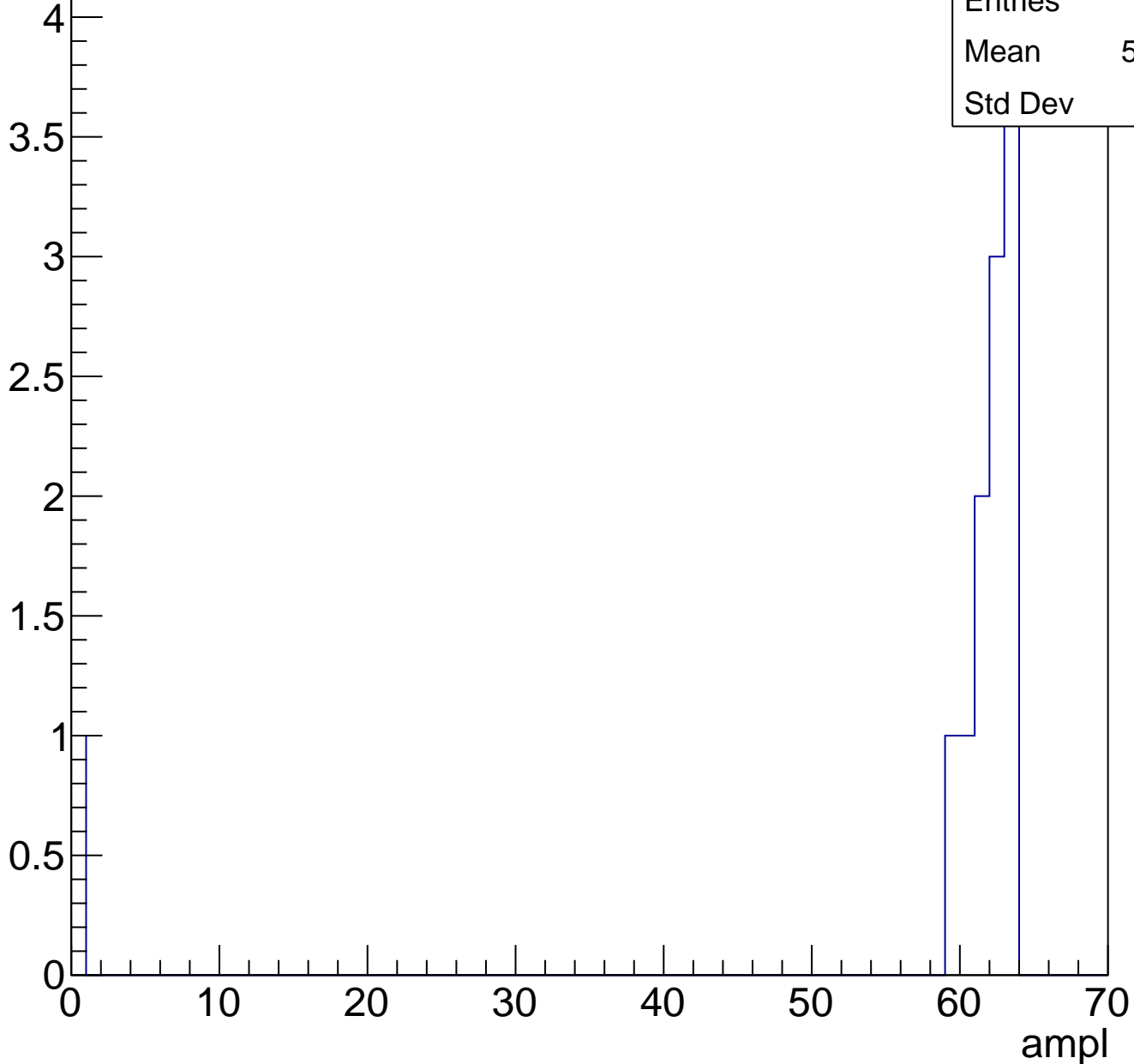
Entries	46
Mean	59.26
Std Dev	2.93



B1L102S, U26-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch74, adc0

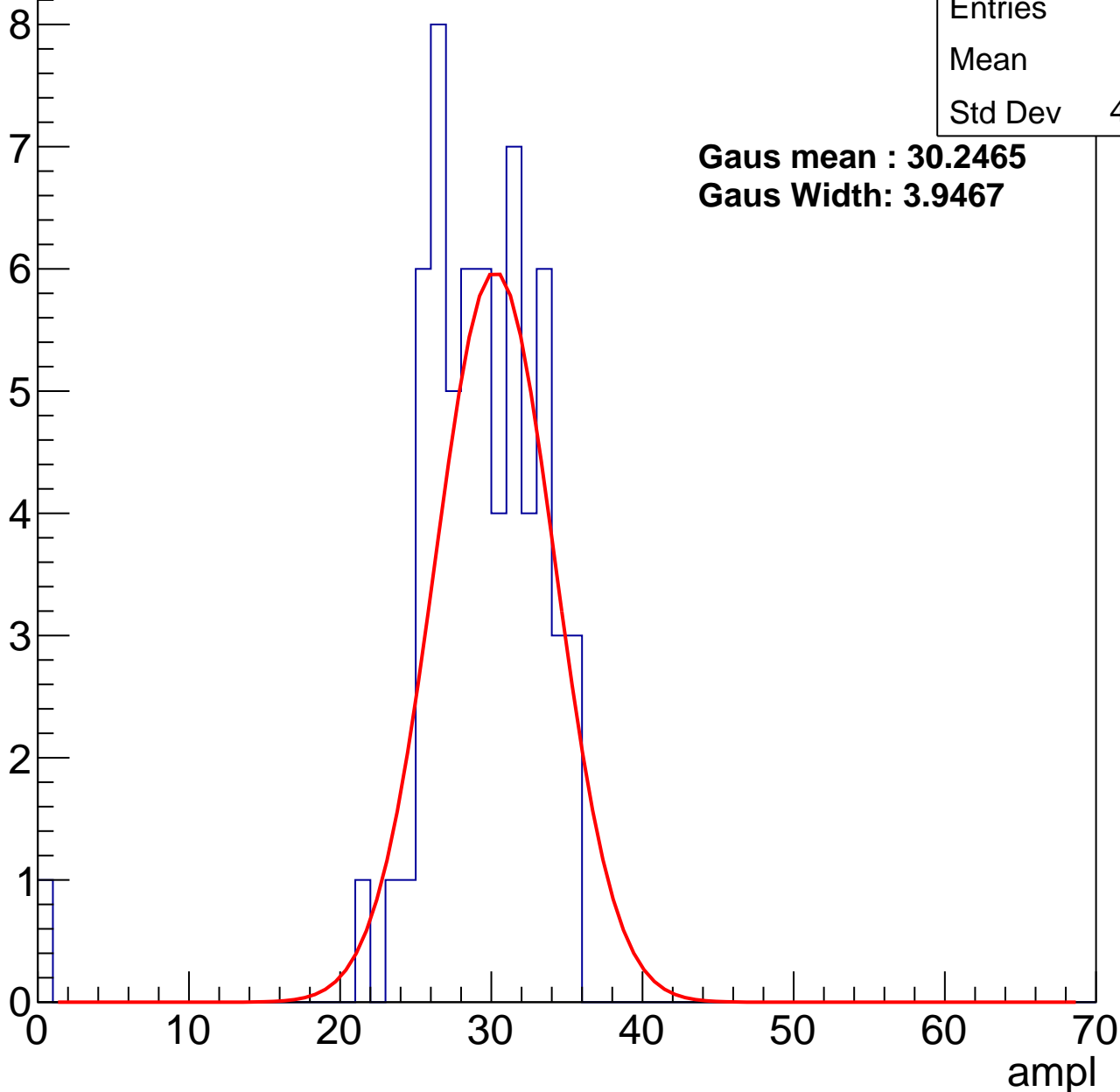
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.6
Std Dev	4.914

Gaus mean : 30.2465

Gaus Width: 3.9467



B1L102S, U26-ch74, adc1

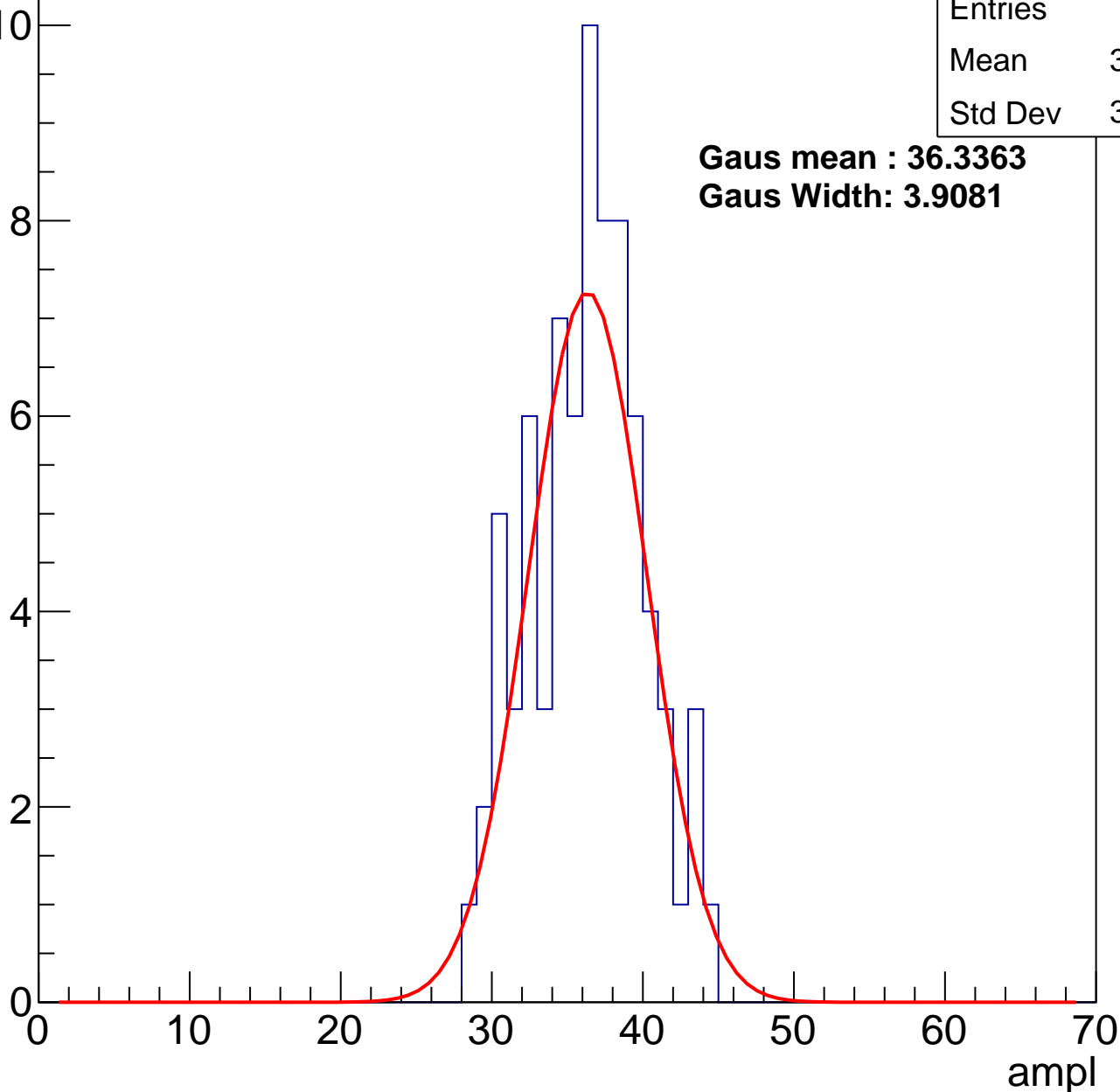
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.84
Std Dev	3.696

Gaus mean : 36.3363

Gaus Width: 3.9081



B1L102S, U26-ch74, adc2

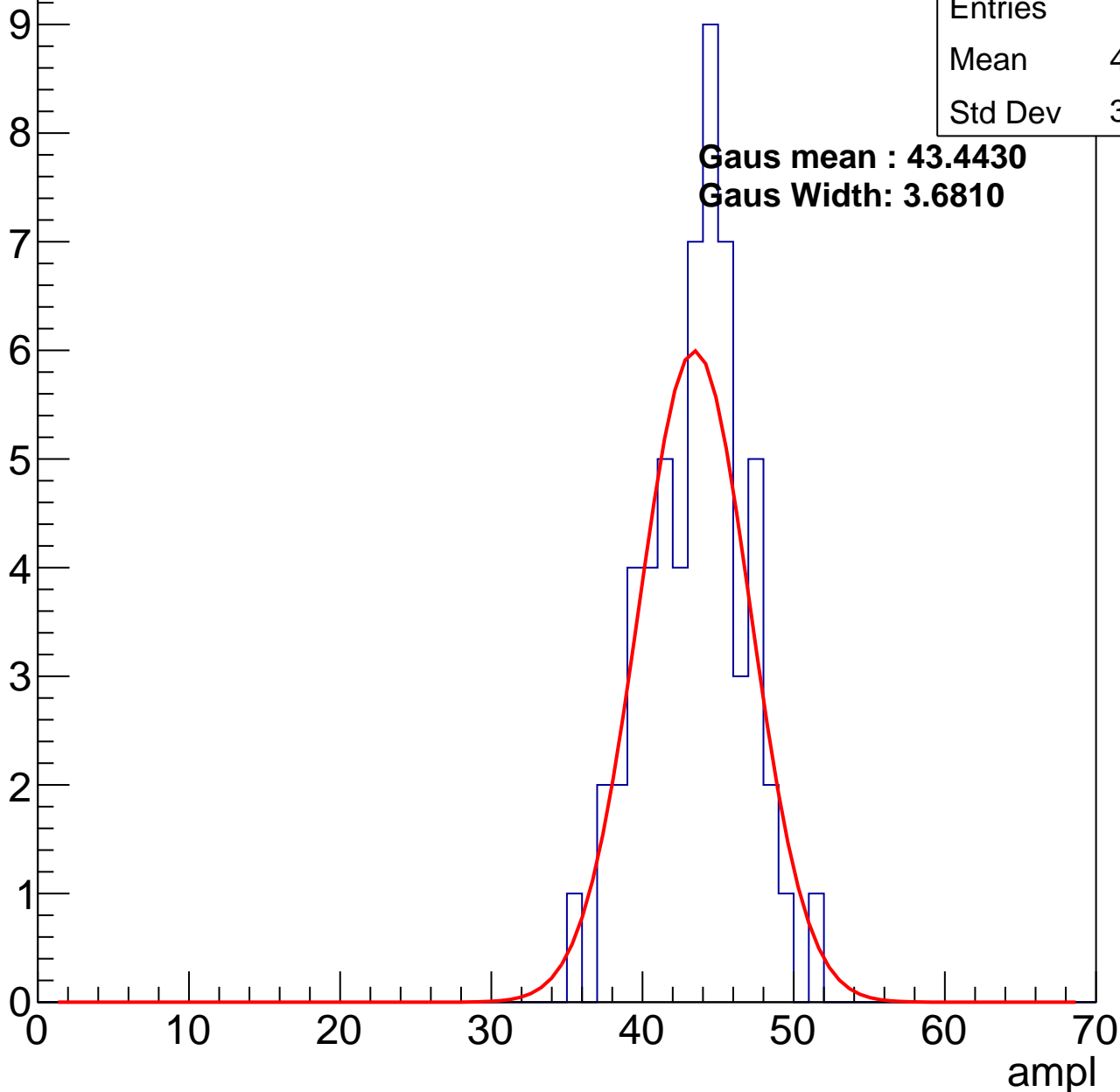
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.07
Std Dev	3.254

Gaus mean : 43.4430

Gaus Width: 3.6810

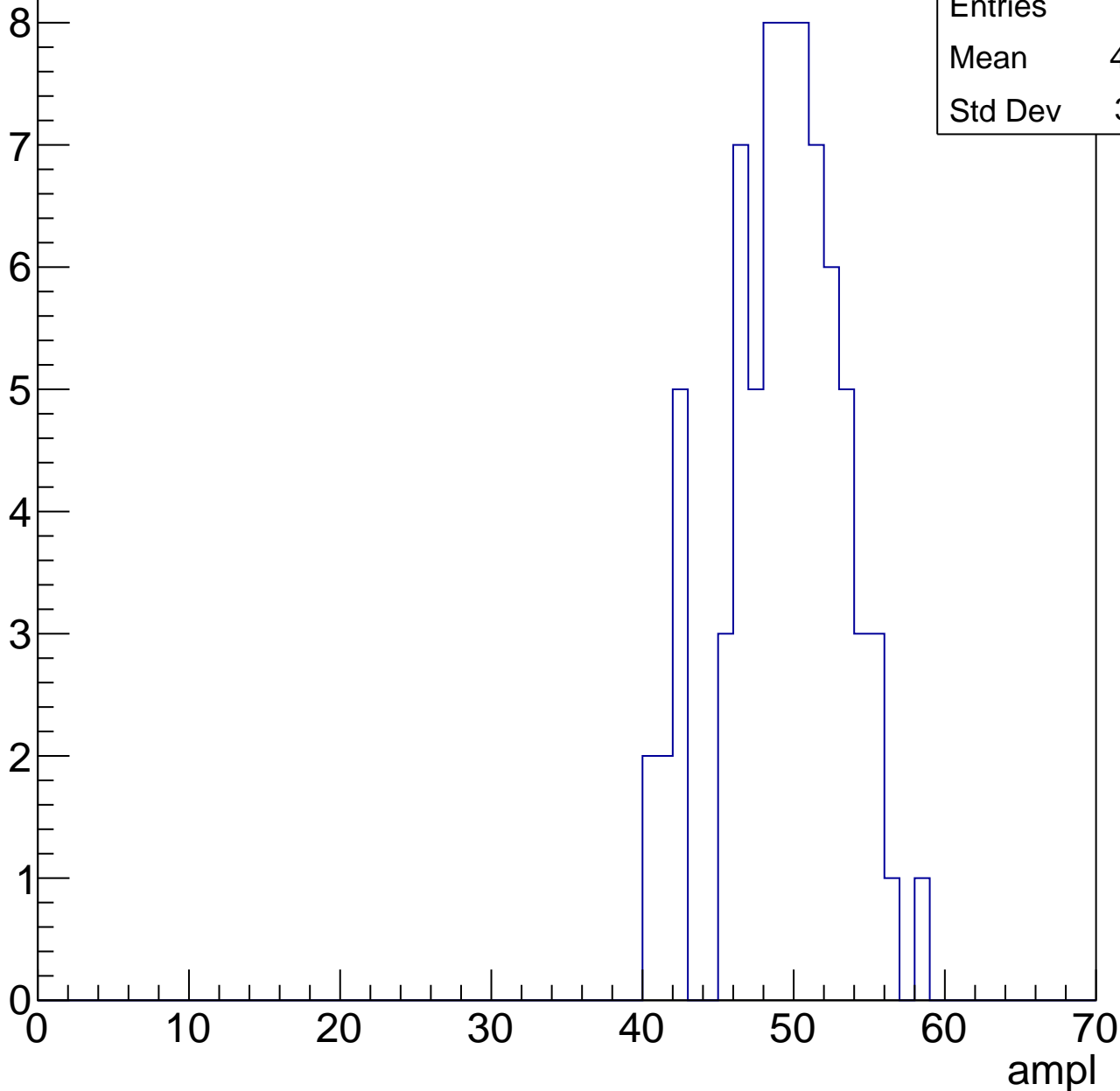


B1L102S, U26-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.85
Std Dev	3.951

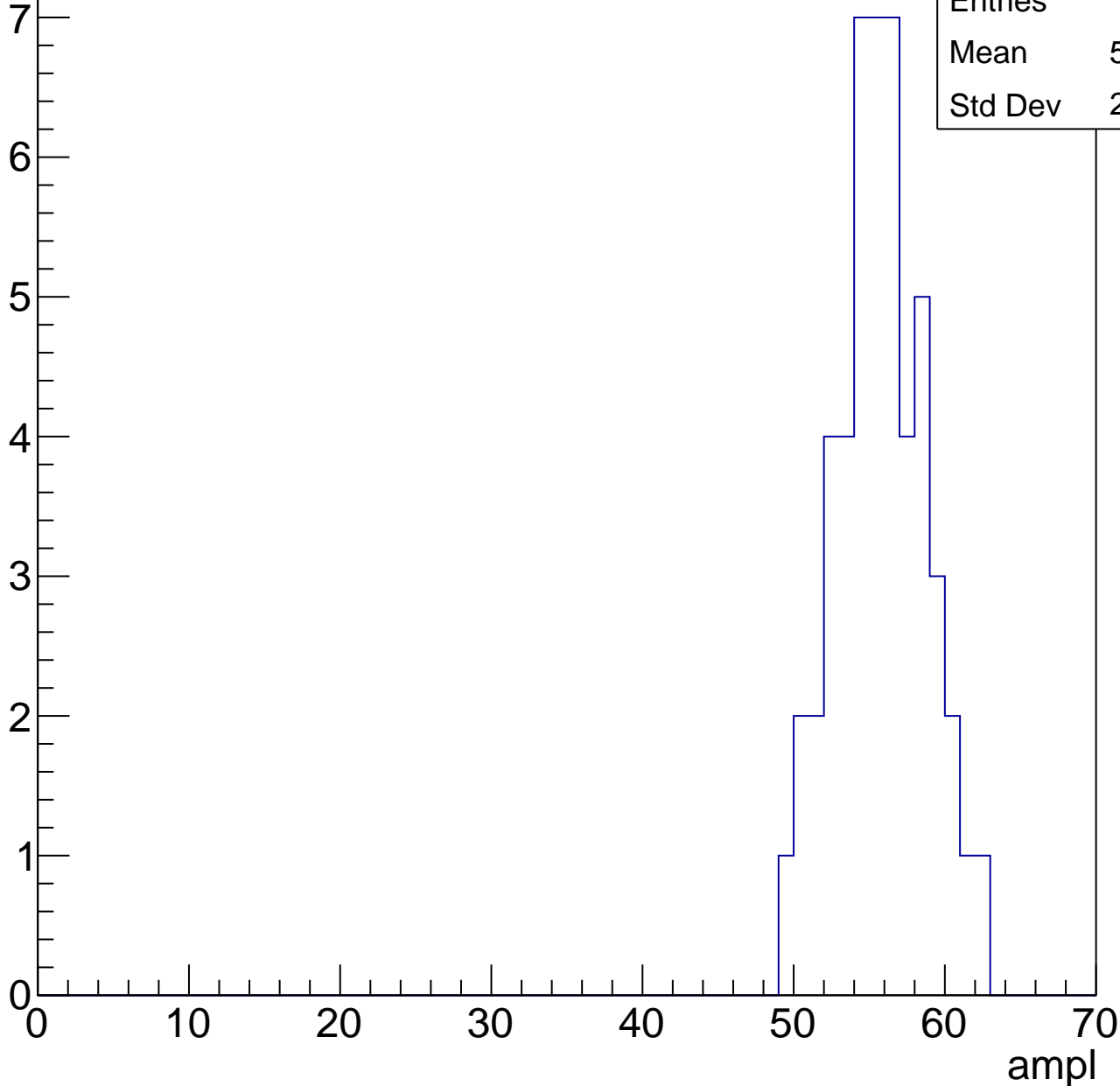


B1L102S, U26-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.28
Std Dev	2.912

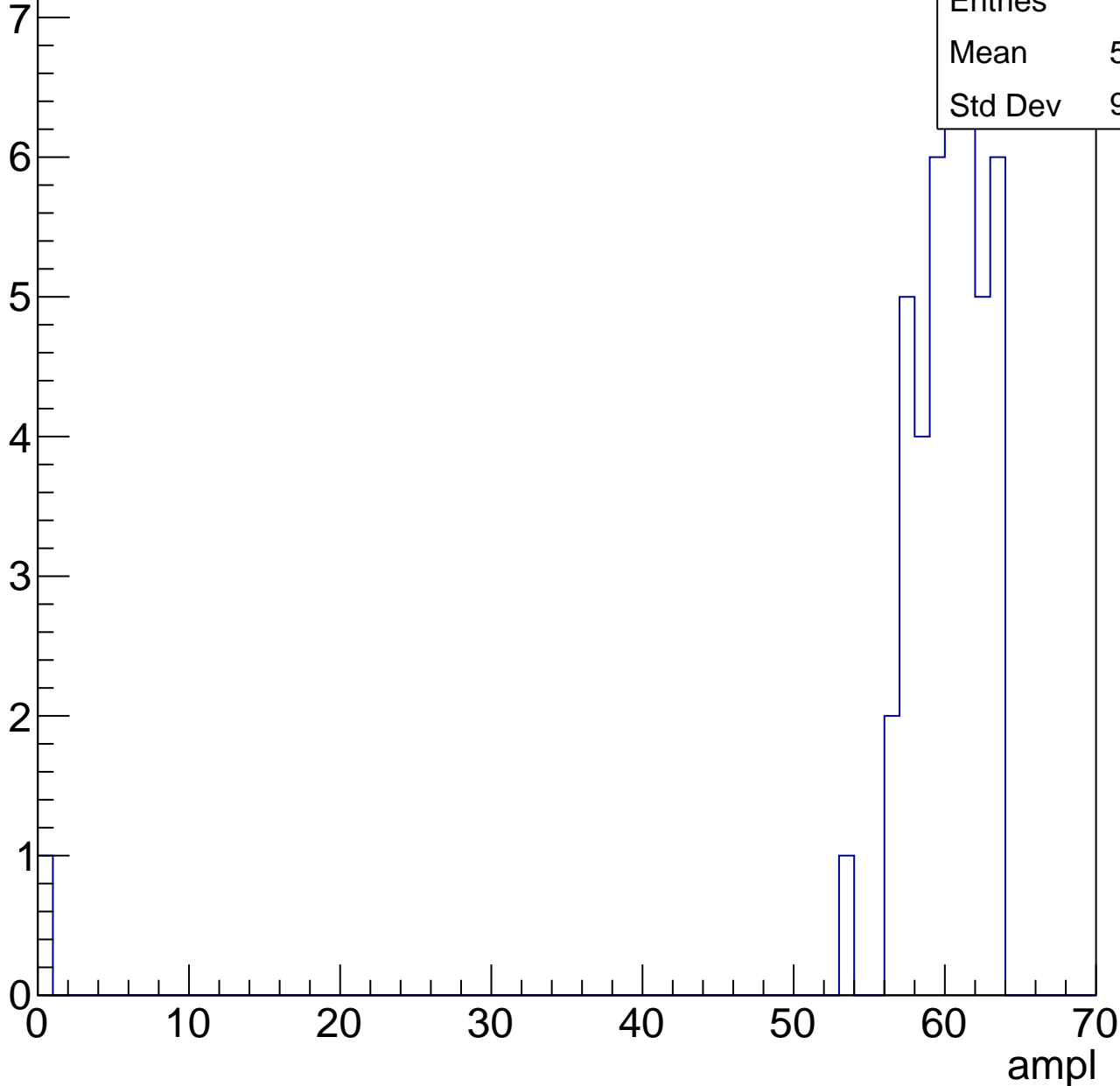


B1L102S, U26-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

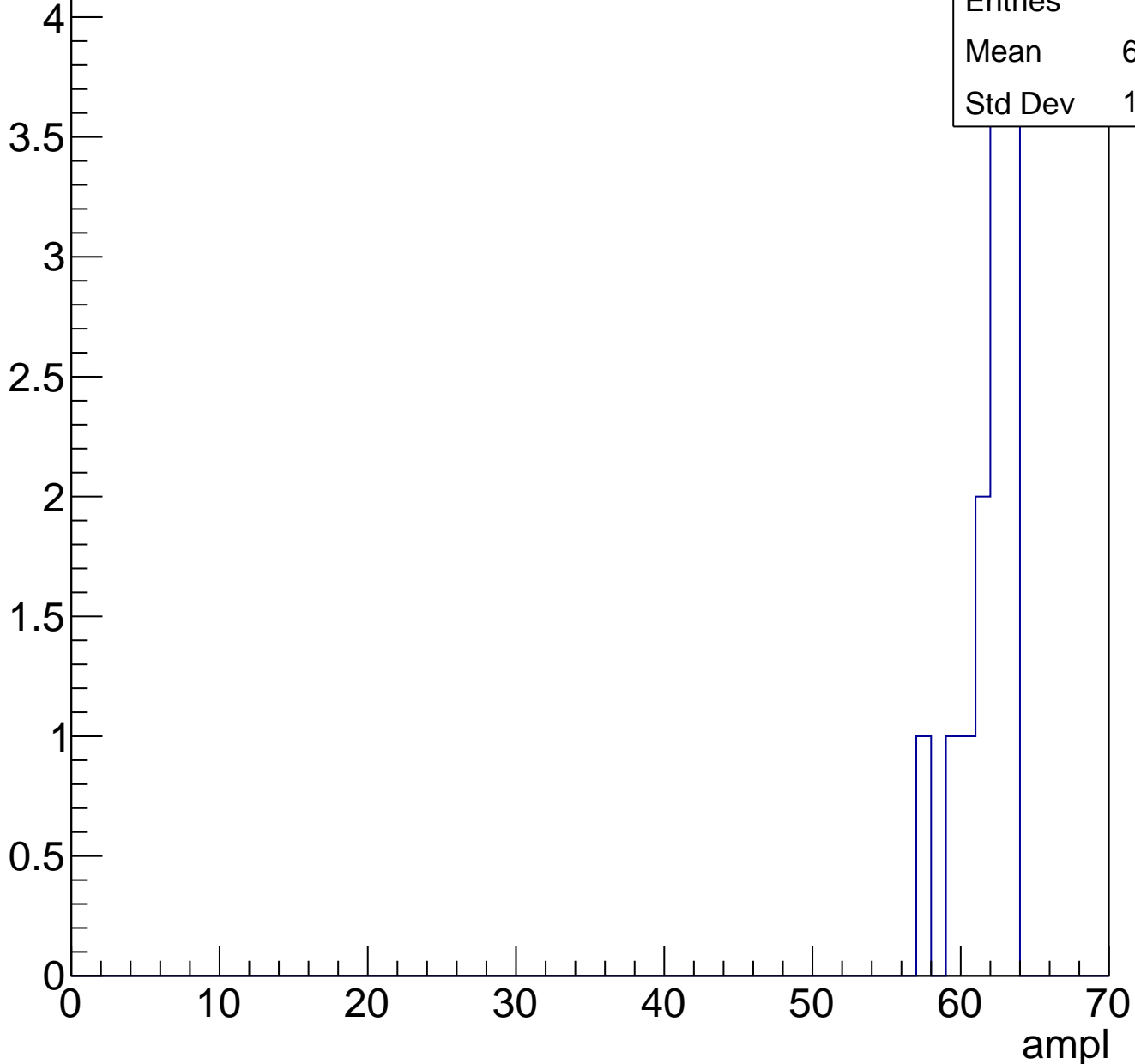
Entries	44
Mean	58.43
Std Dev	9.196



B1L102S, U26-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	13
Mean	61.38
Std Dev	1.734

B1L102S, U26-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch75, adc0

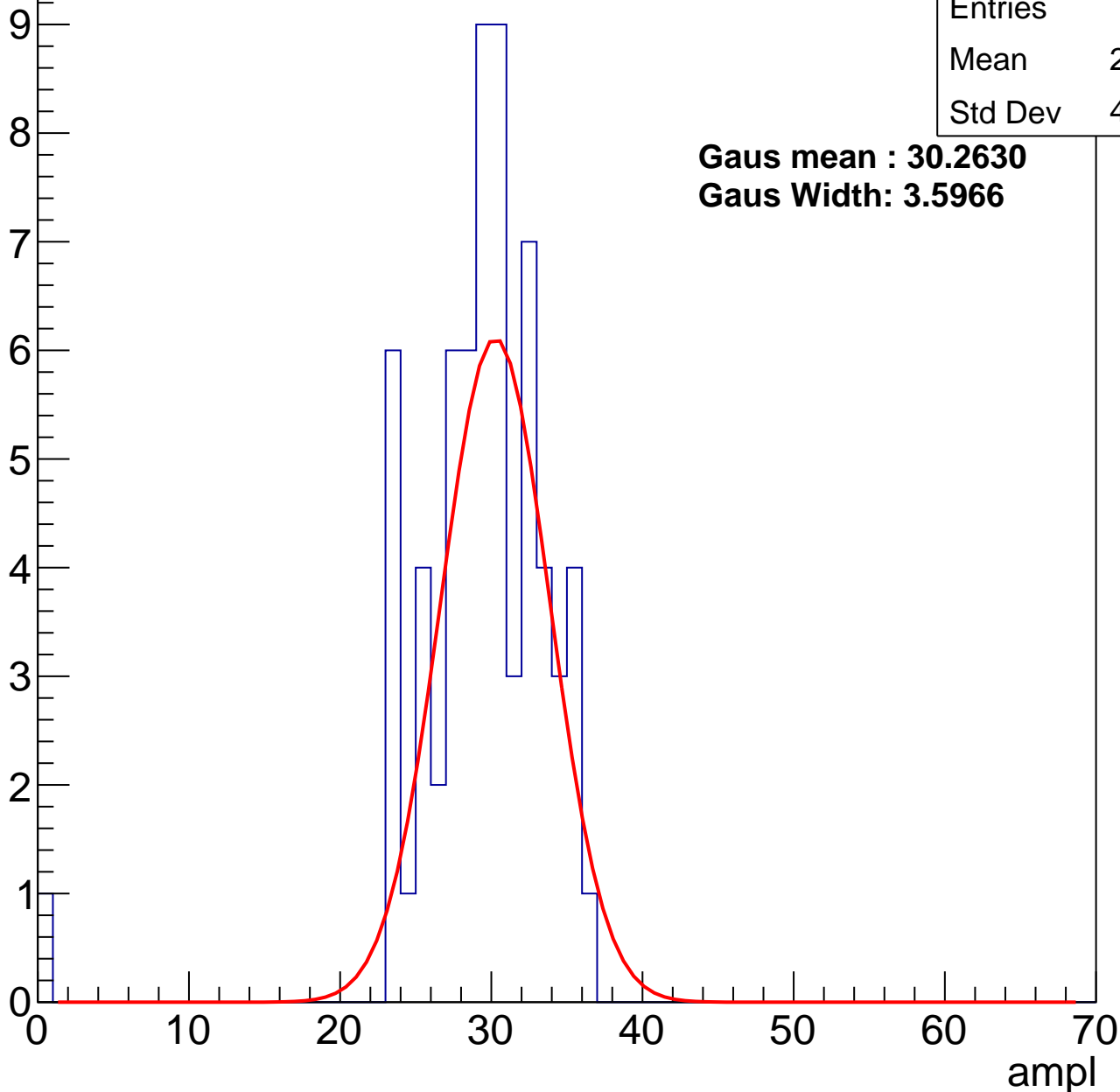
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.82
Std Dev	4.936

Gaus mean : 30.2630

Gaus Width: 3.5966



B1L102S, U26-ch75, adc1

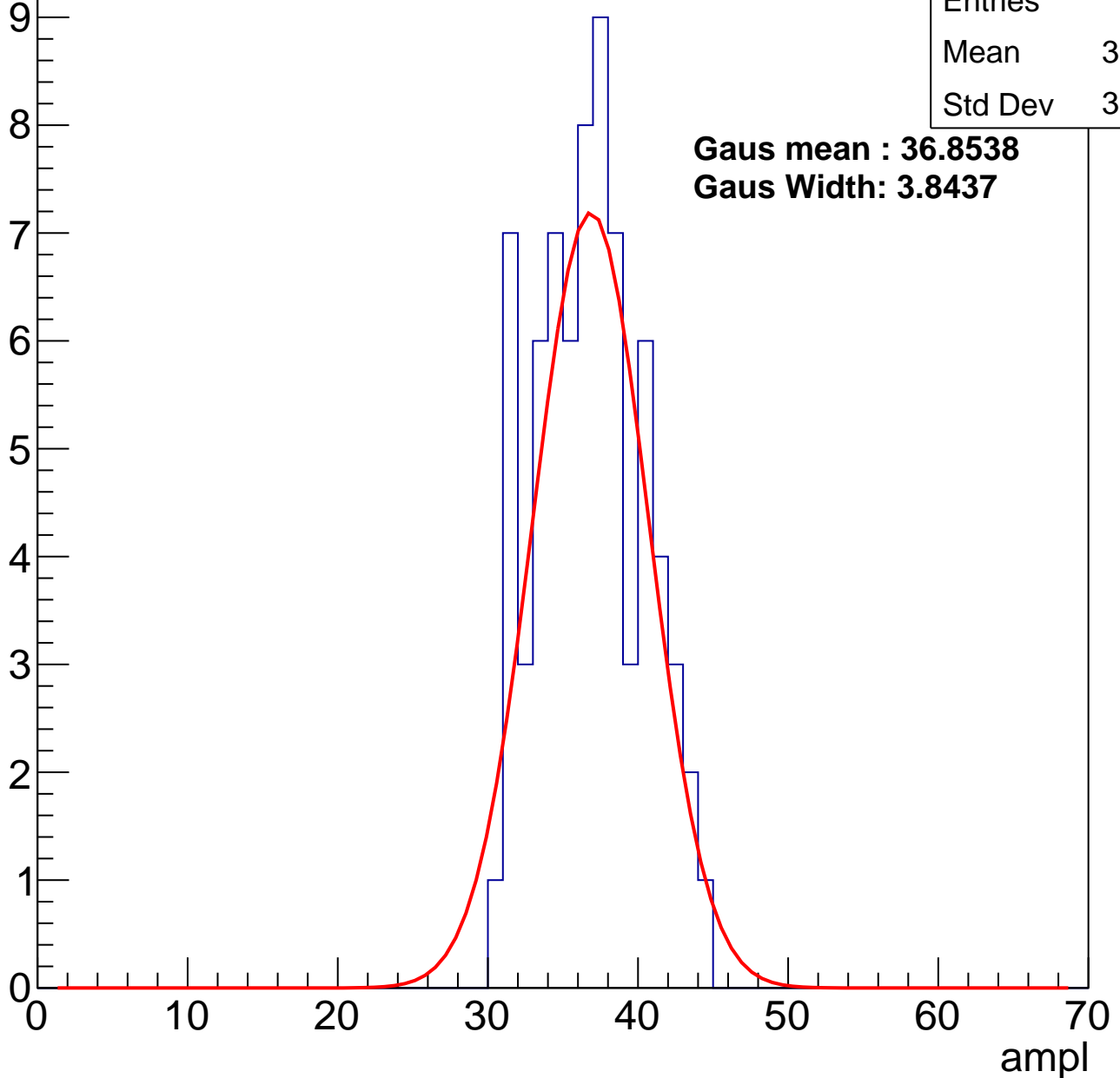
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.34
Std Dev	3.449

Gaus mean : 36.8538

Gaus Width: 3.8437



B1L102S, U26-ch75, adc2

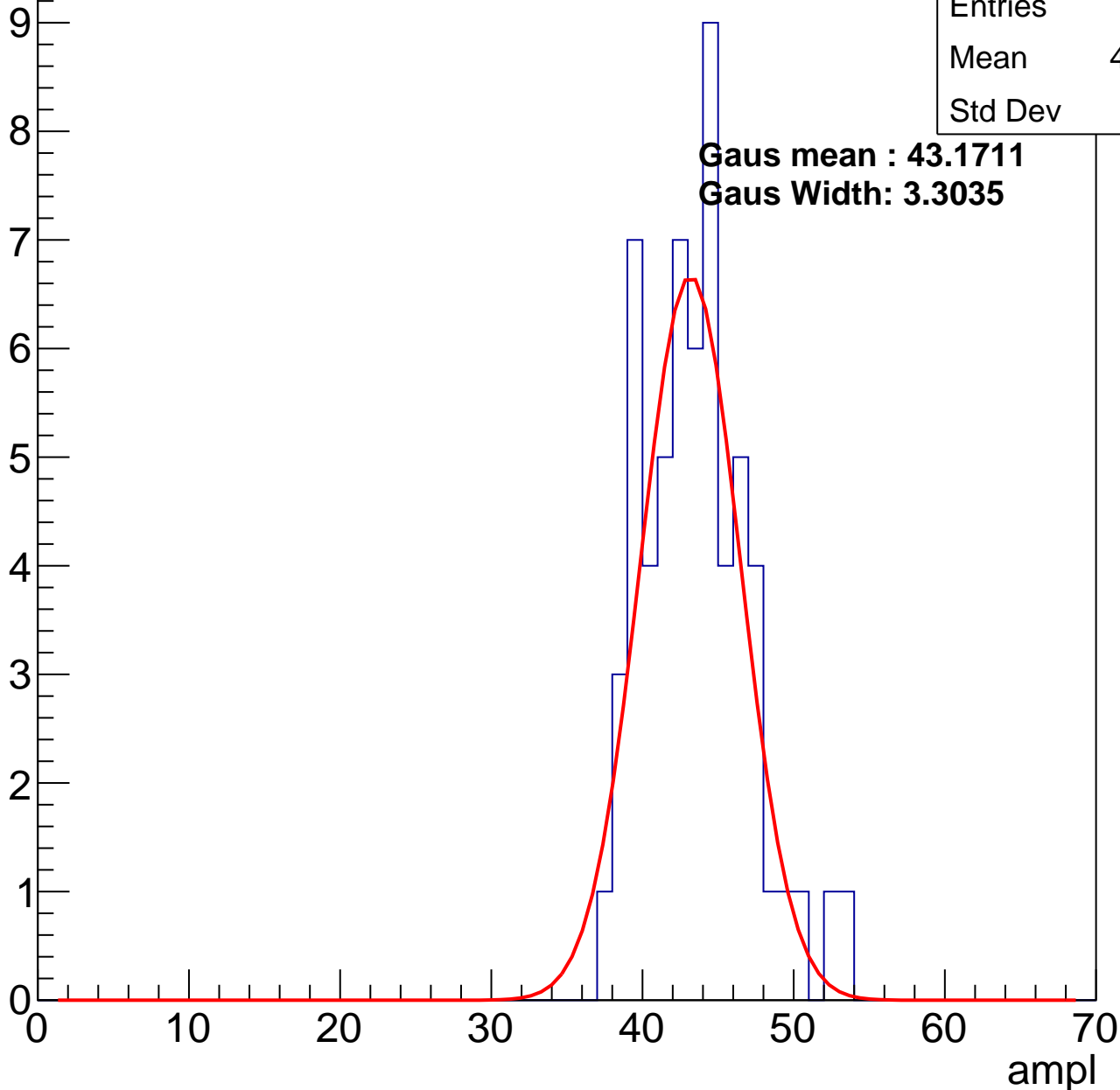
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.12
Std Dev	3.44

Gaus mean : 43.1711

Gaus Width: 3.3035

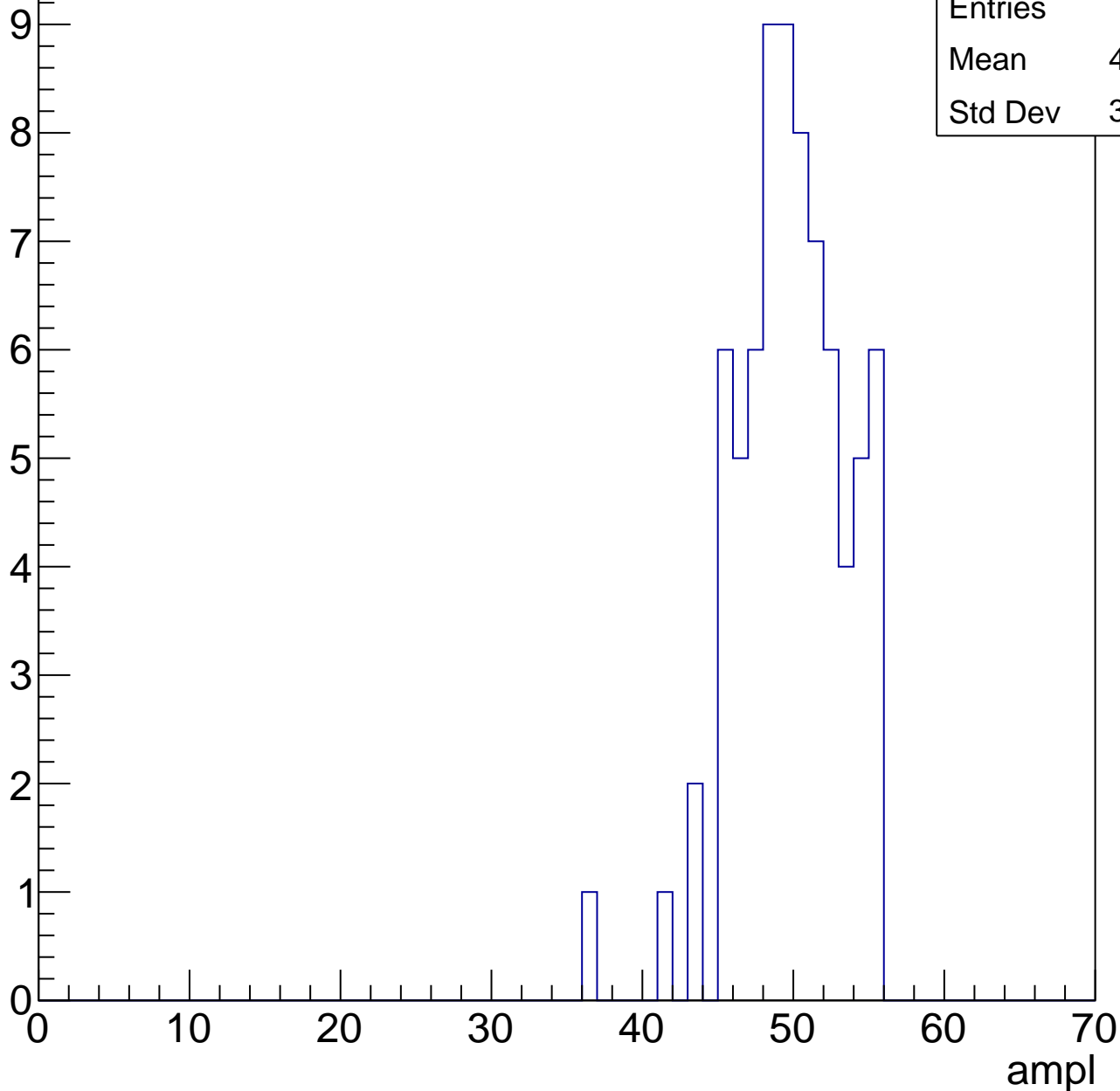


B1L102S, U26-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

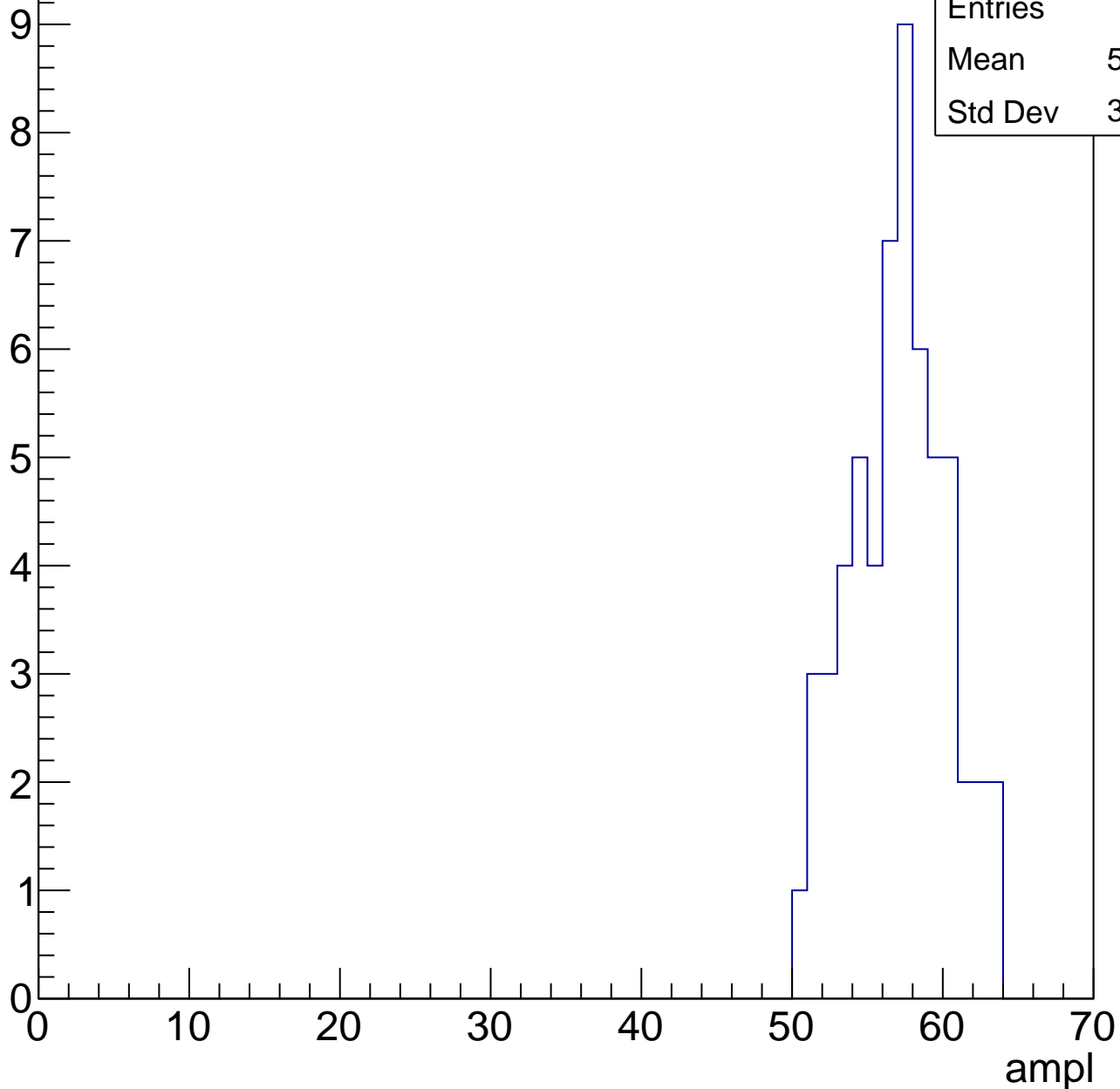
Entries	75
Mean	49.32
Std Dev	3.589



B1L102S, U26-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



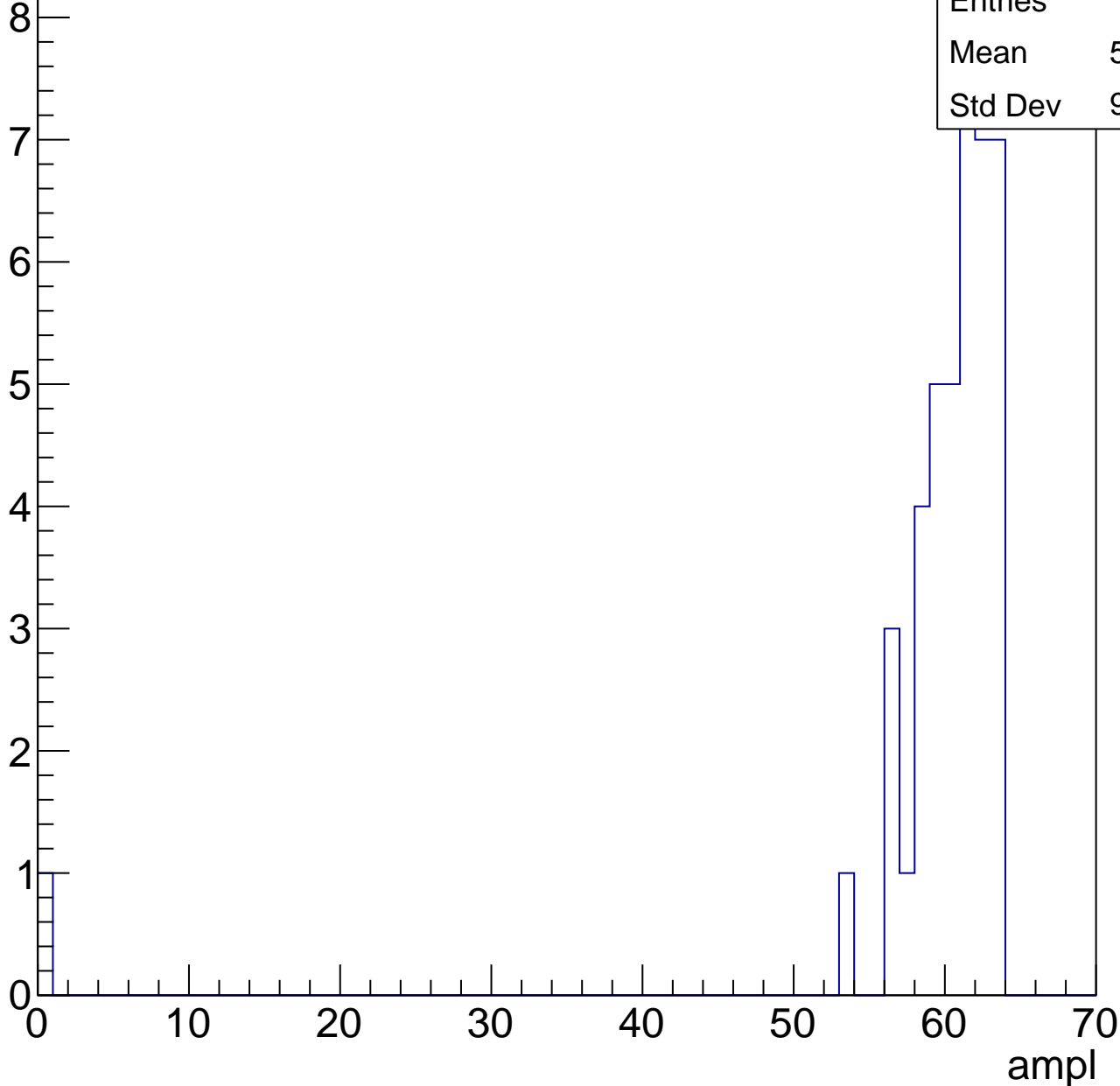
Entries	58
Mean	56.57
Std Dev	3.157

B1L102S, U26-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.76
Std Dev	9.464



B1L102S, U26-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch76, adc0

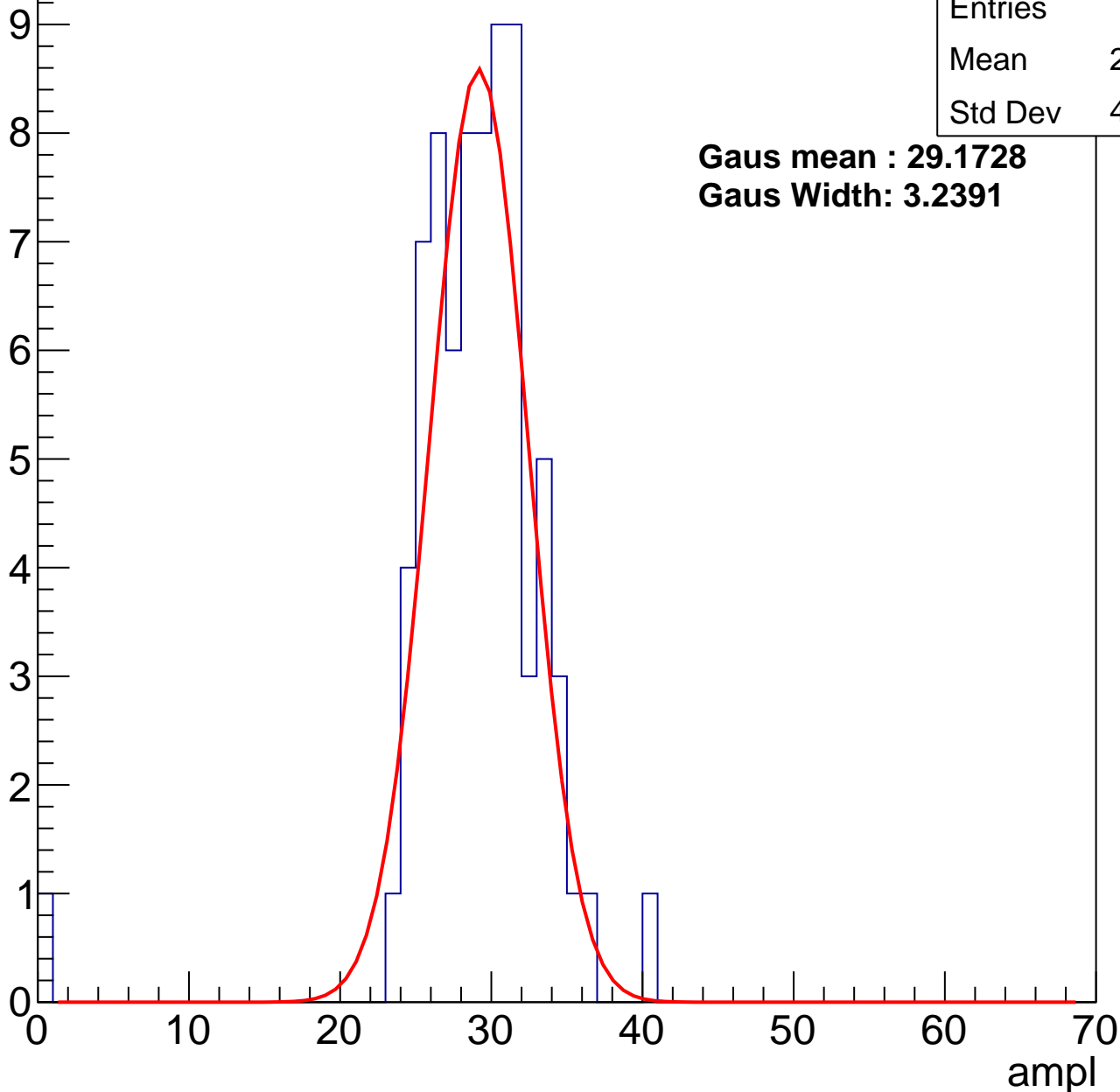
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.57
Std Dev	4.637

Gaus mean : 29.1728

Gaus Width: 3.2391



B1L102S, U26-ch76, adc1

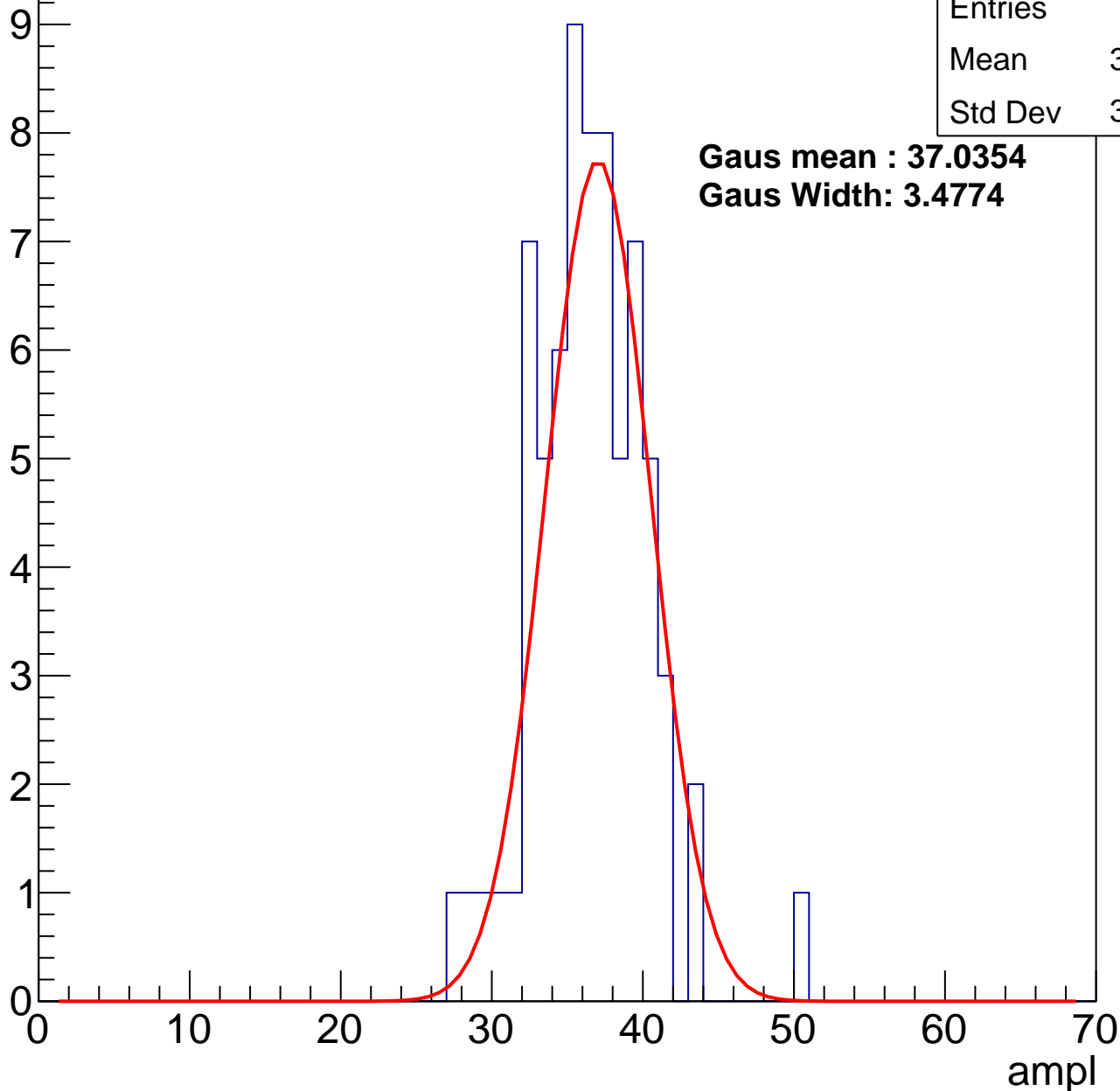
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.04
Std Dev	3.724

Gaus mean : 37.0354

Gaus Width: 3.4774



B1L102S, U26-ch76, adc2

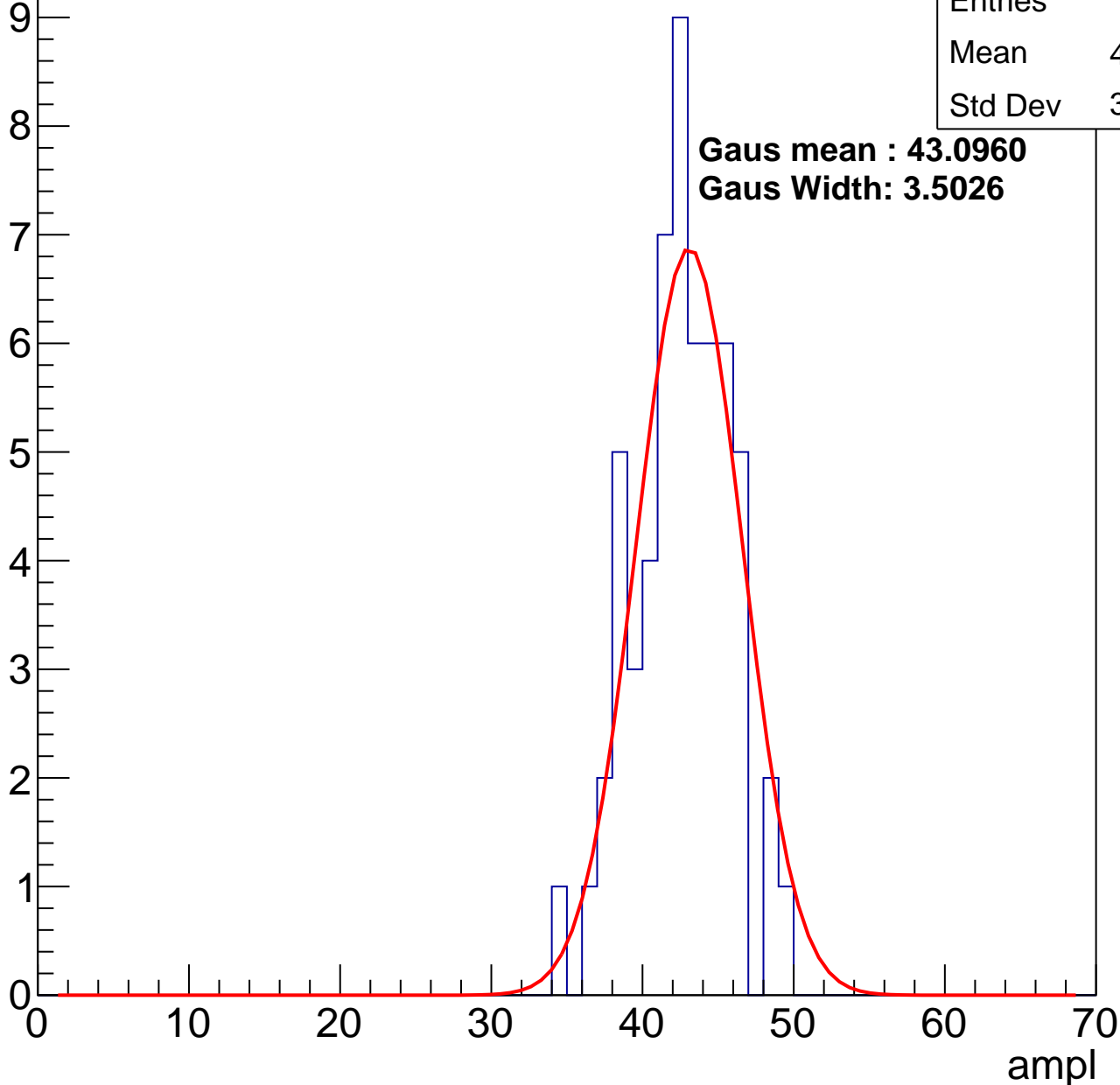
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.12
Std Dev	3.119

Gaus mean : 43.0960

Gaus Width: 3.5026

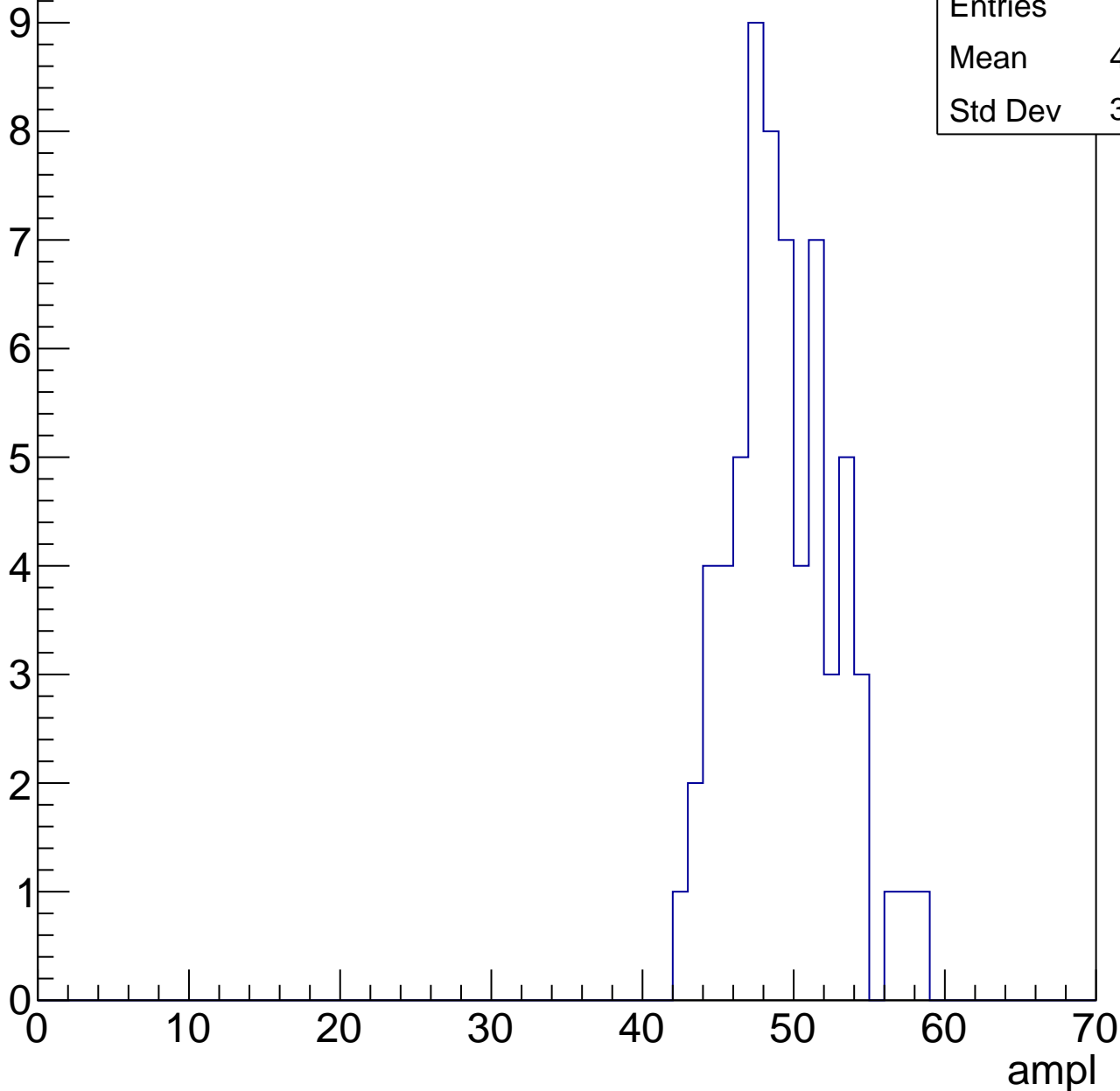


B1L102S, U26-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.85
Std Dev	3.465

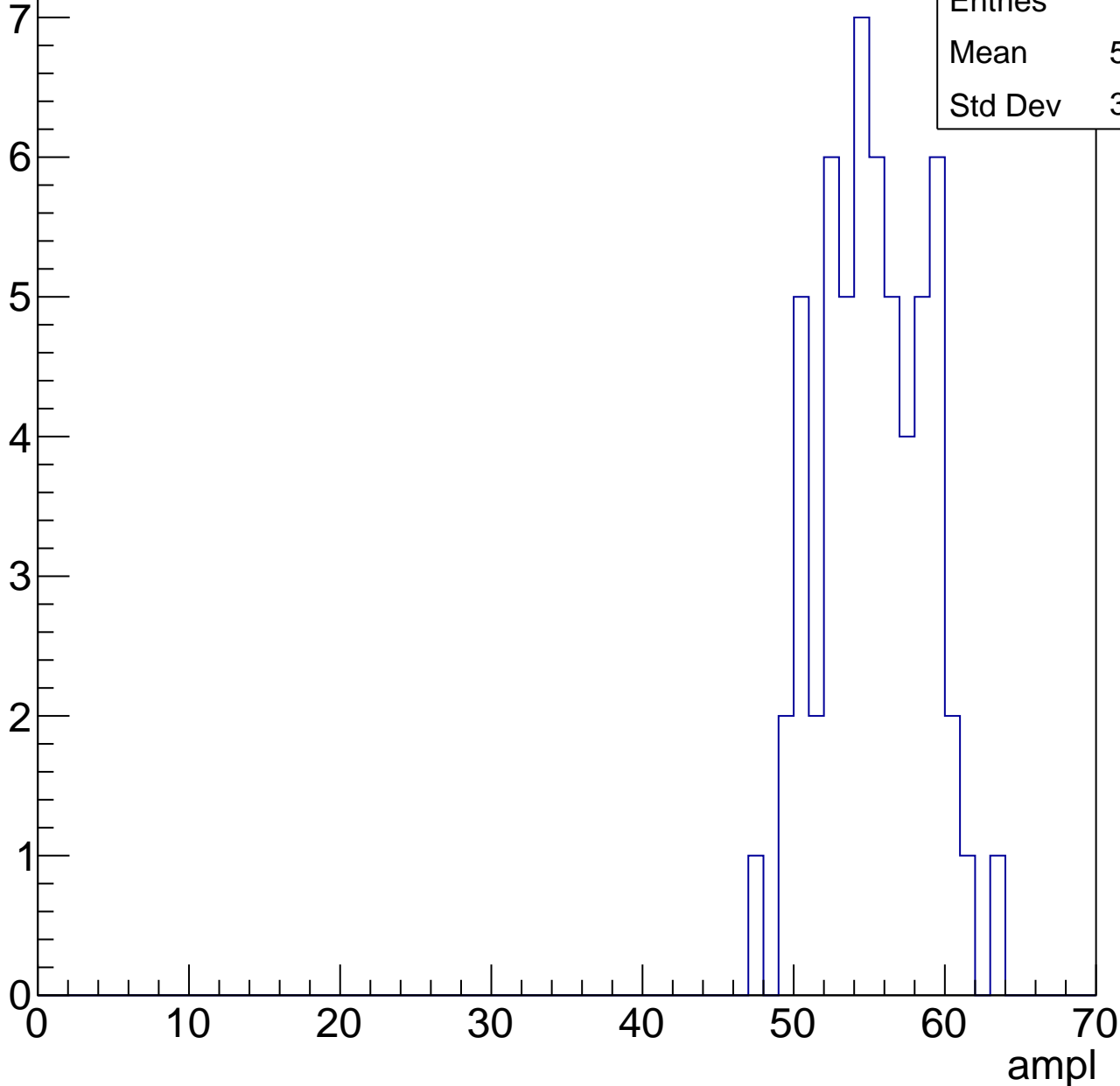


B1L102S, U26-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.79
Std Dev	3.433



B1L102S, U26-ch76, adc5

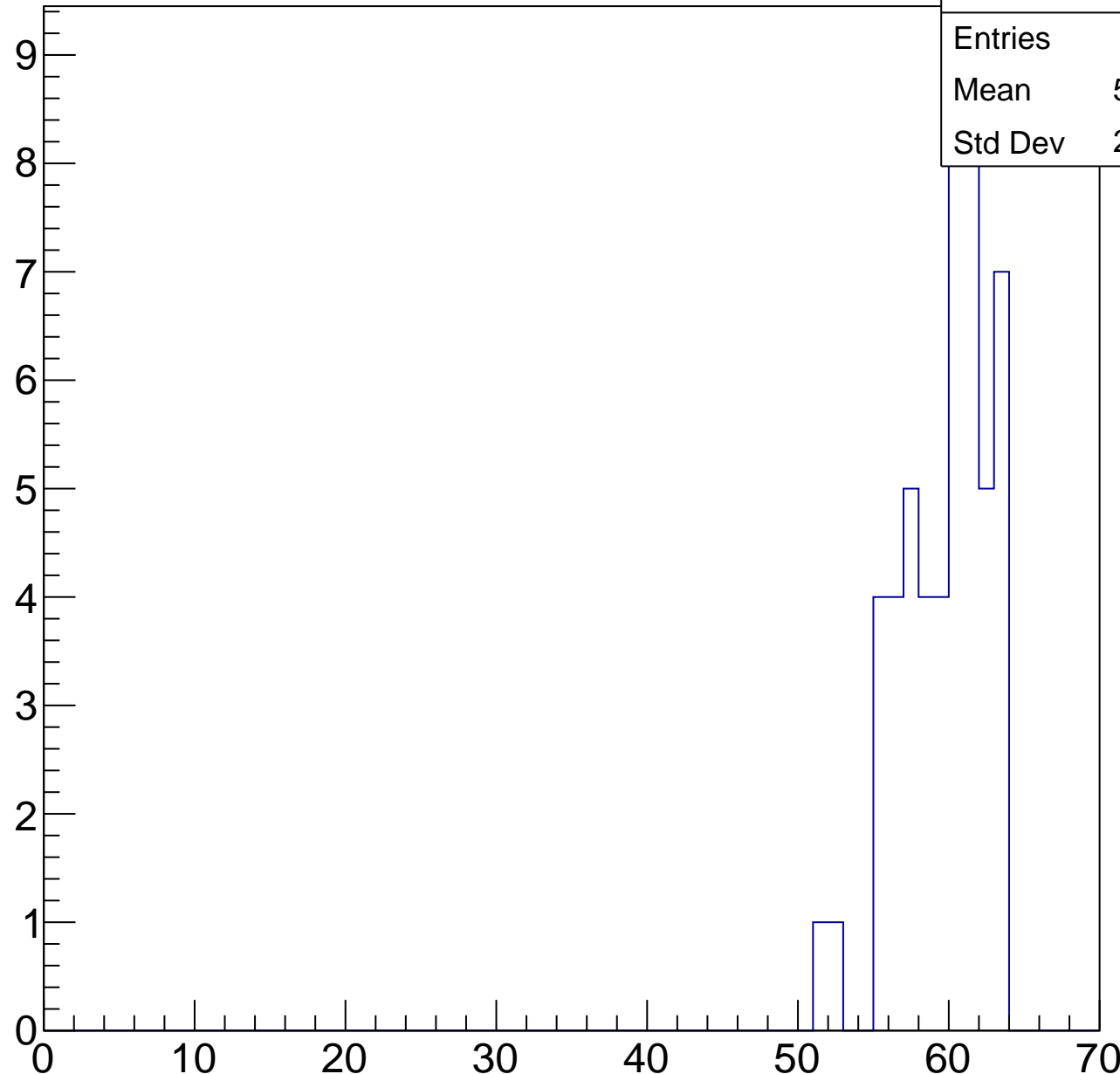
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.23
Std Dev	2.893

ampl

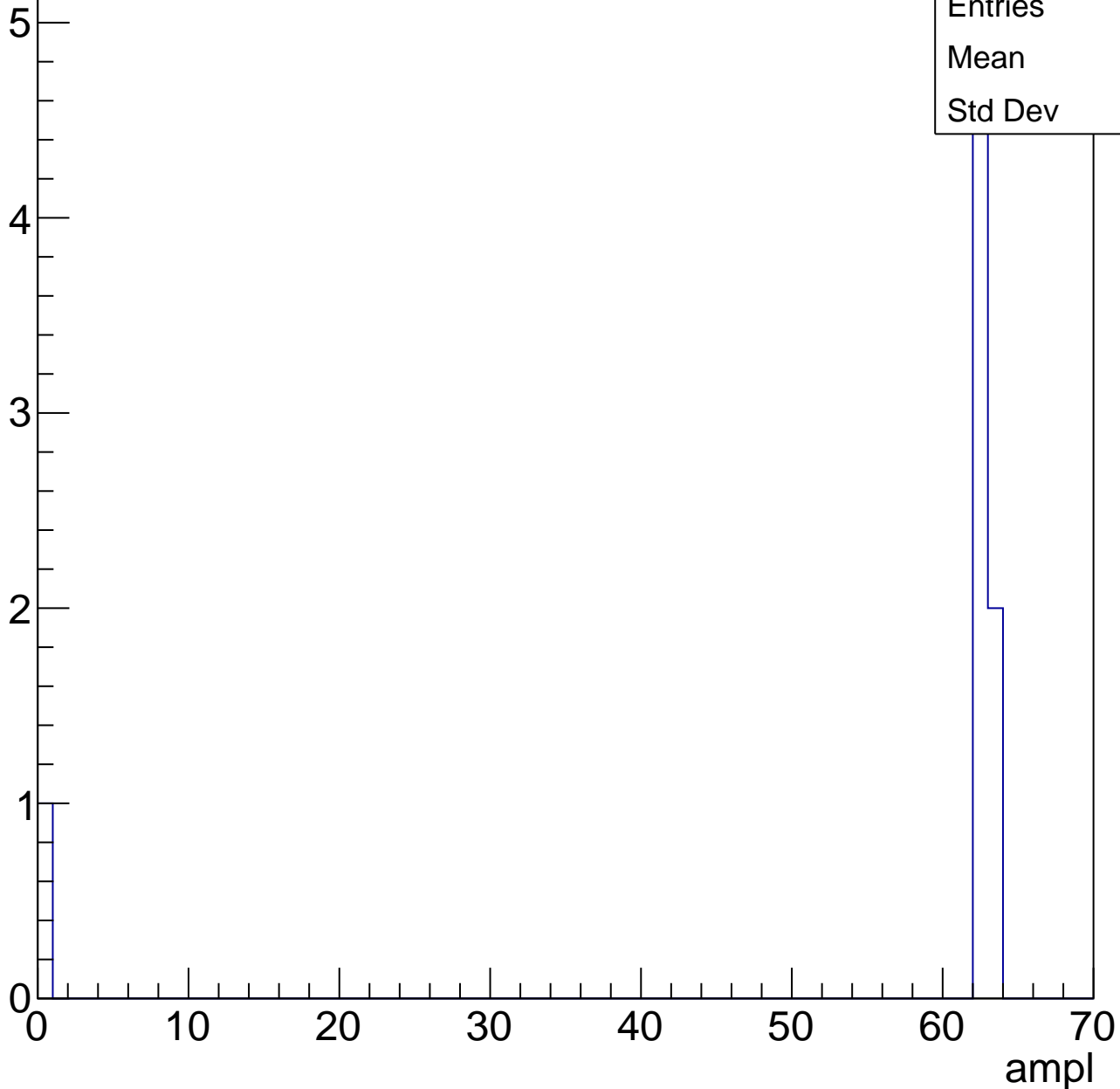


B1L102S, U26-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.5
Std Dev	20.6



B1L102S, U26-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U26-ch77, adc0

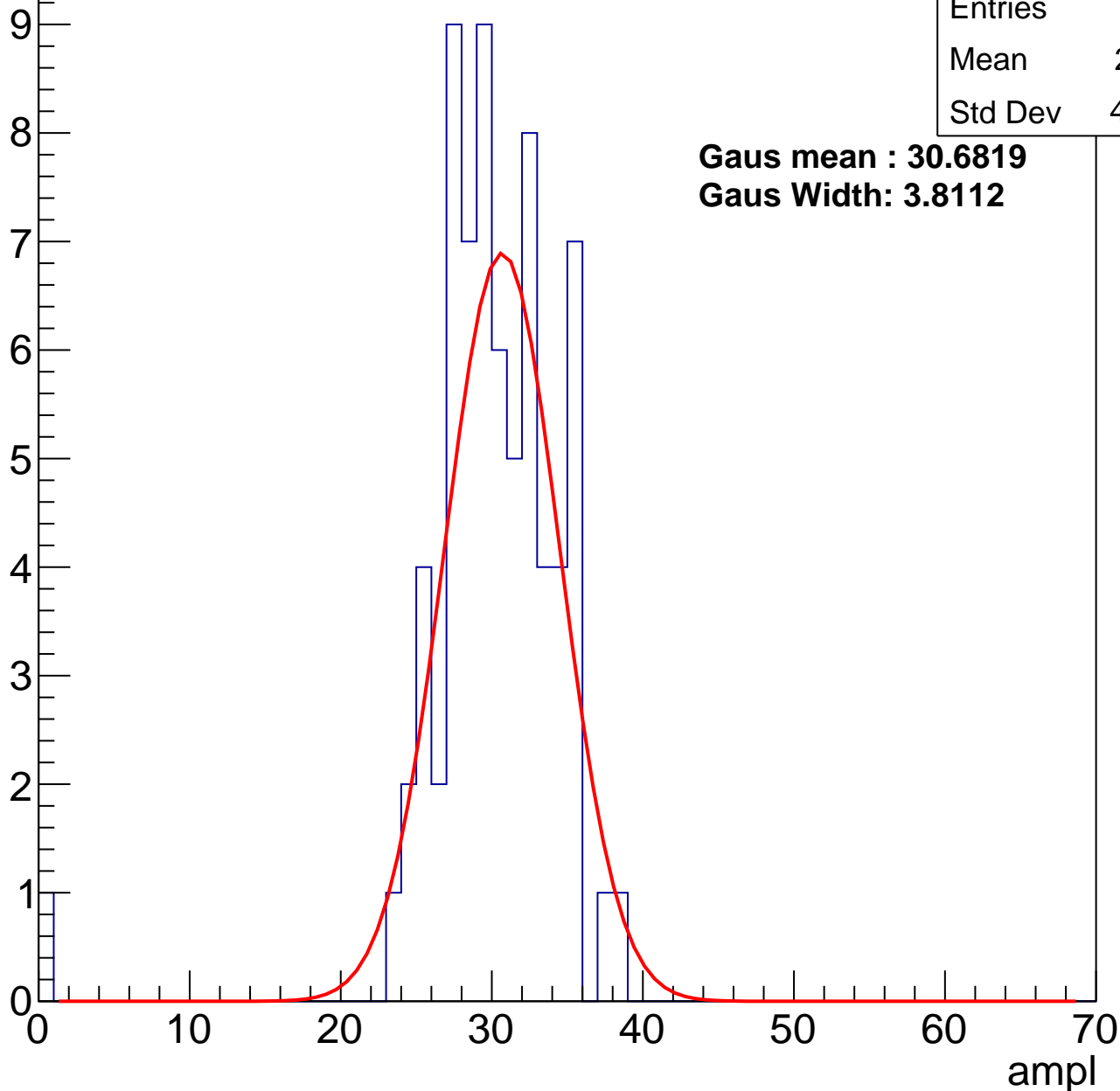
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.61
Std Dev	4.872

Gaus mean : 30.6819

Gaus Width: 3.8112



B1L102S, U26-ch77, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	36.08
Std Dev	3.258

Gaus mean : 36.8353

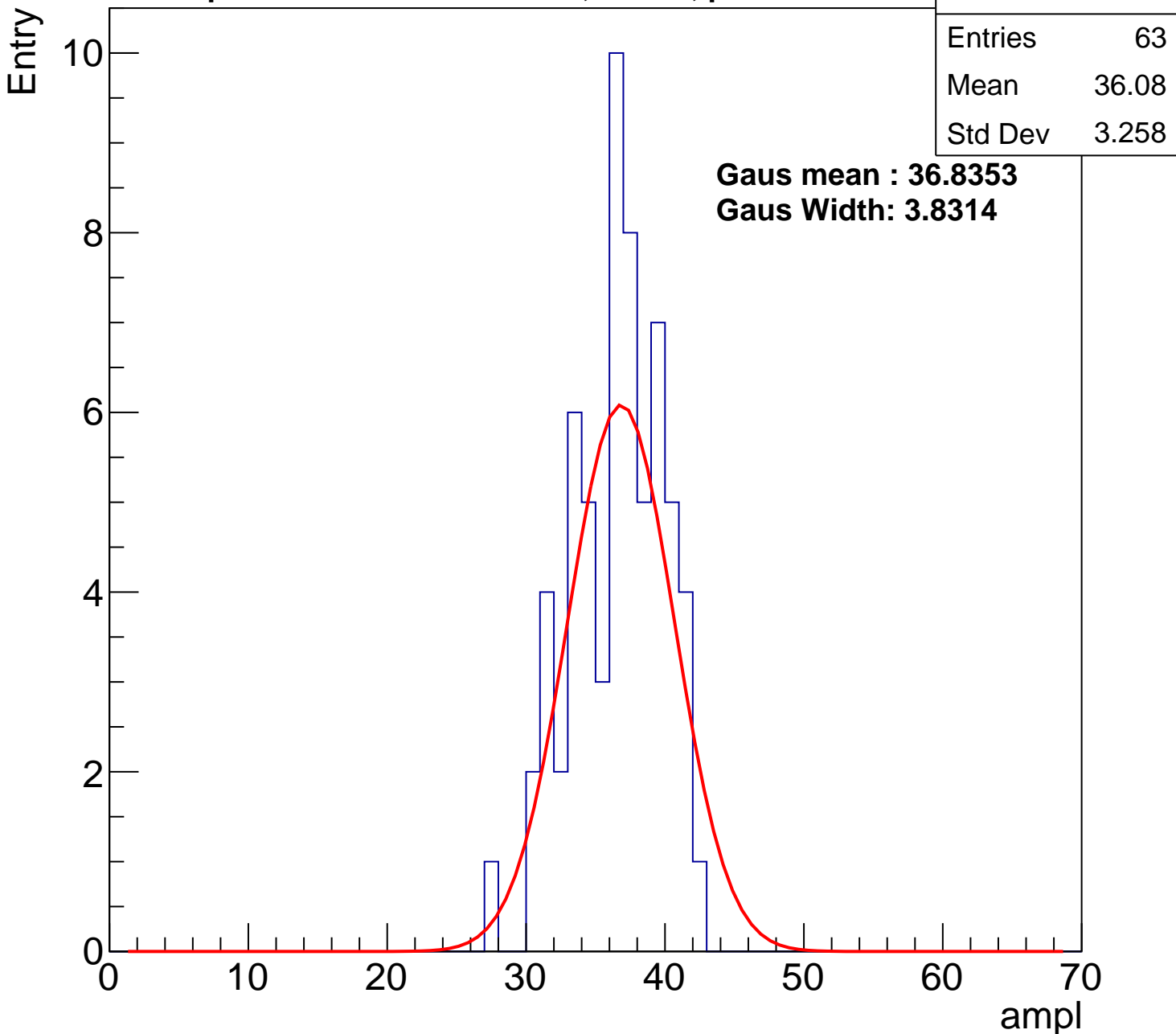
Gaus Width: 3.8314

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch77, adc2

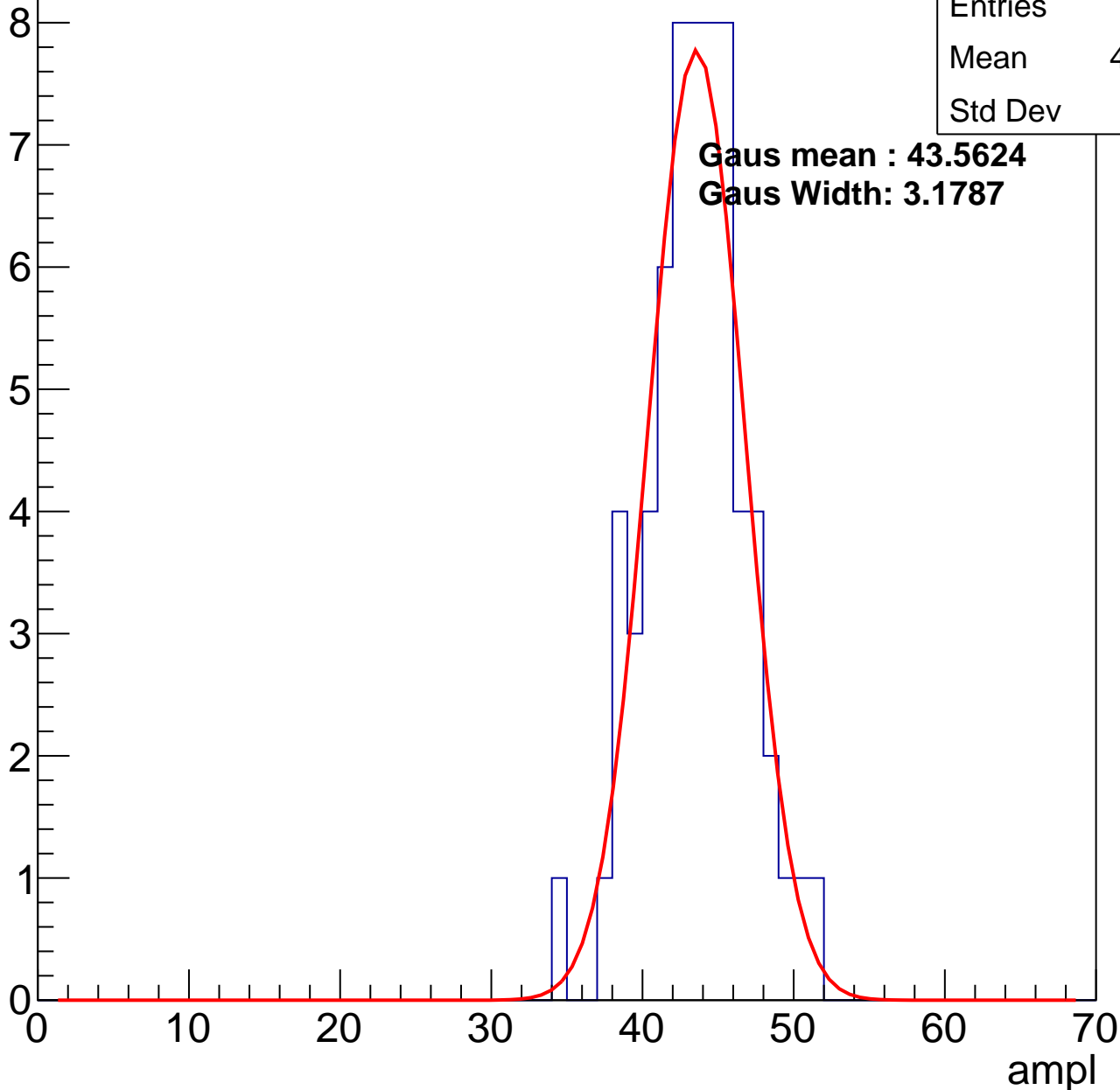
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.06
Std Dev	3.24

Gaus mean : 43.5624

Gaus Width: 3.1787

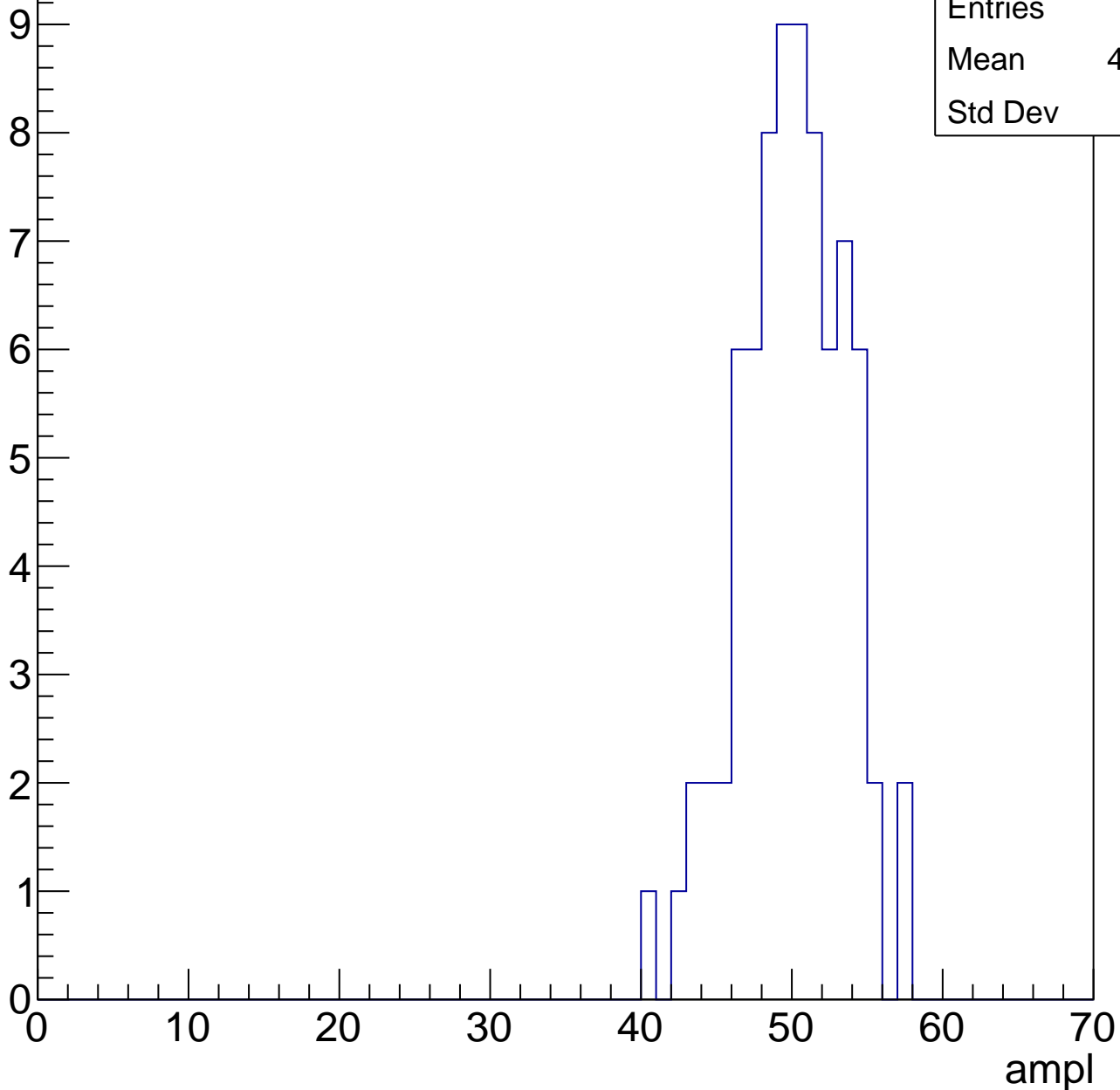


B1L102S, U26-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.58
Std Dev	3.42



B1L102S, U26-ch77, adc4

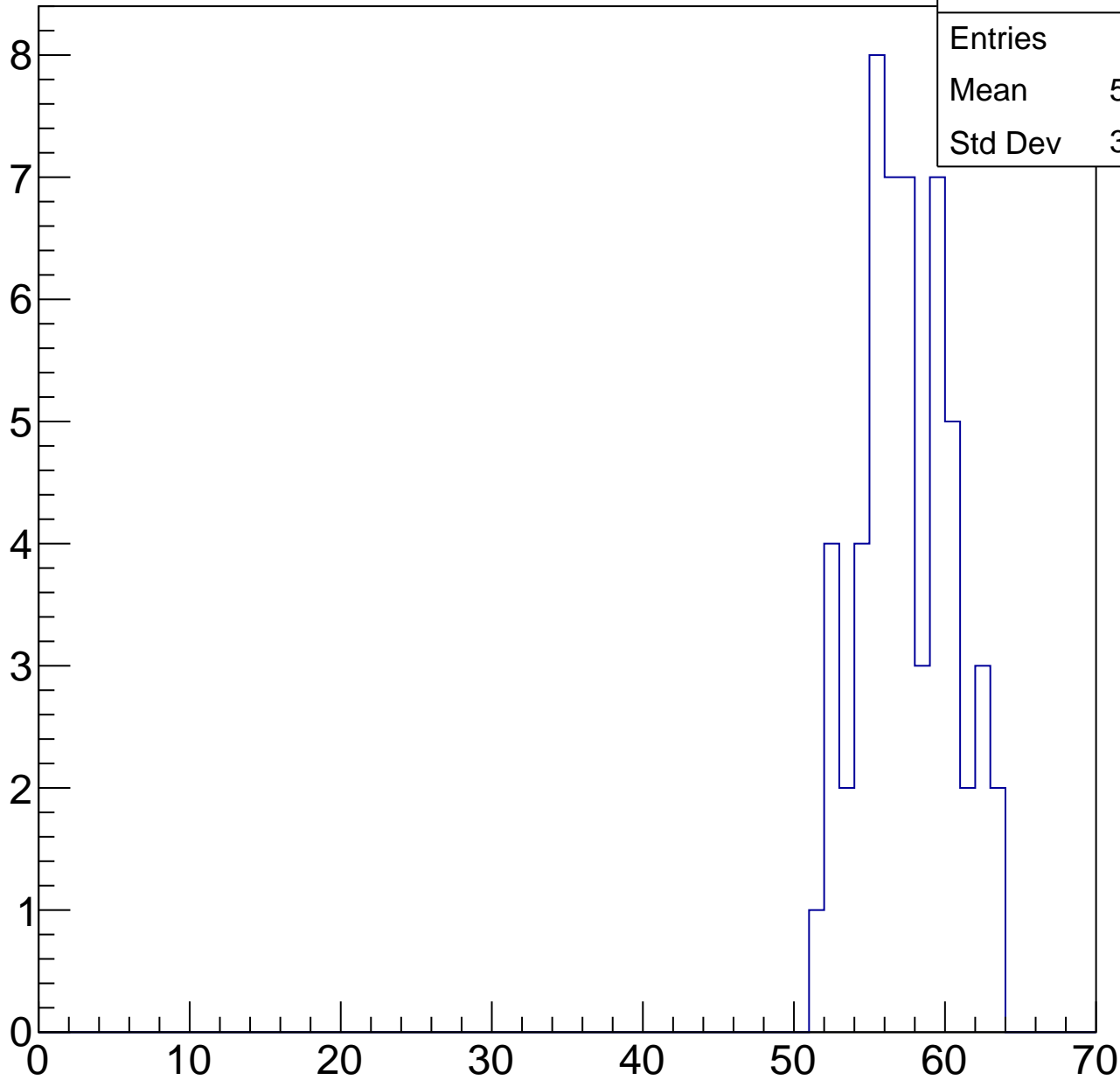
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	56.96
Std Dev	3.009

ampl

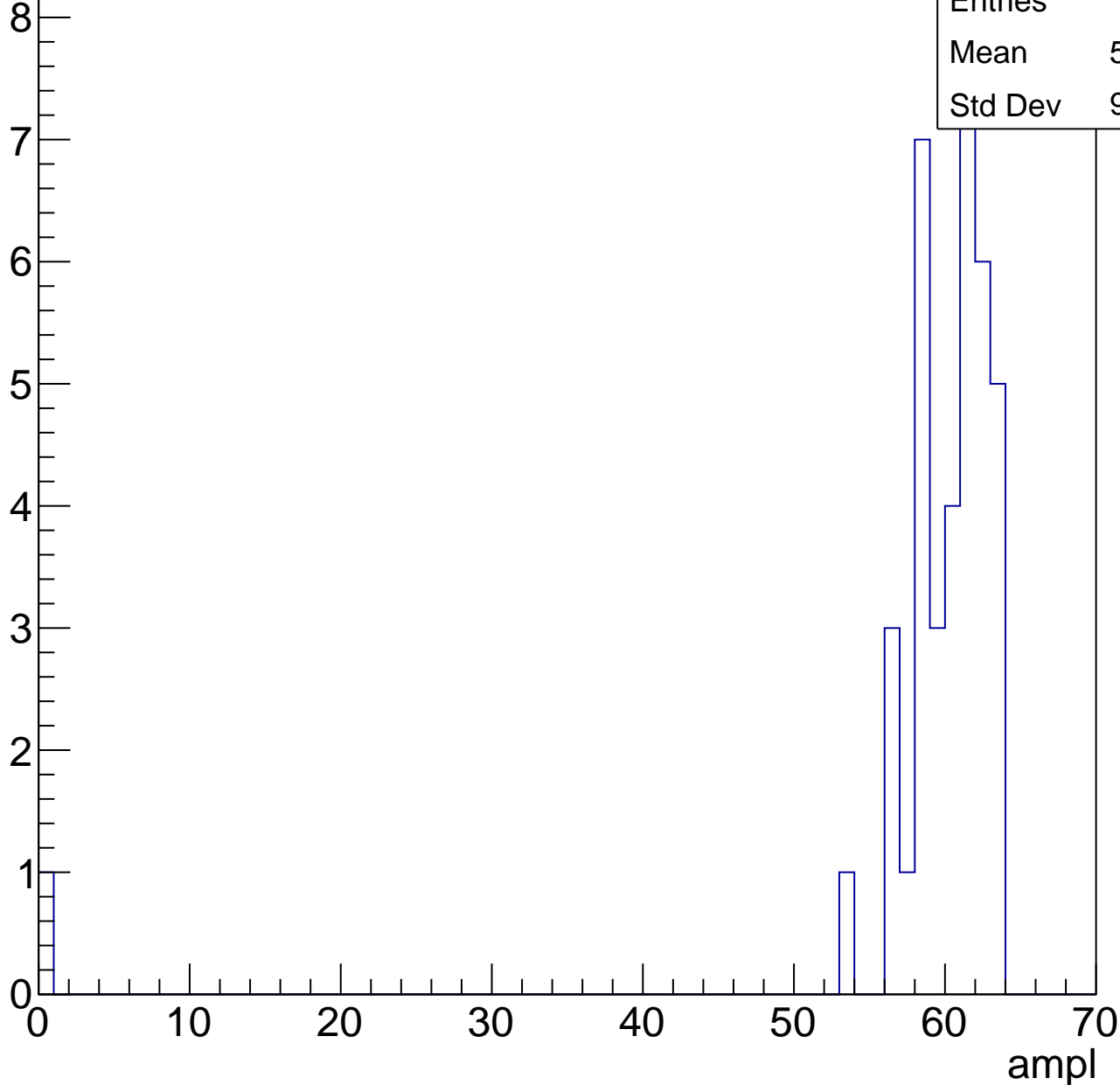


B1L102S, U26-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.36
Std Dev	9.752



B1L102S, U26-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch78, adc0

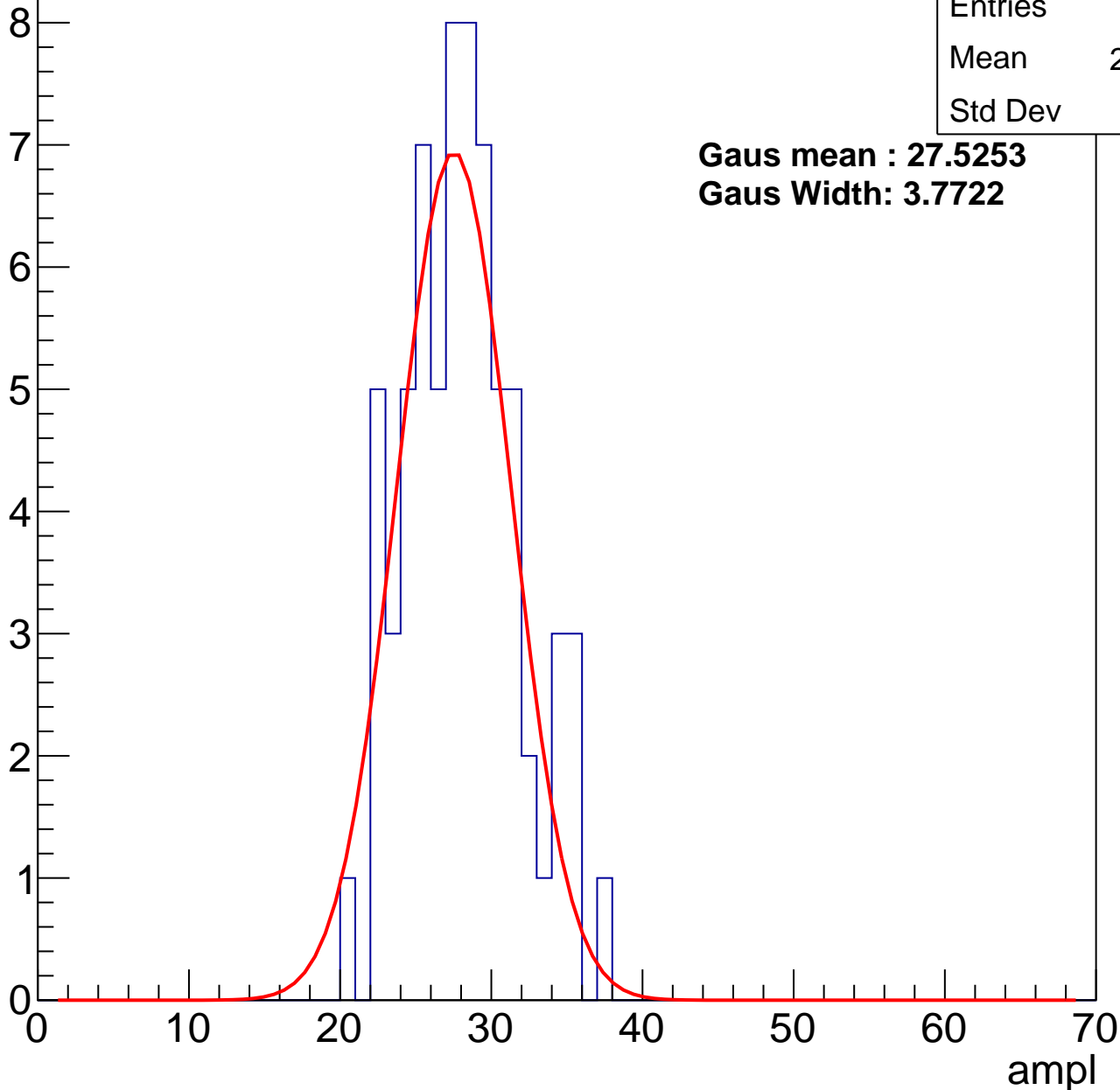
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.72
Std Dev	3.71

Gaus mean : 27.5253

Gaus Width: 3.7722



B1L102S, U26-ch78, adc1

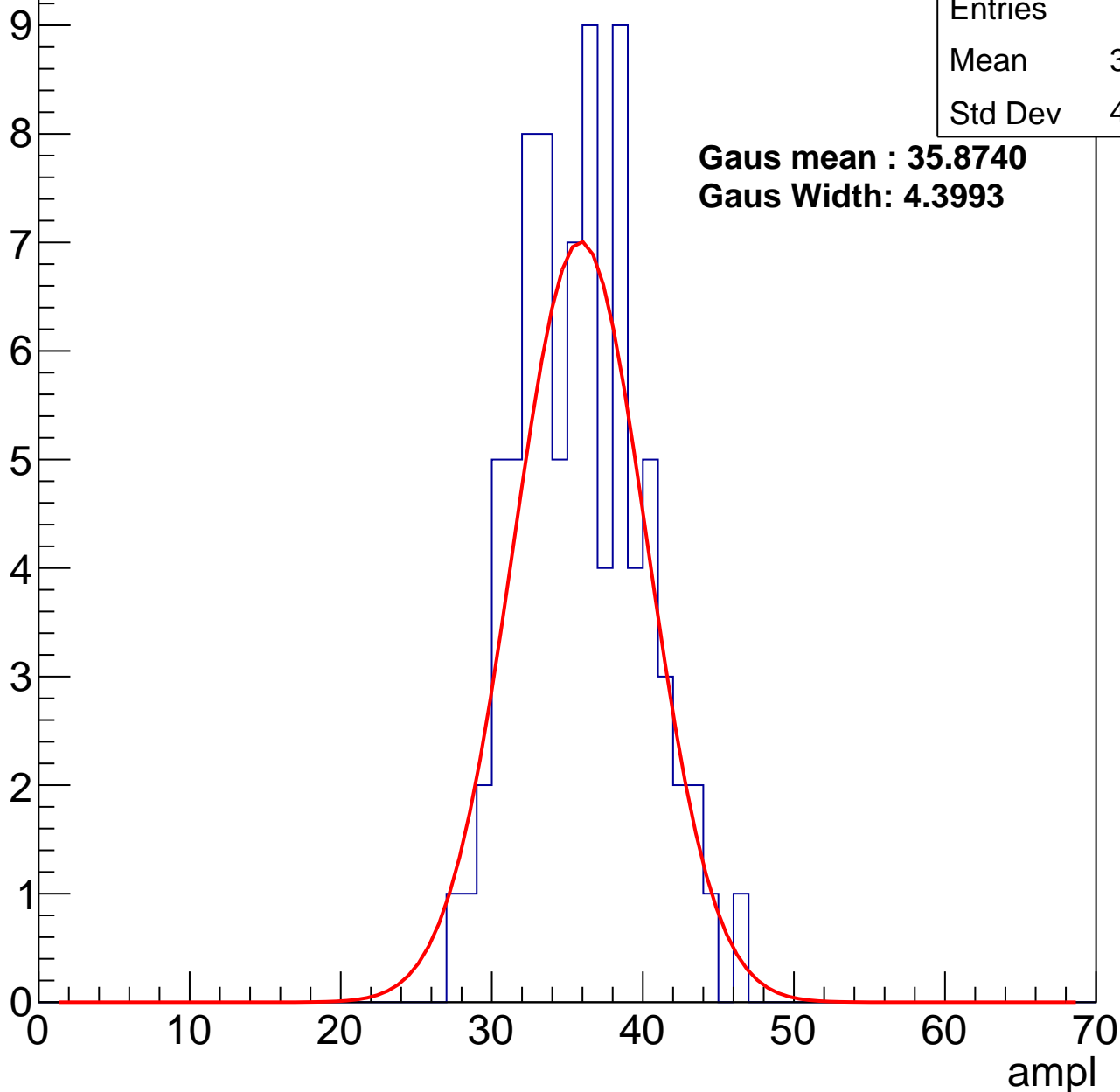
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.44
Std Dev	4.003

Gaus mean : 35.8740

Gaus Width: 4.3993



B1L102S, U26-ch78, adc2

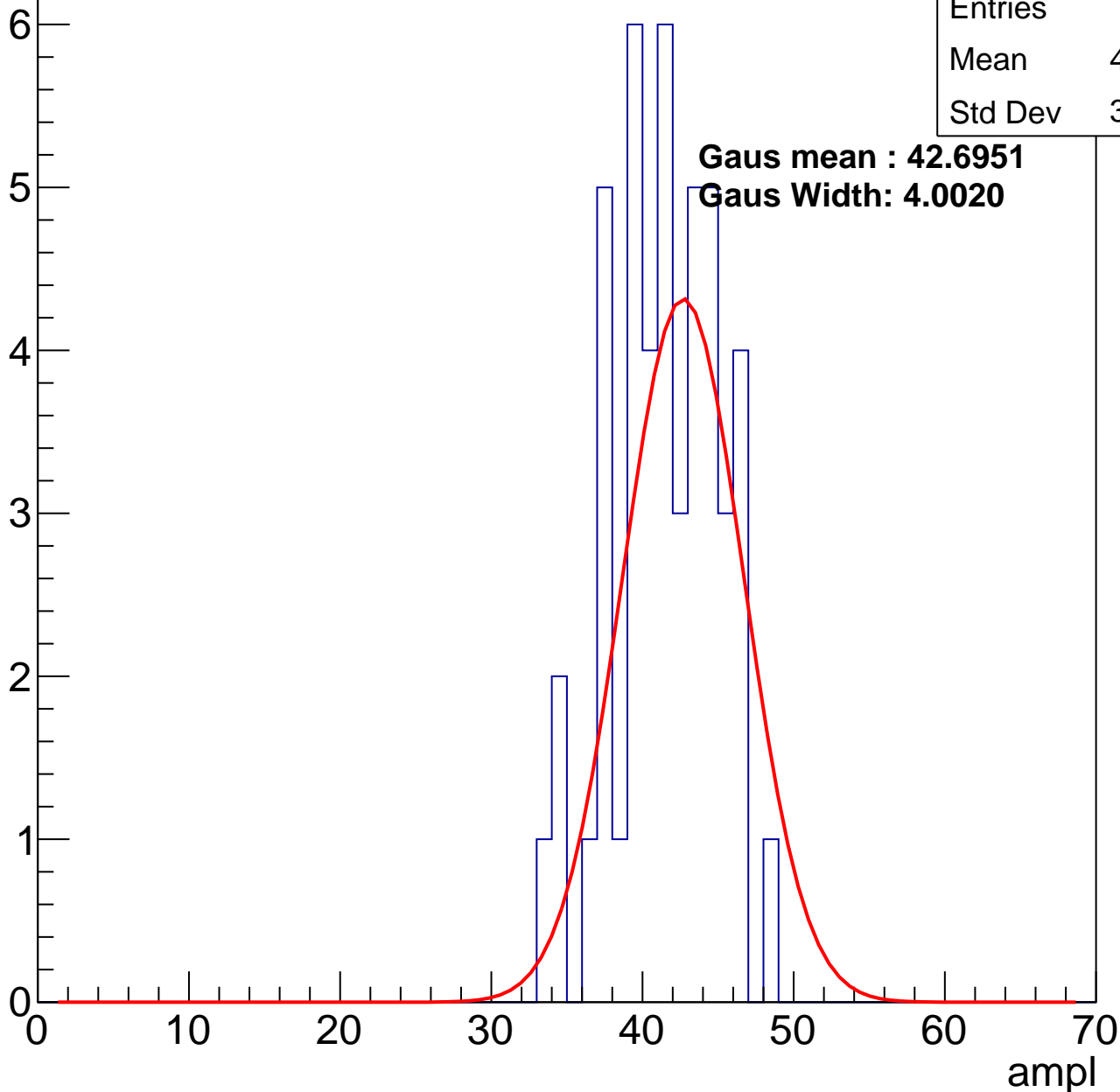
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	41.02
Std Dev	3.479

Gaus mean : 42.6951

Gaus Width: 4.0020

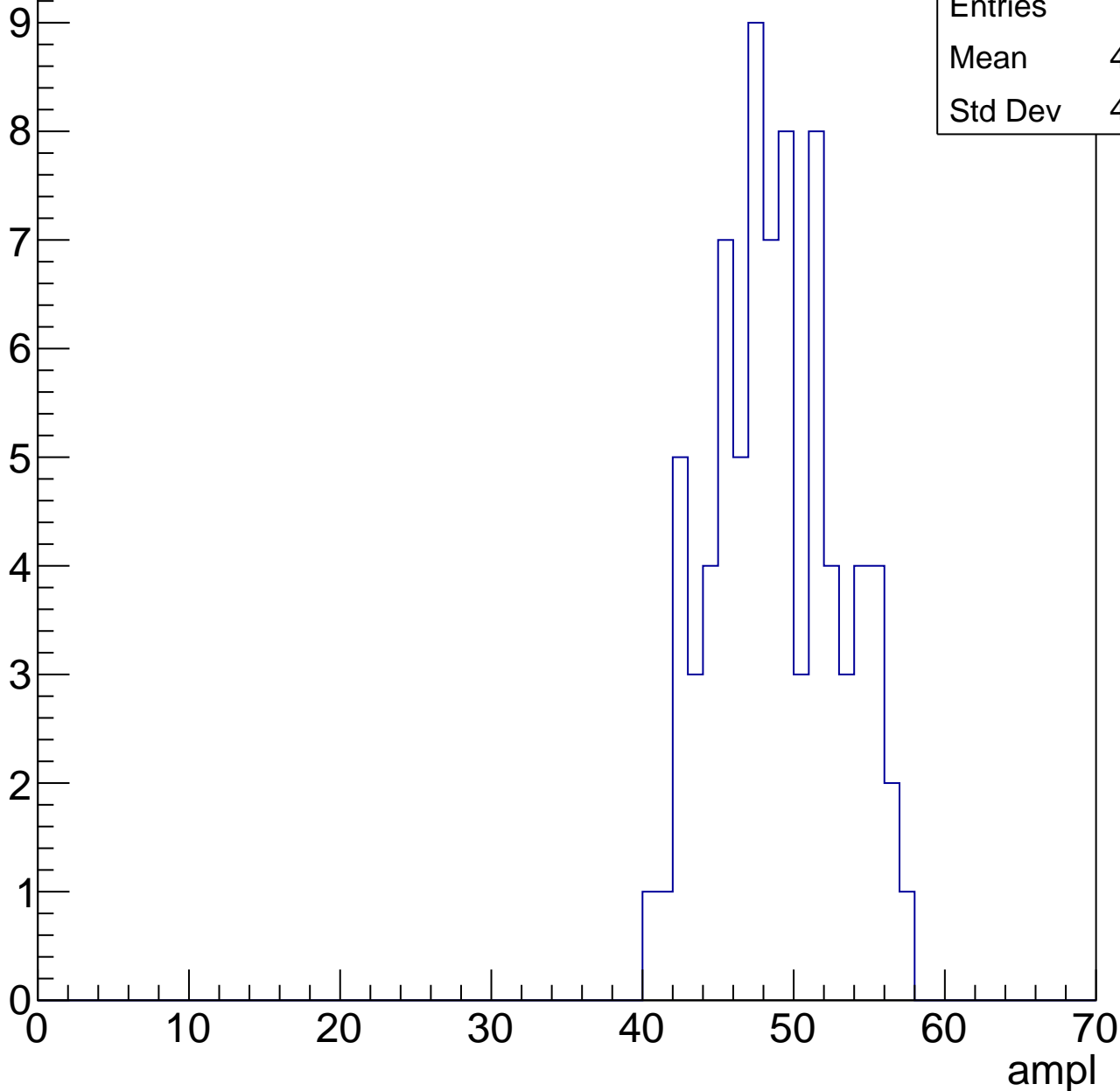


B1L102S, U26-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	48.38
Std Dev	4.045

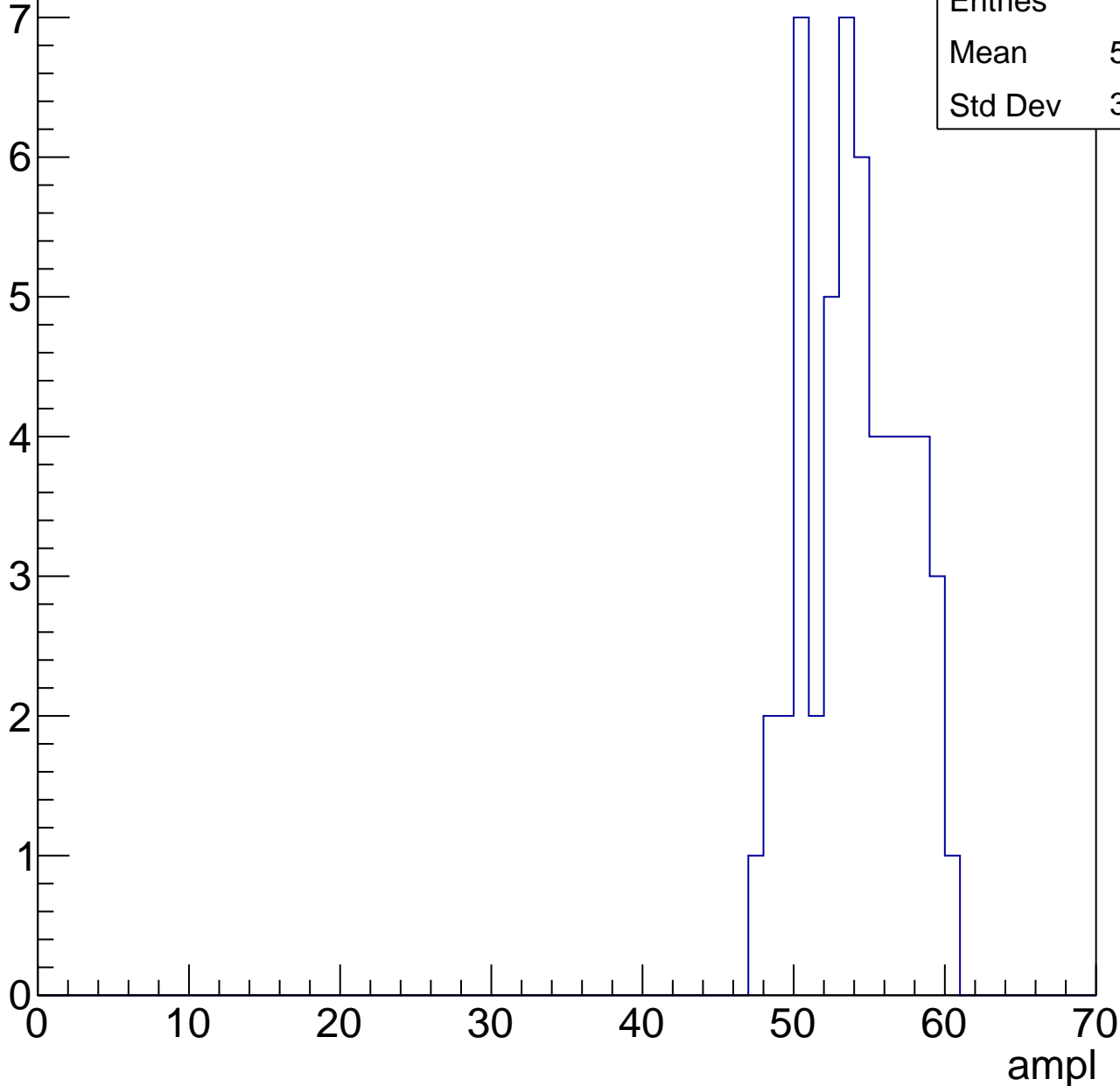


B1L102S, U26-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.63
Std Dev	3.258

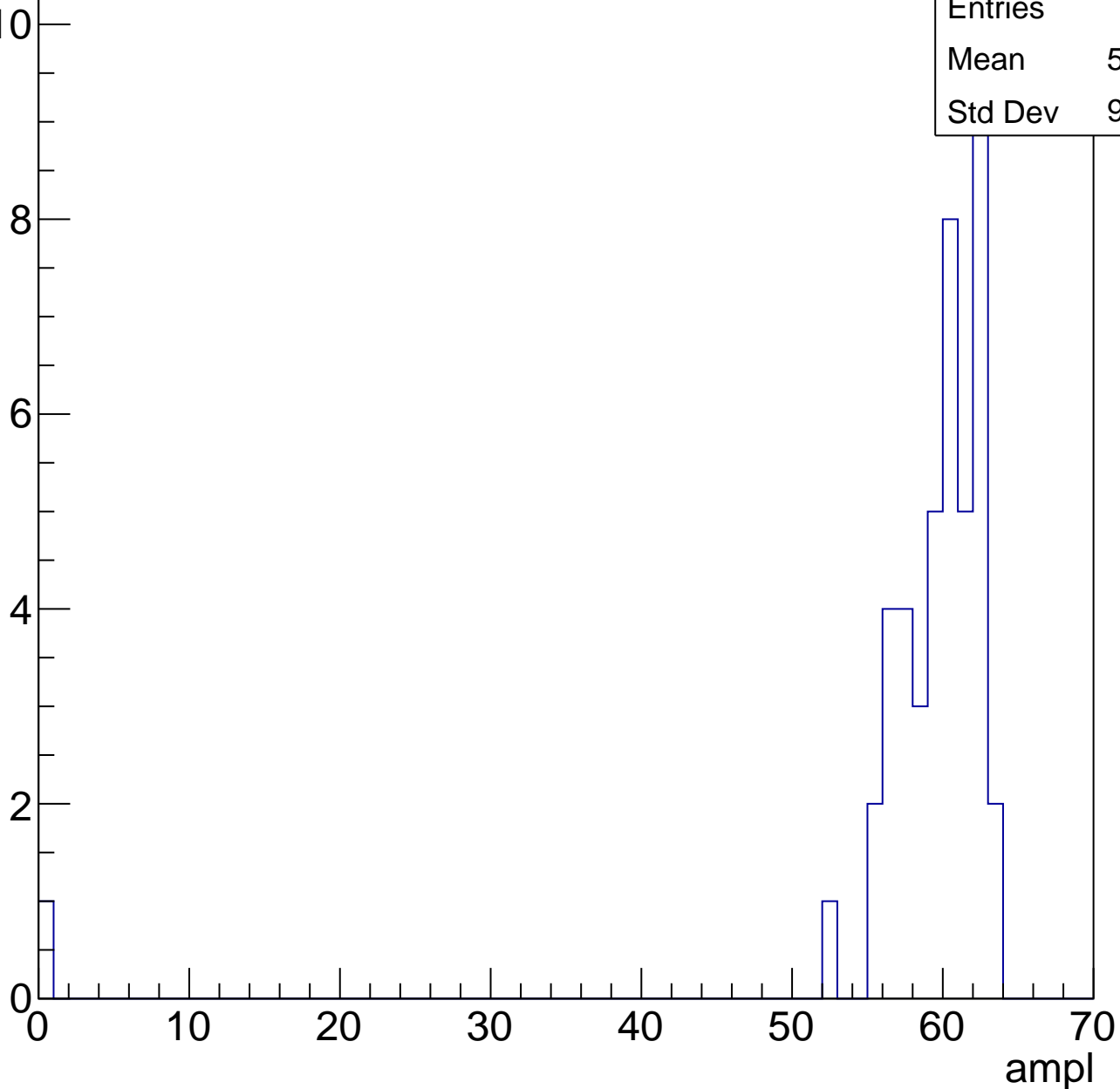


B1L102S, U26-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.09
Std Dev	9.104

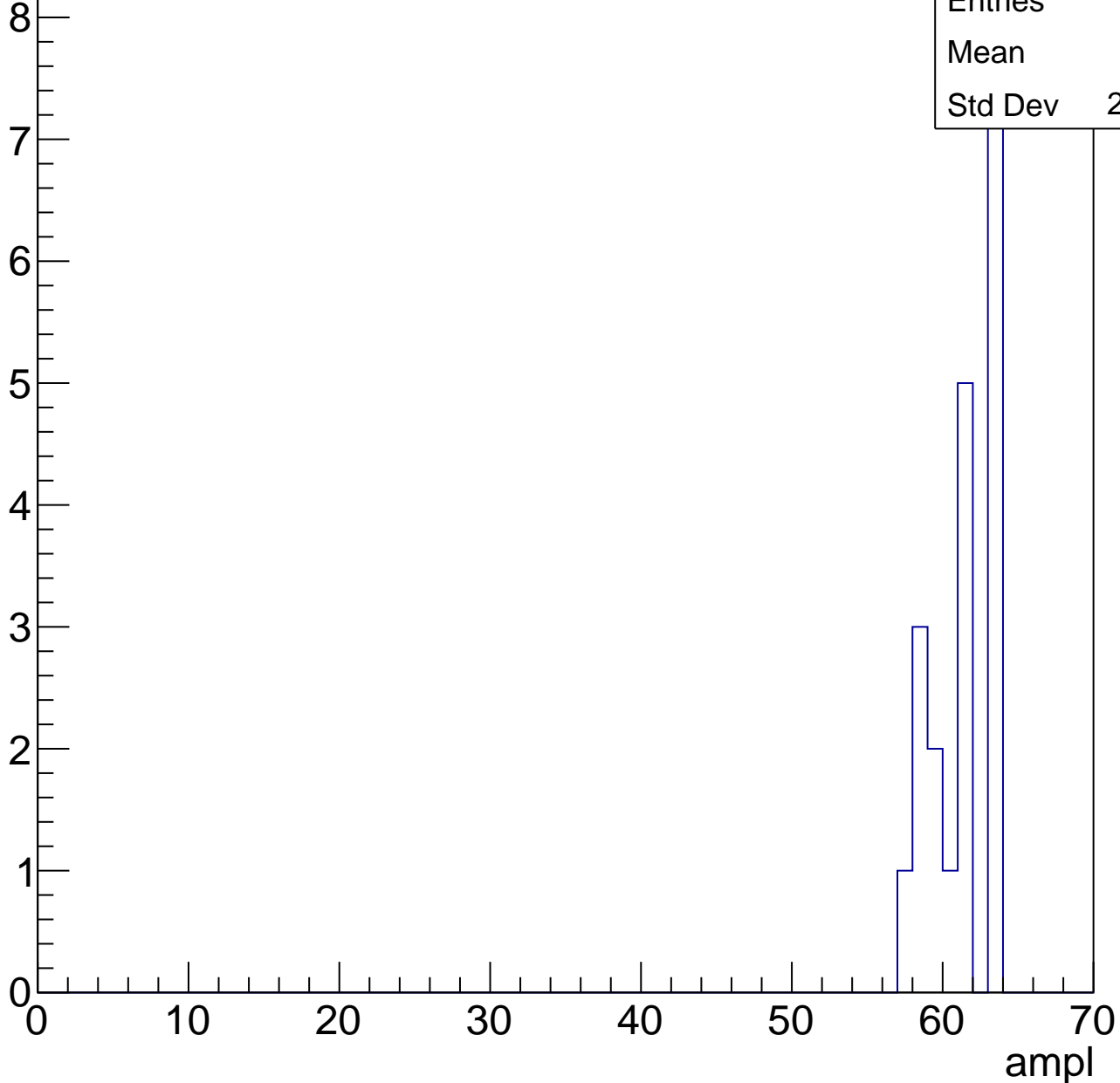


B1L102S, U26-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

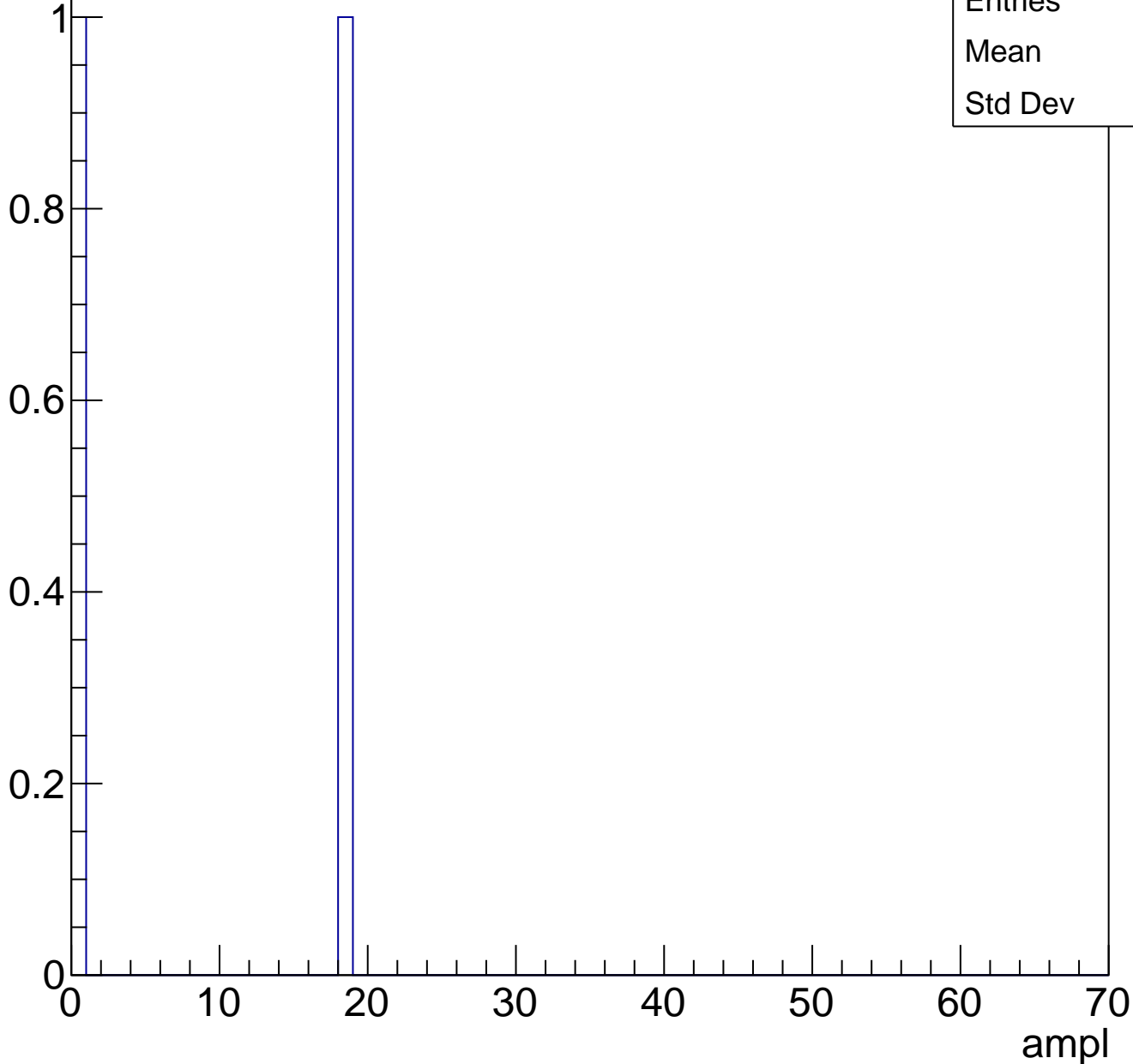
Entries	20
Mean	60.9
Std Dev	2.047



B1L102S, U26-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9
Std Dev	9

B1L102S, U26-ch79, adc0

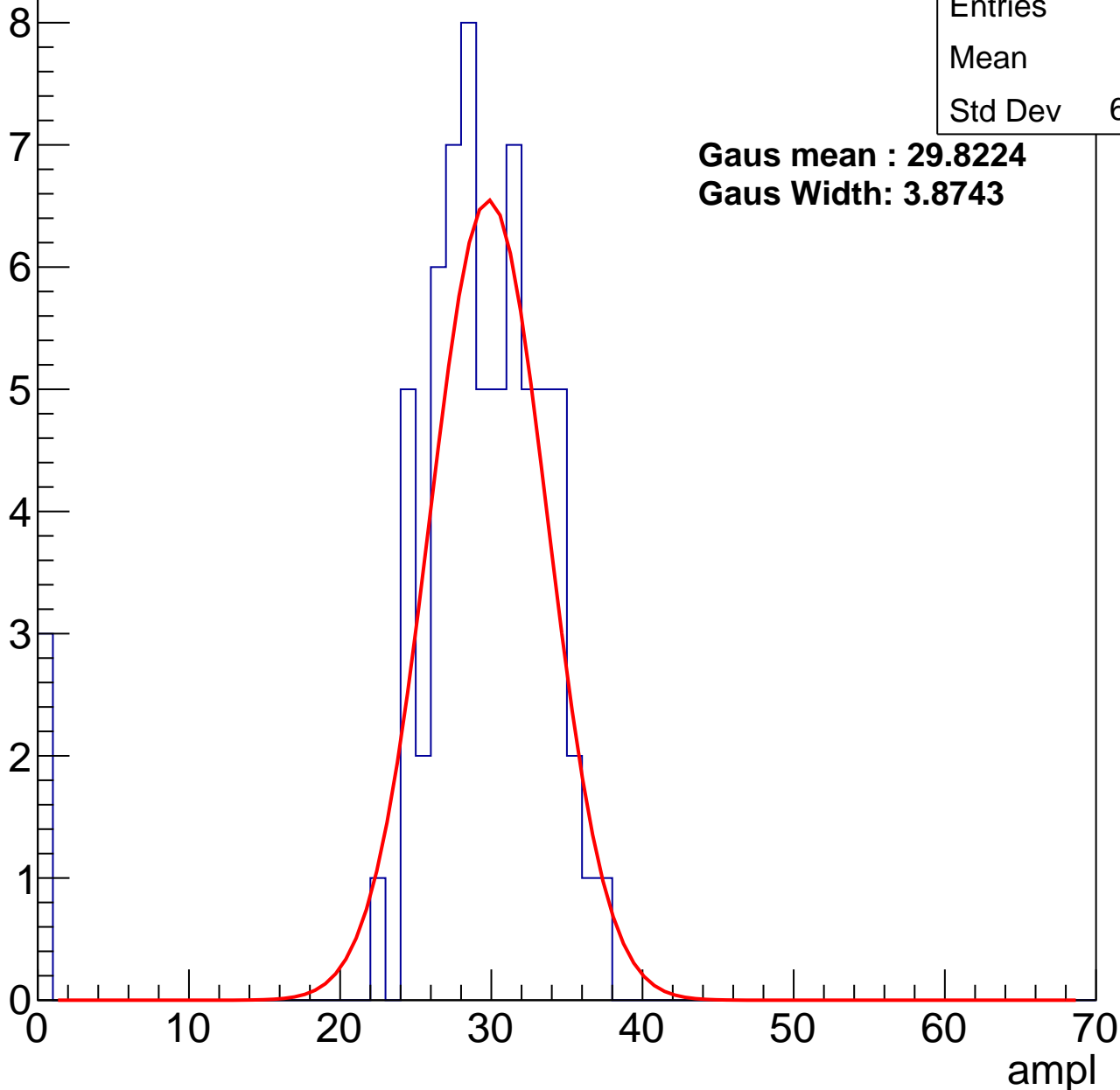
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.1
Std Dev	6.894

Gaus mean : 29.8224

Gaus Width: 3.8743



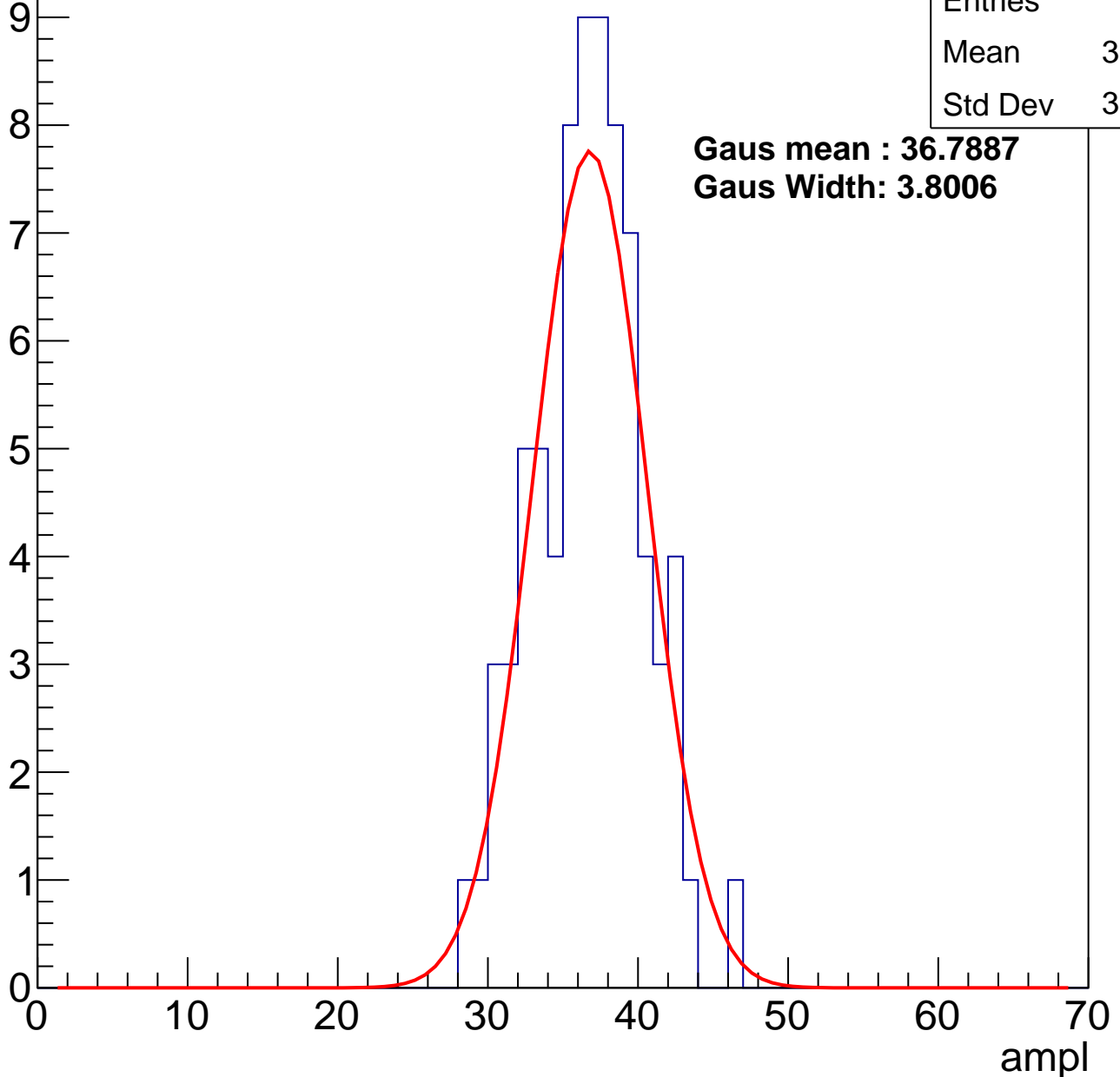
B1L102S, U26-ch79, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.25
Std Dev	3.584

Gaus mean : 36.7887
Gaus Width: 3.8006



B1L102S, U26-ch79, adc2

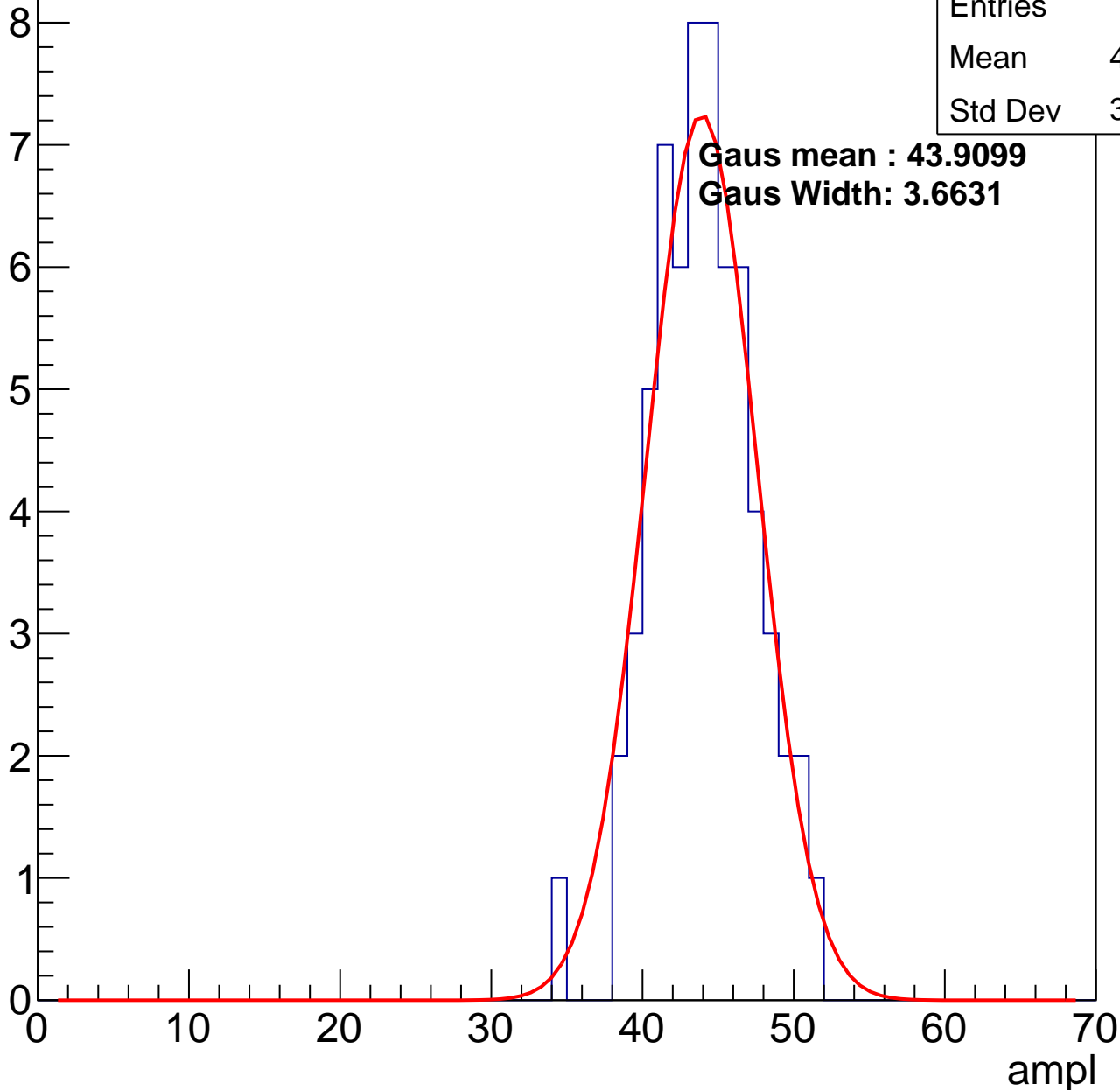
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.58
Std Dev	3.297

Gaus mean : 43.9099

Gaus Width: 3.6631

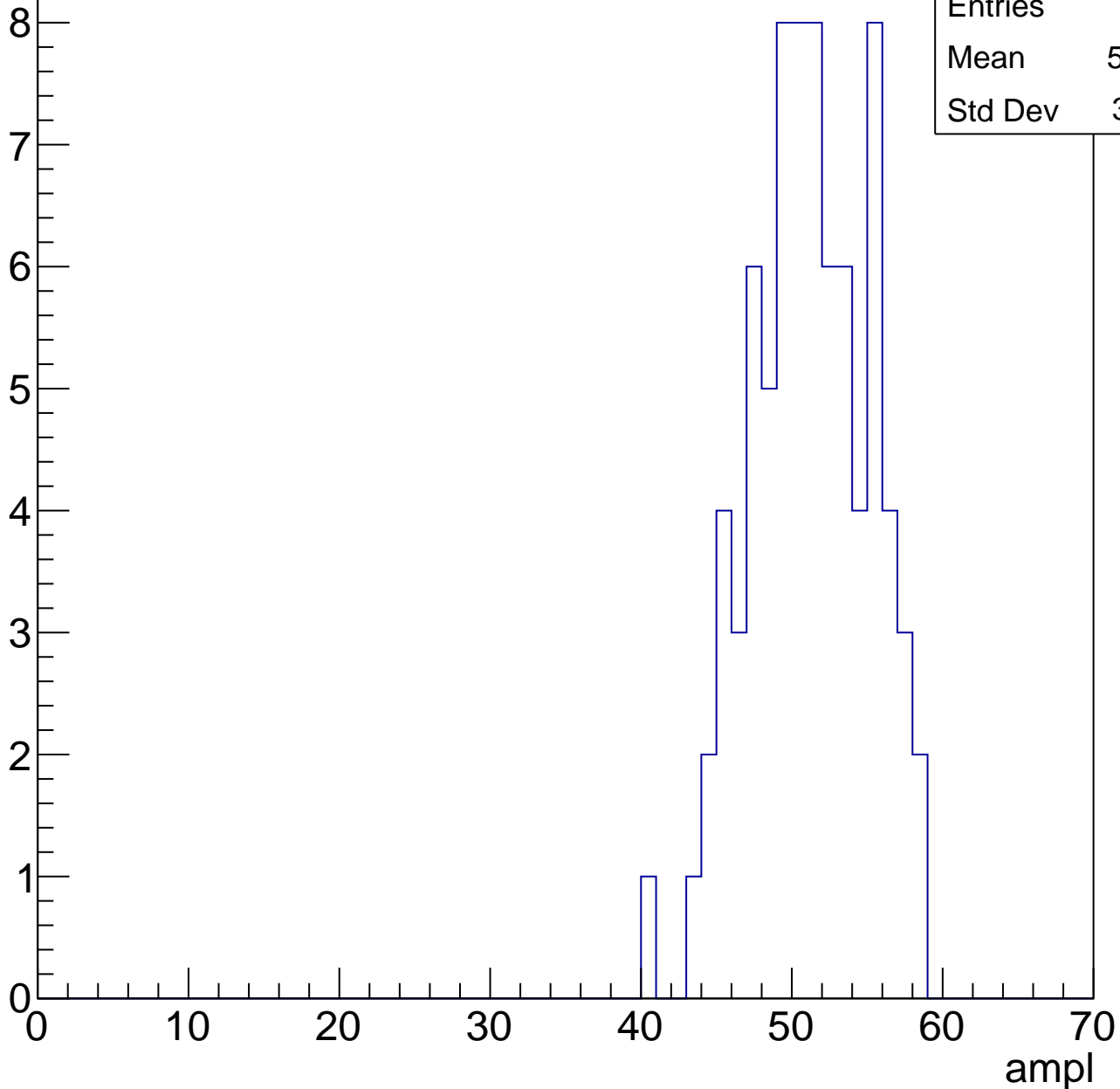


B1L102S, U26-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50.73
Std Dev	3.861

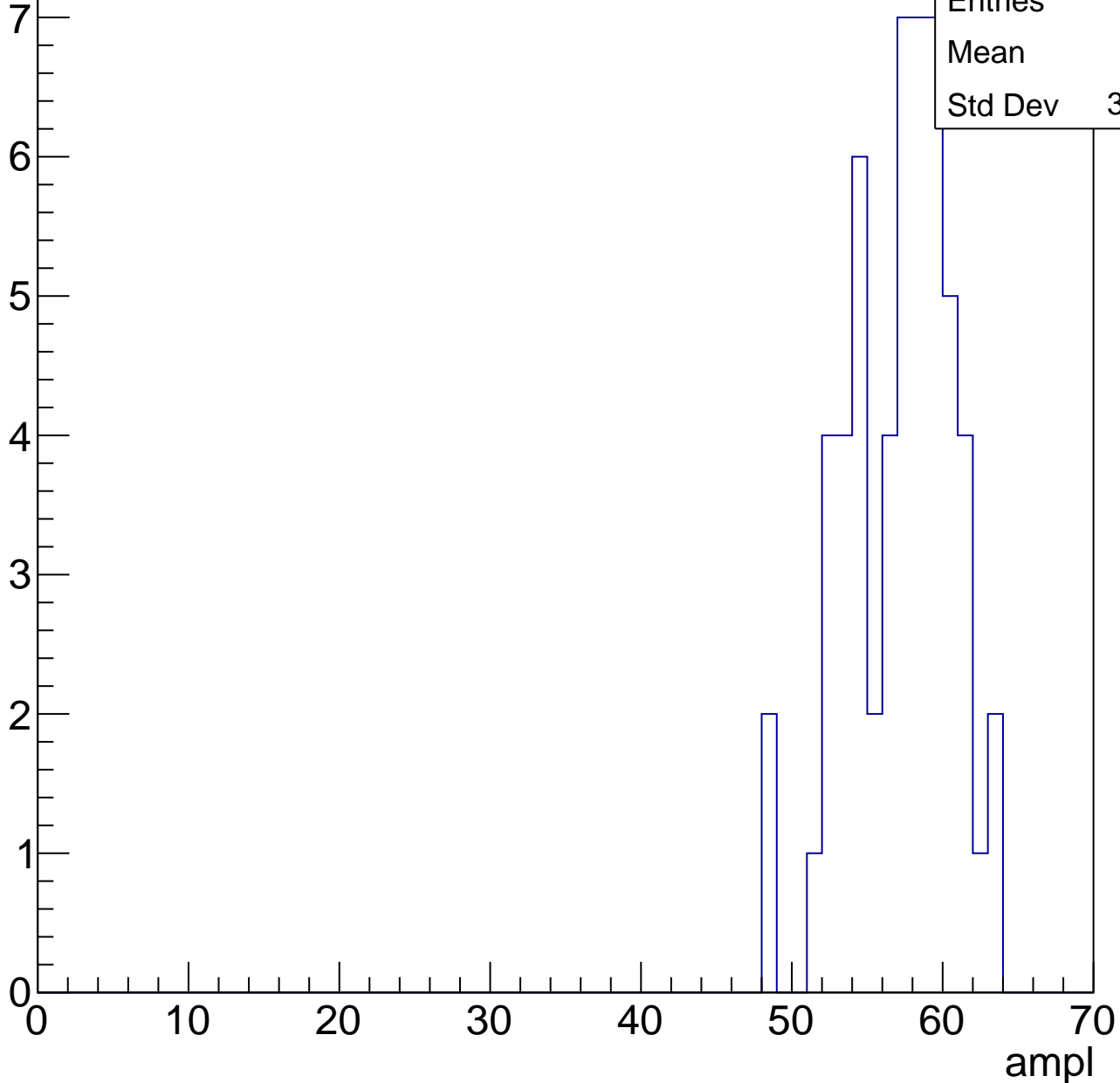


B1L102S, U26-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.7
Std Dev	3.443

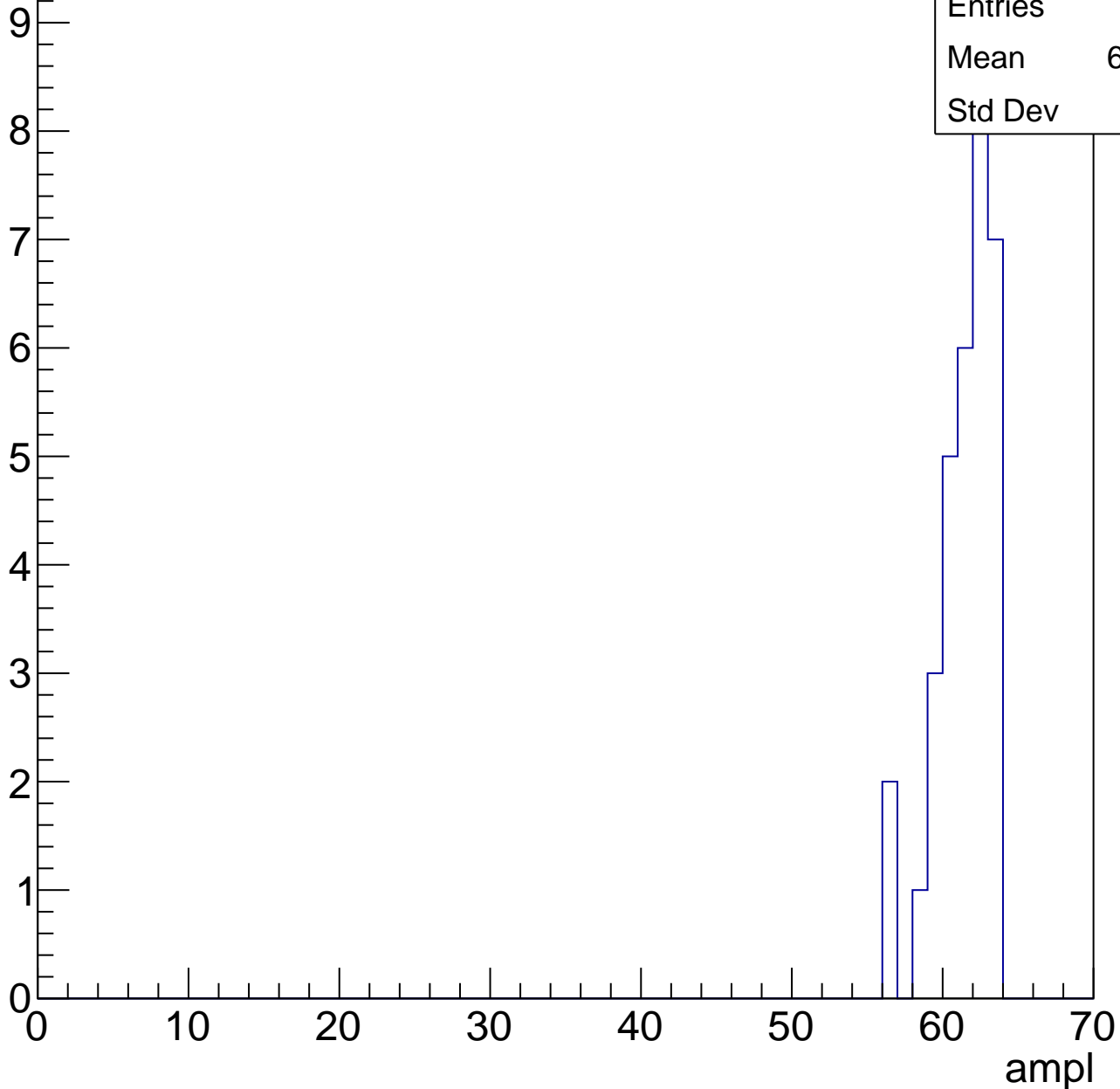


B1L102S, U26-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

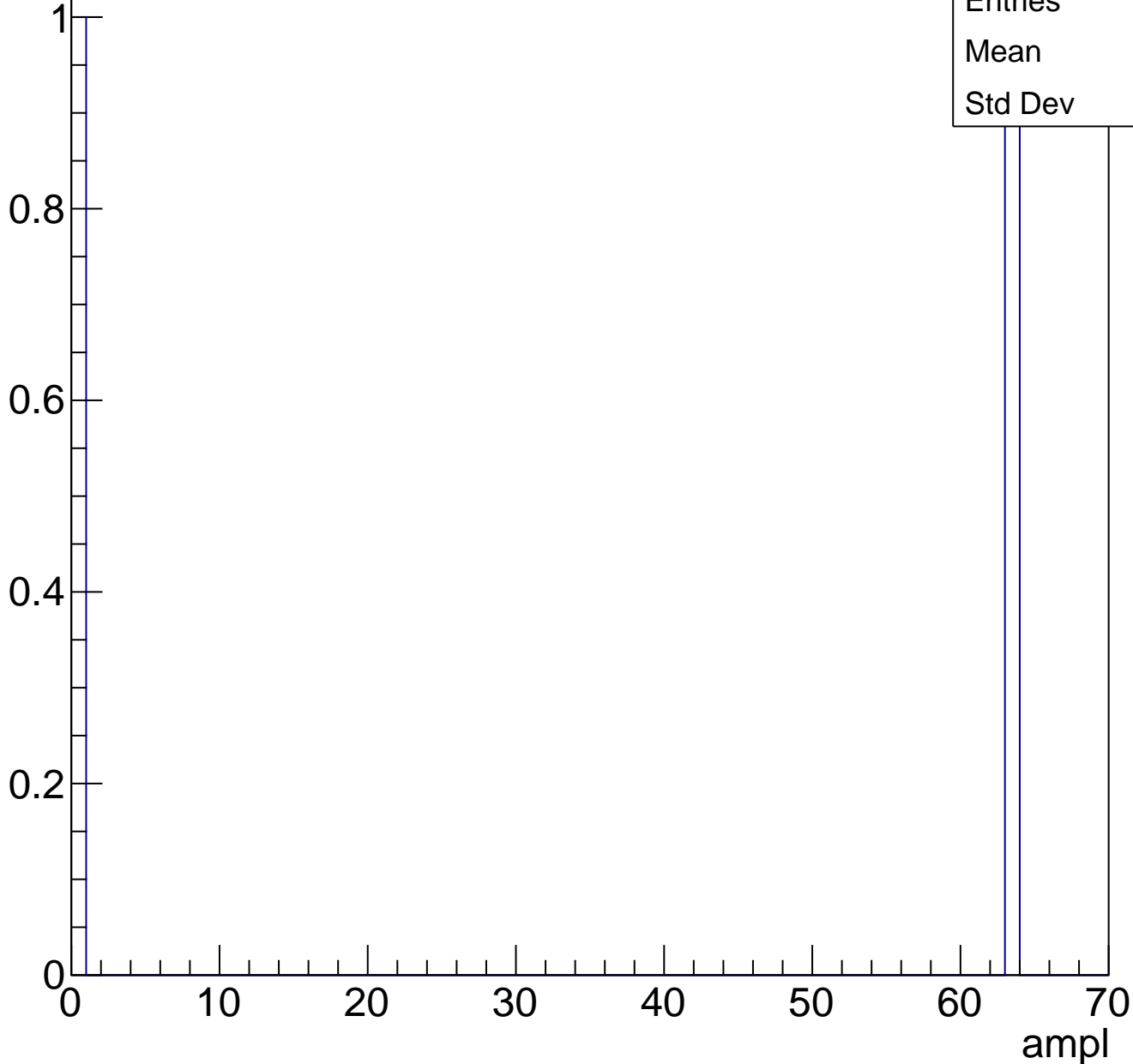
Entries	33
Mean	60.97
Std Dev	1.85



B1L102S, U26-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch80, adc0

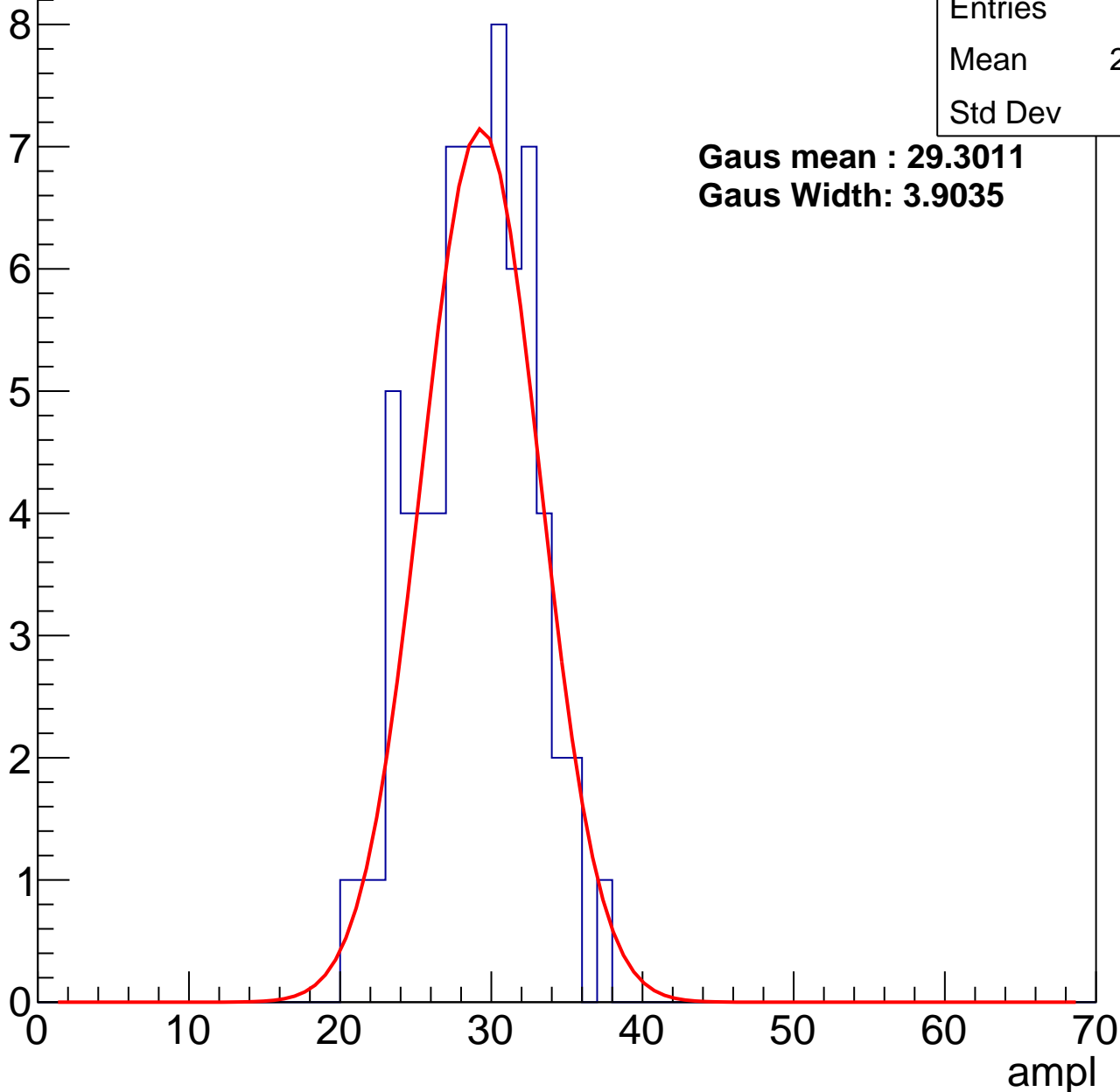
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.49
Std Dev	3.63

Gaus mean : 29.3011

Gaus Width: 3.9035



B1L102S, U26-ch80, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	37.05
Std Dev	3.586

Gaus mean : 37.6320

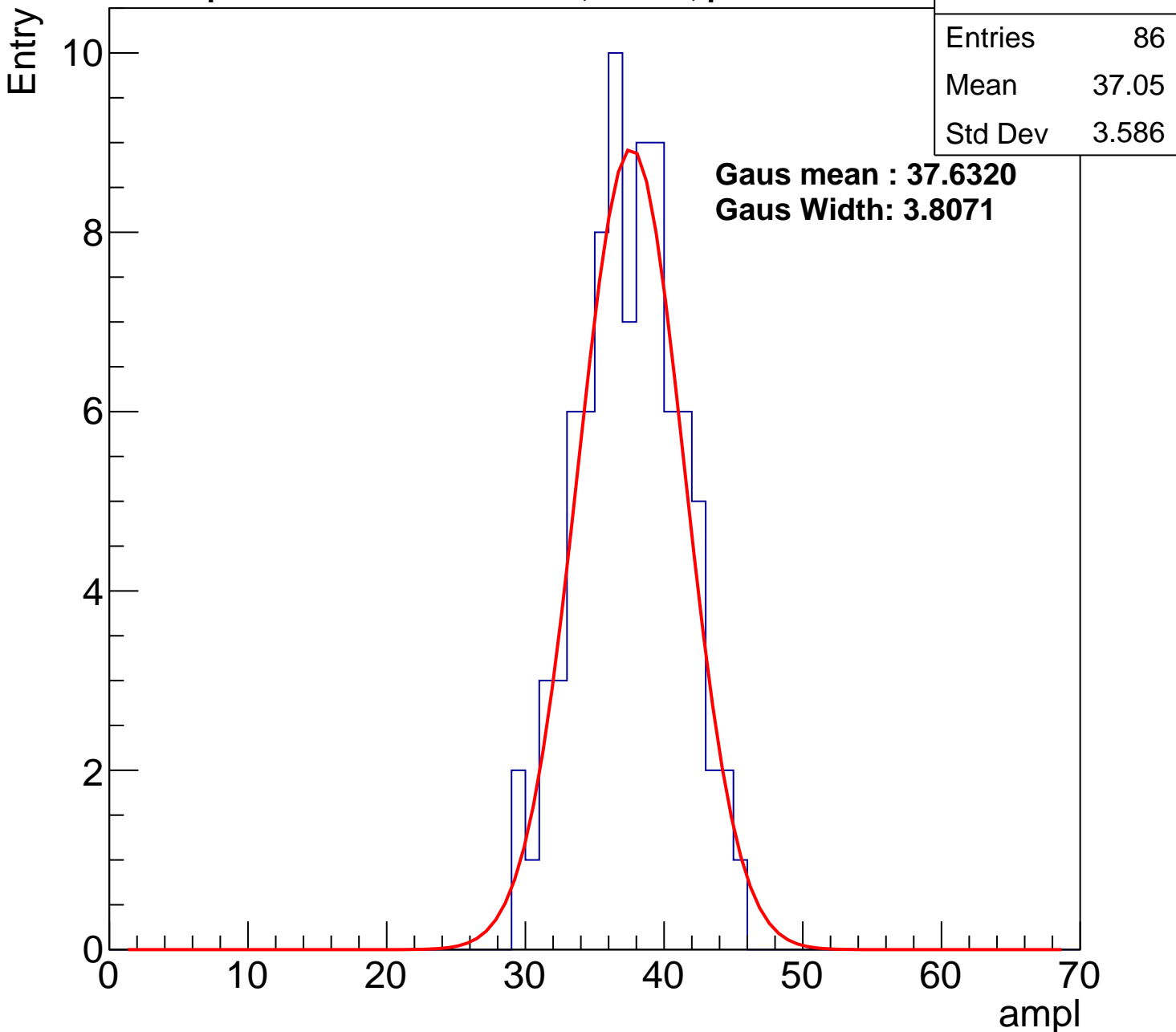
Gaus Width: 3.8071

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch80, adc2

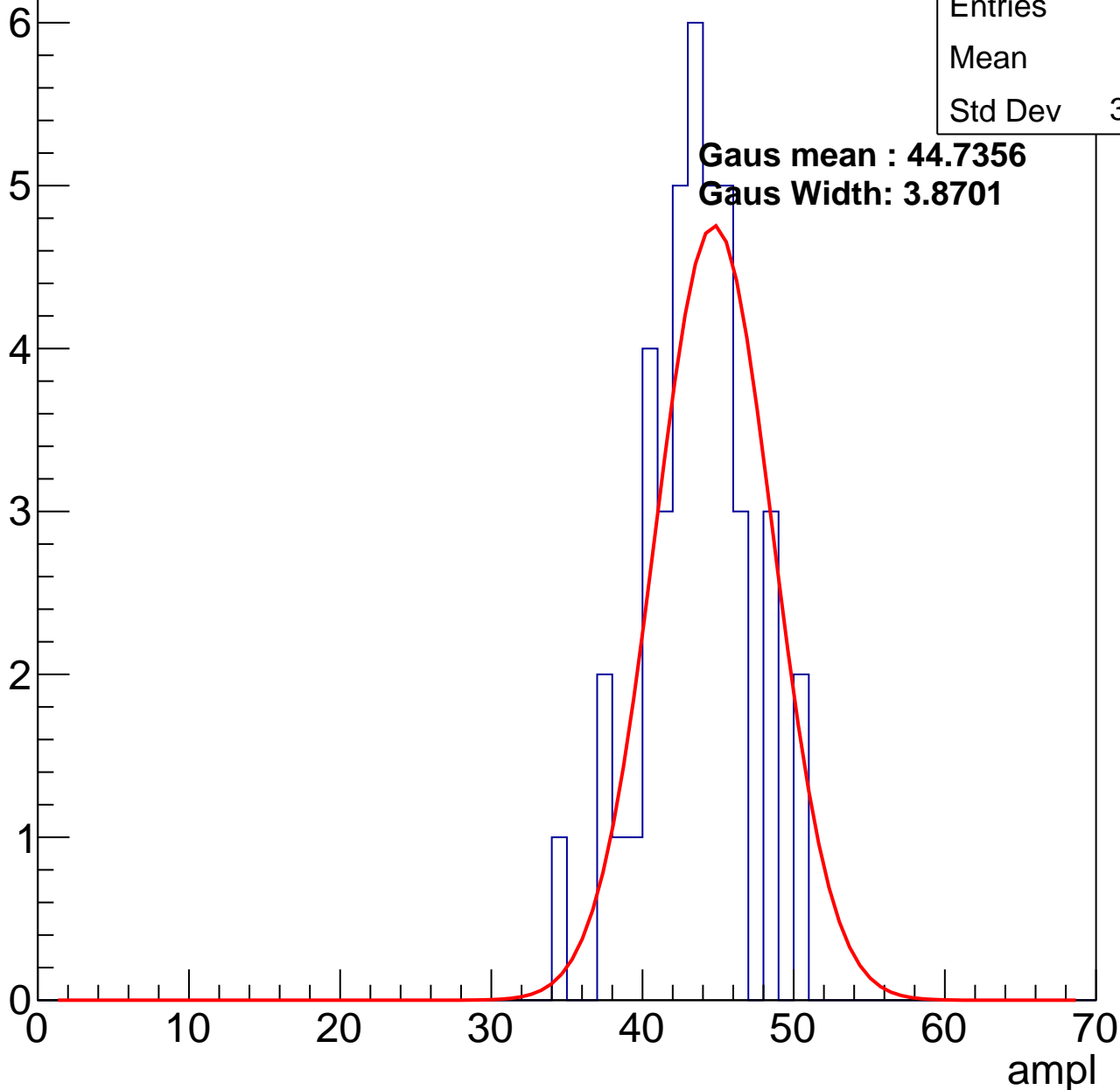
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	43
Std Dev	3.393

Gaus mean : 44.7356

Gaus Width: 3.8701



B1L102S, U26-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	48.97
Std Dev	3.378

Entry

10

8

6

4

2

0

0

10

20

30

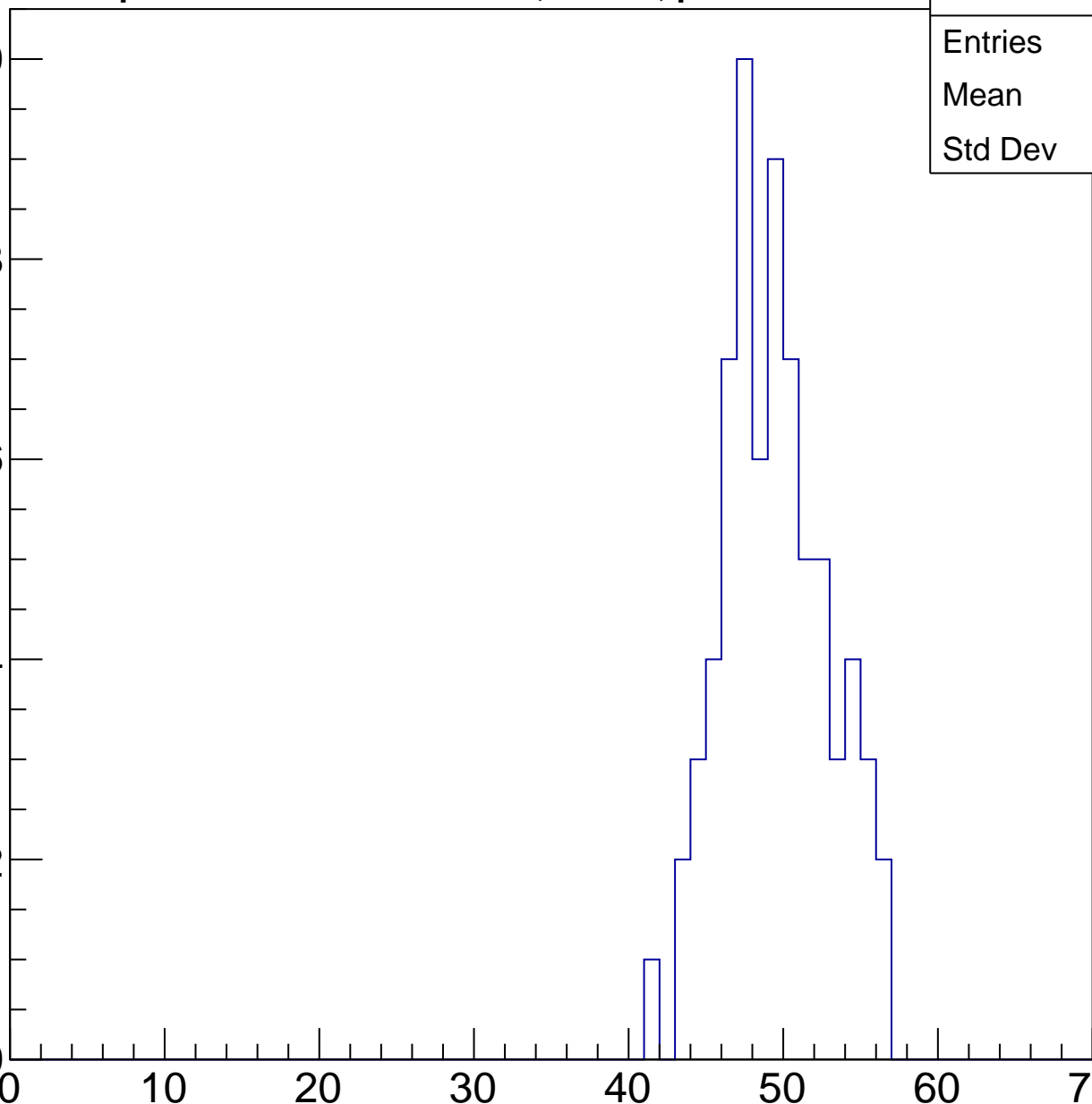
40

50

60

70

ampl

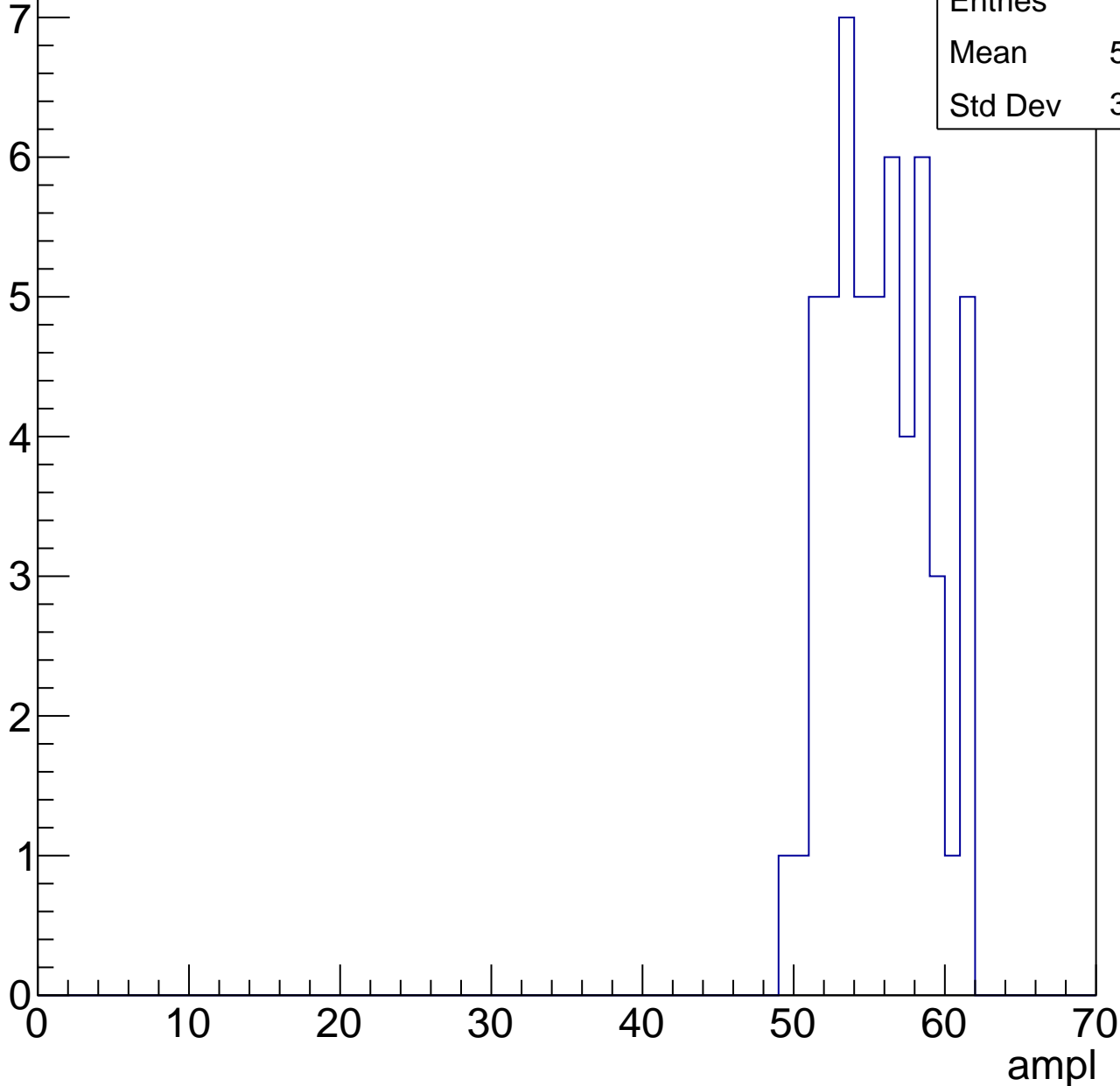


B1L102S, U26-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.26
Std Dev	3.175

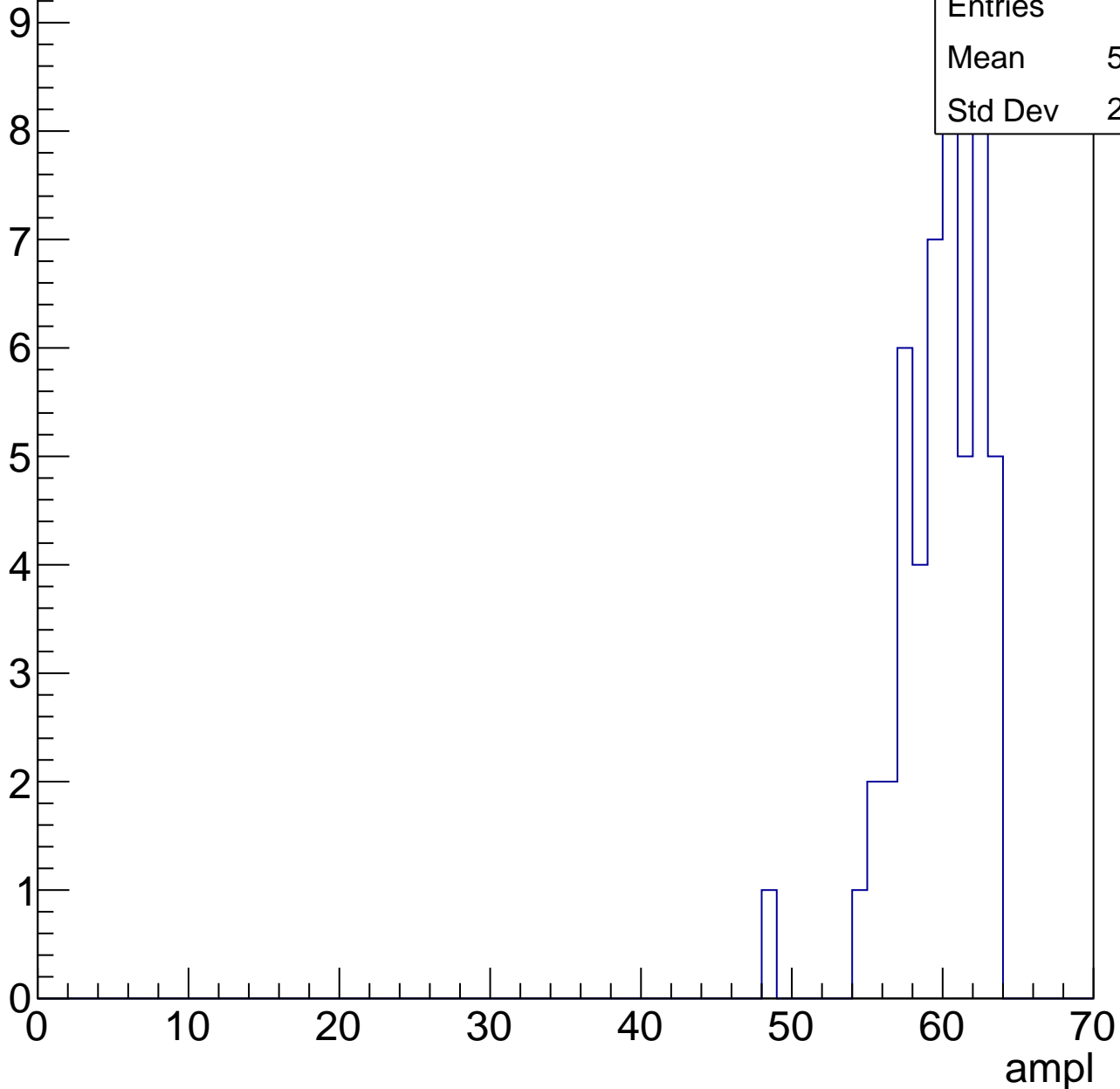


B1L102S, U26-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	59.38
Std Dev	2.849

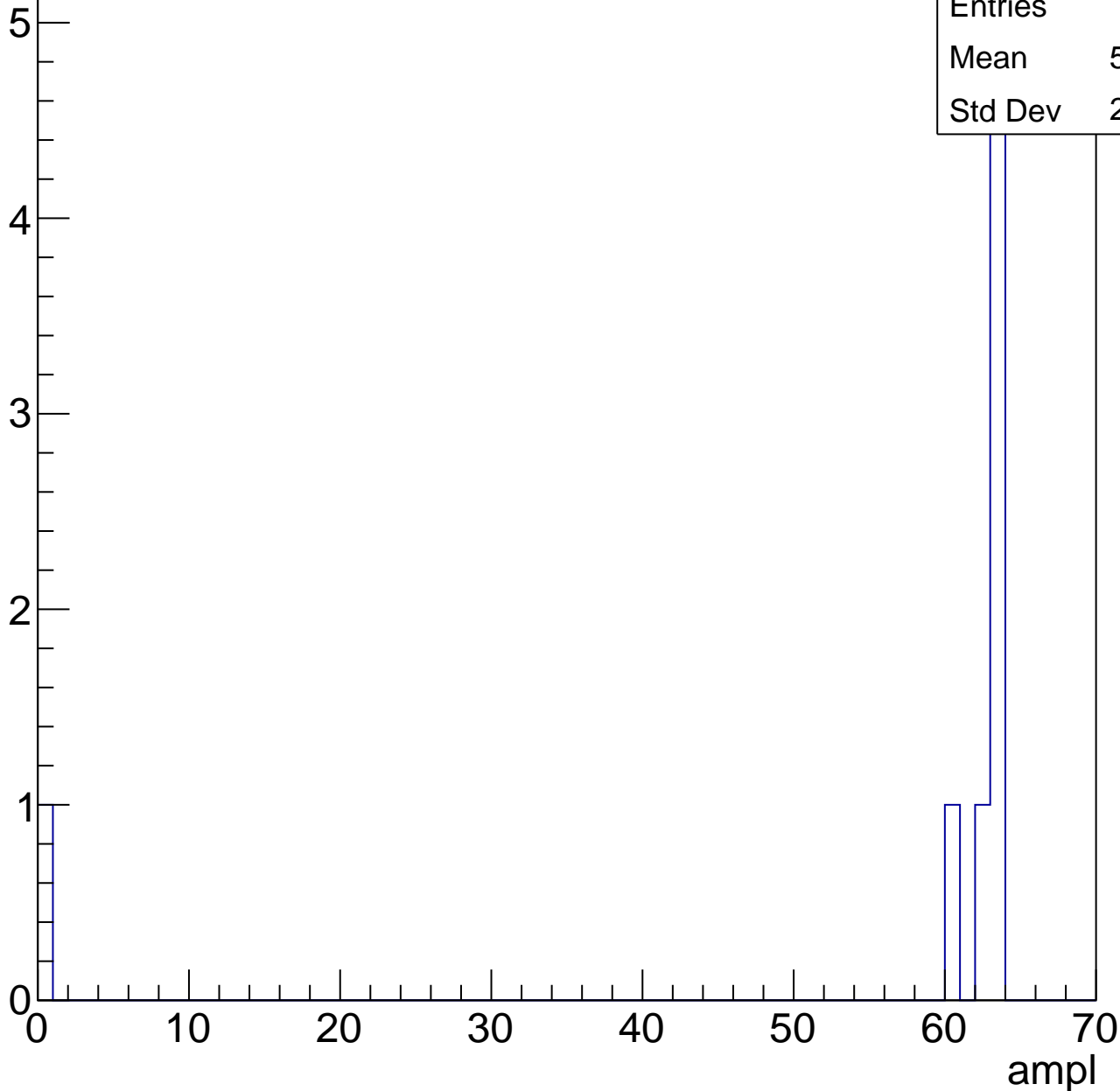


B1L102S, U26-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.62
Std Dev	20.67



B1L102S, U26-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch81, adc0

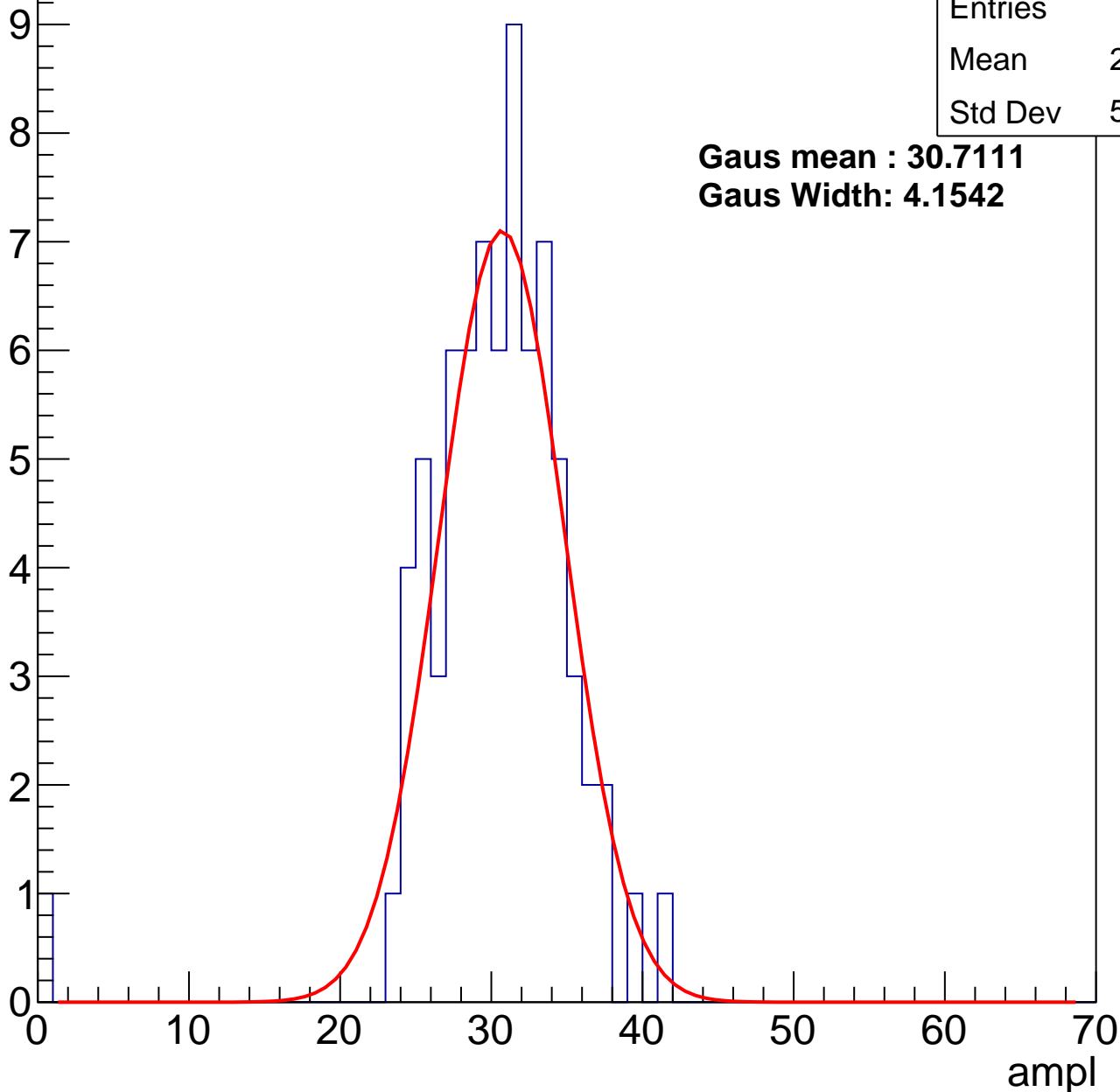
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.84
Std Dev	5.128

Gaus mean : 30.7111

Gaus Width: 4.1542



B1L102S, U26-ch81, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.87
Std Dev	3.075

Gaus mean : 37.0552

Gaus Width: 3.5182

10

8

6

4

2

0

0

10

20

30

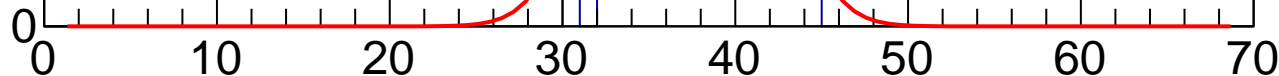
40

50

60

70

ampl



B1L102S, U26-ch81, adc2

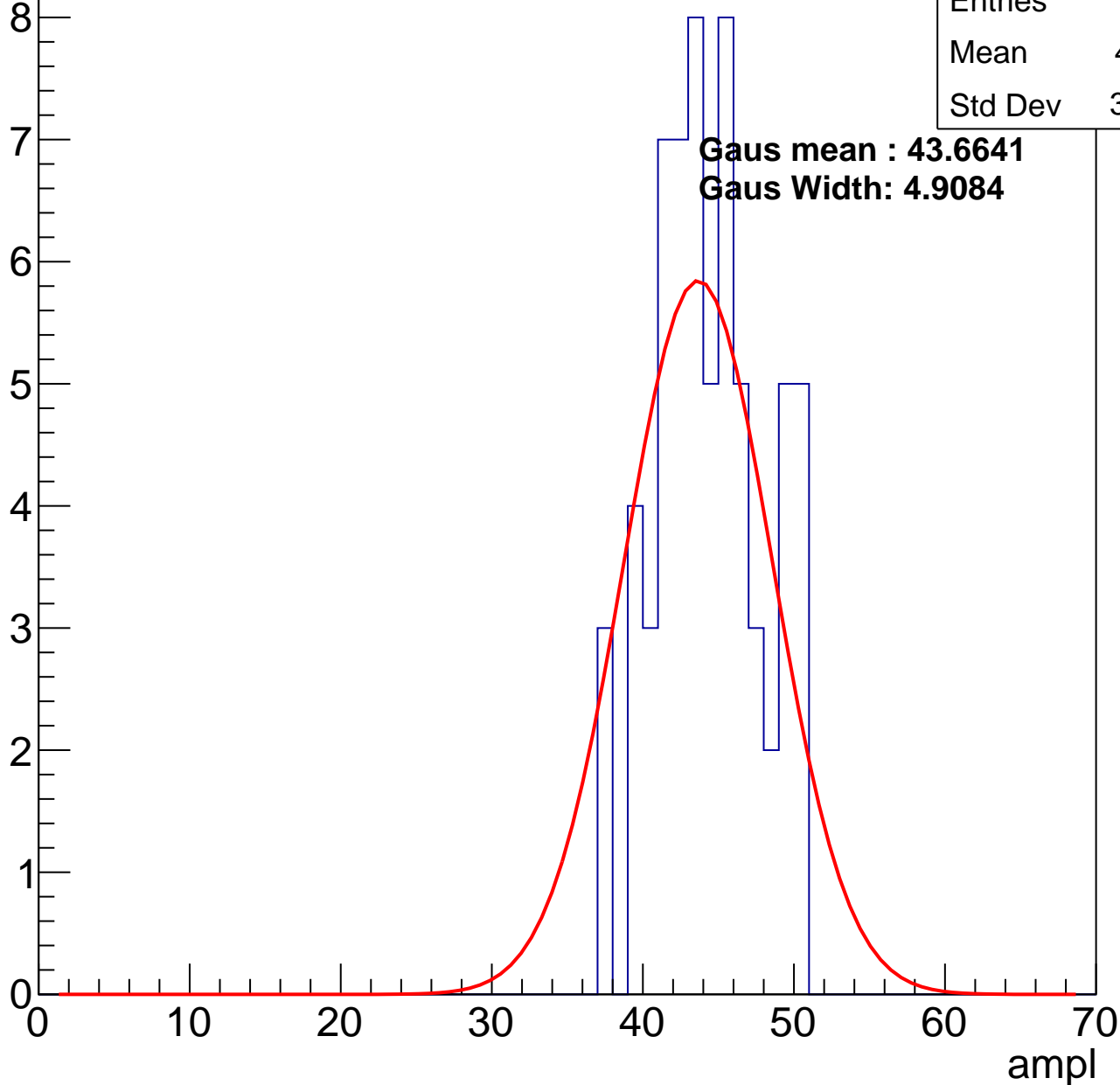
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.91
Std Dev	3.476

Gaus mean : 43.6641

Gaus Width: 4.9084

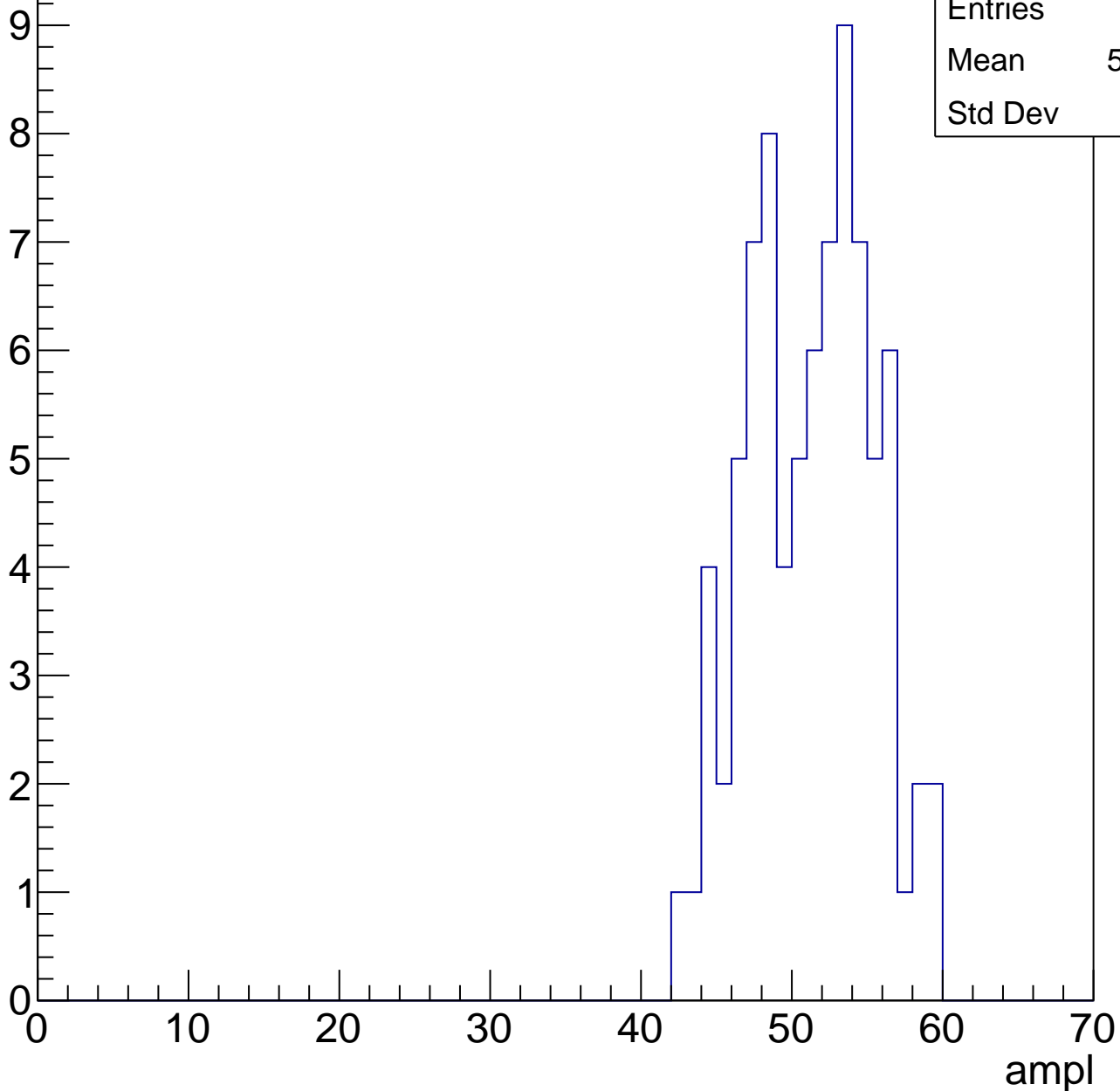


B1L102S, U26-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	50.82
Std Dev	4.04

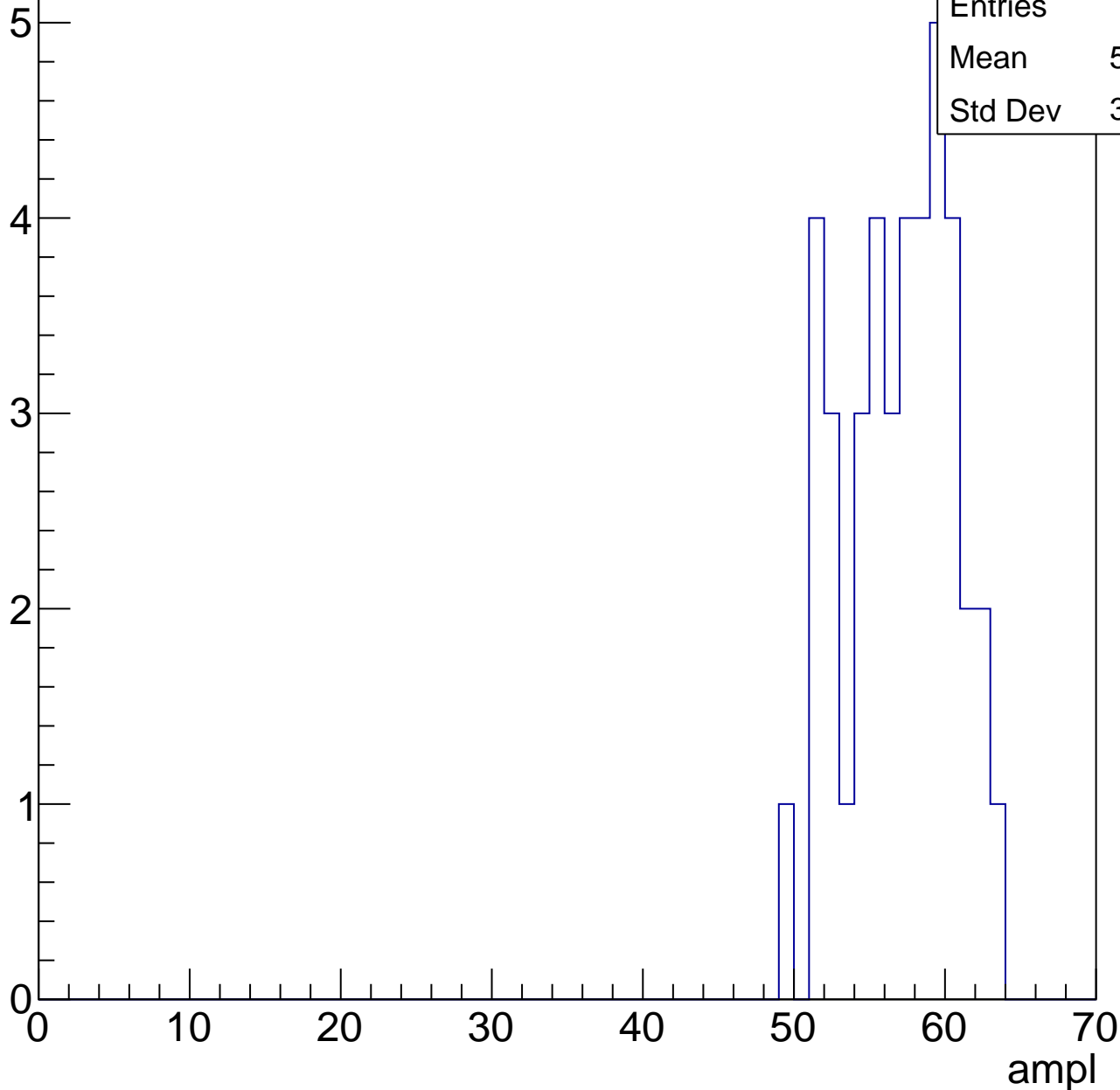


B1L102S, U26-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	56.49
Std Dev	3.535



B1L102S, U26-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

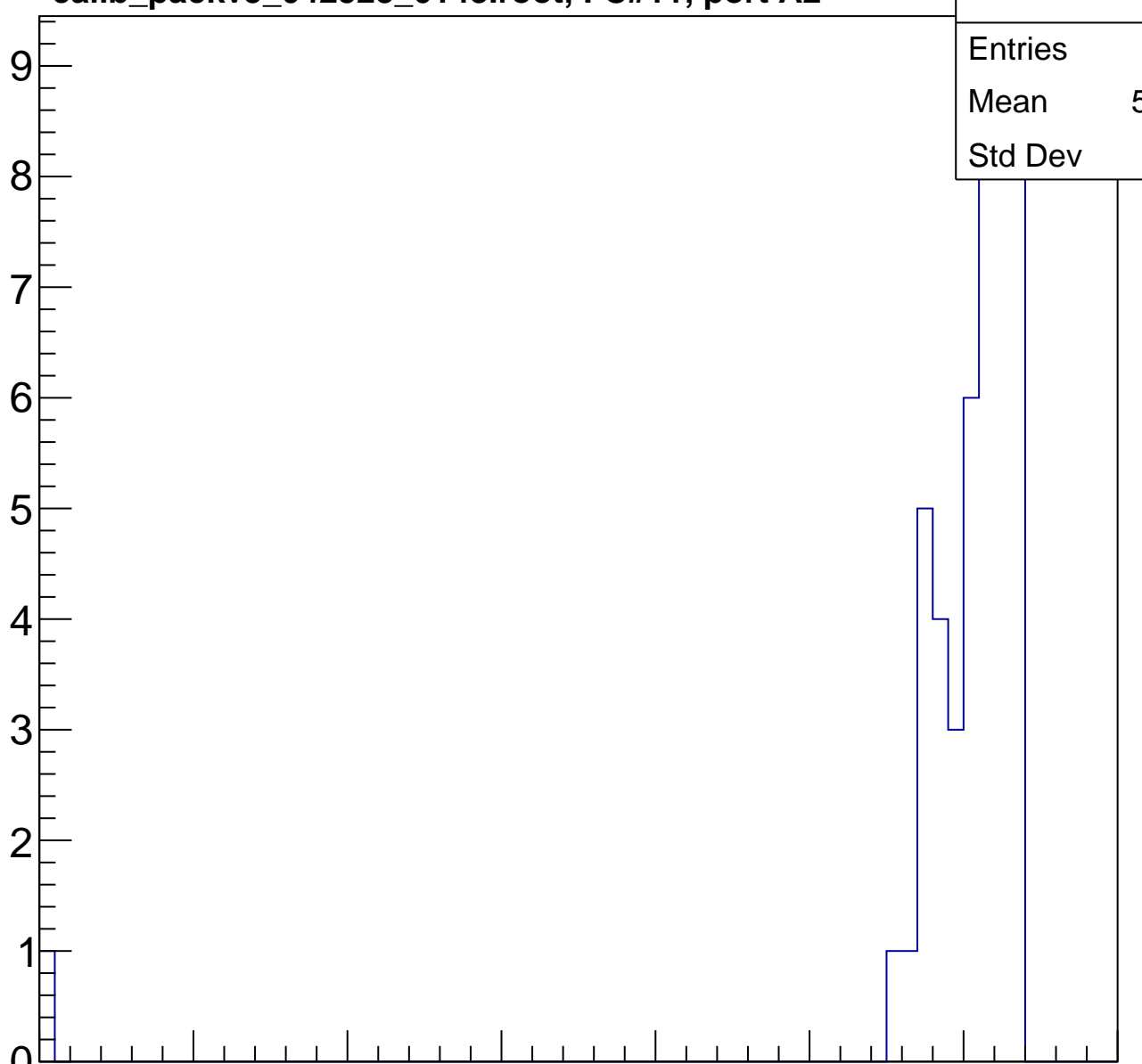
Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.04
Std Dev	9.07

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch82, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	28.13
Std Dev	4.95

Gaus mean : 28.5298

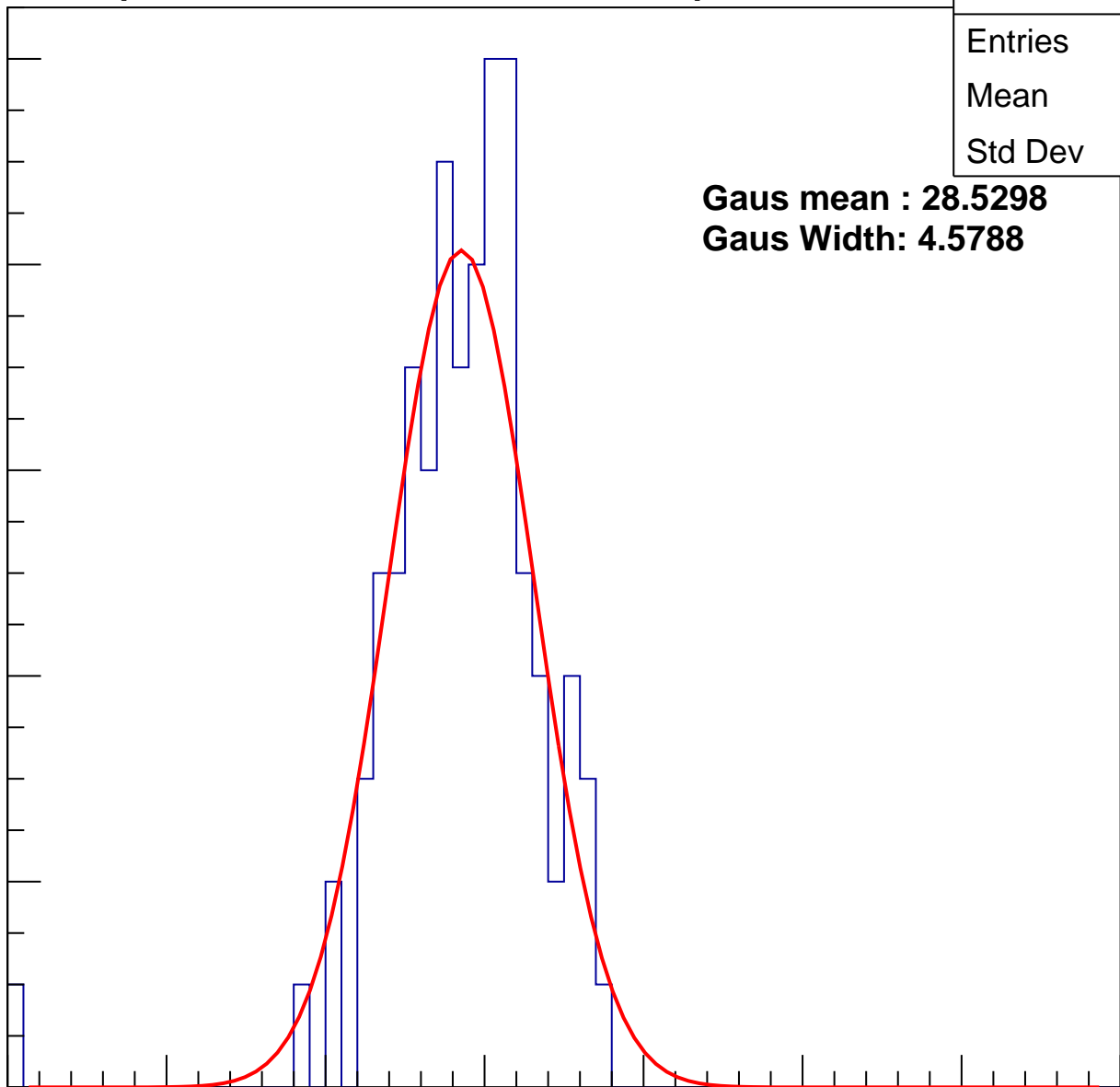
Gaus Width: 4.5788

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch82, adc1

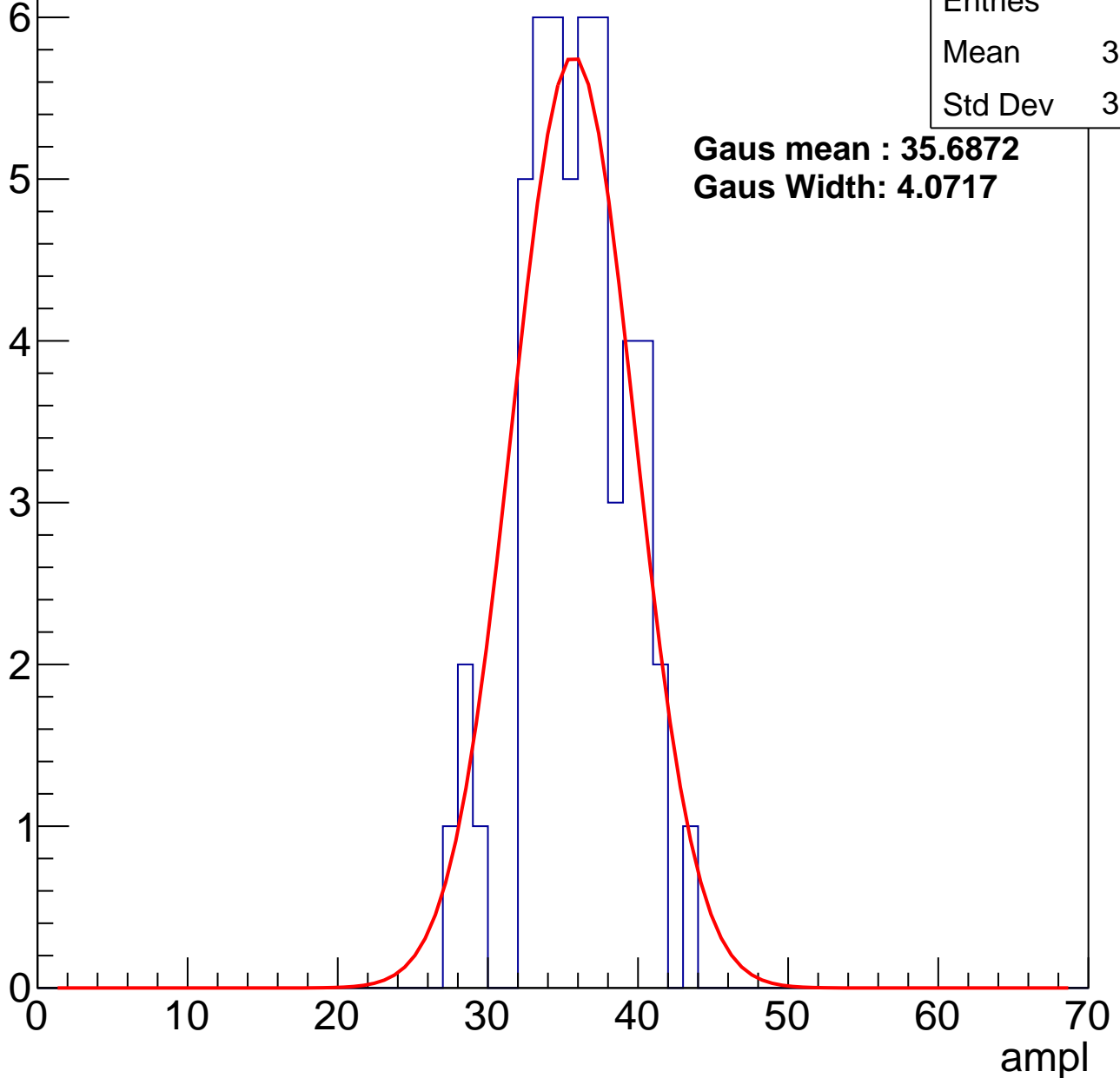
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.42
Std Dev	3.455

Gaus mean : 35.6872

Gaus Width: 4.0717



B1L102S, U26-ch82, adc2

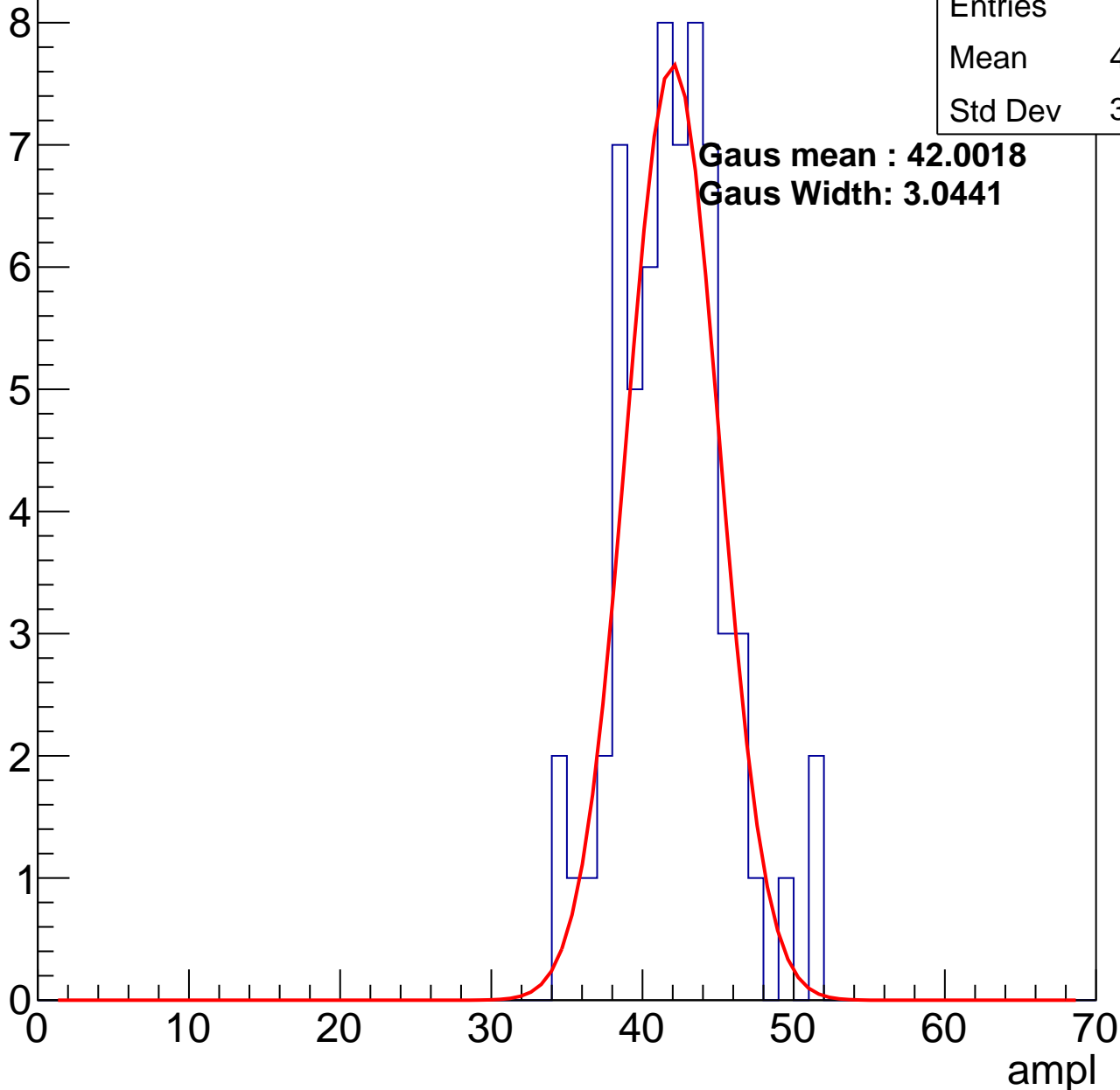
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.55
Std Dev	3.504

Gaus mean : 42.0018

Gaus Width: 3.0441

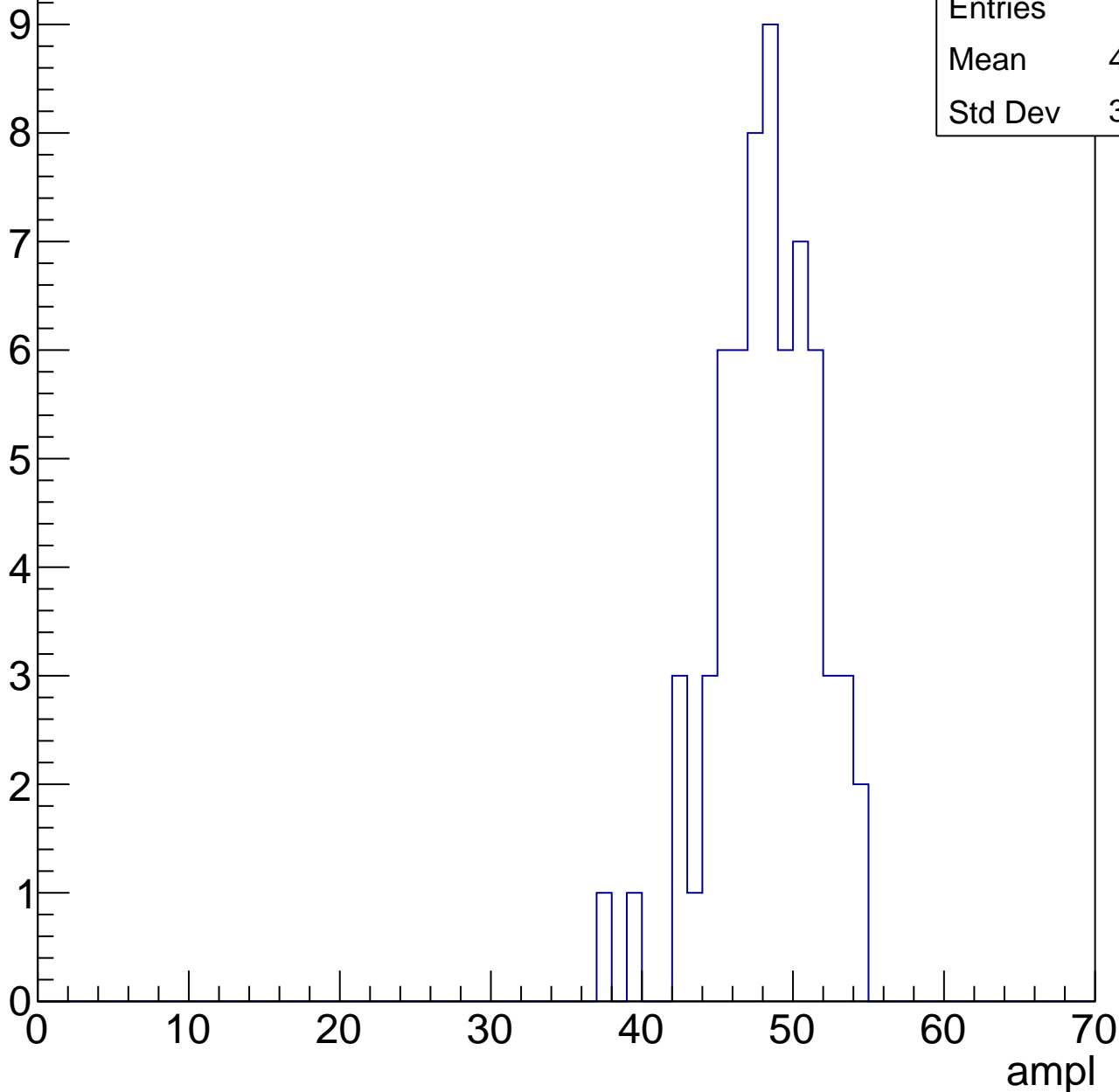


B1L102S, U26-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.75
Std Dev	3.392

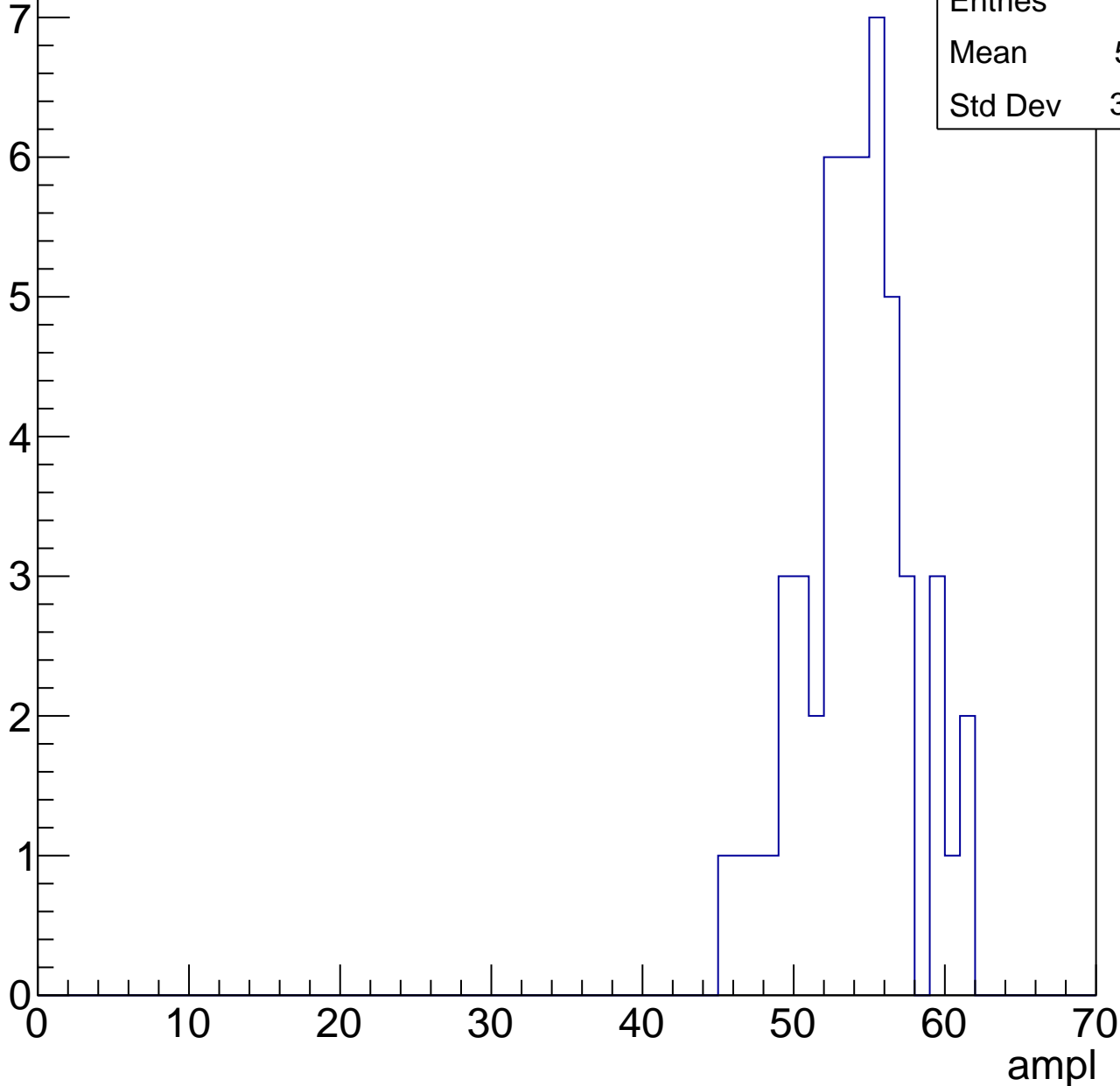


B1L102S, U26-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	53.61
Std Dev	3.587

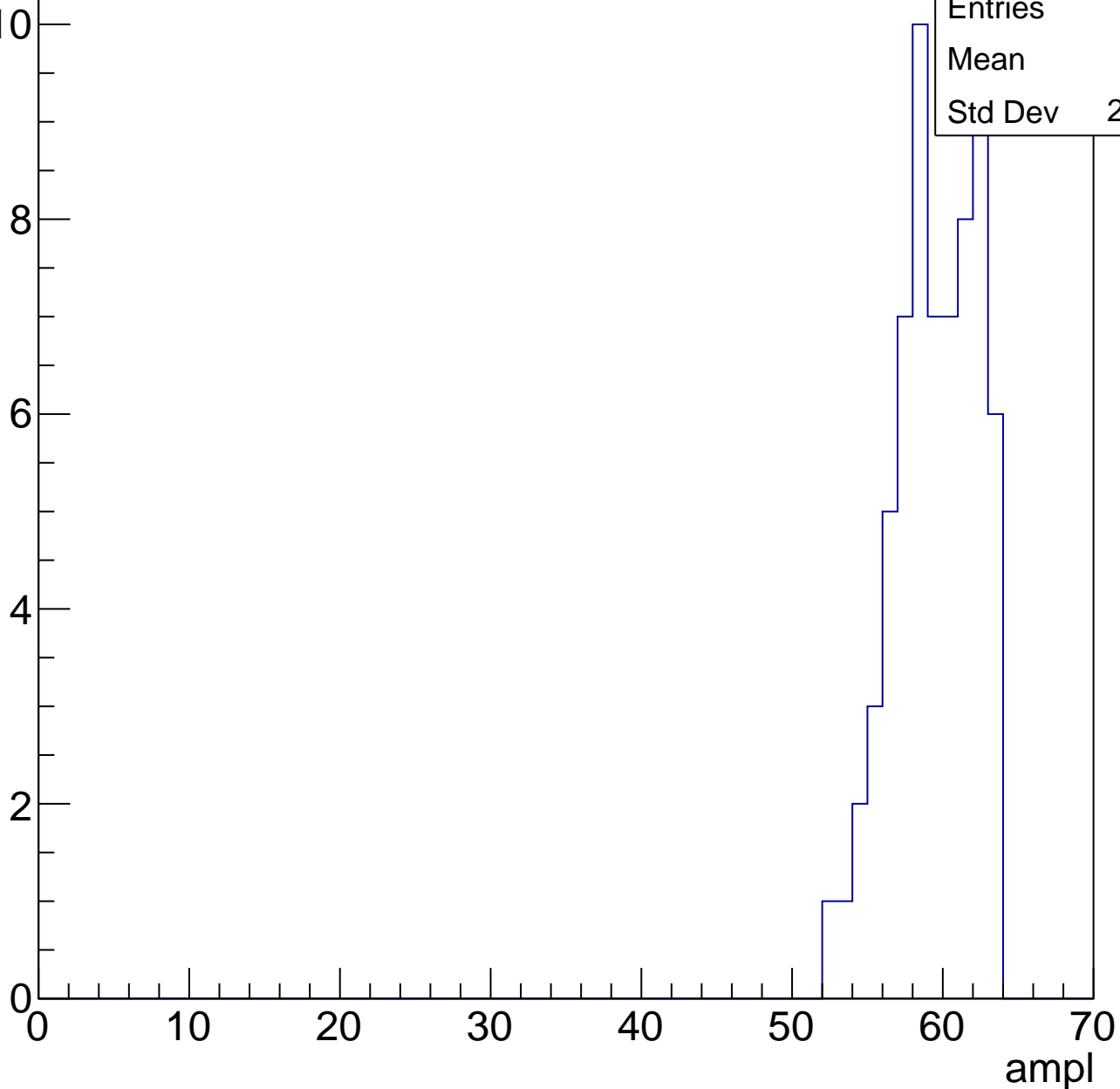


B1L102S, U26-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

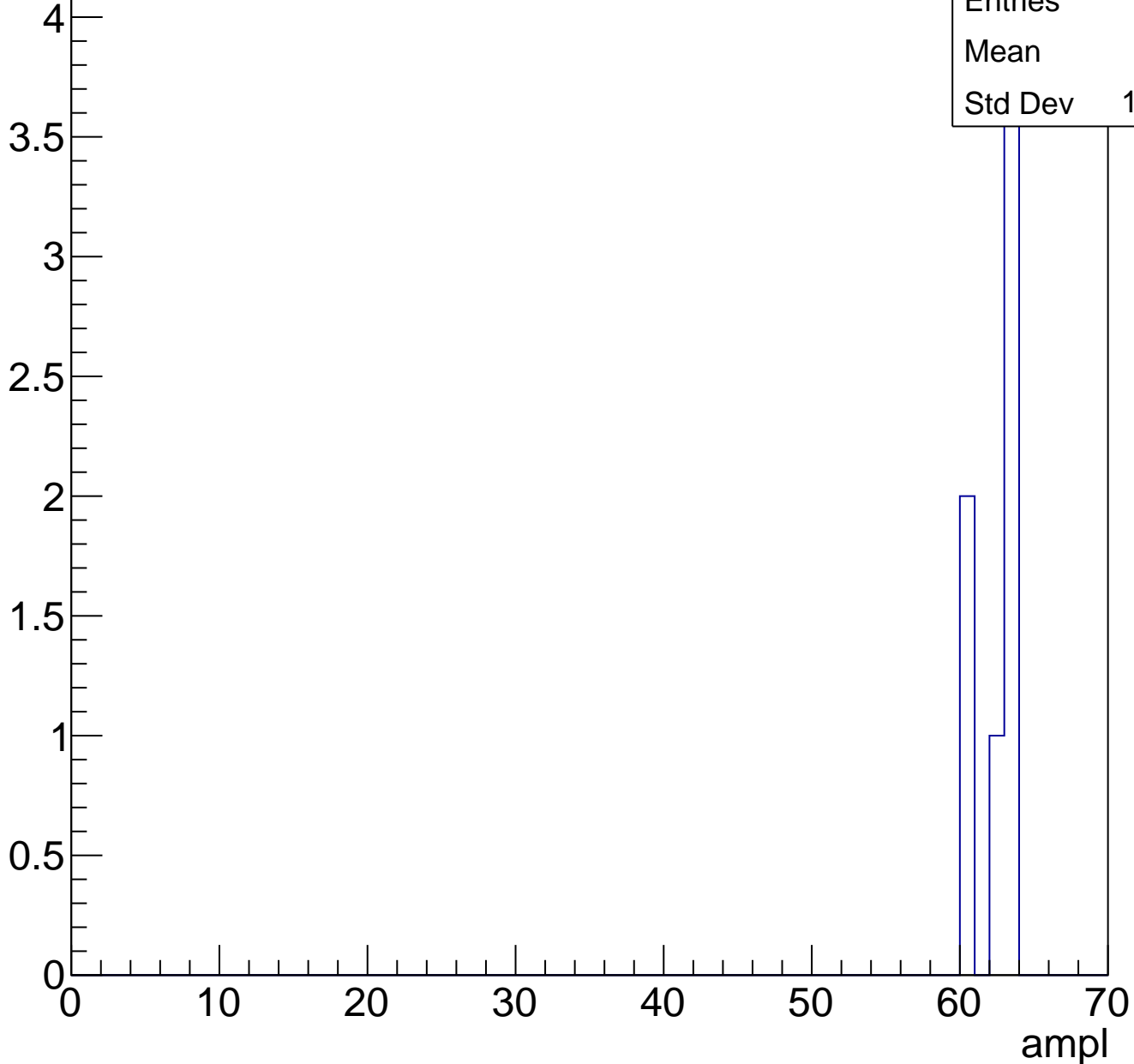
Entries	66
Mean	59
Std Dev	2.702



B1L102S, U26-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch83, adc0

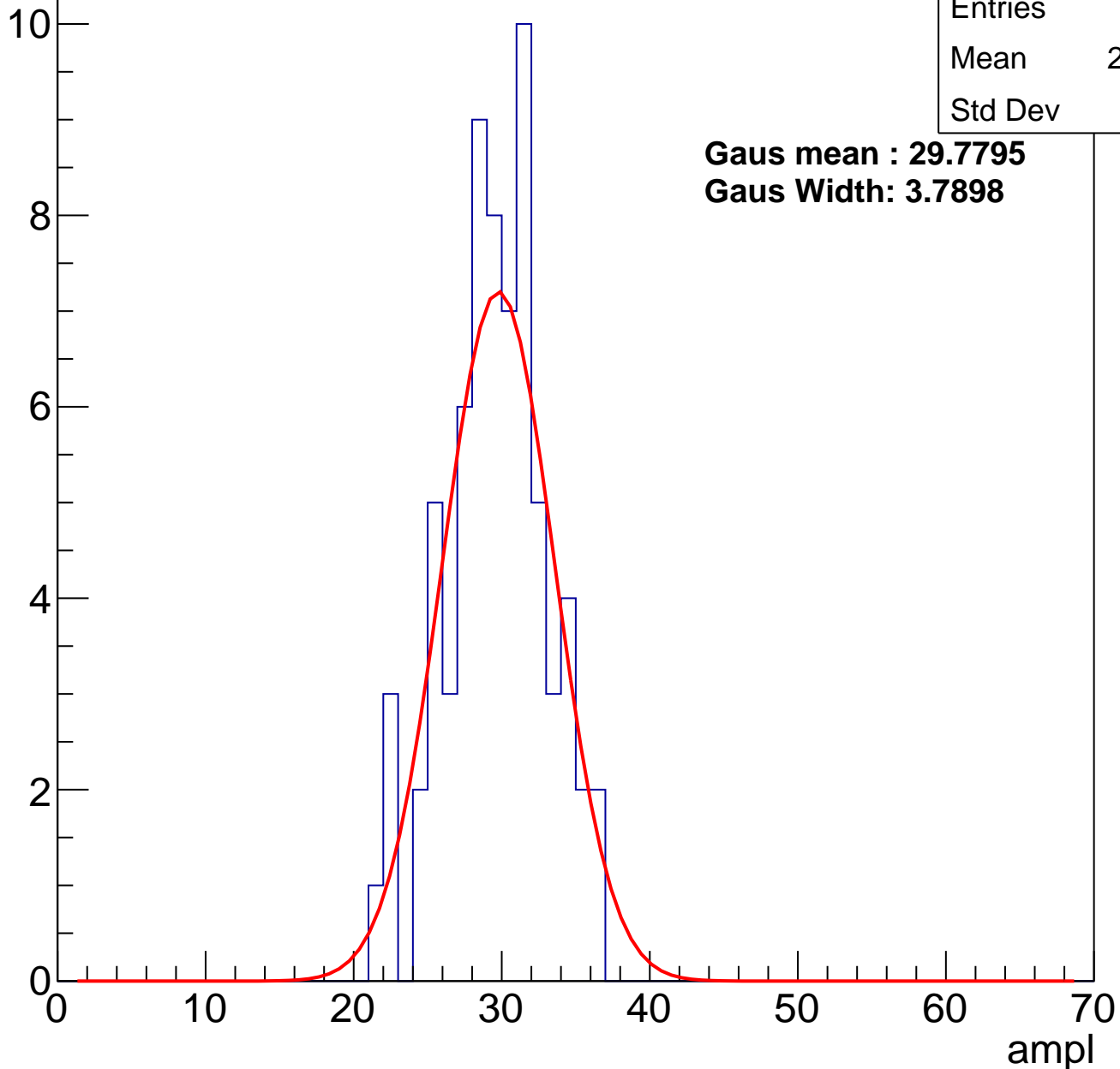
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	29.16
Std Dev	3.4

Gaus mean : 29.7795

Gaus Width: 3.7898

Entry



B1L102S, U26-ch83, adc1

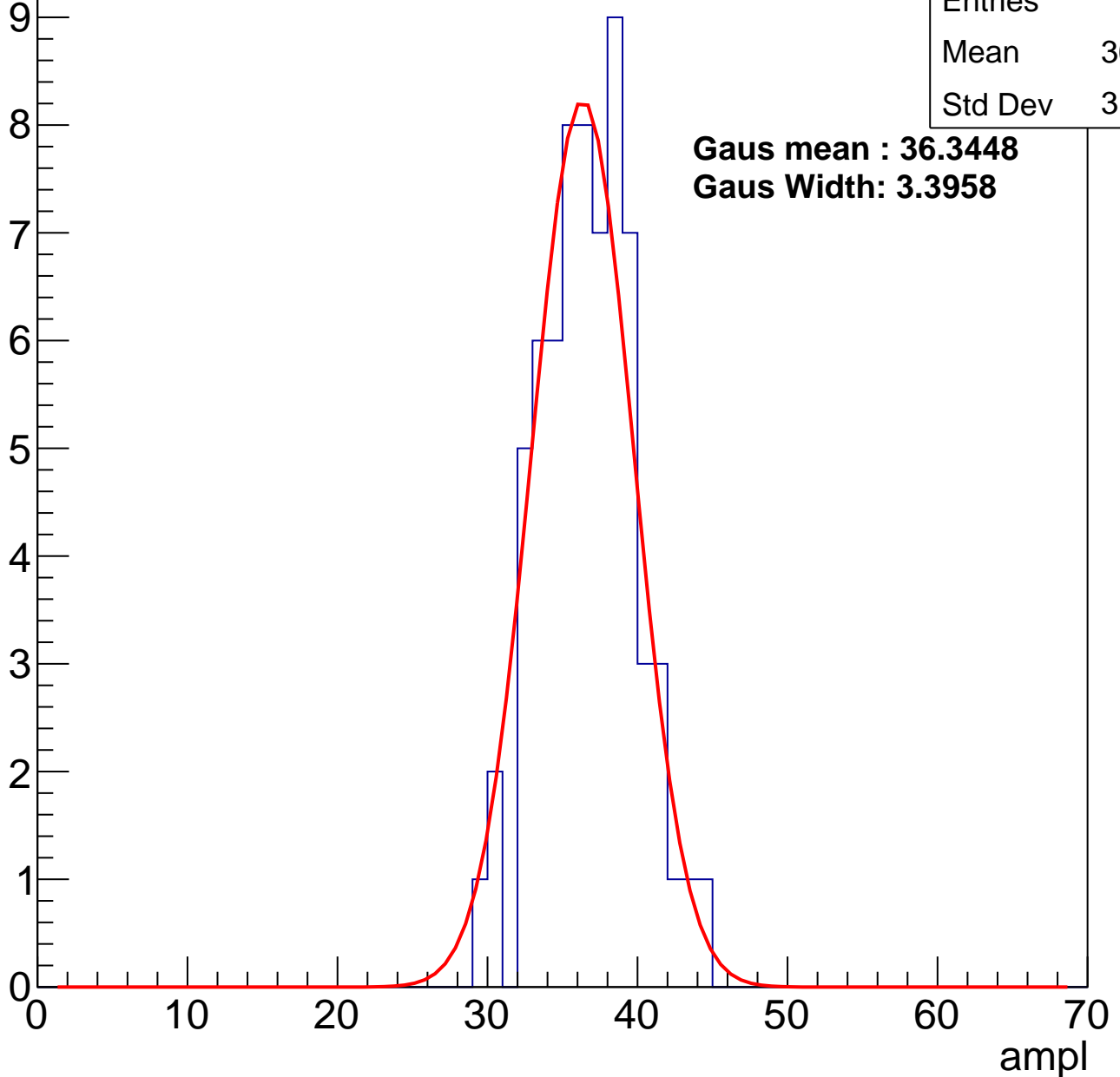
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.25
Std Dev	3.117

Gaus mean : 36.3448

Gaus Width: 3.3958



B1L102S, U26-ch83, adc2

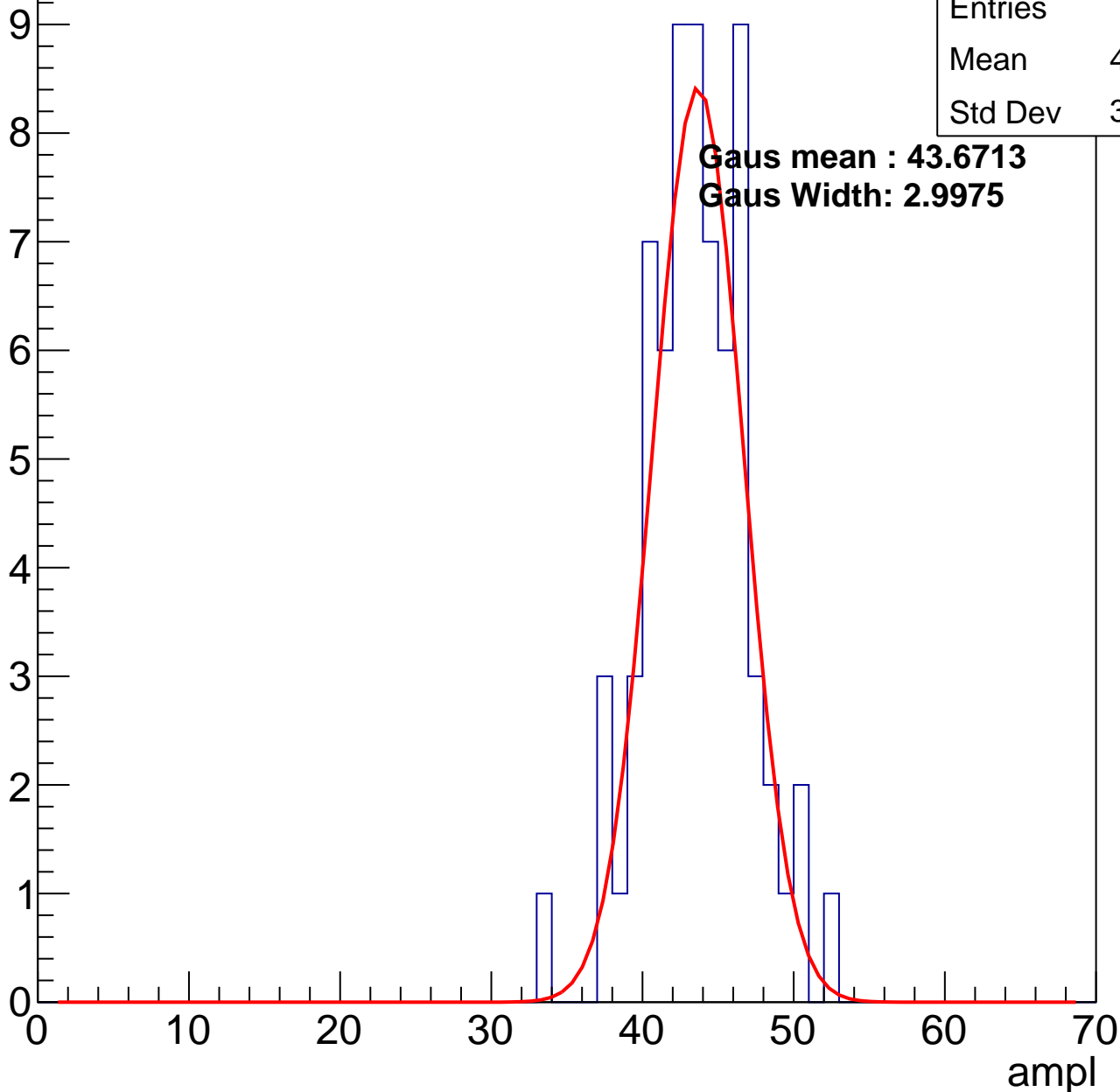
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.14
Std Dev	3.382

Gaus mean : 43.6713

Gaus Width: 2.9975

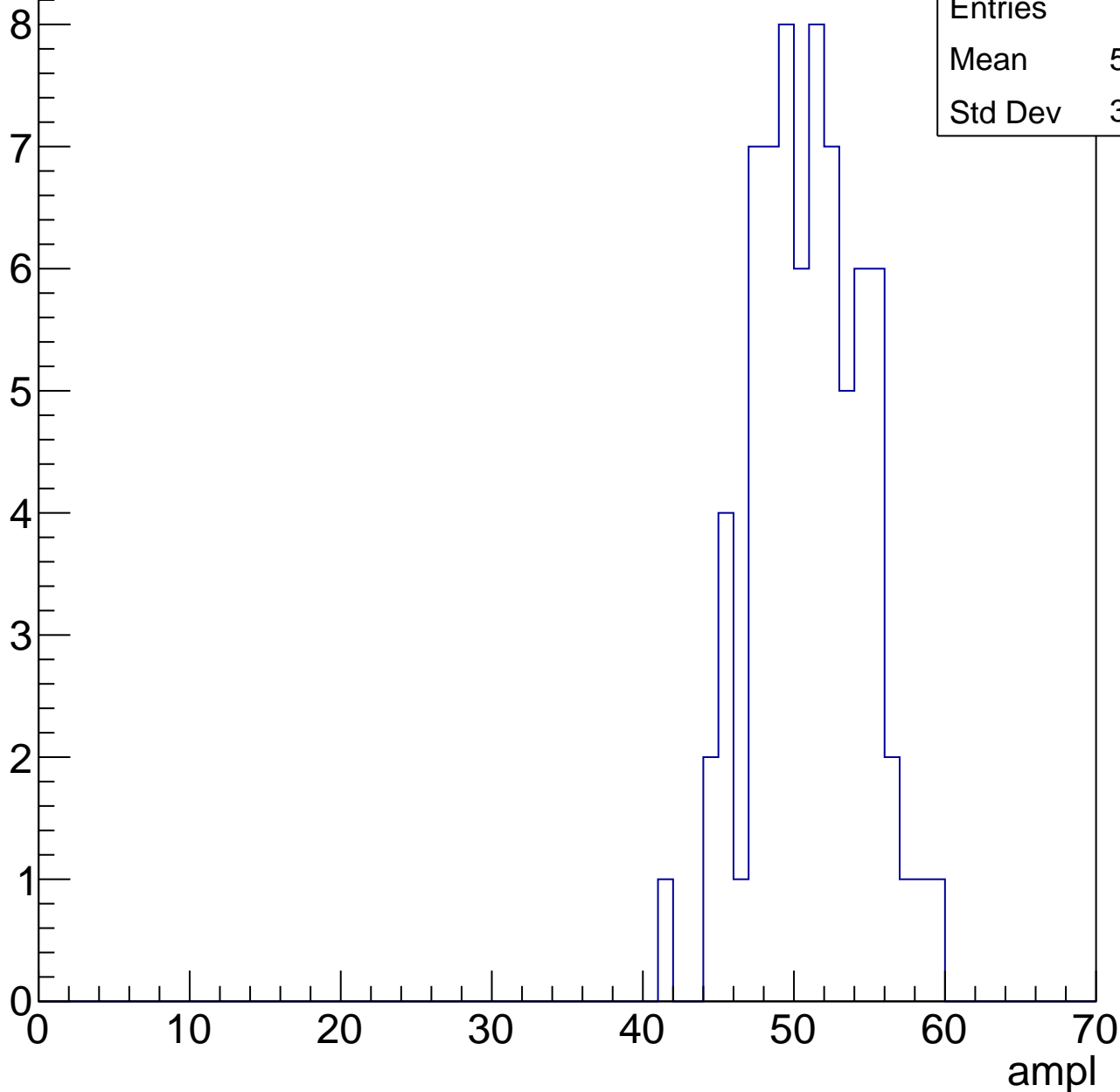


B1L102S, U26-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.53
Std Dev	3.589

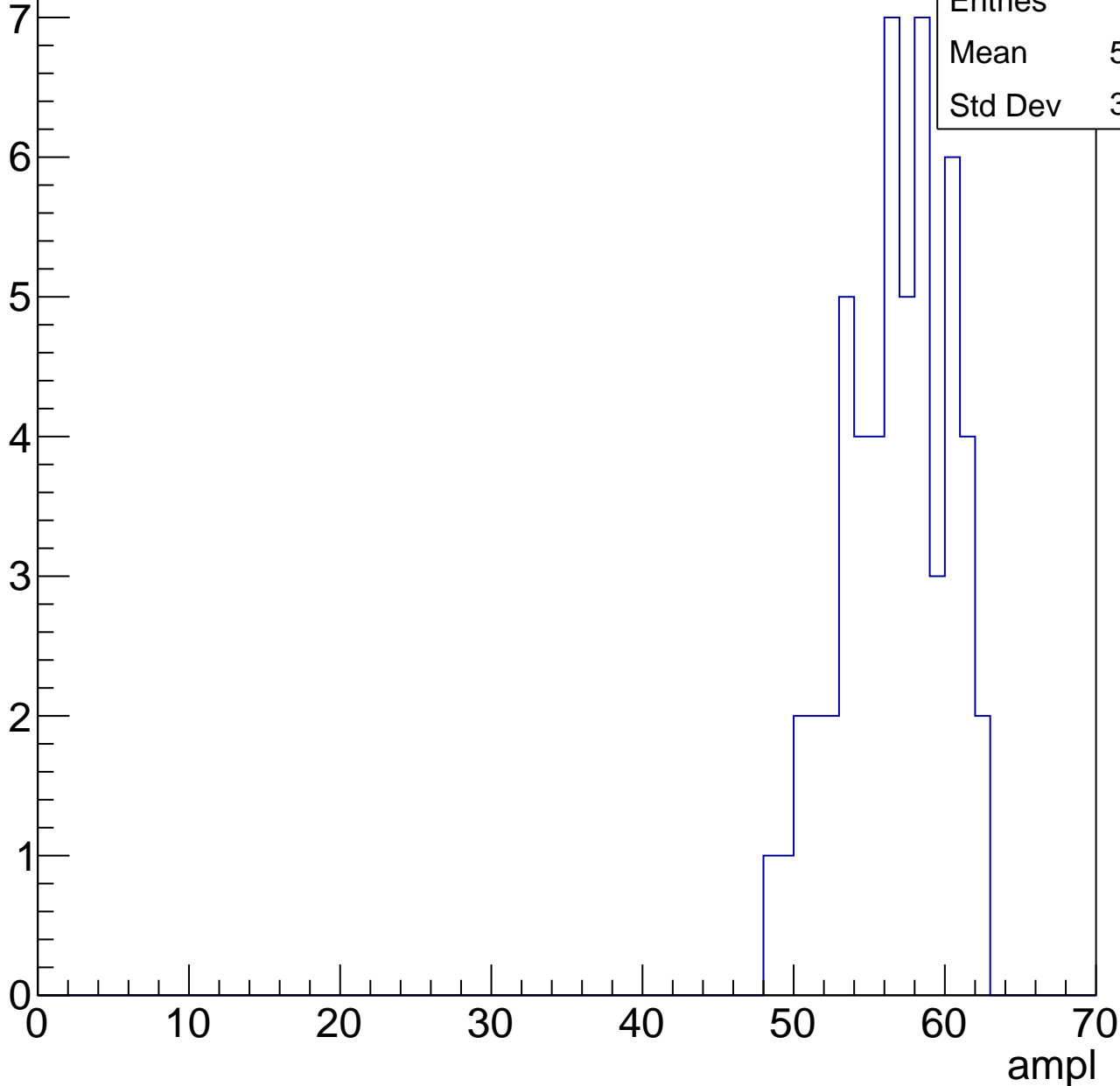


B1L102S, U26-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.22
Std Dev	3.457

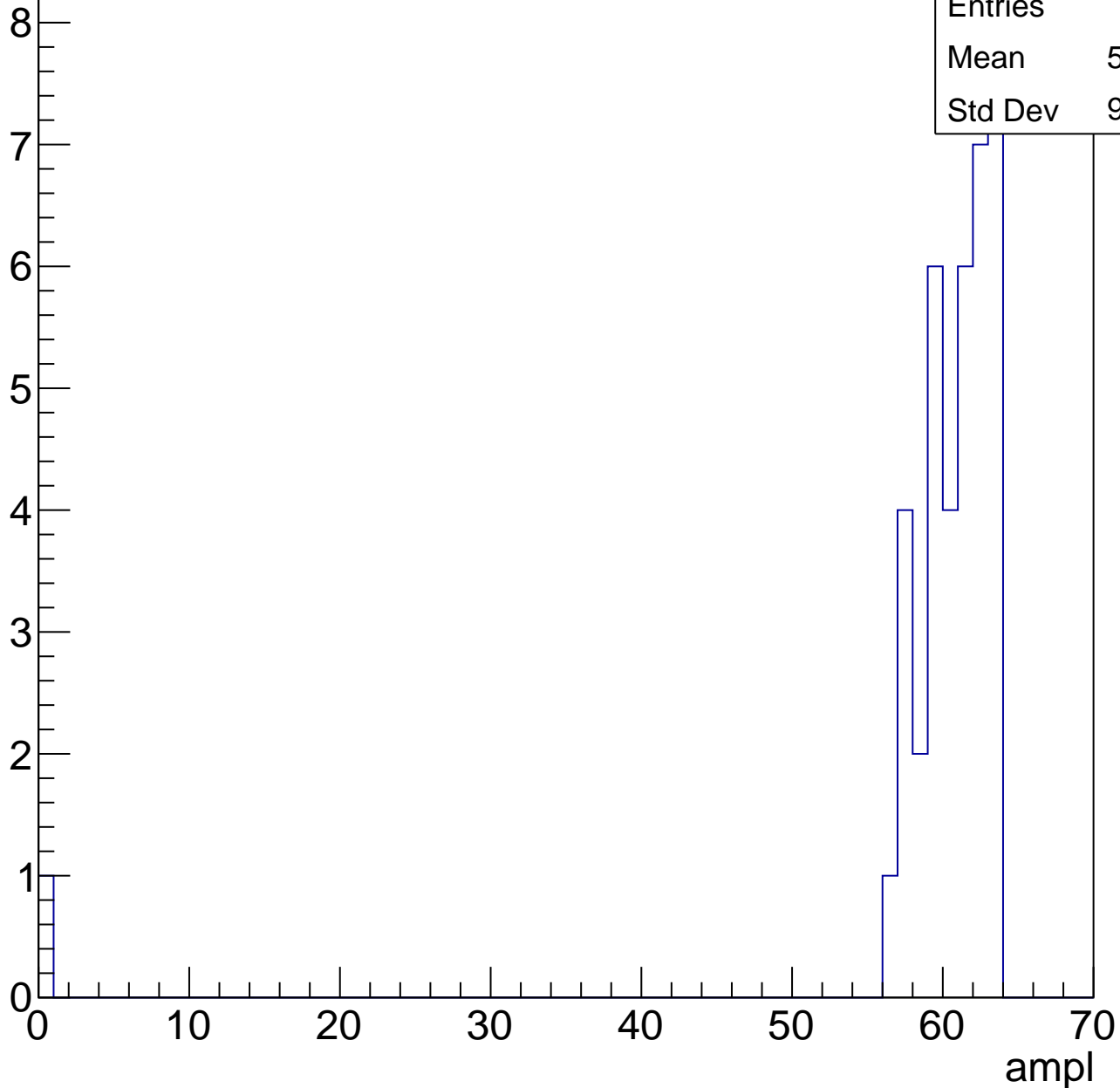


B1L102S, U26-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.92
Std Dev	9.775



B1L102S, U26-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

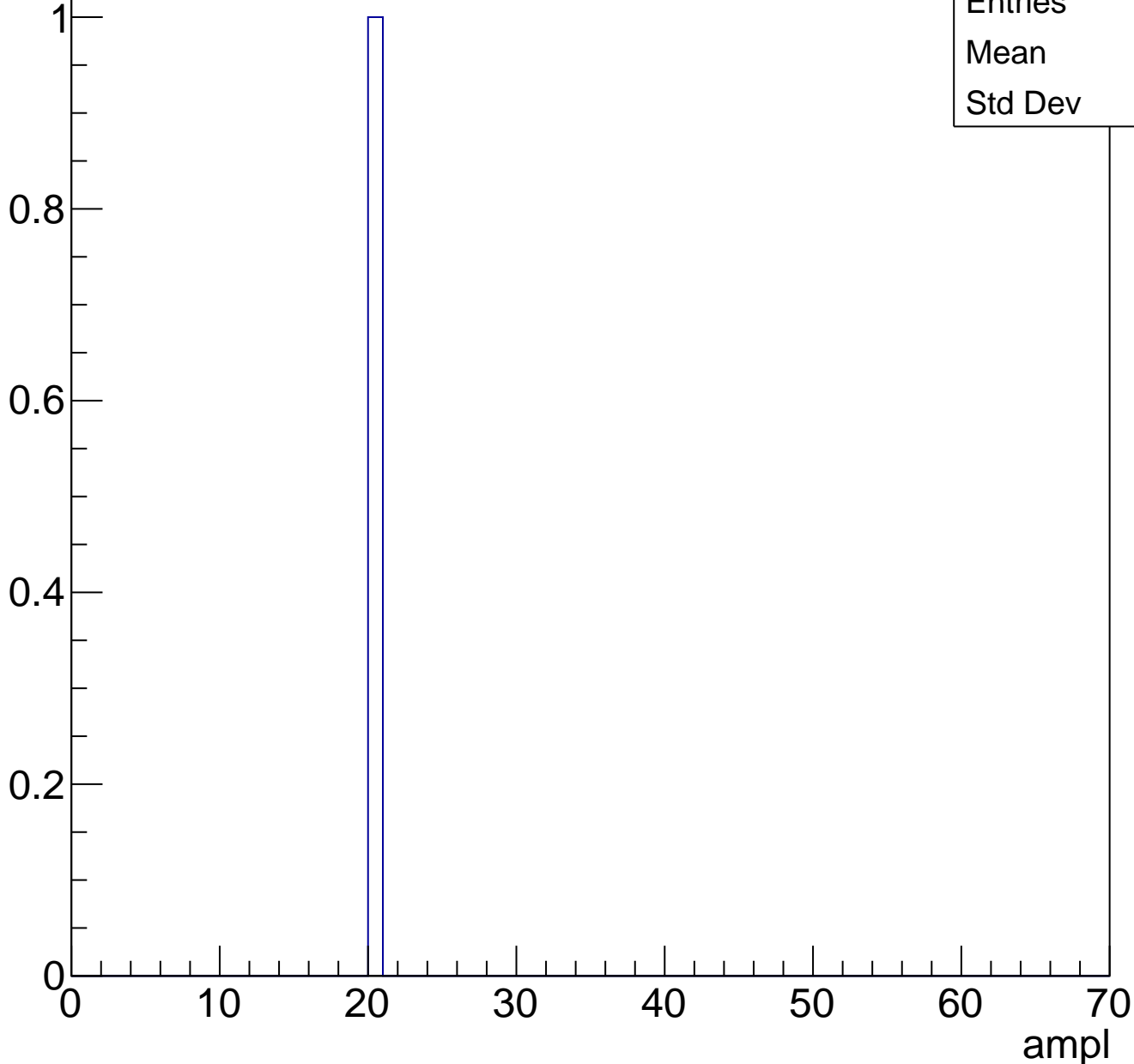
ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U26-ch84, adc0

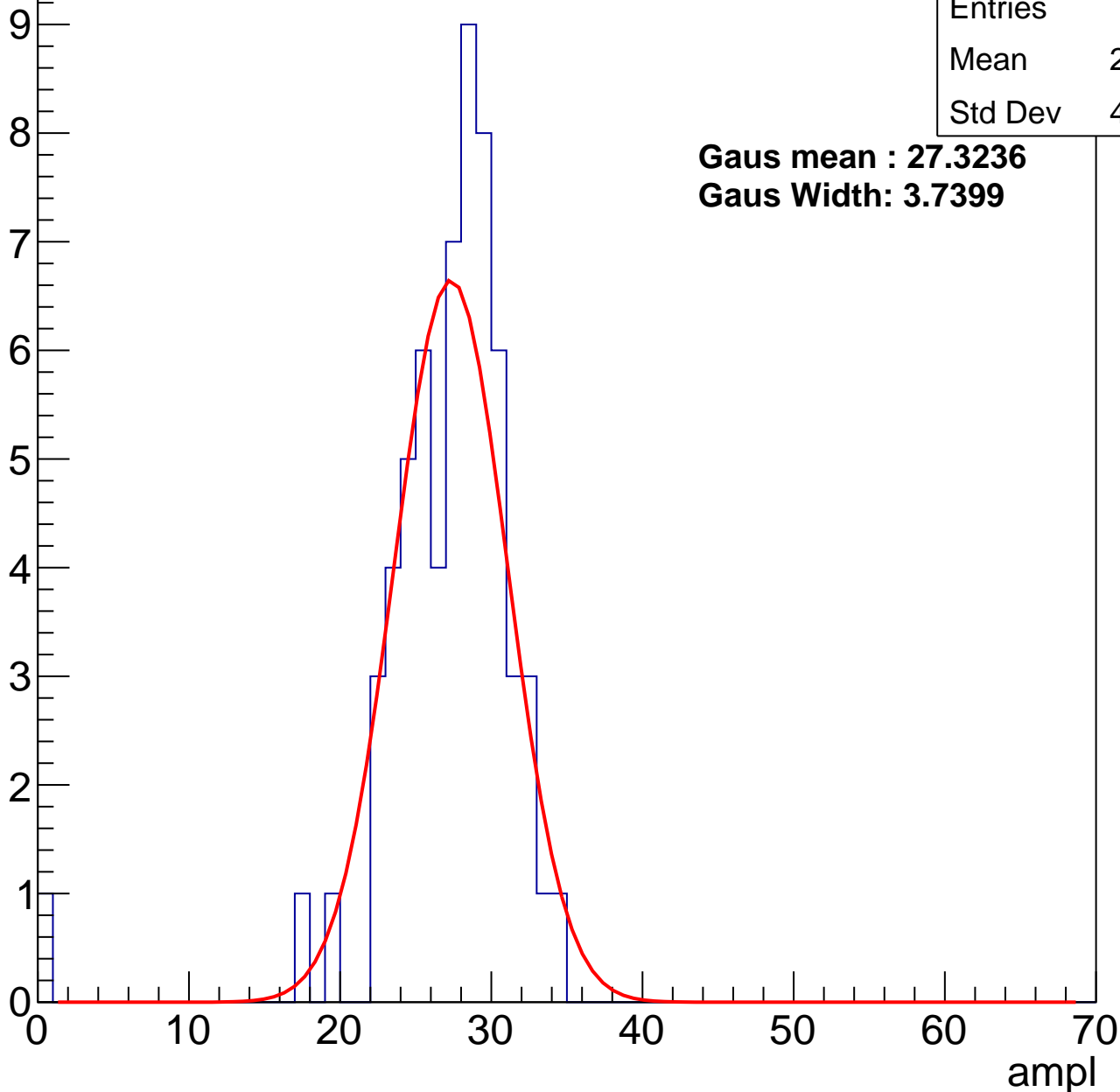
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	26.62
Std Dev	4.712

Gaus mean : 27.3236

Gaus Width: 3.7399



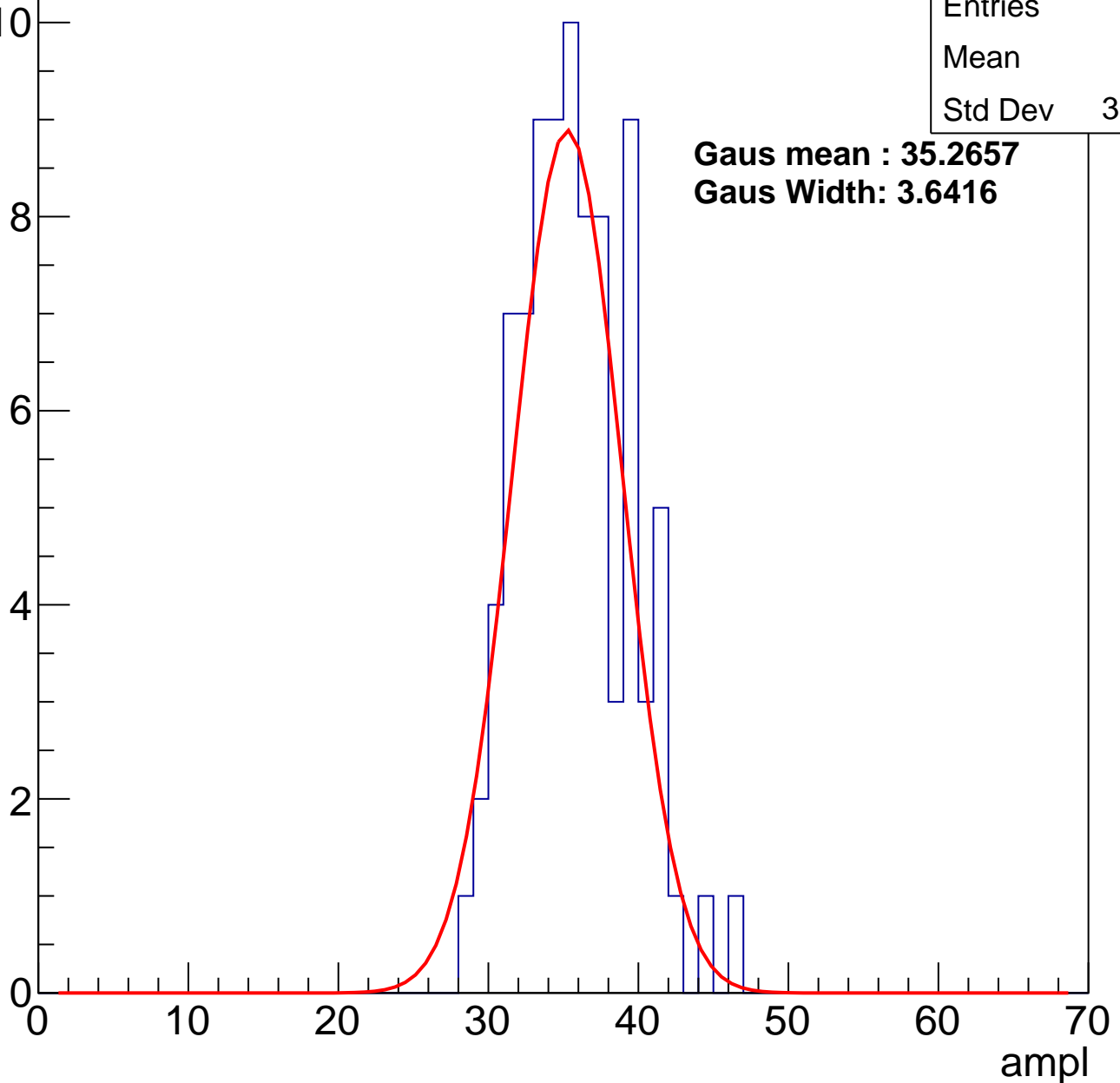
B1L102S, U26-ch84, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	35.3
Std Dev	3.615

Gaus mean : 35.2657
Gaus Width: 3.6416



B1L102S, U26-ch84, adc2

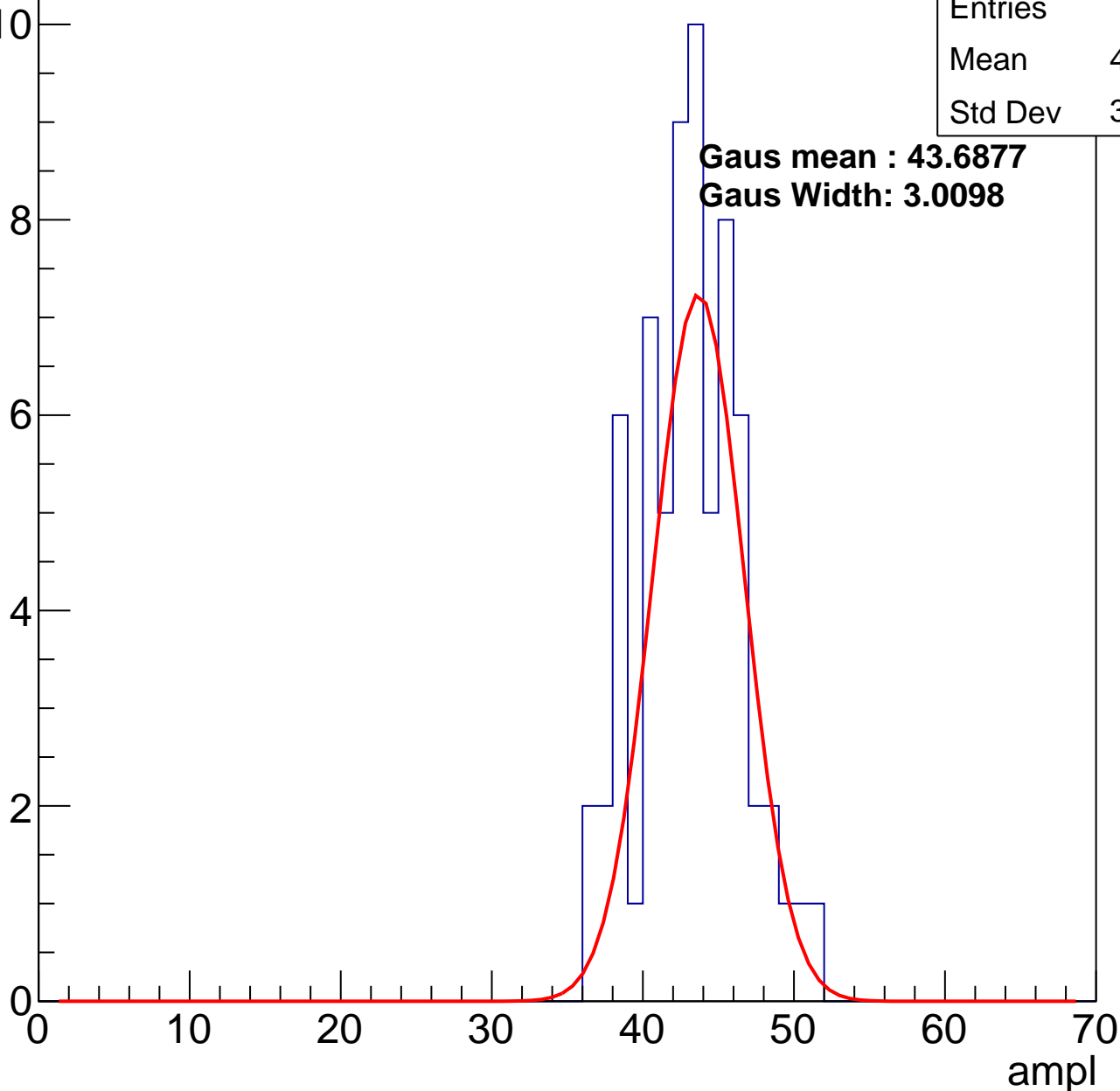
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.68
Std Dev	3.305

Gaus mean : 43.6877

Gaus Width: 3.0098

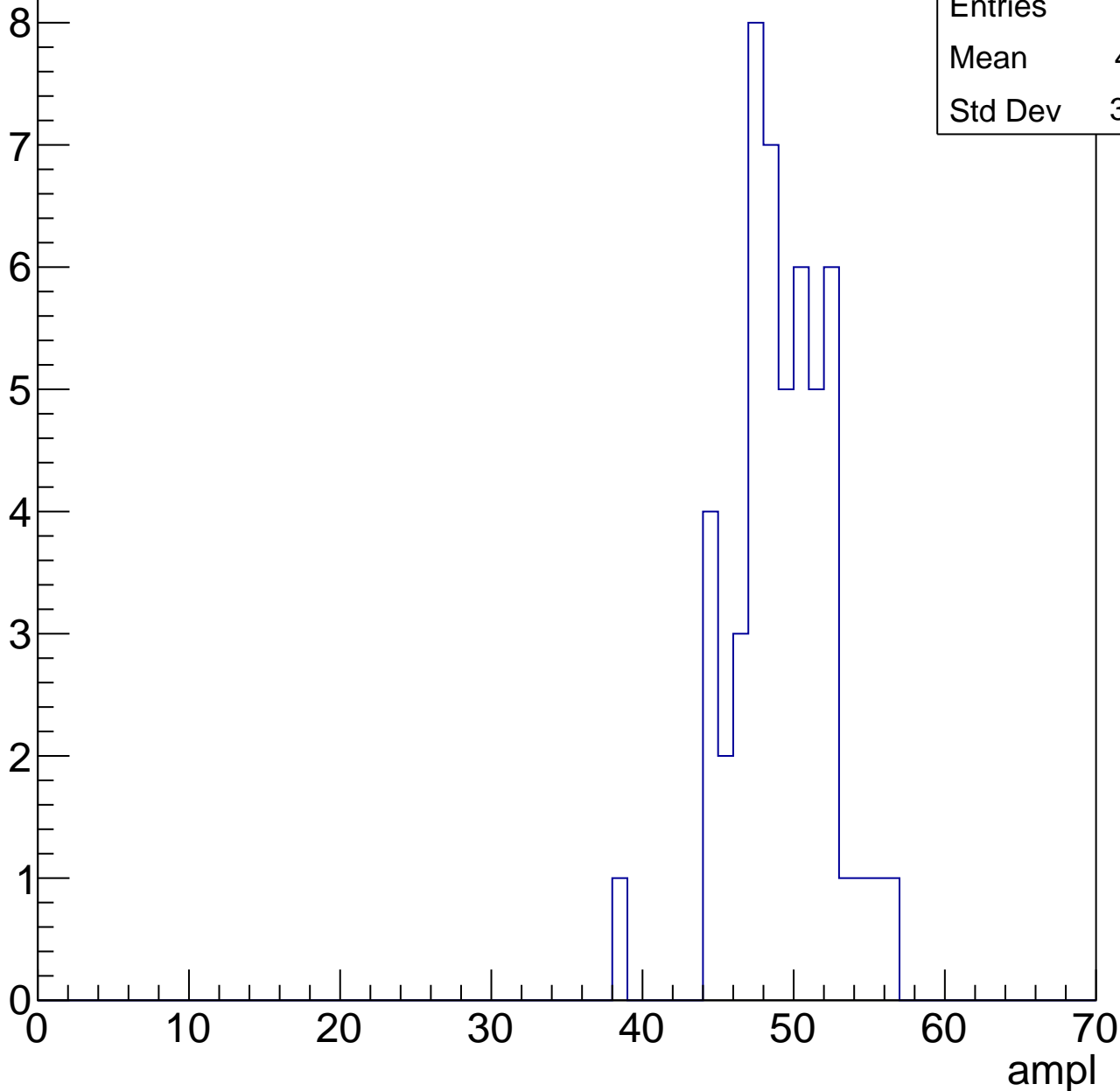


B1L102S, U26-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	48.71
Std Dev	3.195

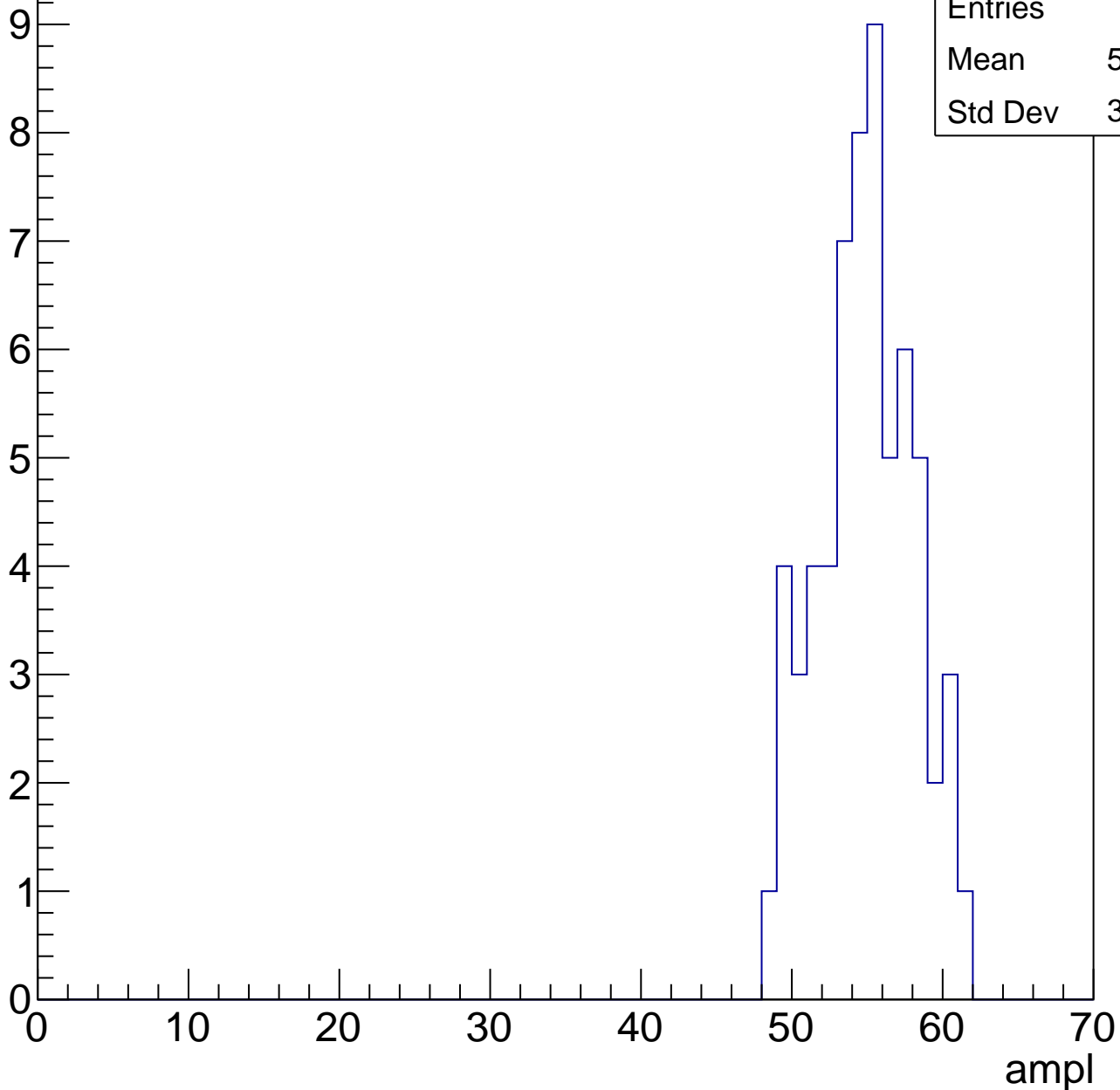


B1L102S, U26-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.44
Std Dev	3.114

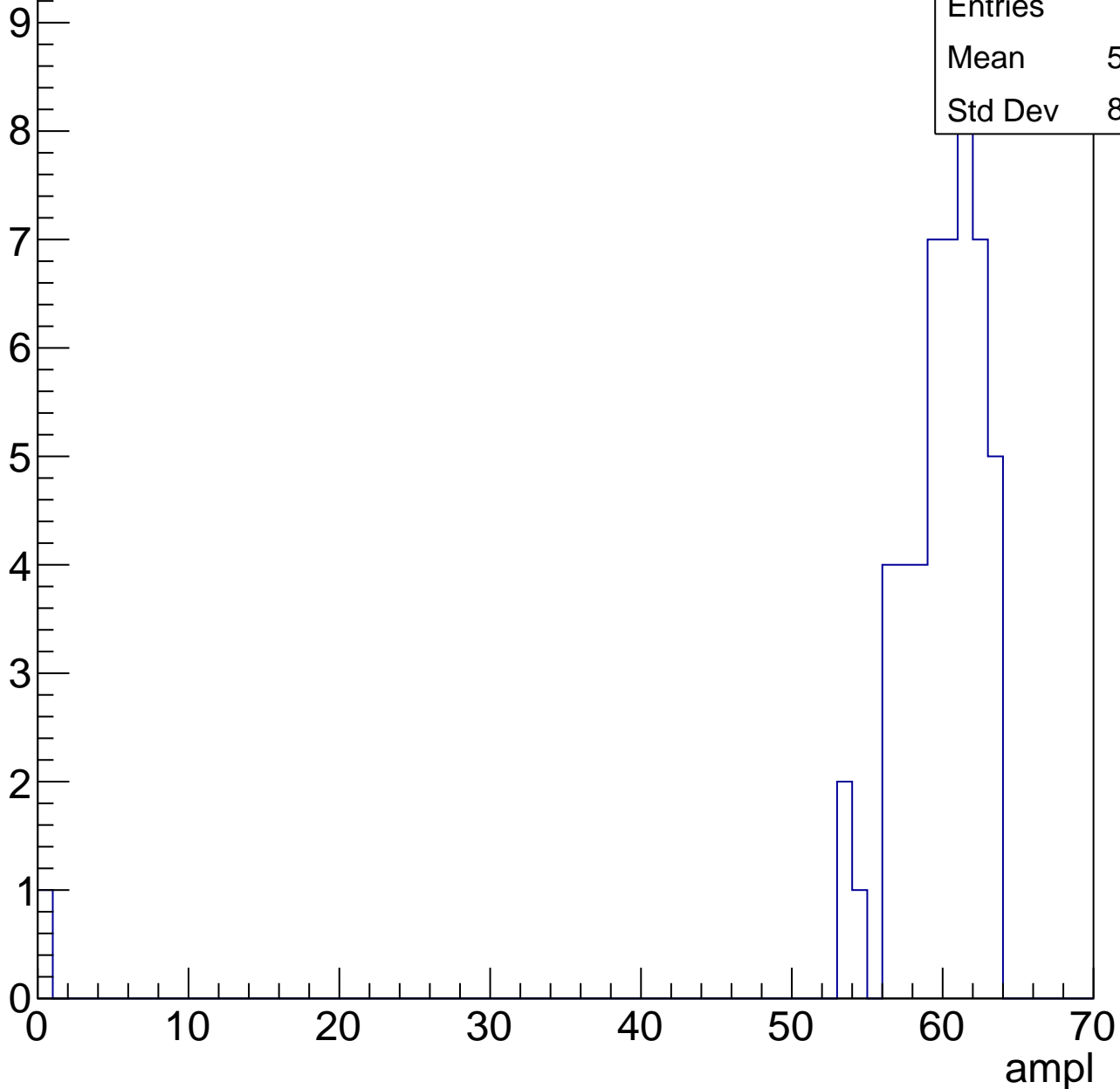


B1L102S, U26-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.33
Std Dev	8.629

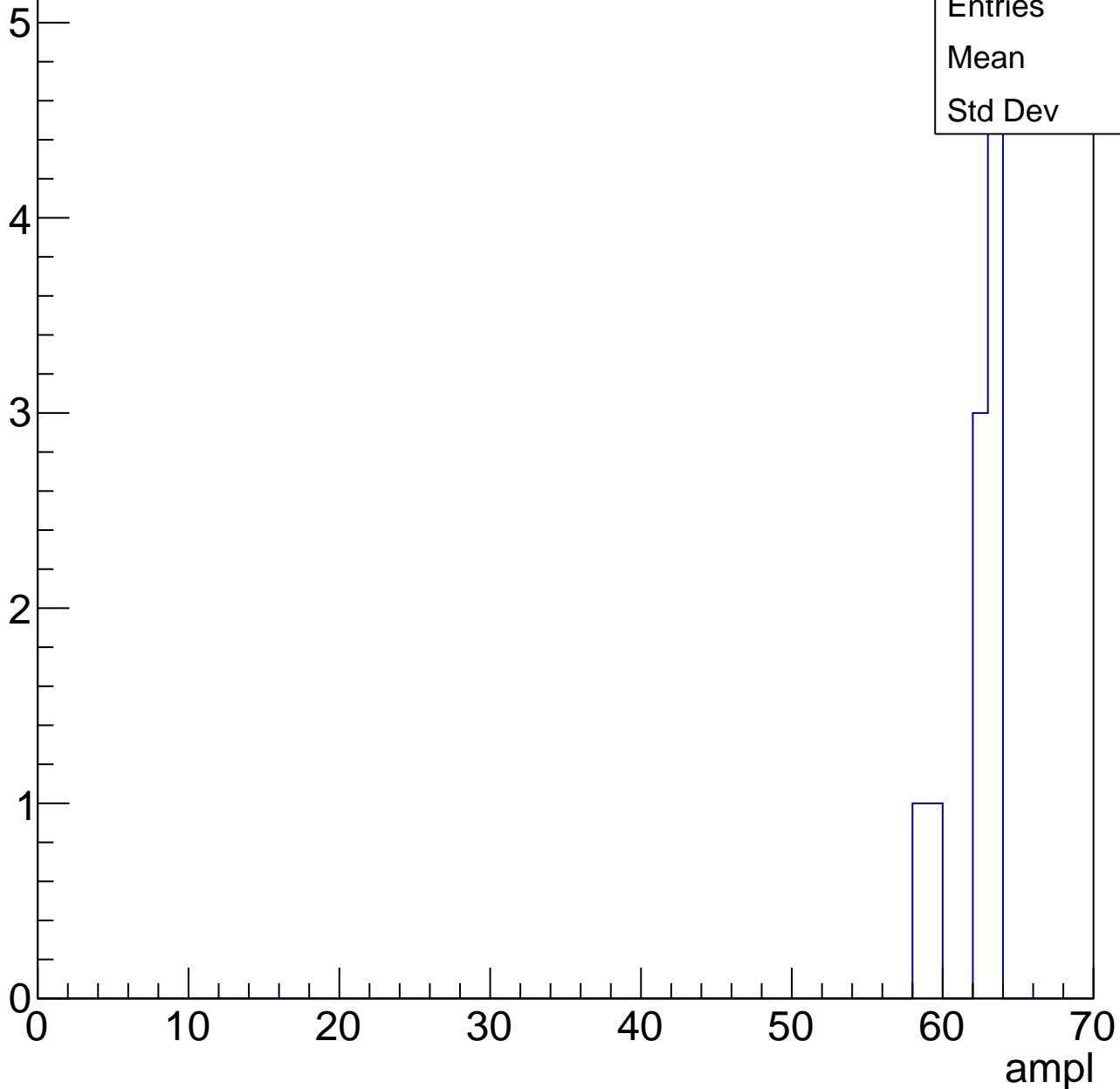


B1L102S, U26-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.8
Std Dev	1.72



B1L102S, U26-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch85, adc0

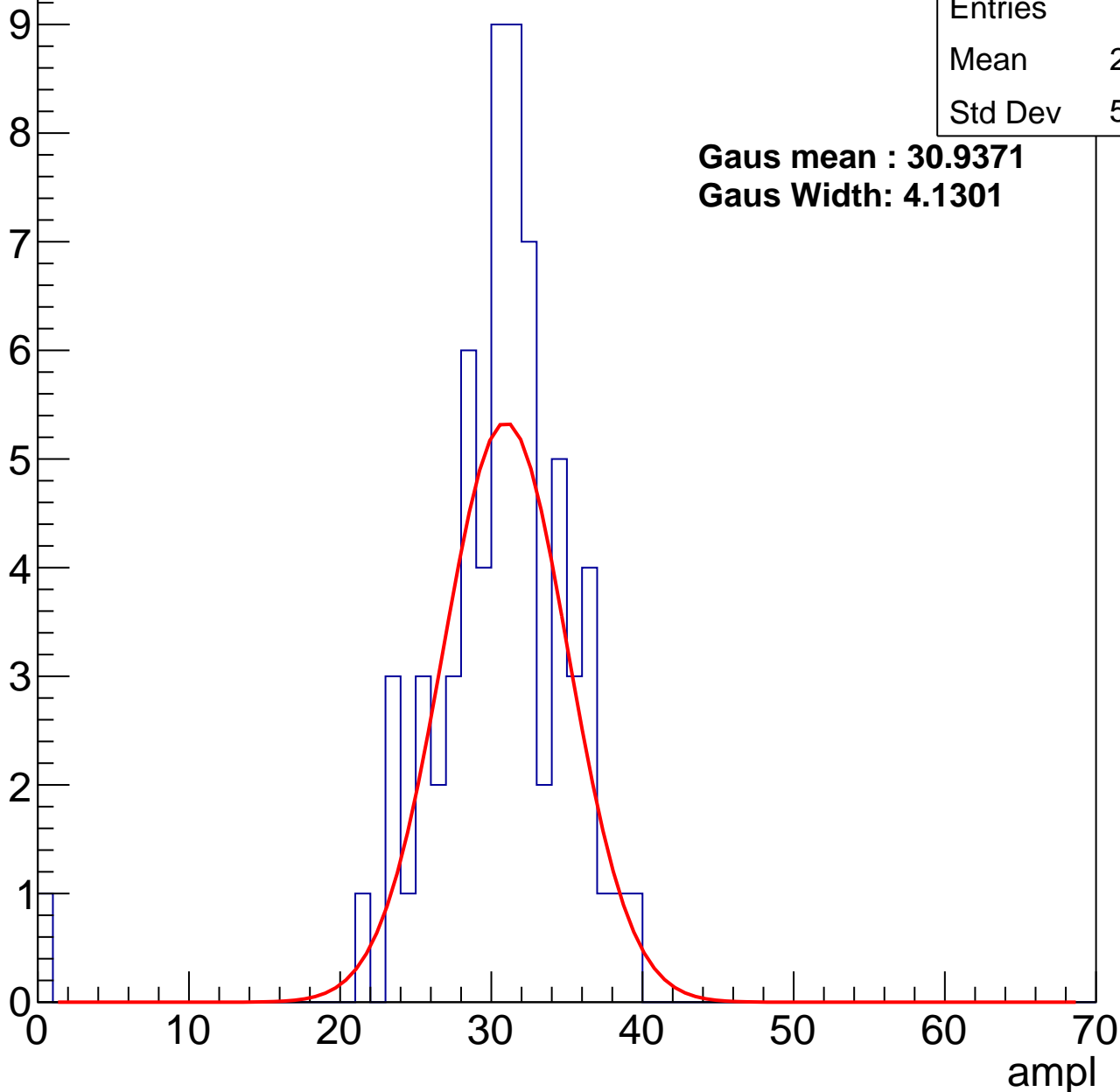
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.97
Std Dev	5.329

Gaus mean : 30.9371

Gaus Width: 4.1301



B1L102S, U26-ch85, adc1

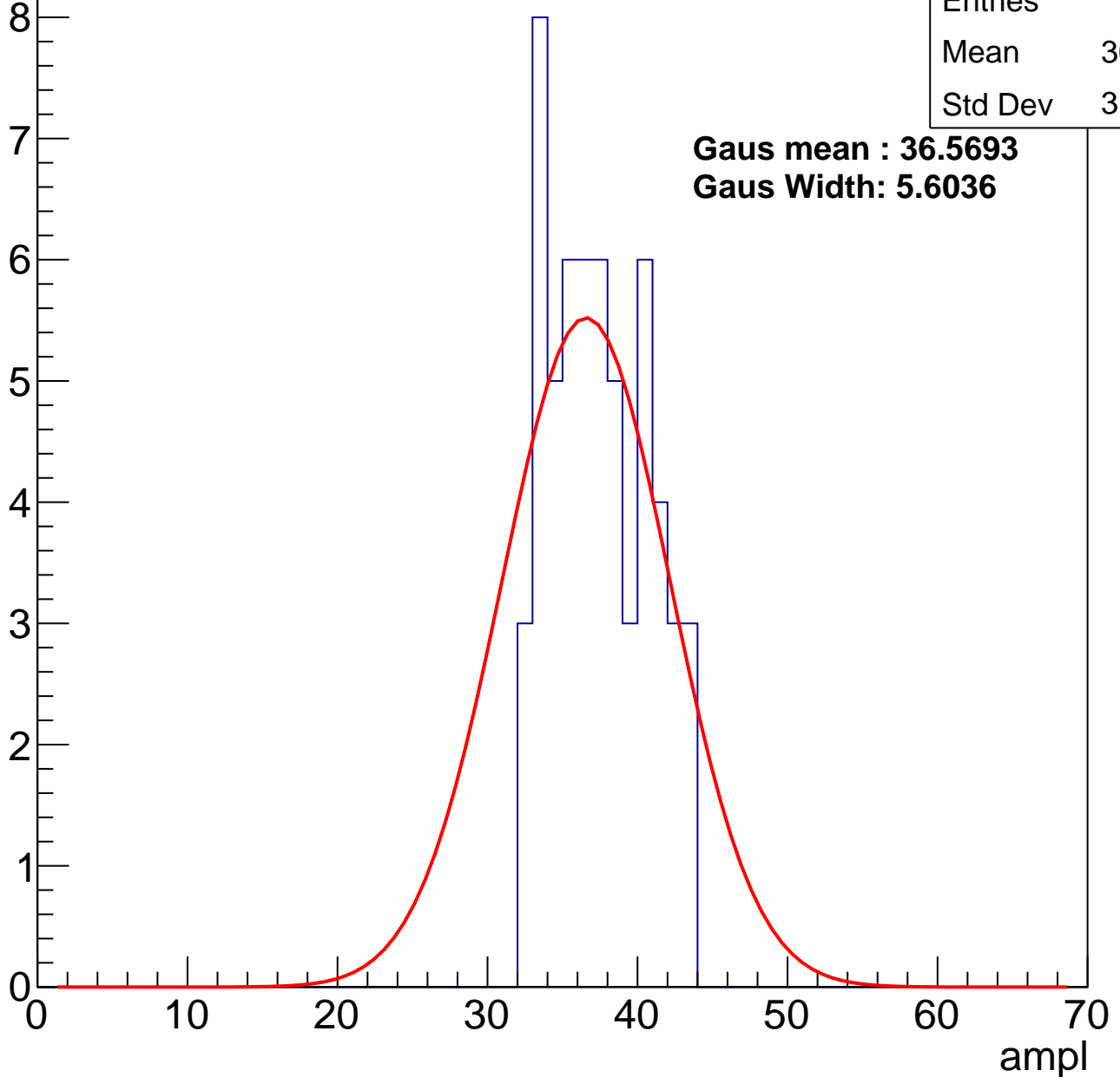
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.97
Std Dev	3.205

Gaus mean : 36.5693

Gaus Width: 5.6036



B1L102S, U26-ch85, adc2

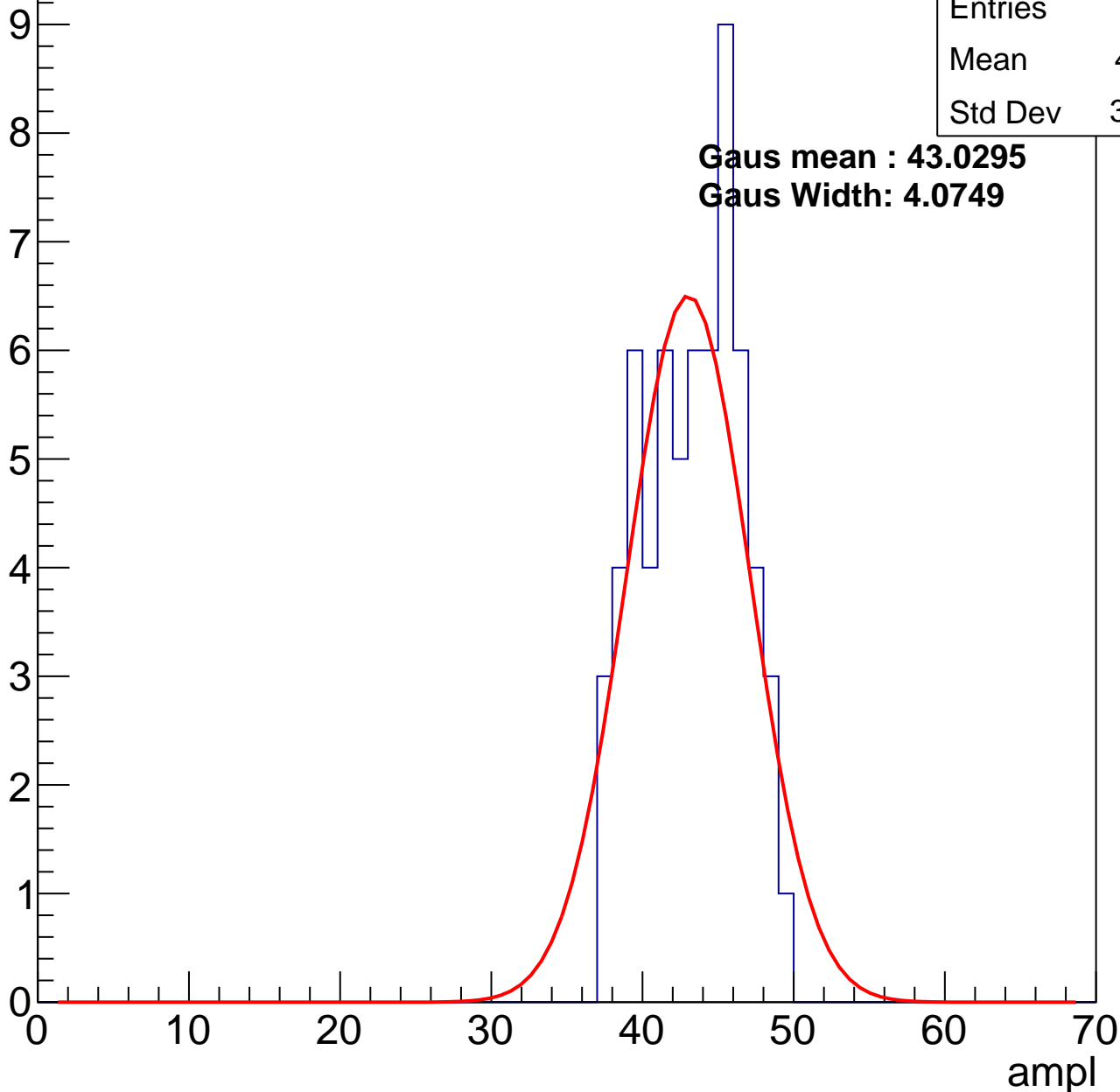
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.81
Std Dev	3.182

Gaus mean : 43.0295

Gaus Width: 4.0749



B1L102S, U26-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

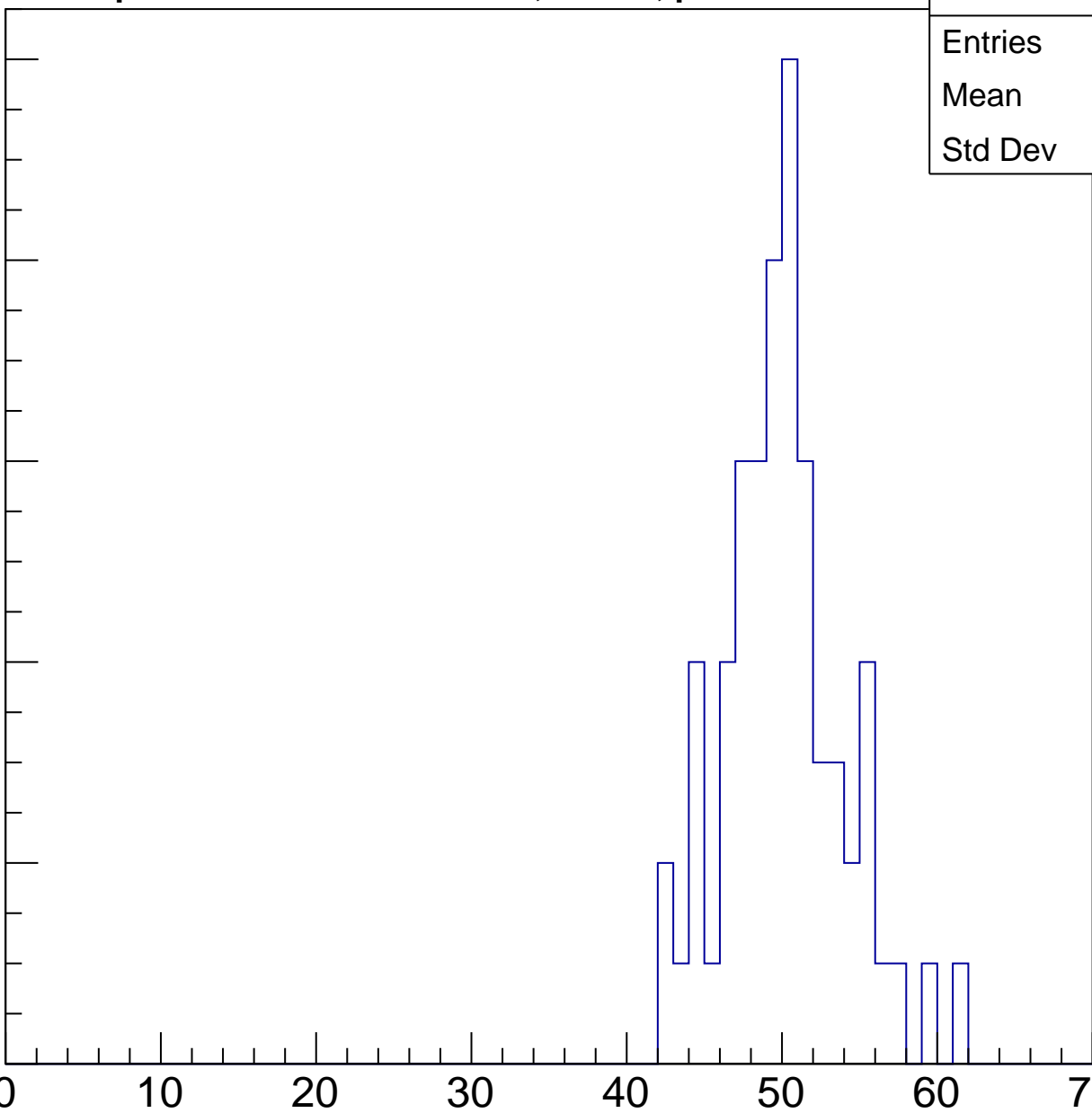
Entries	64
Mean	49.62
Std Dev	3.855

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

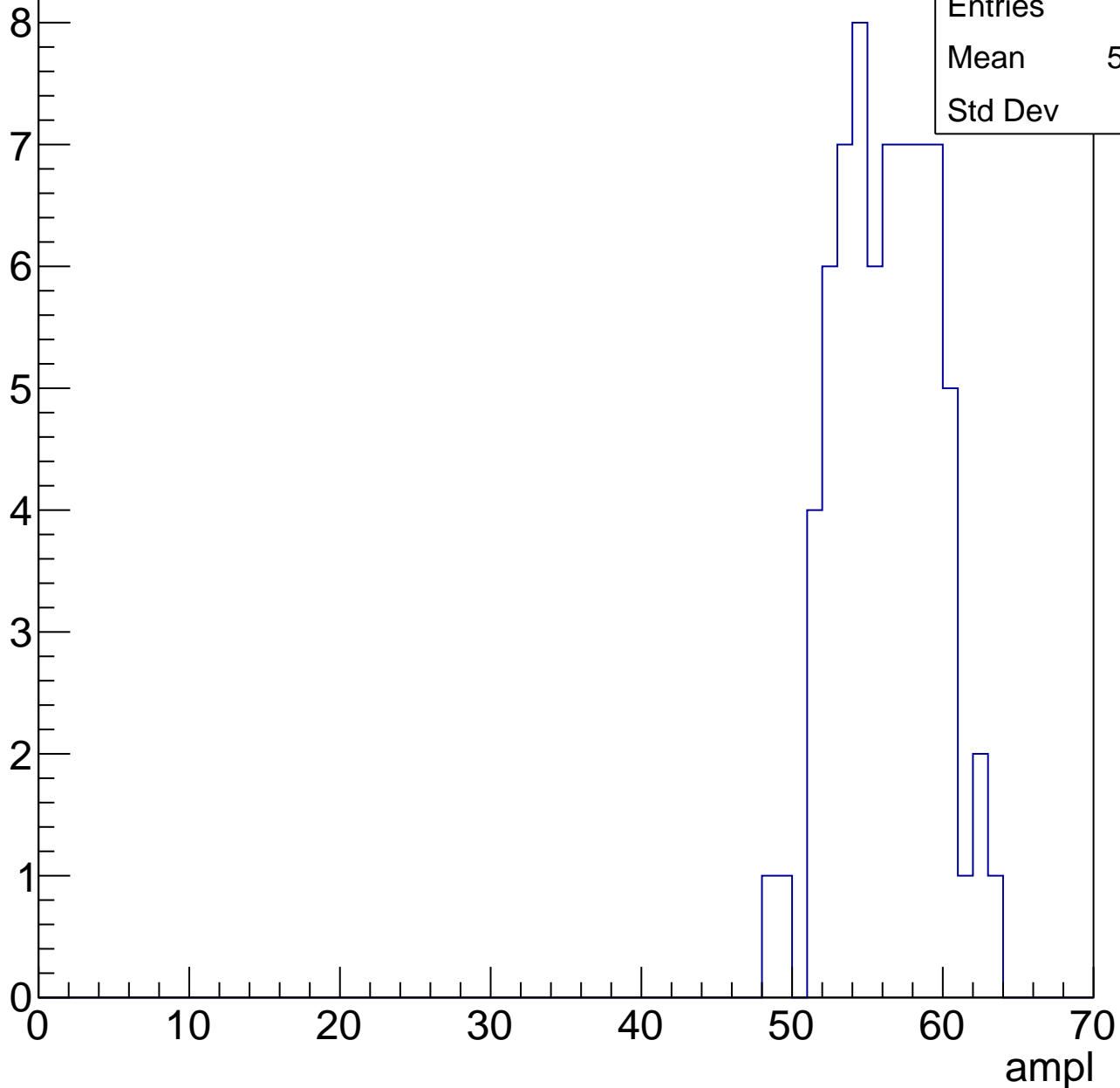


B1L102S, U26-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.77
Std Dev	3.23

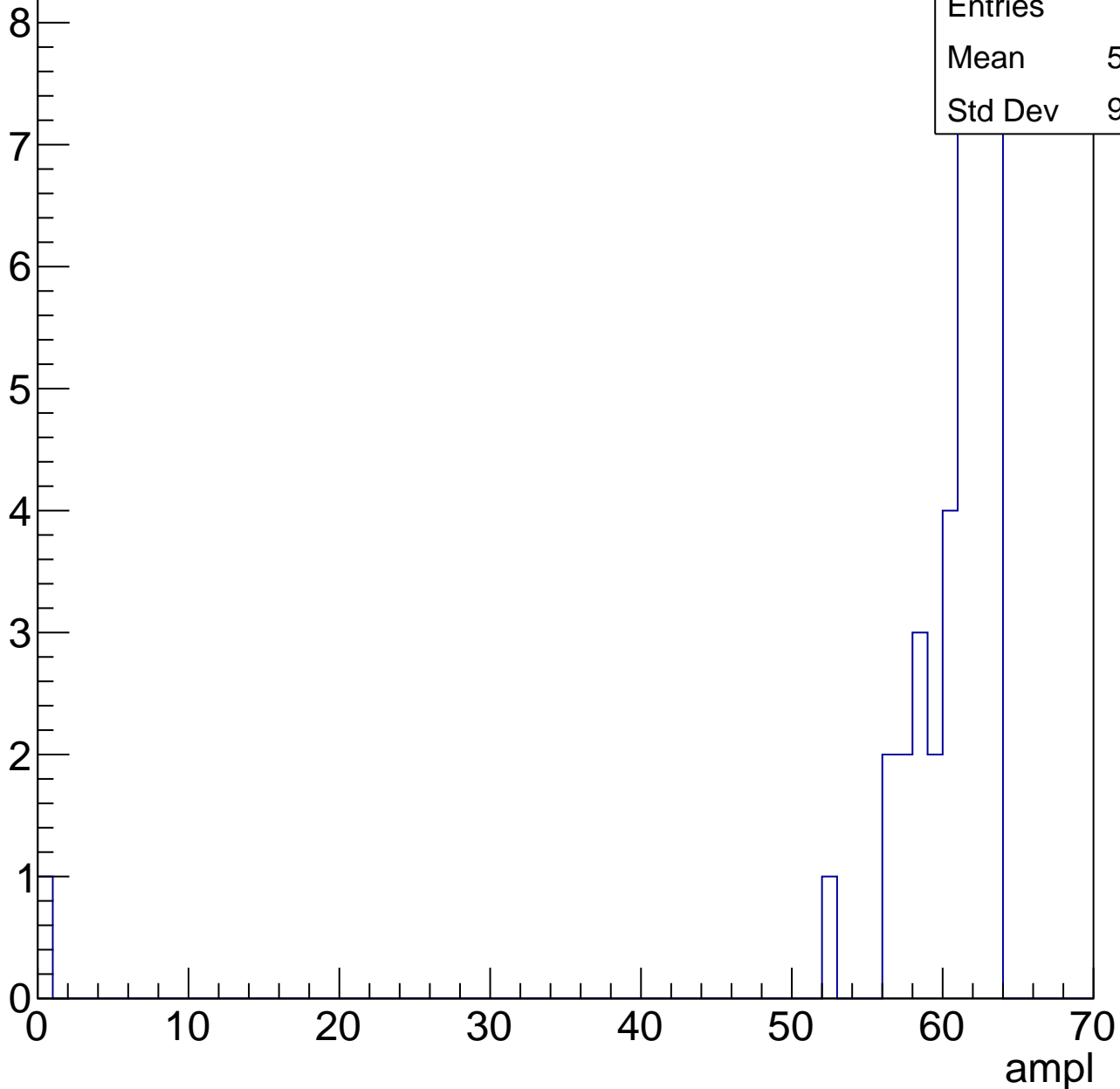


B1L102S, U26-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.92
Std Dev	9.864



B1L102S, U26-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch86, adc0

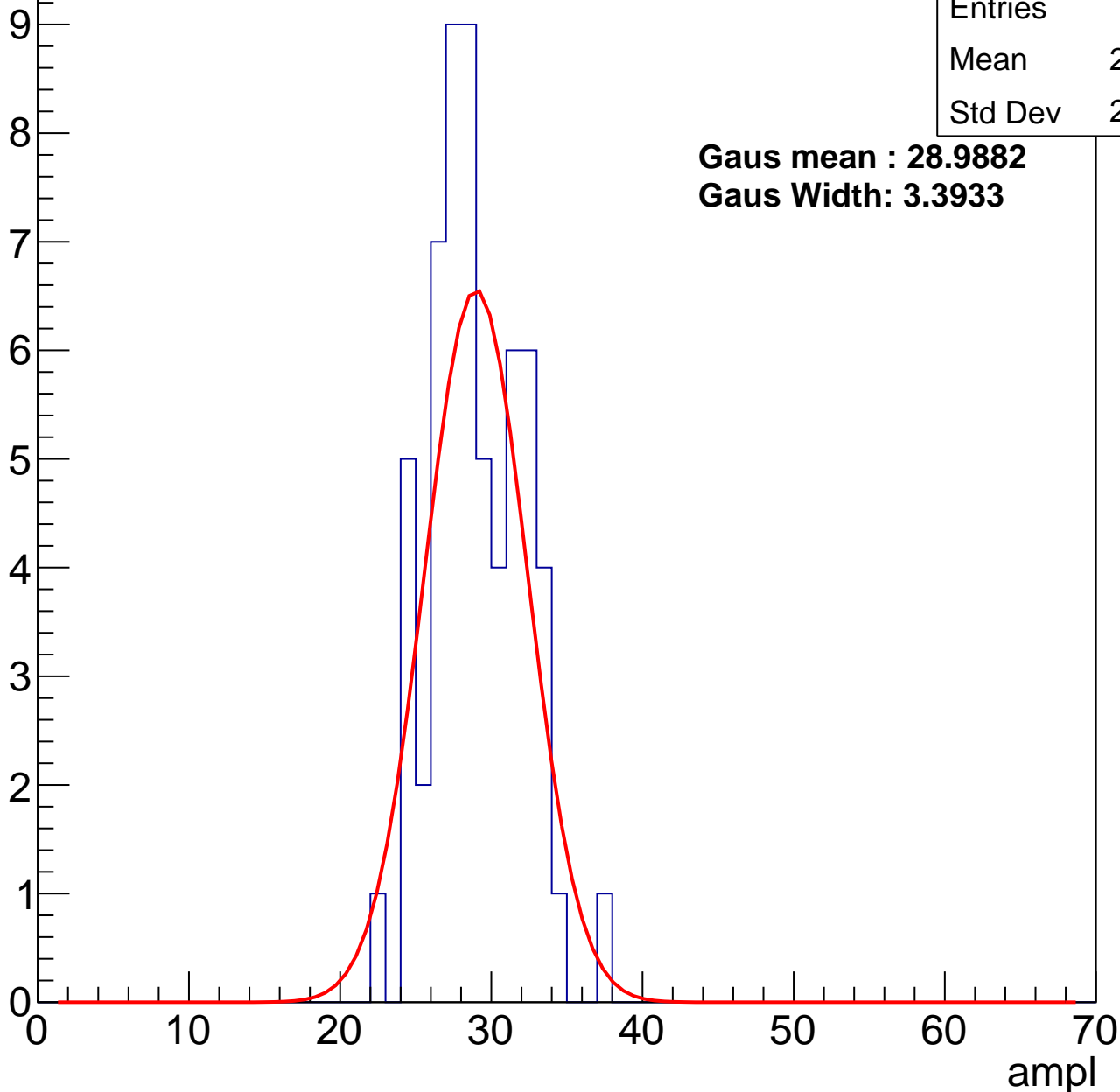
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.58
Std Dev	2.996

Gaus mean : 28.9882

Gaus Width: 3.3933



B1L102S, U26-ch86, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	34.07
Std Dev	3.766

Gaus mean : 34.1192

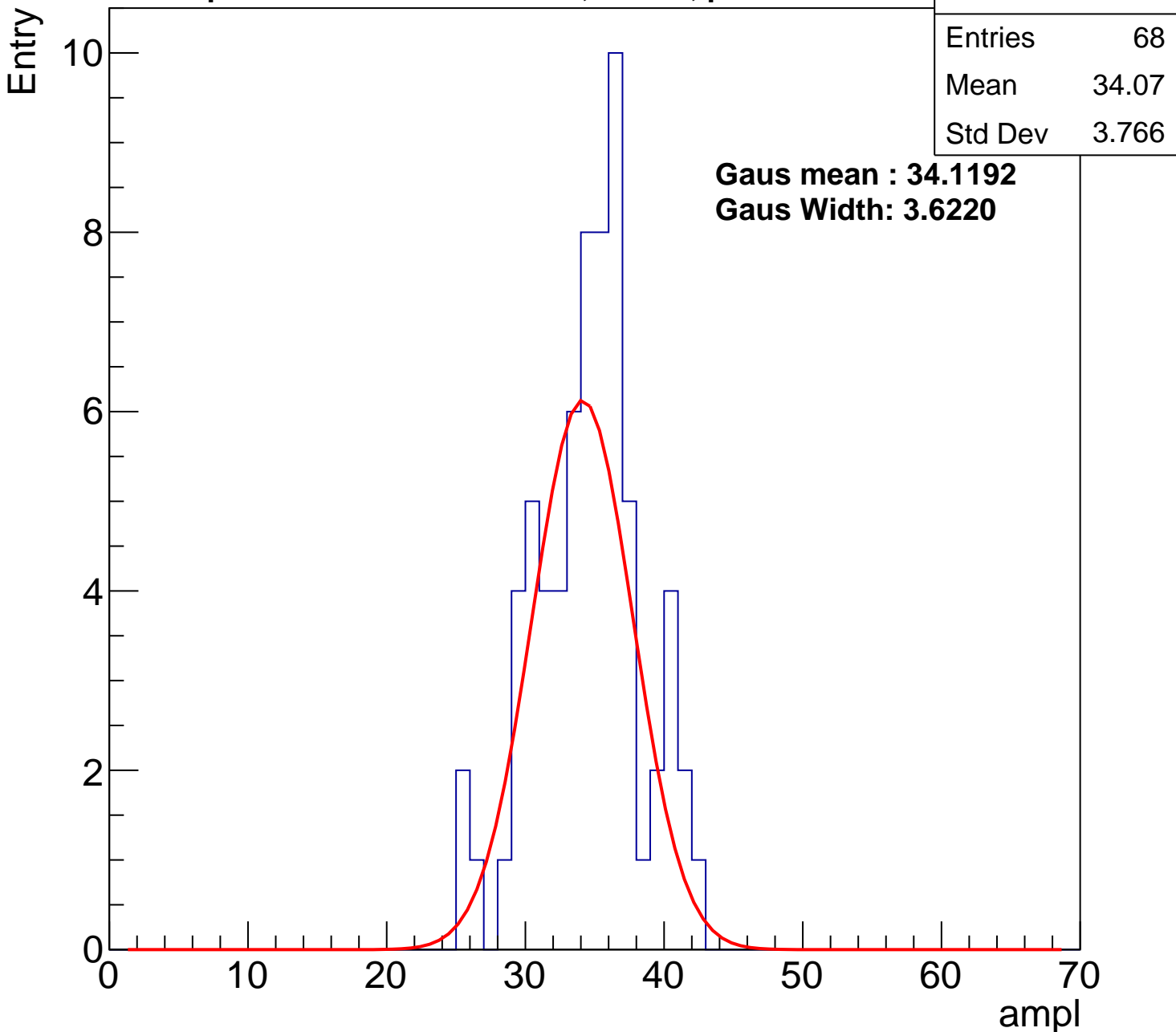
Gaus Width: 3.6220

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch86, adc2

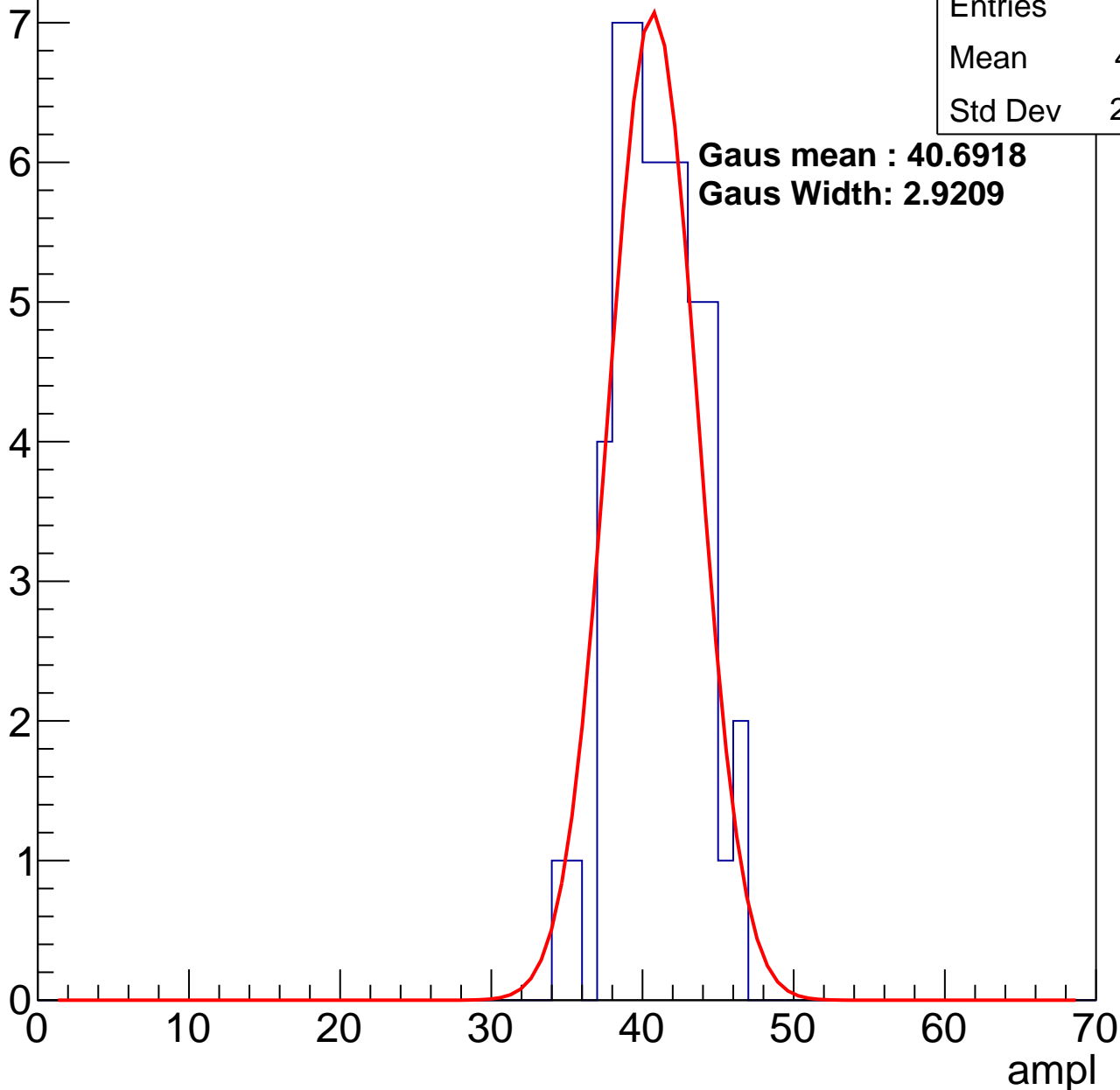
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	40.51
Std Dev	2.696

Gaus mean : 40.6918

Gaus Width: 2.9209



B1L102S, U26-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	47.49
Std Dev	4.107

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

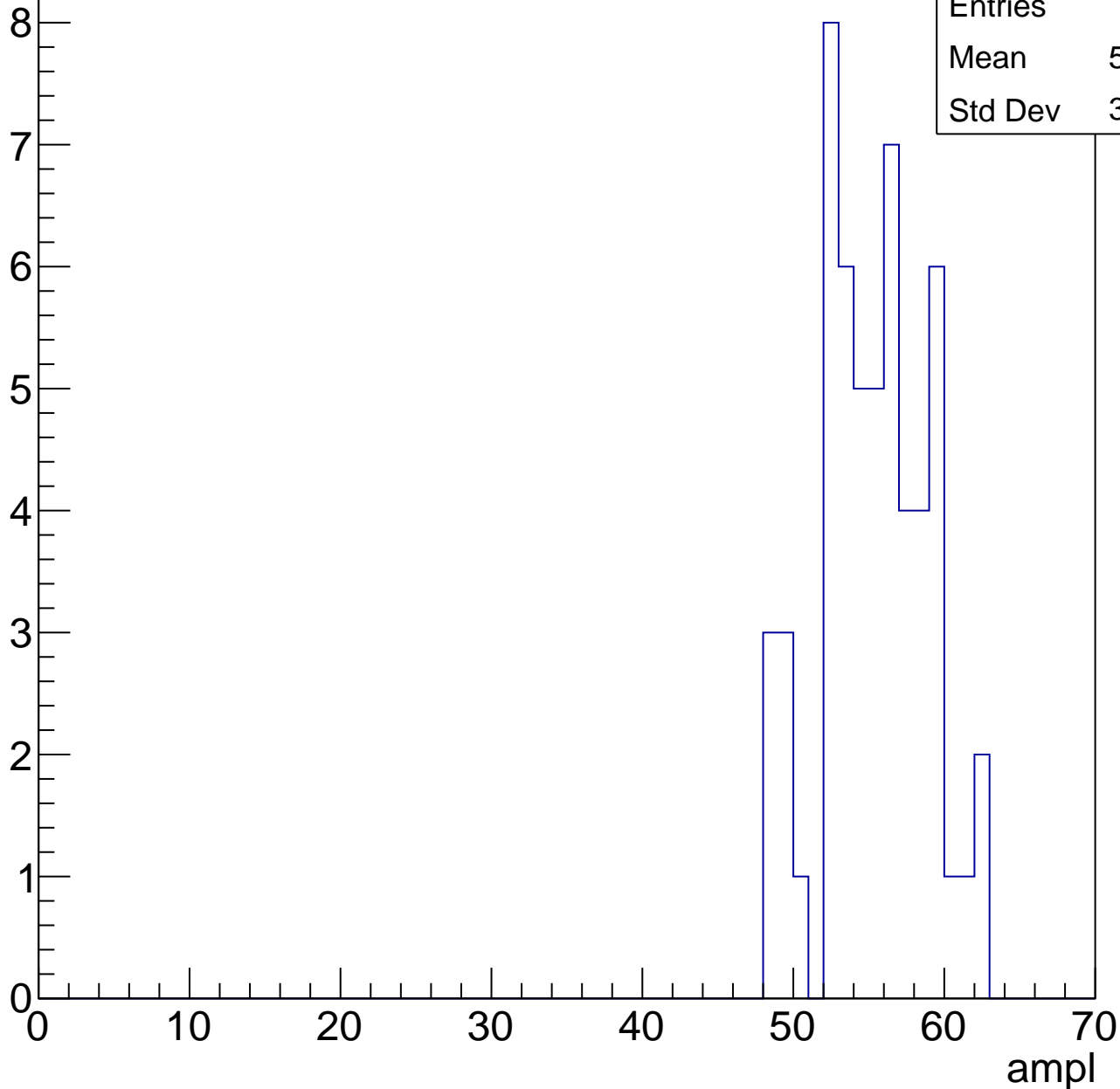
10

B1L102S, U26-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.84
Std Dev	3.519

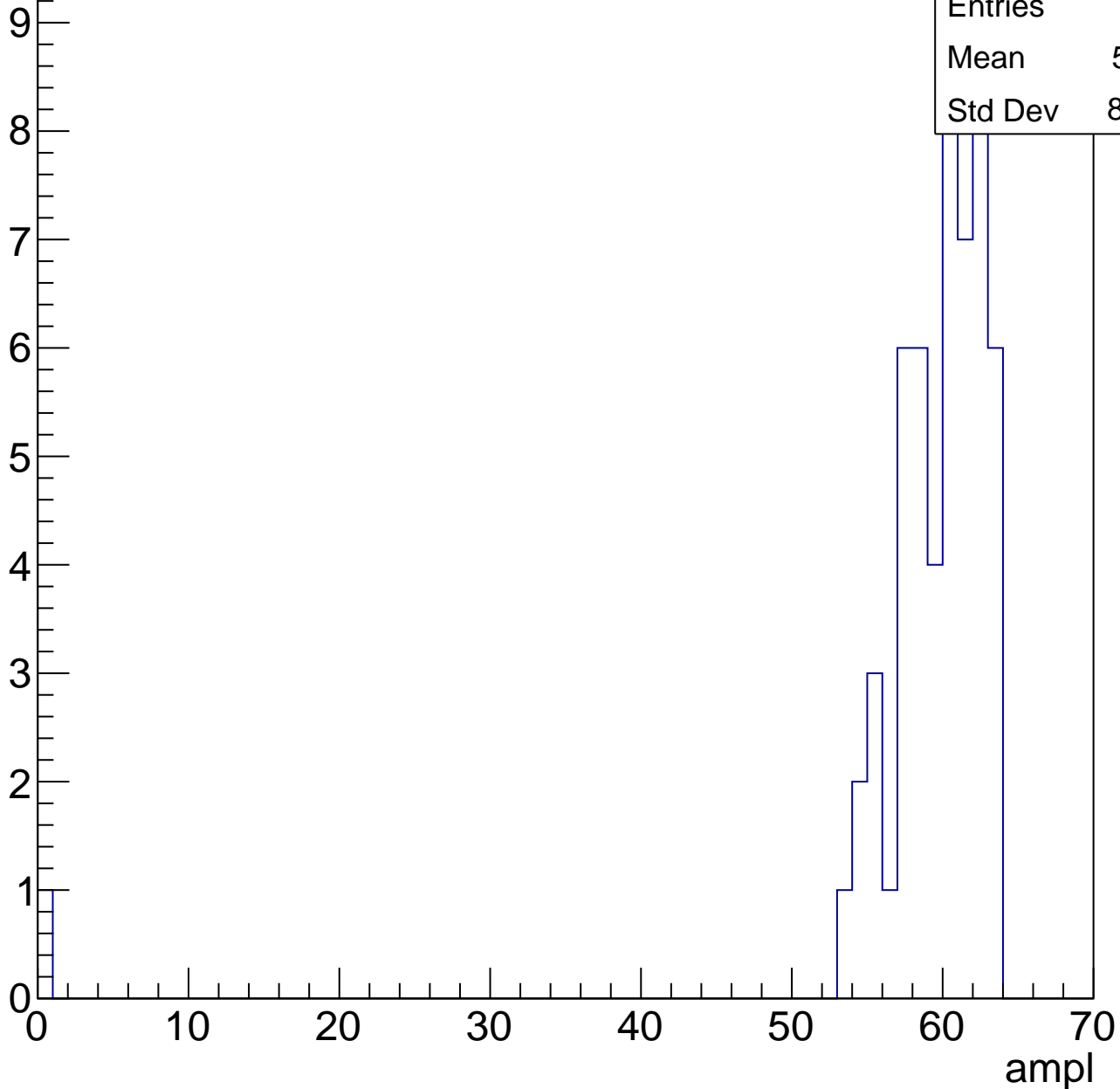


B1L102S, U26-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

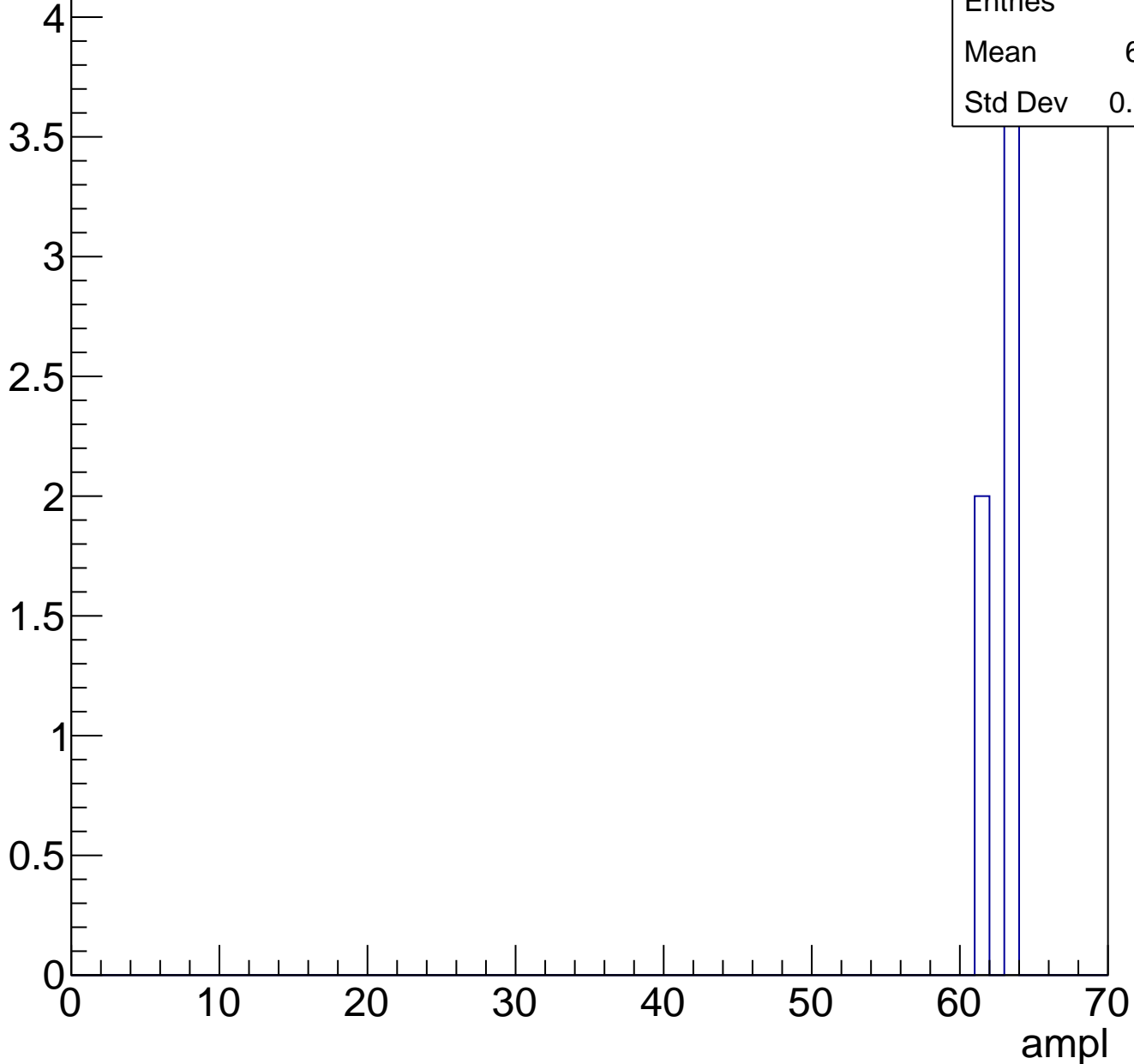
Entries	54
Mean	58.31
Std Dev	8.426



B1L102S, U26-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch87, adc0

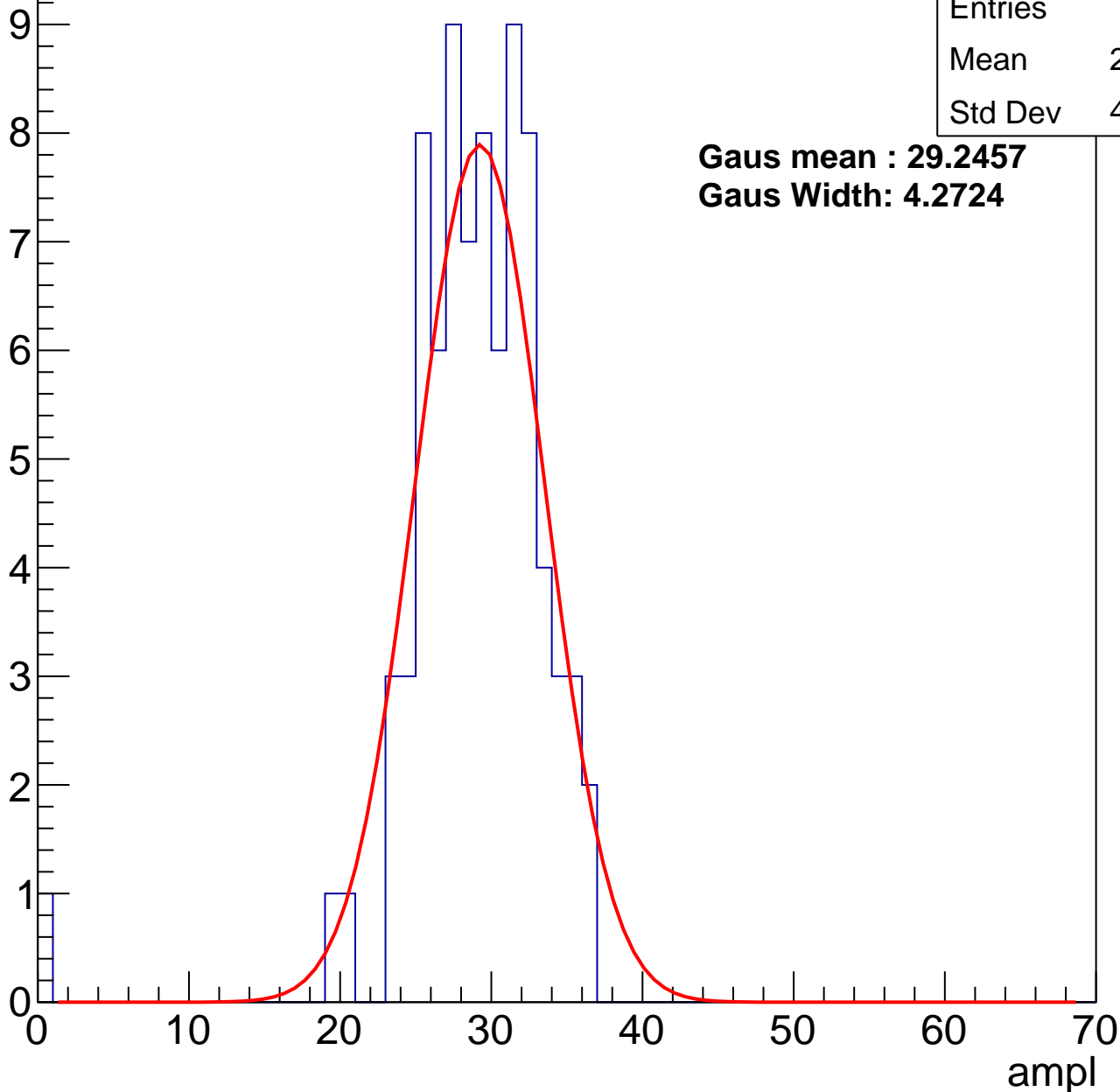
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.45
Std Dev	4.768

Gaus mean : 29.2457

Gaus Width: 4.2724



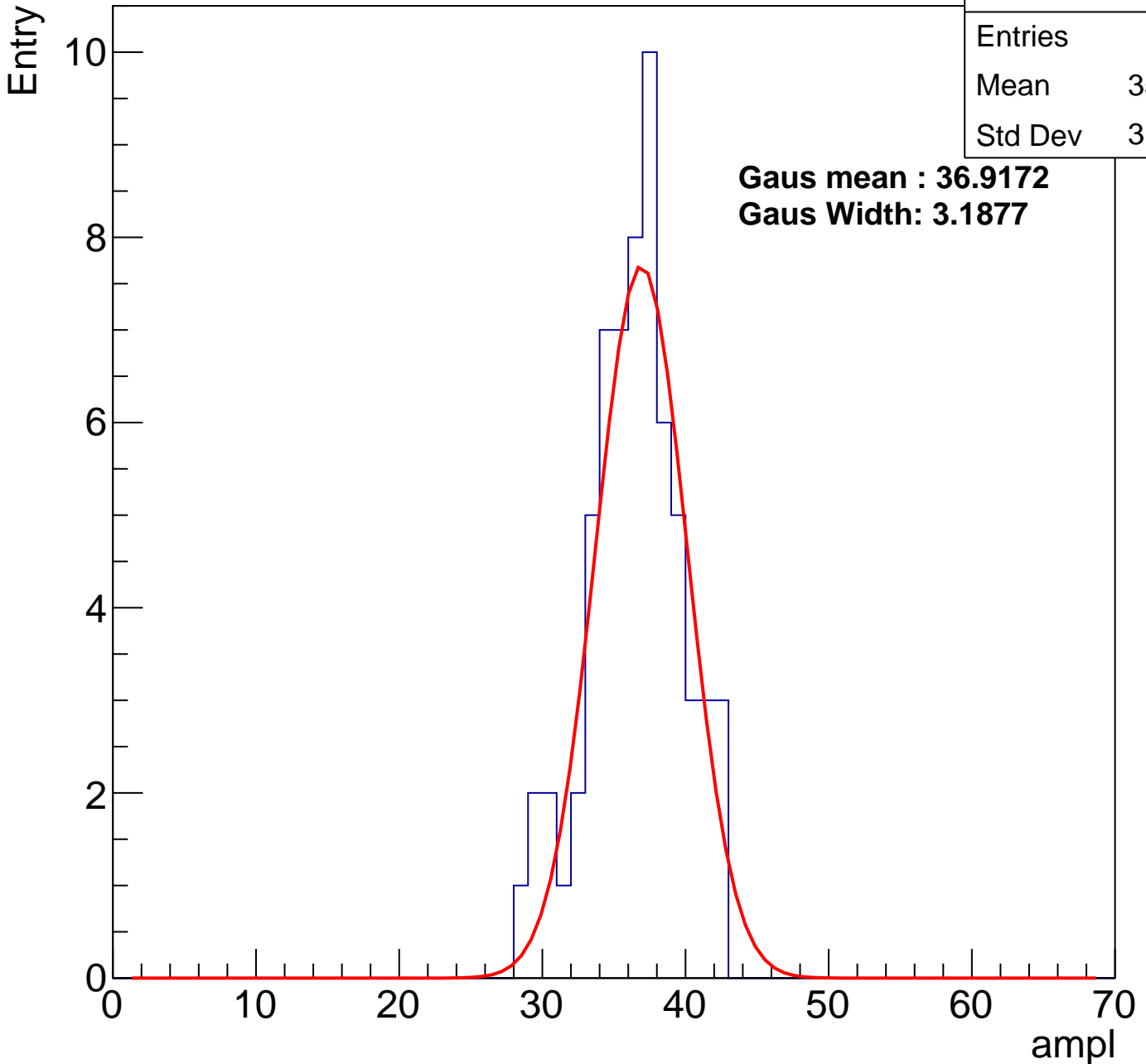
B1L102S, U26-ch87, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	35.98
Std Dev	3.237

Gaus mean : 36.9172

Gaus Width: 3.1877



B1L102S, U26-ch87, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.59
Std Dev	3.626

Gaus mean : 43.0931

Gaus Width: 4.4513

10

8

6

4

2

0

0

10

20

30

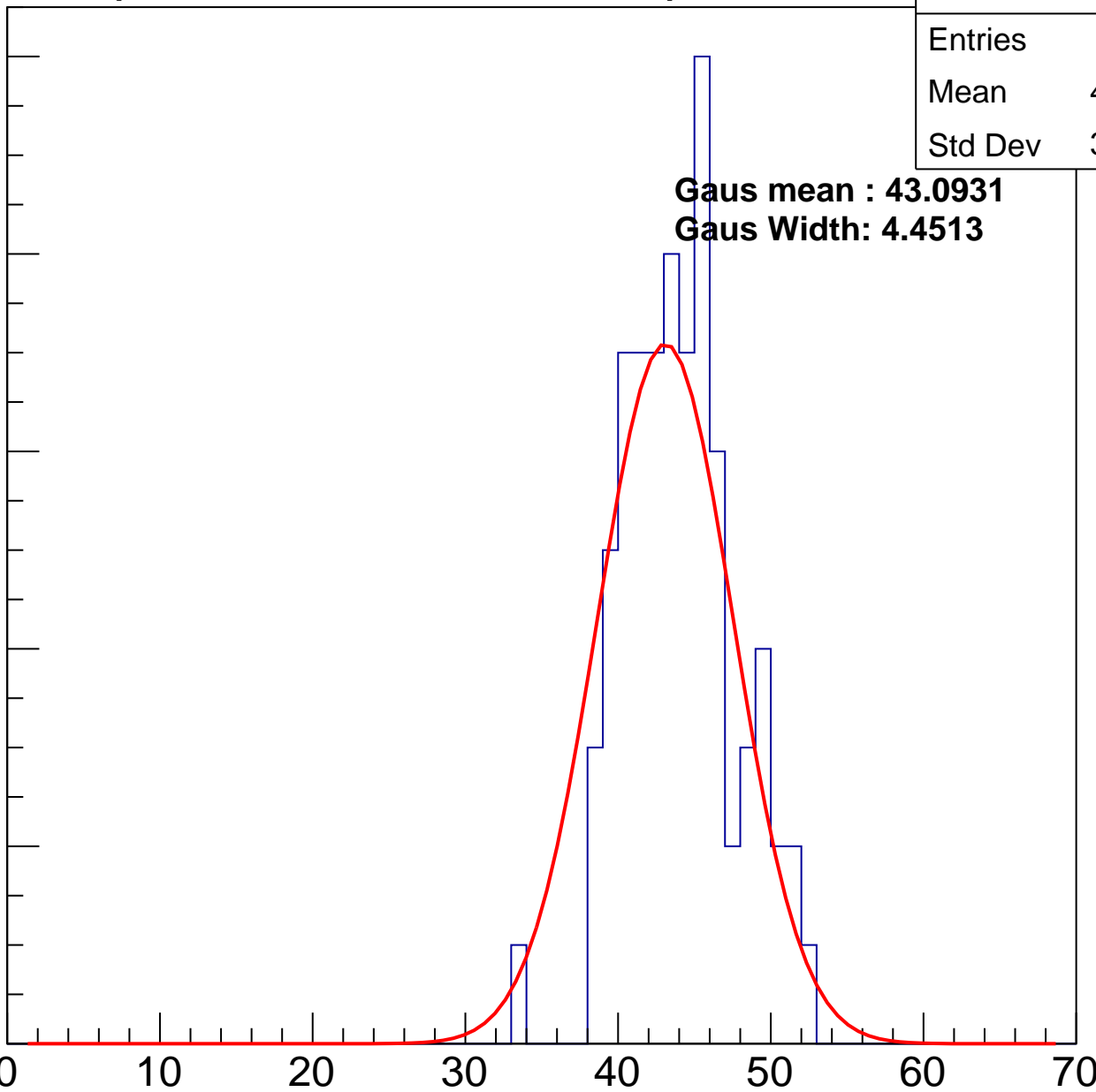
40

50

60

70

ampl

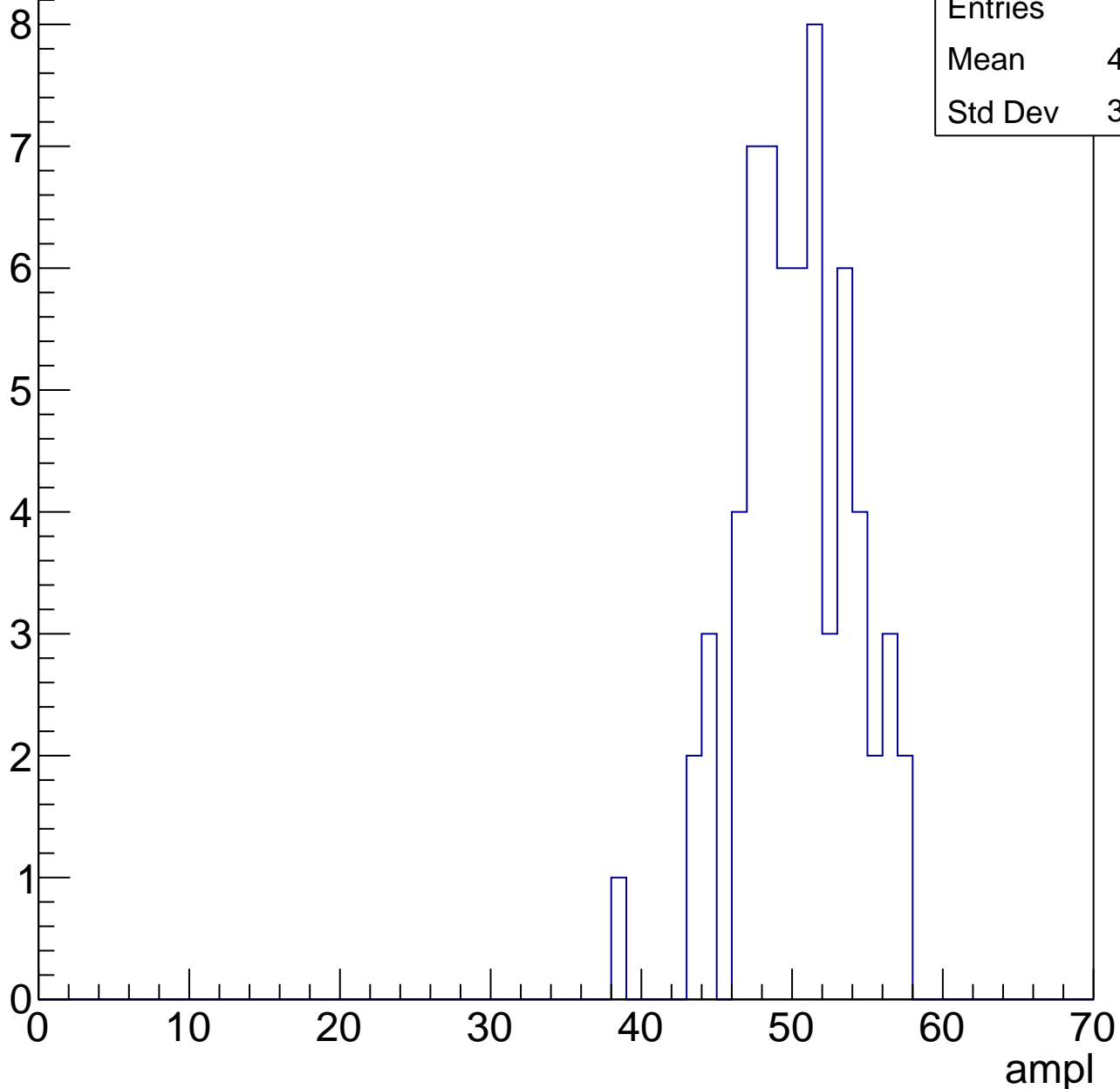


B1L102S, U26-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.83
Std Dev	3.756

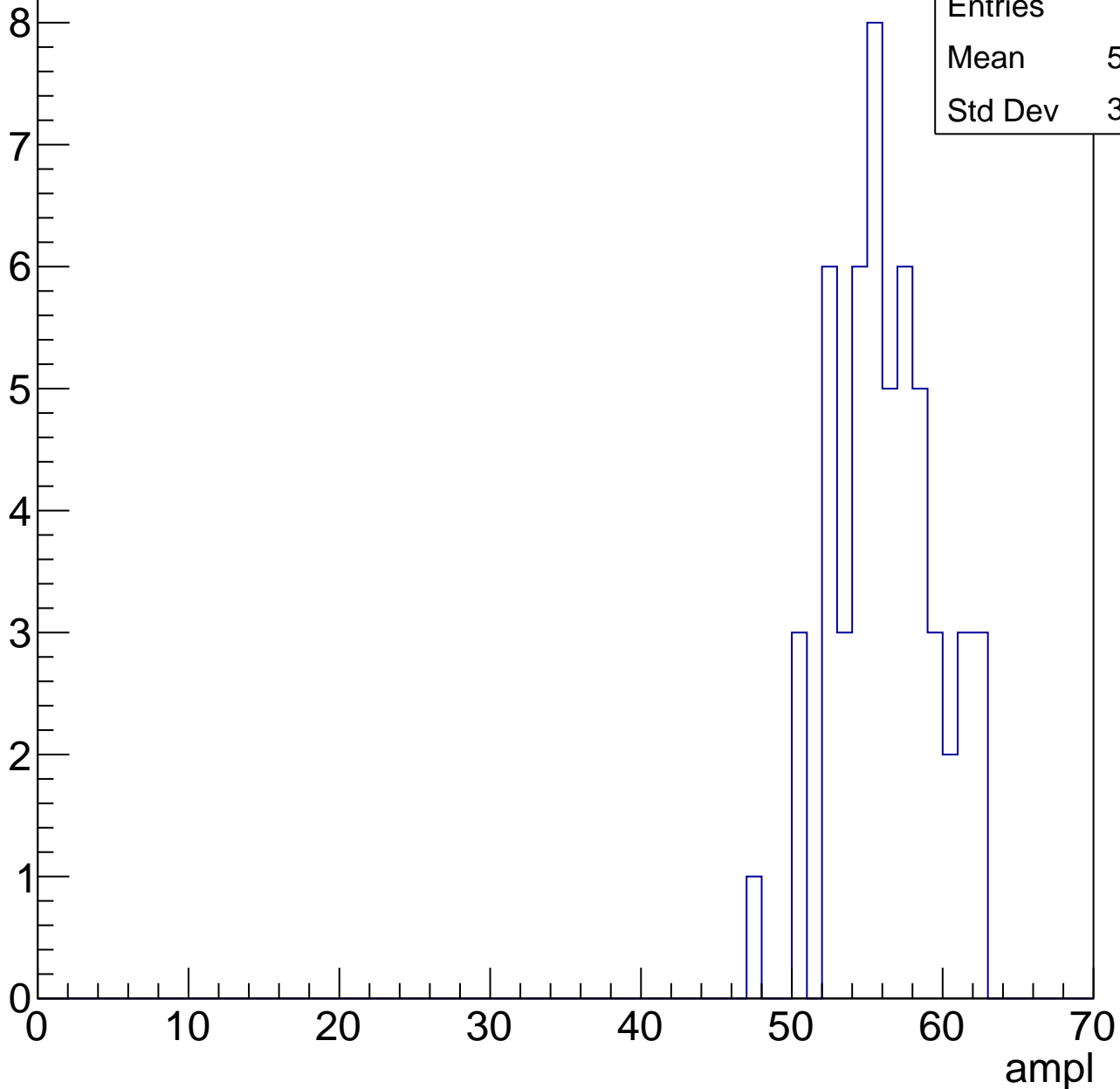


B1L102S, U26-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.74
Std Dev	3.357

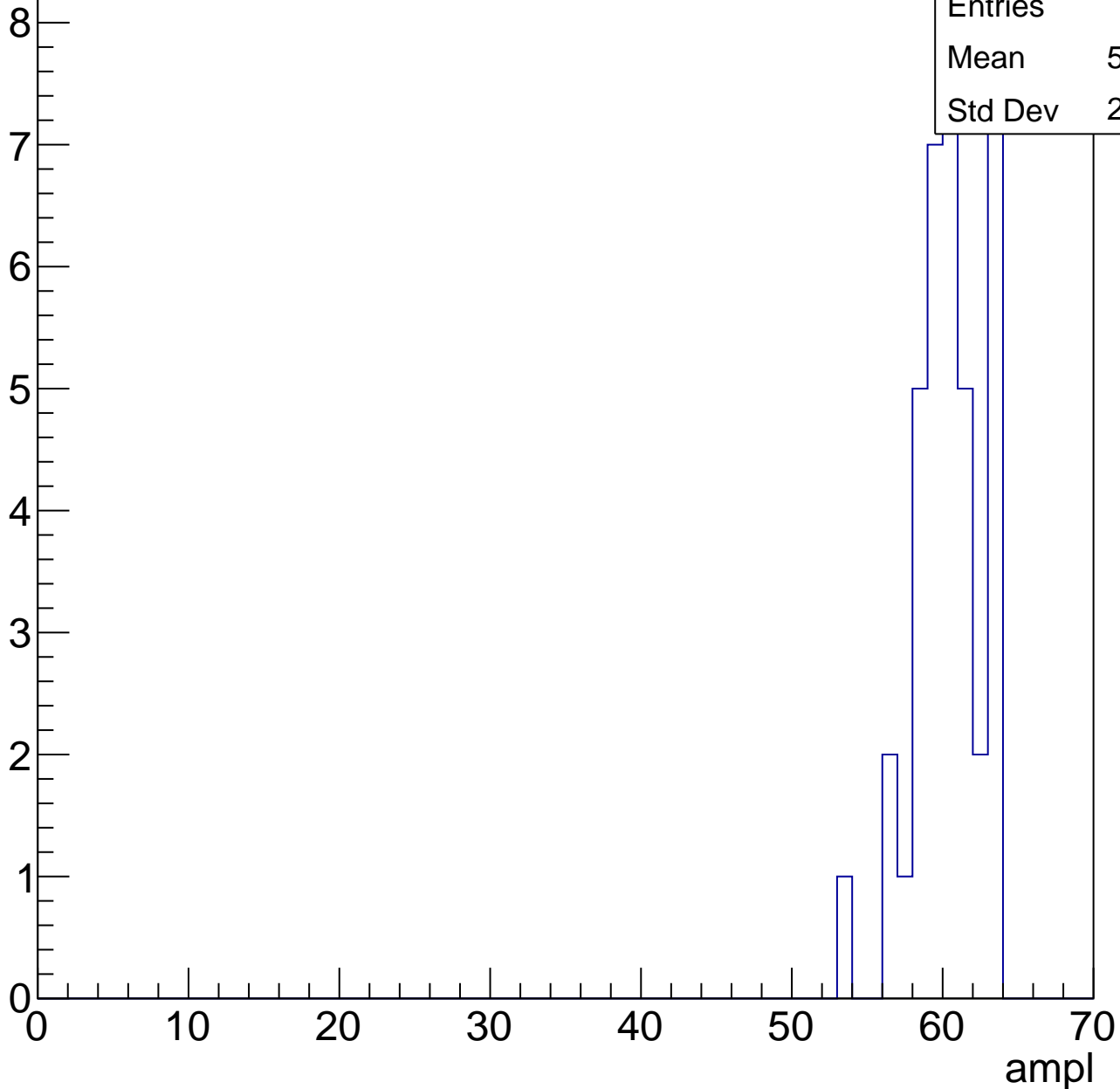


B1L102S, U26-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.95
Std Dev	2.275

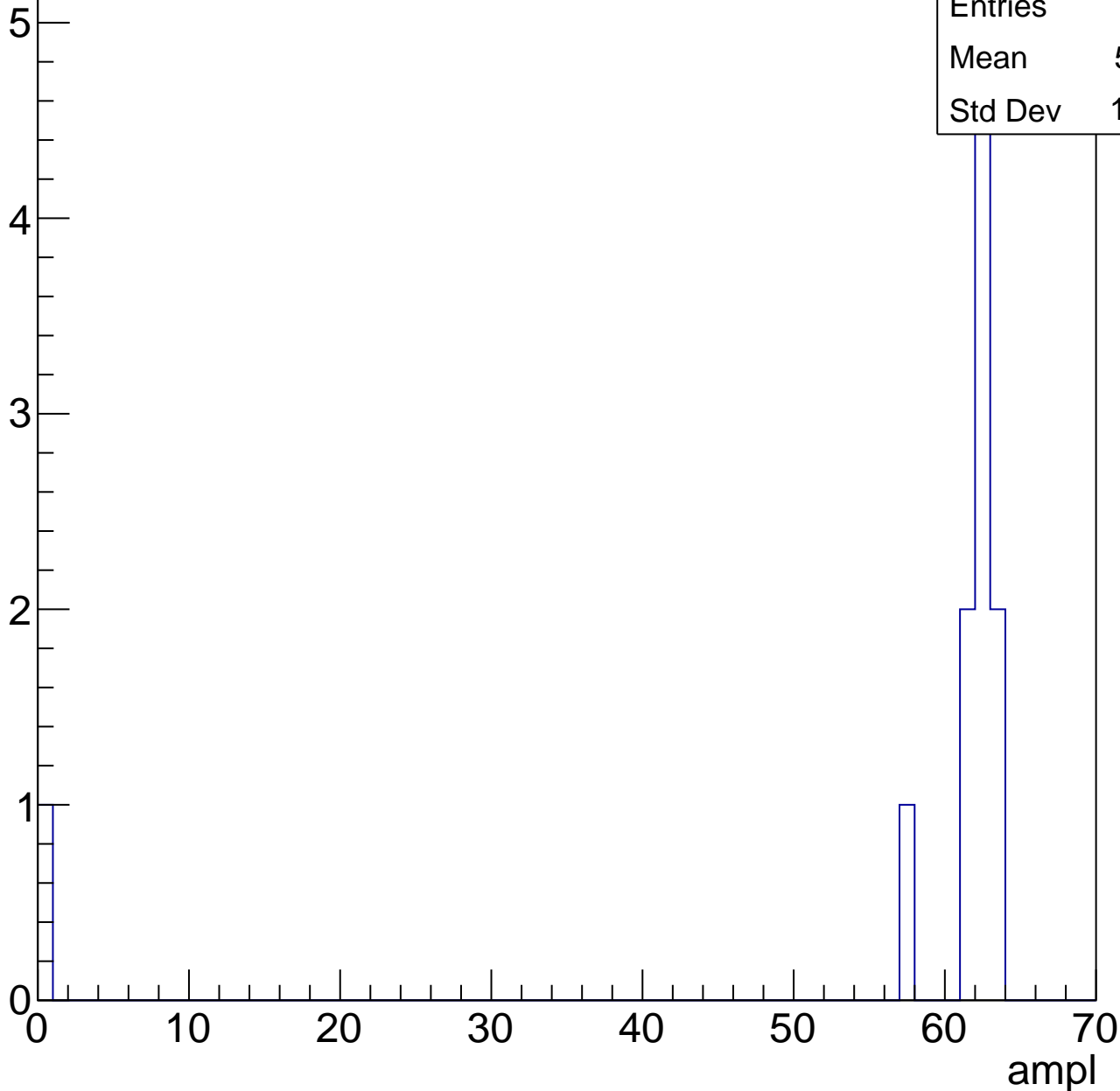


B1L102S, U26-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	55.91
Std Dev	17.75



B1L102S, U26-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch88, adc0

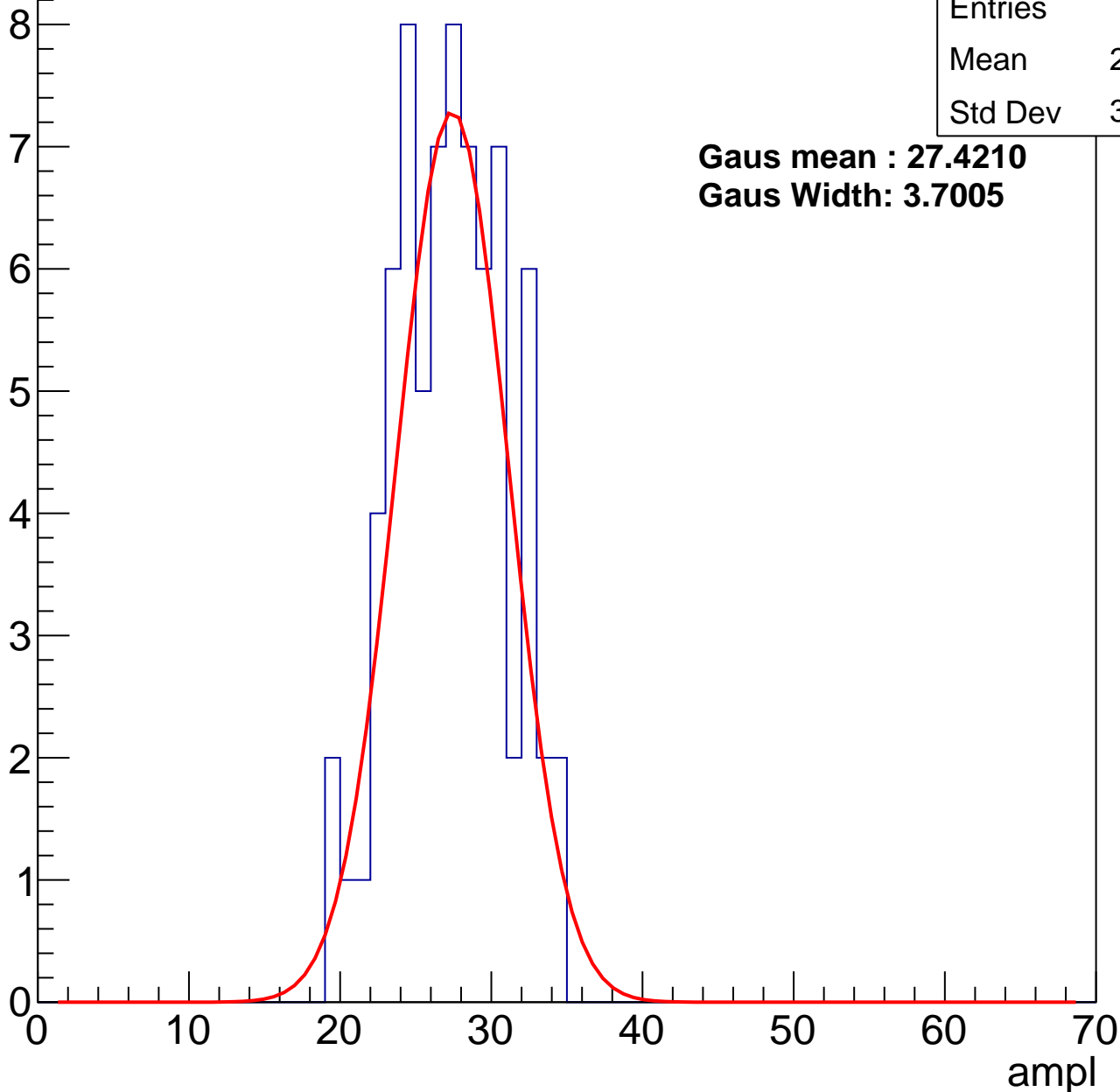
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	26.86
Std Dev	3.588

Gaus mean : 27.4210

Gaus Width: 3.7005



B1L102S, U26-ch88, adc1

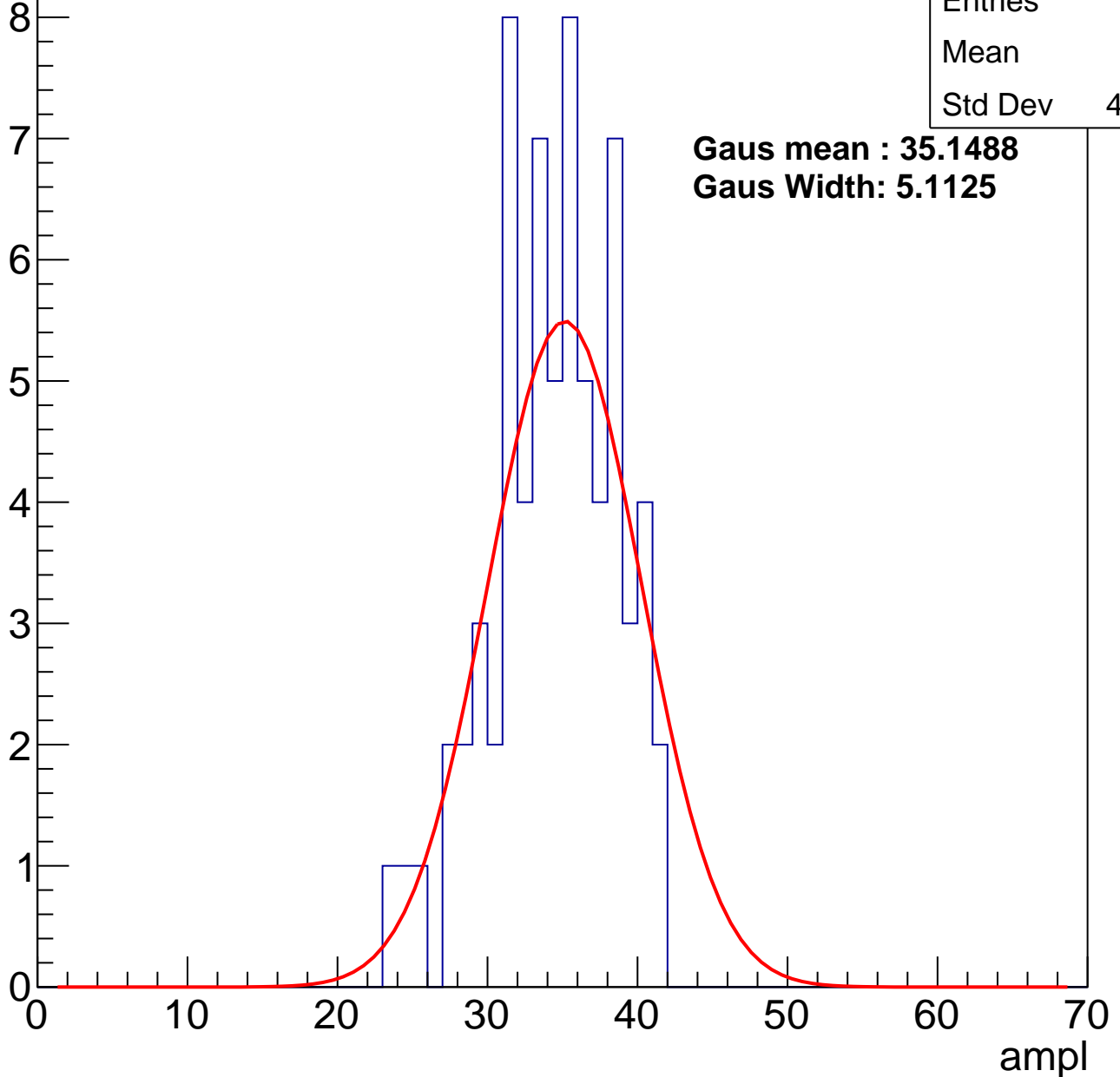
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	33.9
Std Dev	4.111

Gaus mean : 35.1488

Gaus Width: 5.1125



B1L102S, U26-ch88, adc2

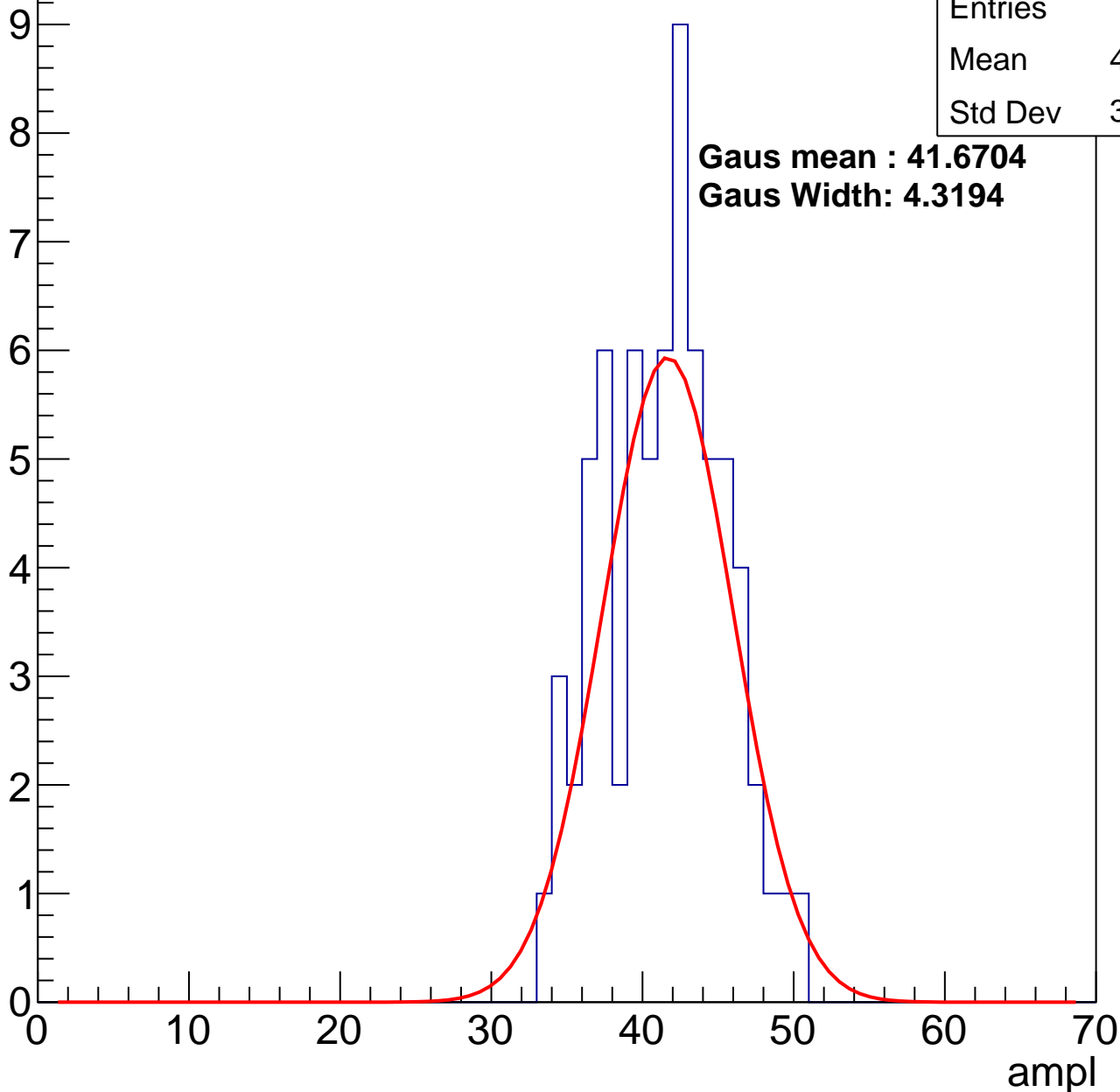
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.99
Std Dev	3.919

Gaus mean : 41.6704

Gaus Width: 4.3194

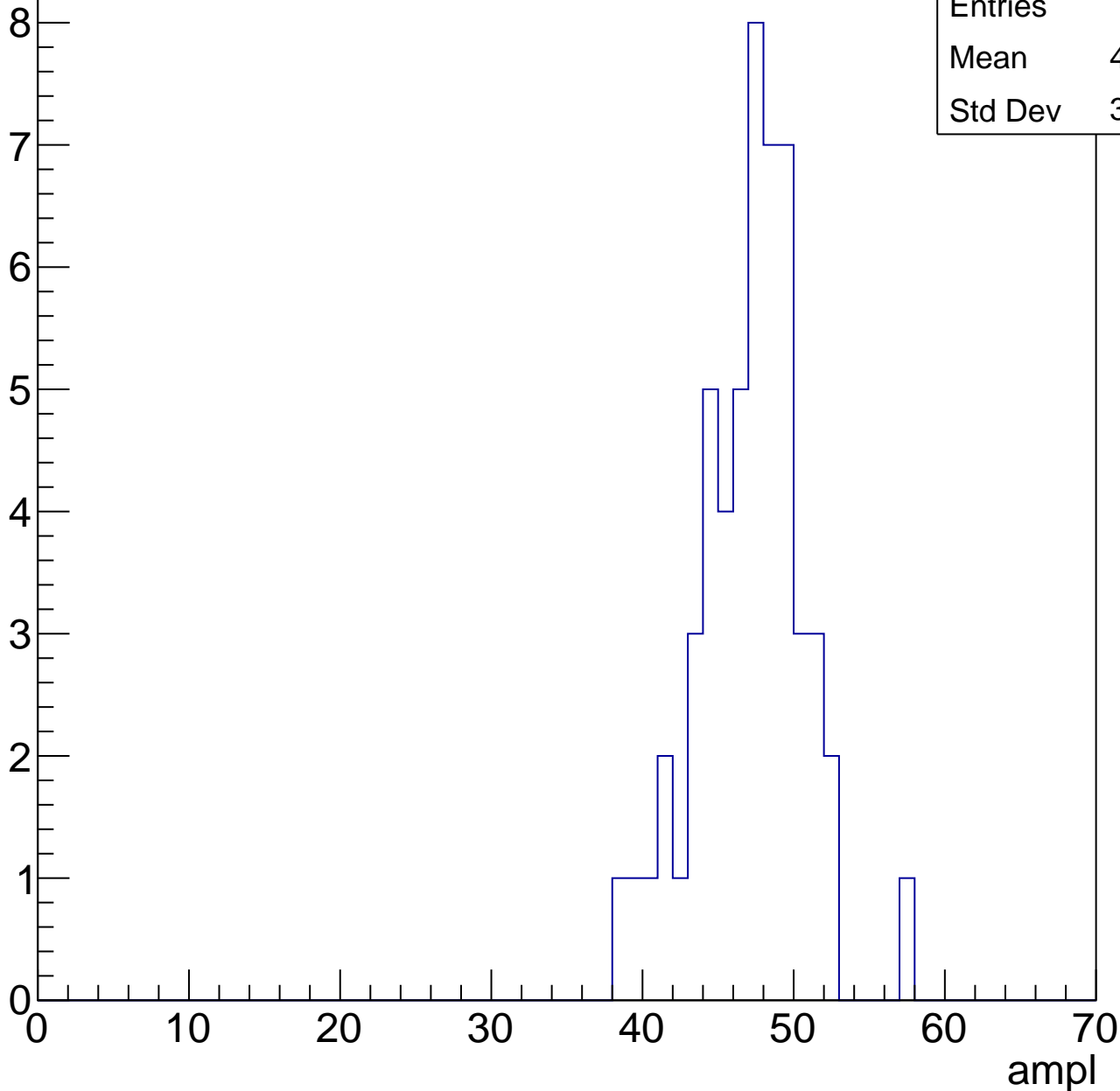


B1L102S, U26-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	46.65
Std Dev	3.492

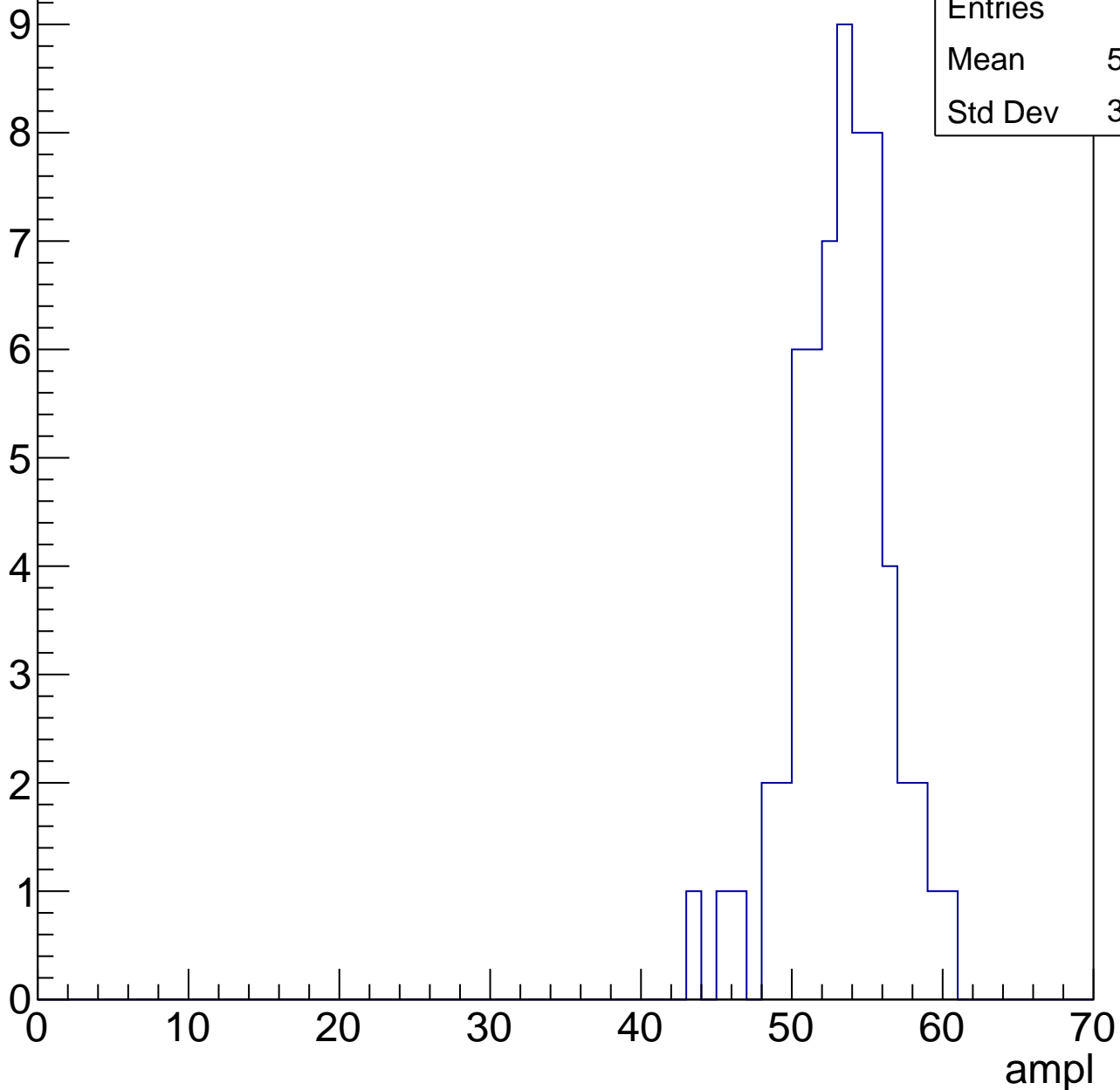


B1L102S, U26-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	52.79
Std Dev	3.189

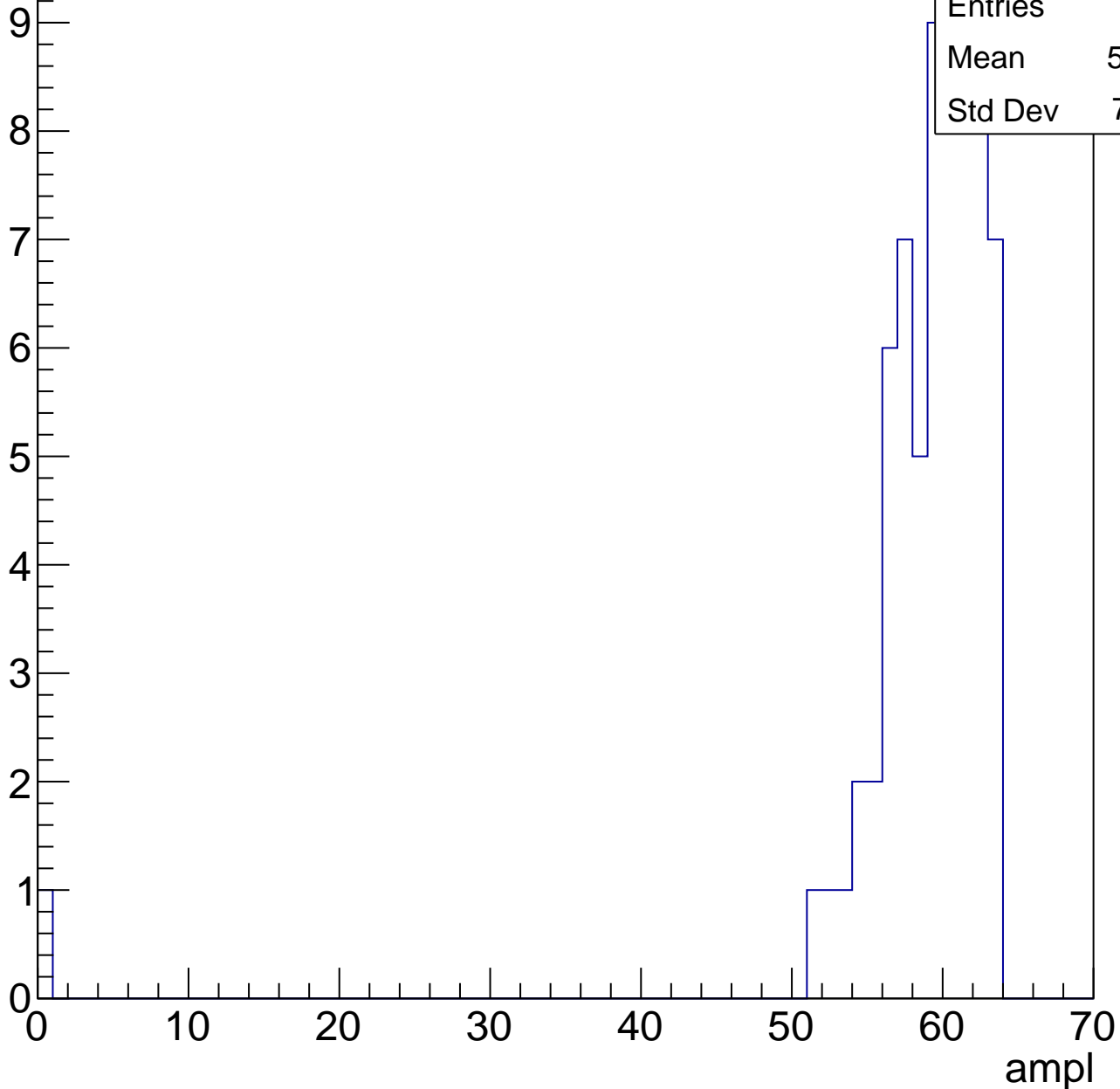


B1L102S, U26-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	58.13
Std Dev	7.701



B1L102S, U26-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	60.67
Std Dev	2

B1L102S, U26-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch89, adc0

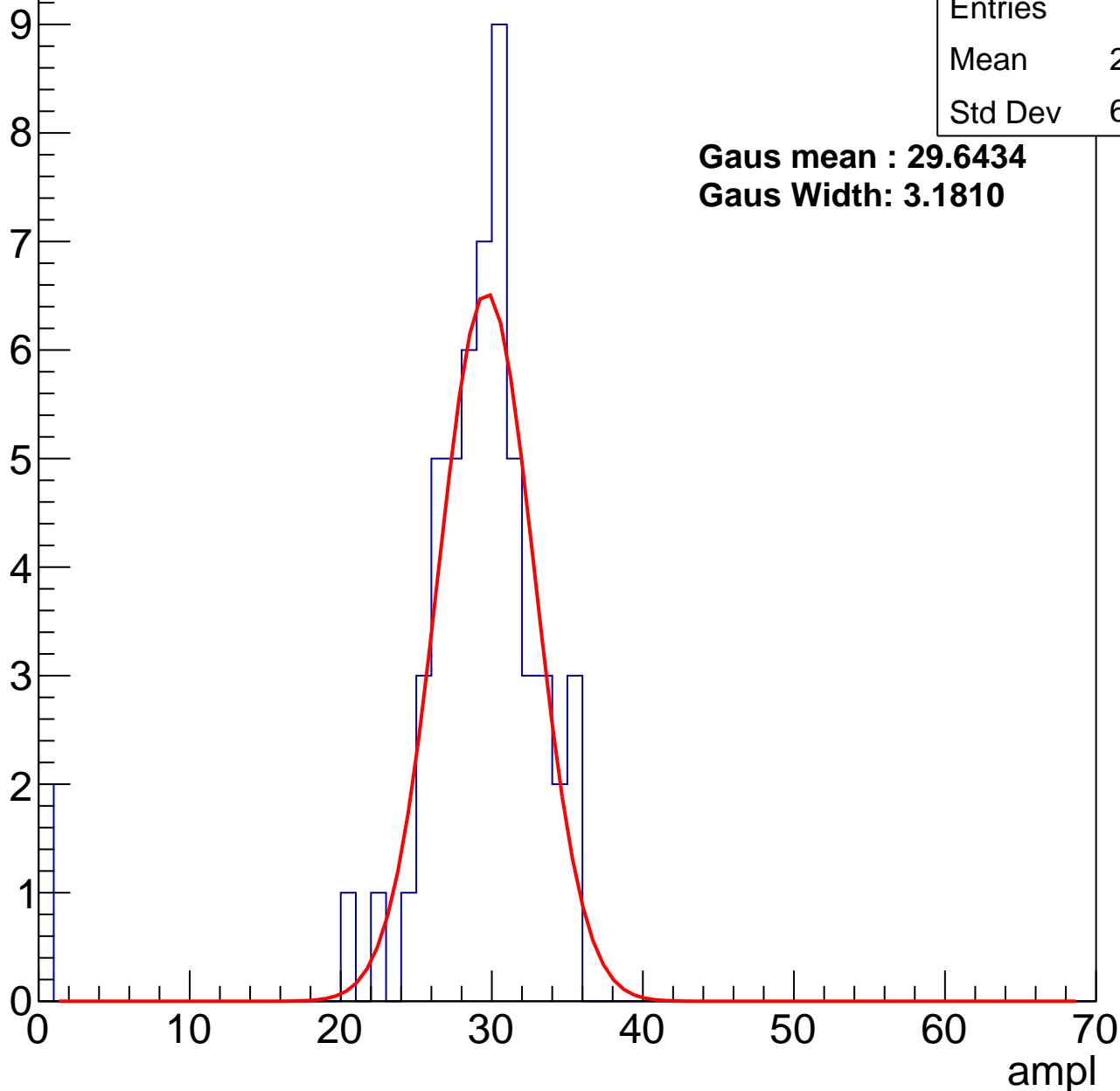
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.04
Std Dev	6.219

Gaus mean : 29.6434

Gaus Width: 3.1810



B1L102S, U26-ch89, adc1

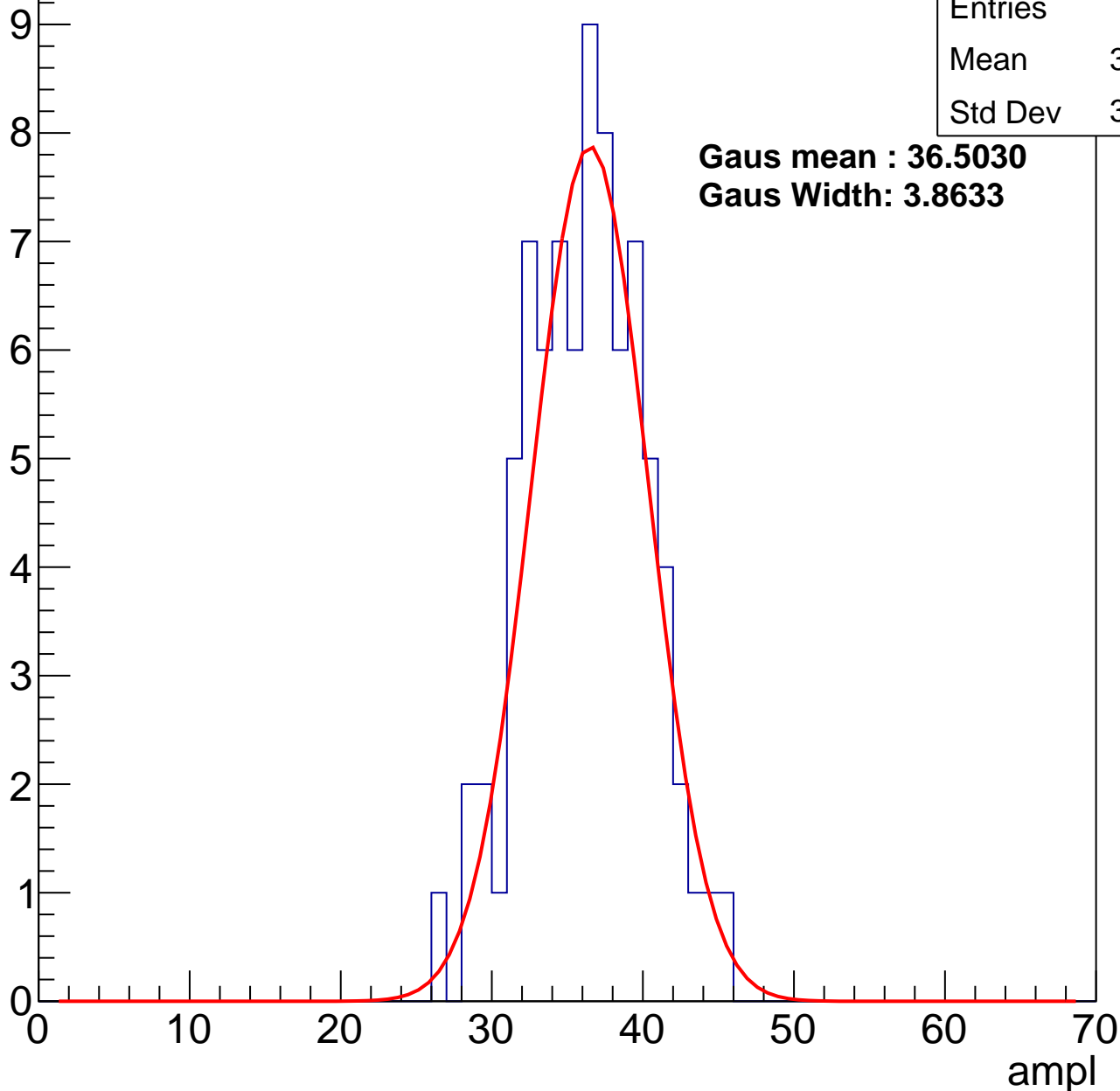
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.75
Std Dev	3.886

Gaus mean : 36.5030

Gaus Width: 3.8633



B1L102S, U26-ch89, adc2

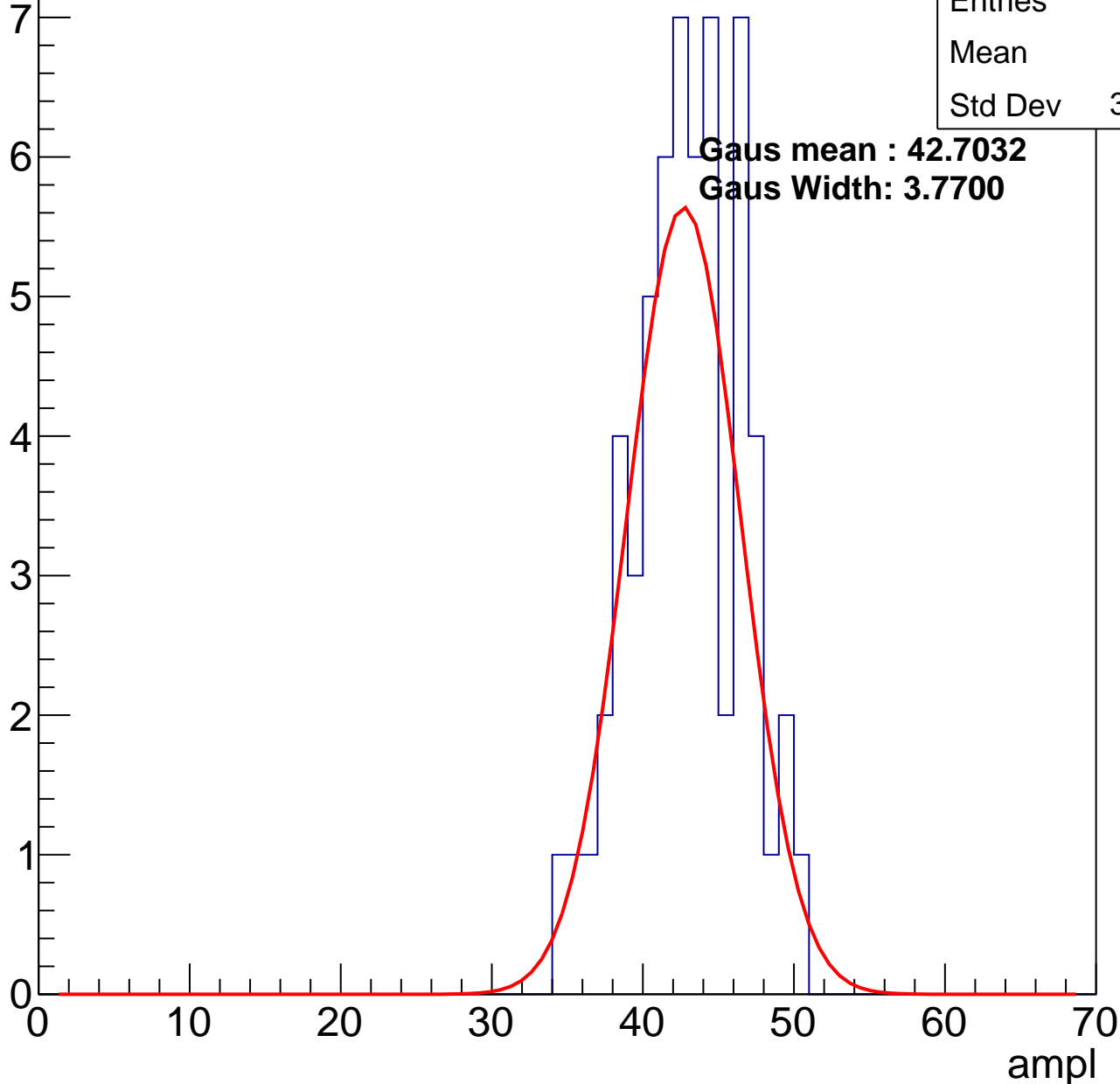
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.5
Std Dev	3.566

Gaus mean : 42.7032

Gaus Width: 3.7700



B1L102S, U26-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	49.35
Std Dev	3.657

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

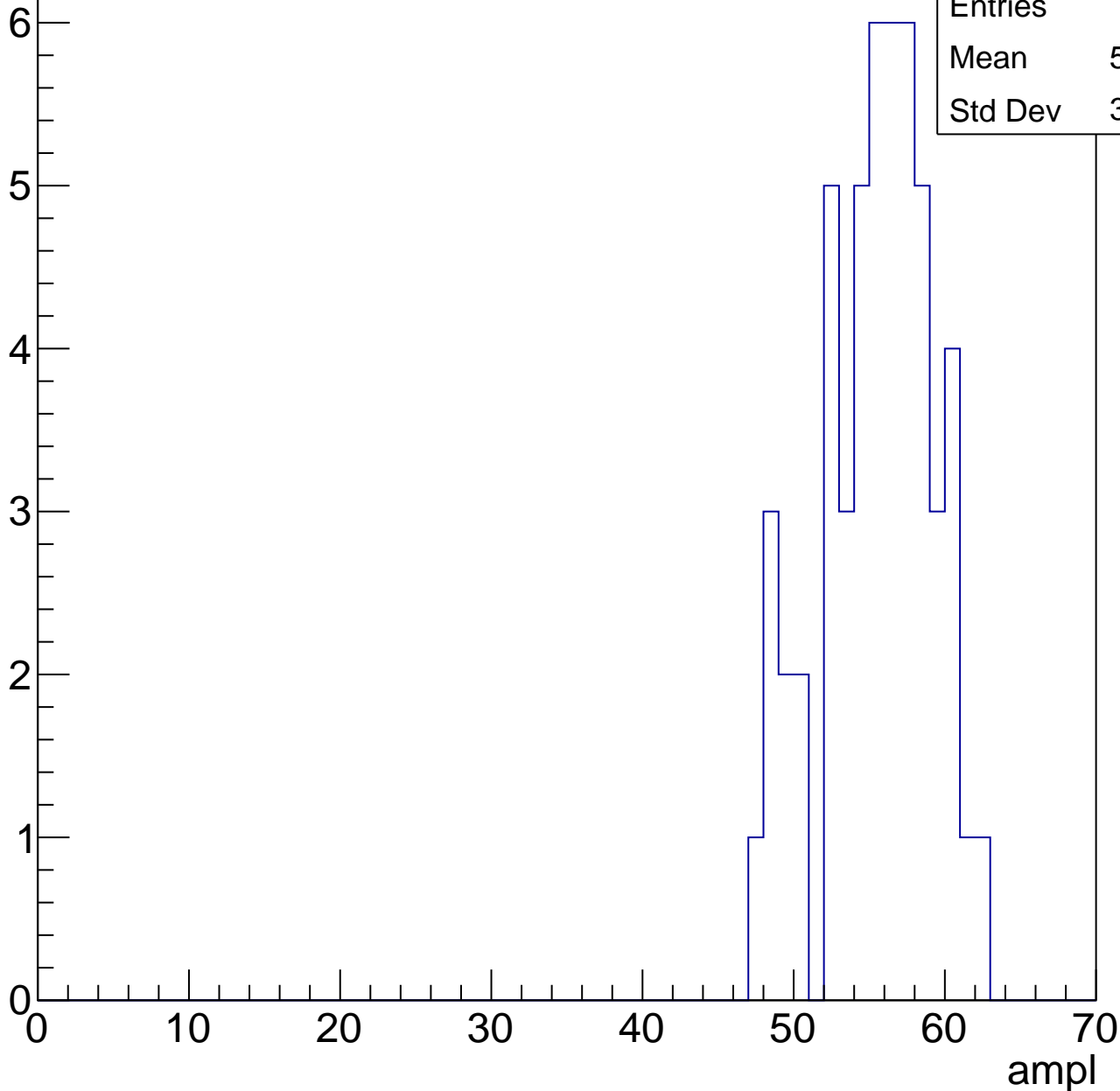
70

B1L102S, U26-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.02
Std Dev	3.642

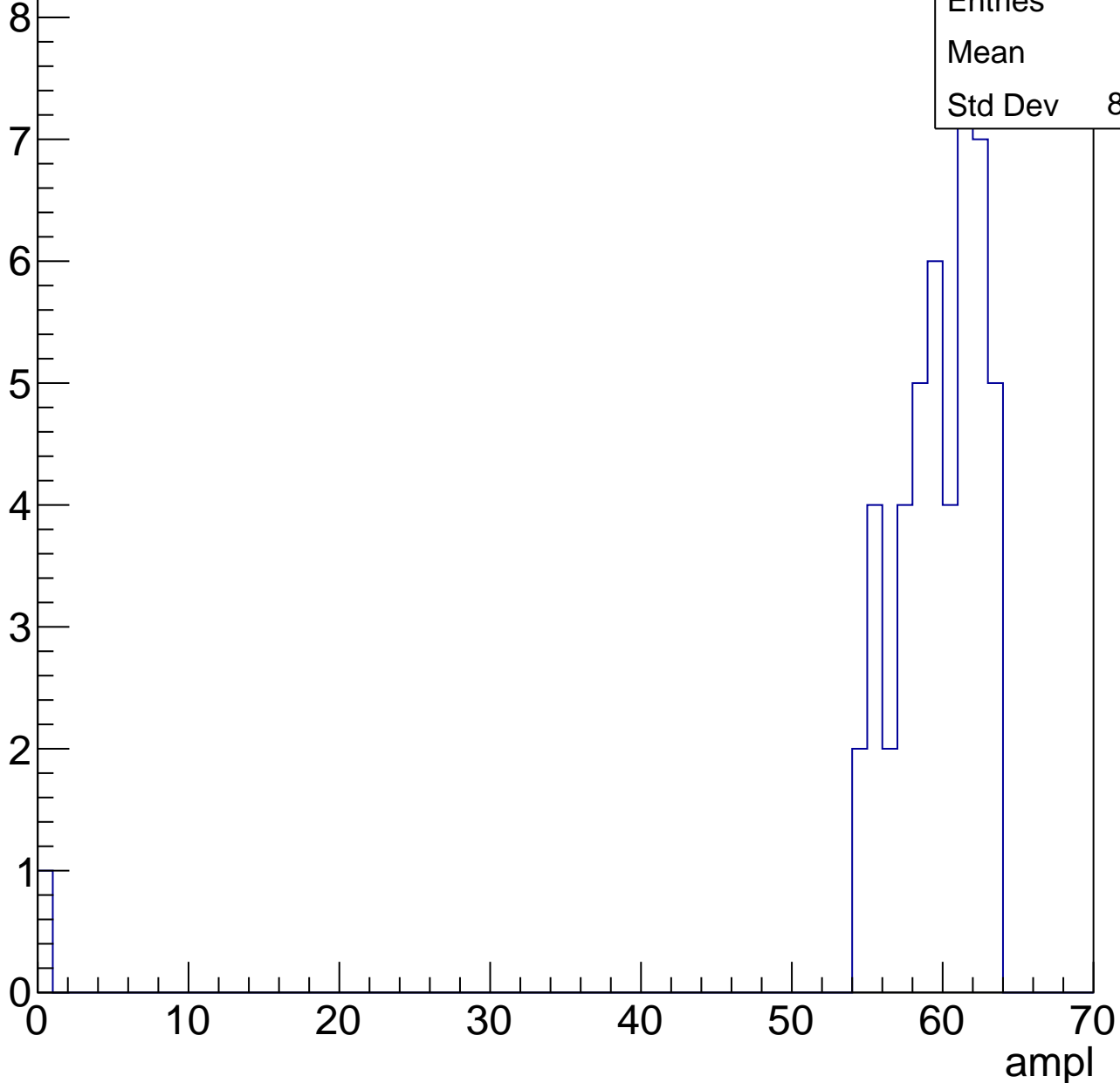


B1L102S, U26-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.1
Std Dev	8.868

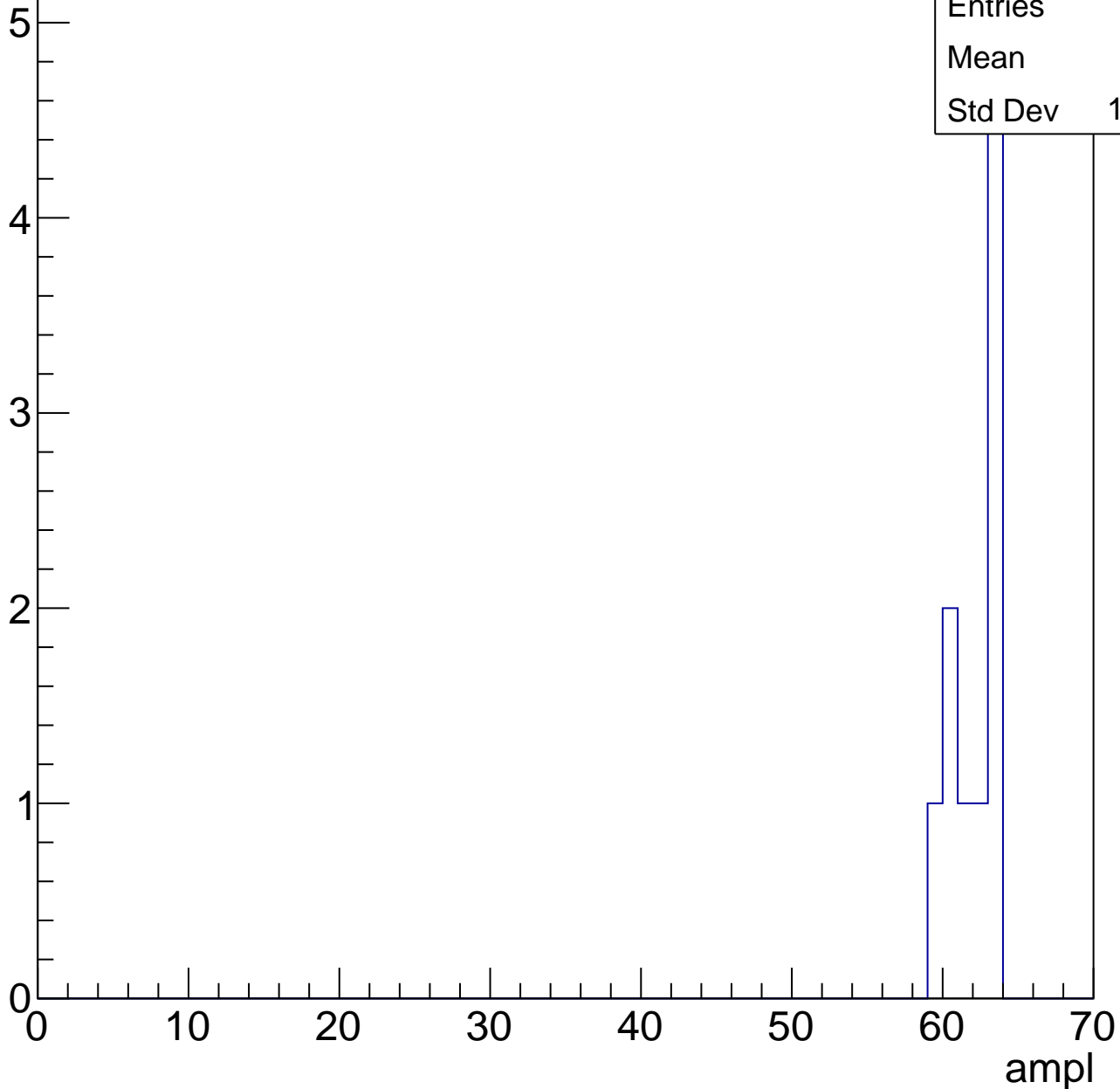


B1L102S, U26-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.7
Std Dev	1.487



B1L102S, U26-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch90, adc0

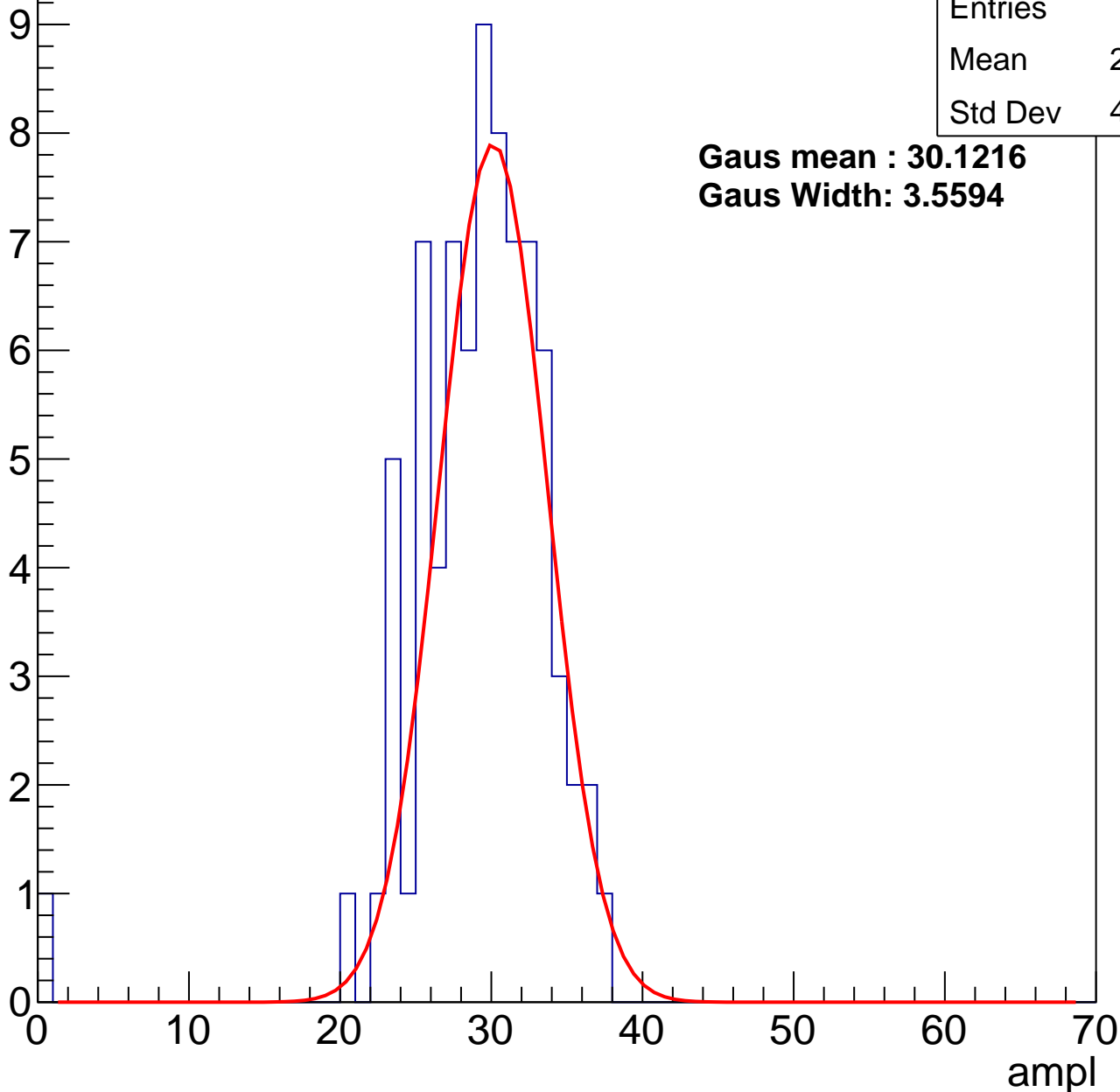
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.69
Std Dev	4.876

Gaus mean : 30.1216

Gaus Width: 3.5594



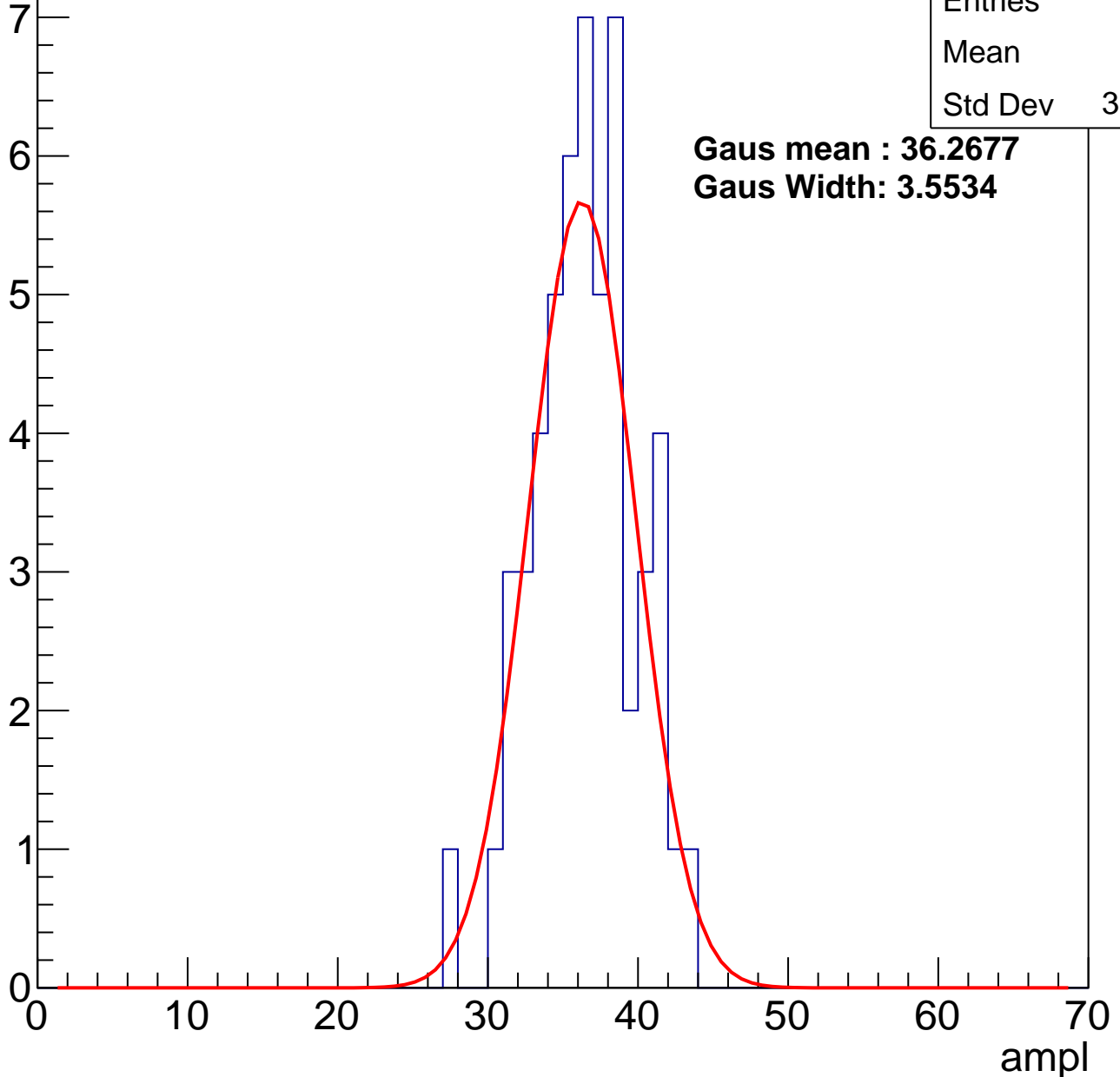
B1L102S, U26-ch90, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	36
Std Dev	3.325

Gaus mean : 36.2677
Gaus Width: 3.5534



B1L102S, U26-ch90, adc2

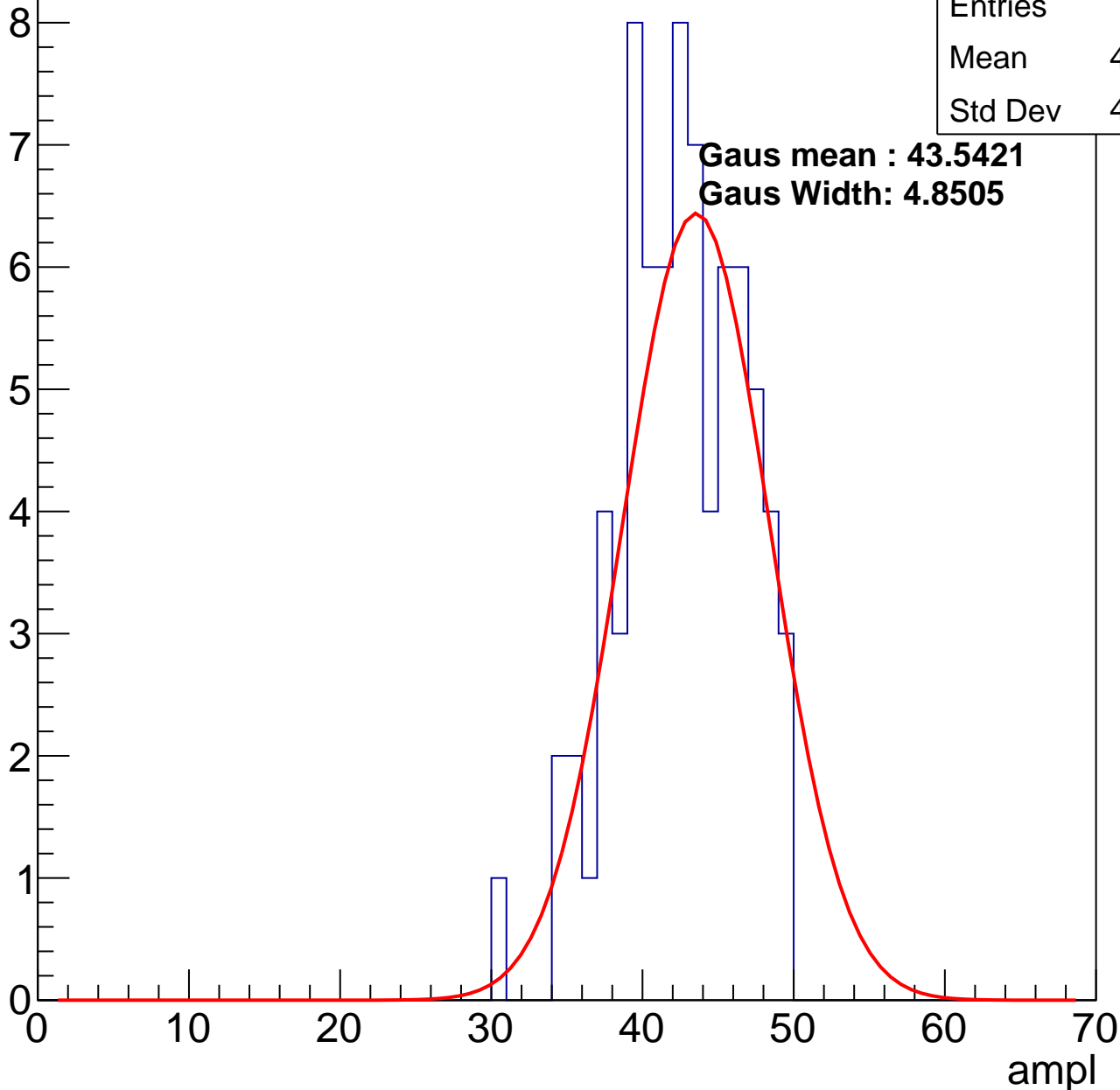
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.07
Std Dev	4.047

Gaus mean : 43.5421

Gaus Width: 4.8505

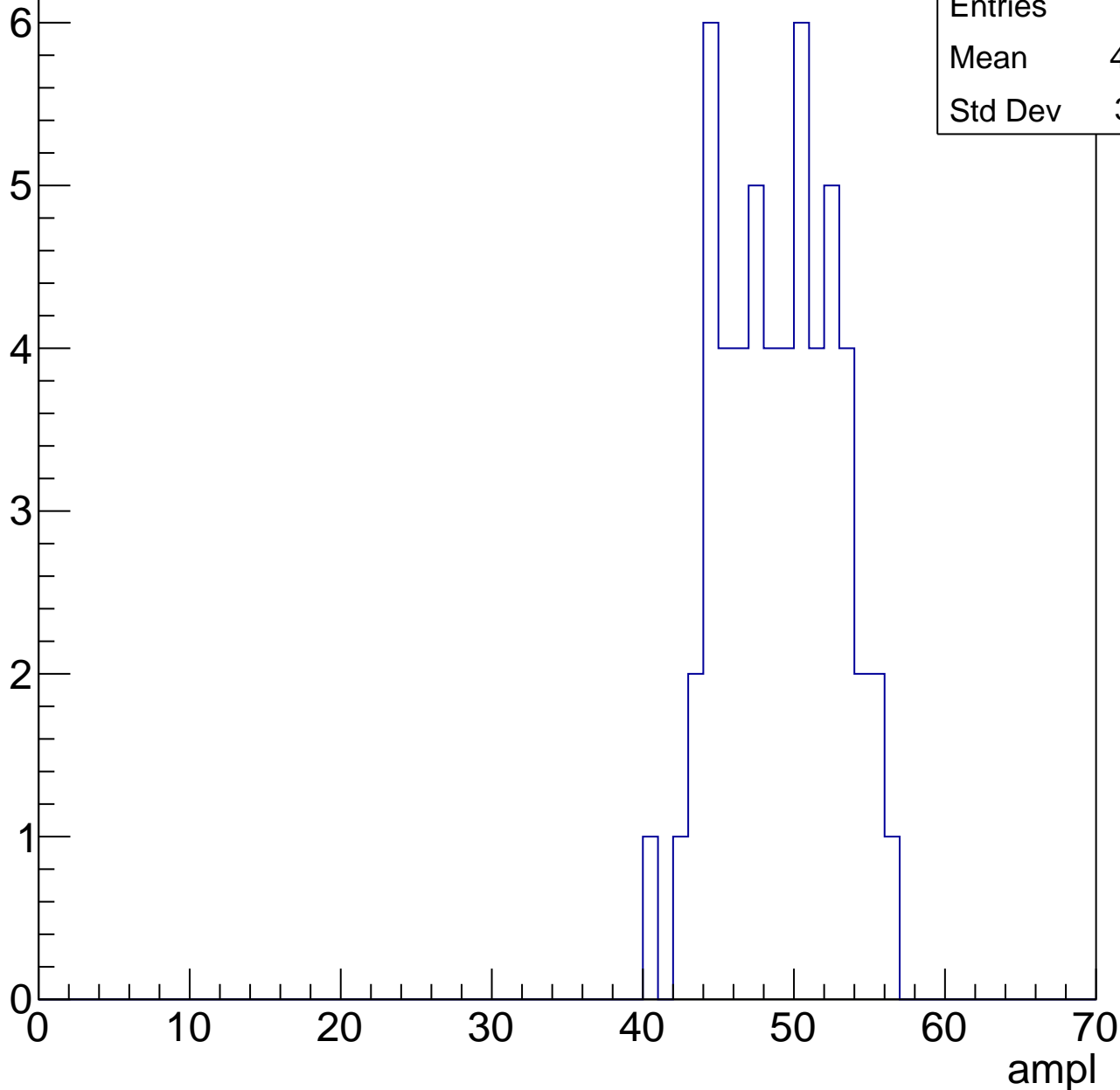


B1L102S, U26-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

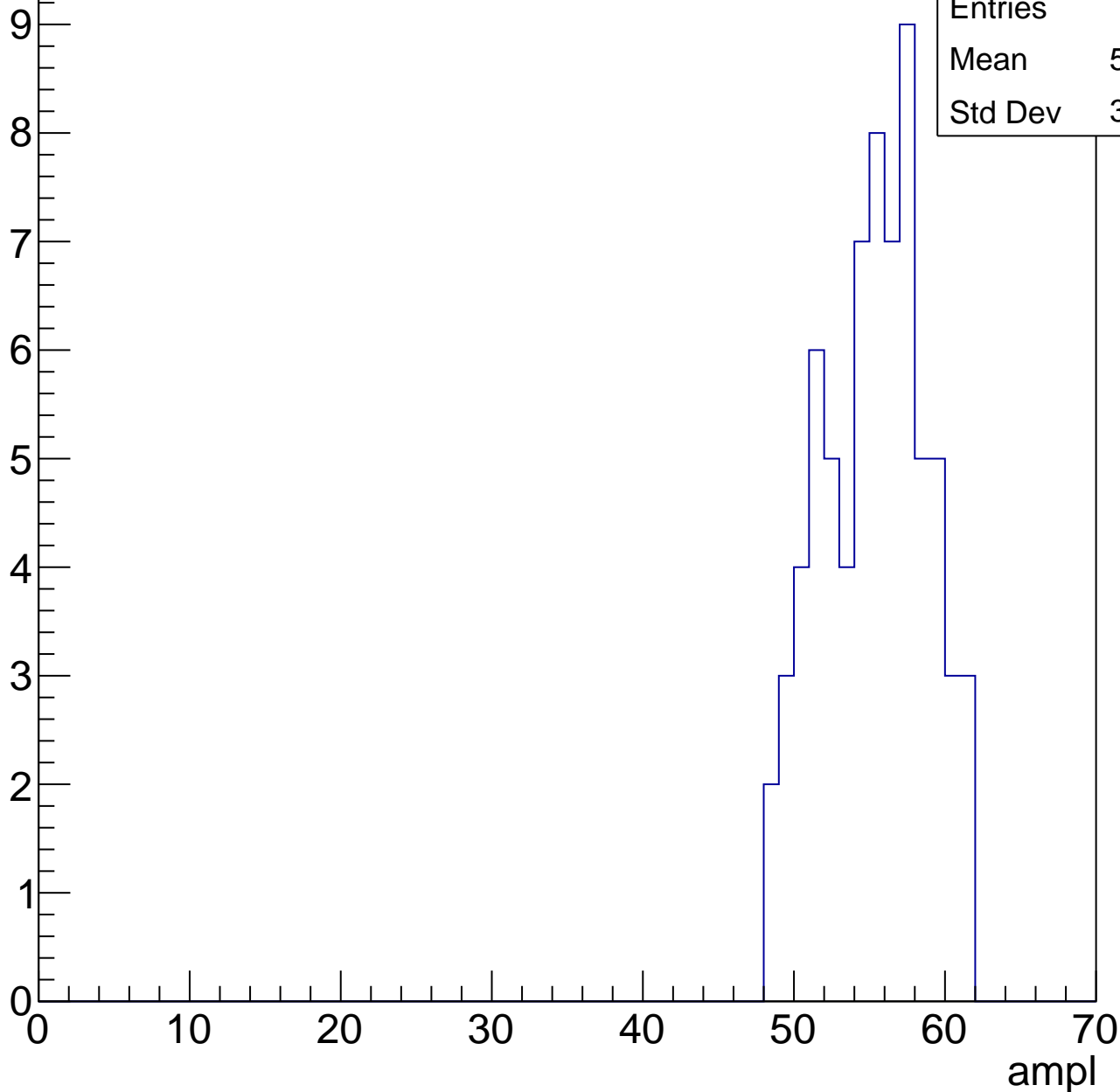
Entries	55
Mean	48.53
Std Dev	3.731



B1L102S, U26-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



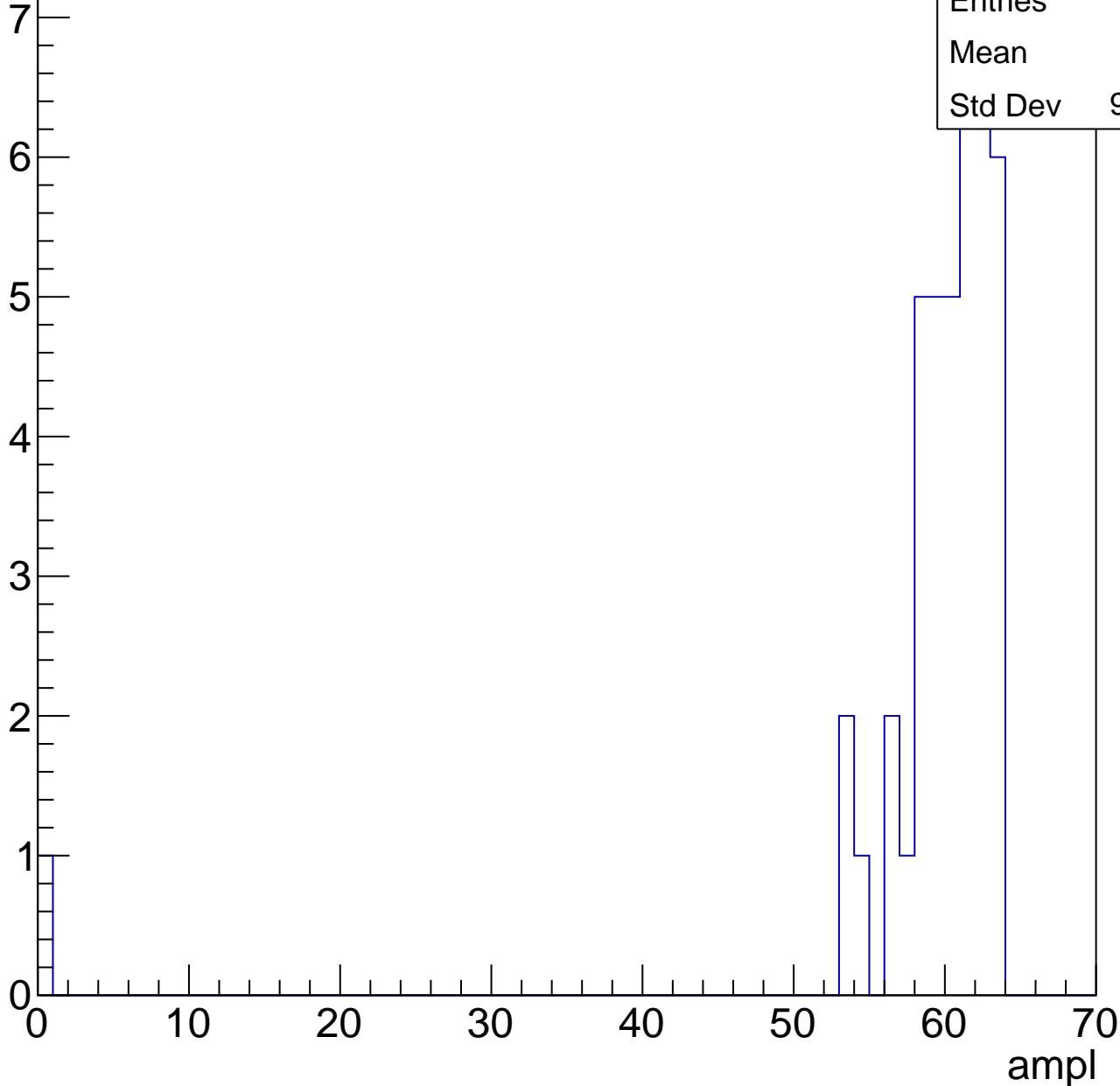
Entries	71
Mean	54.82
Std Dev	3.396

B1L102S, U26-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

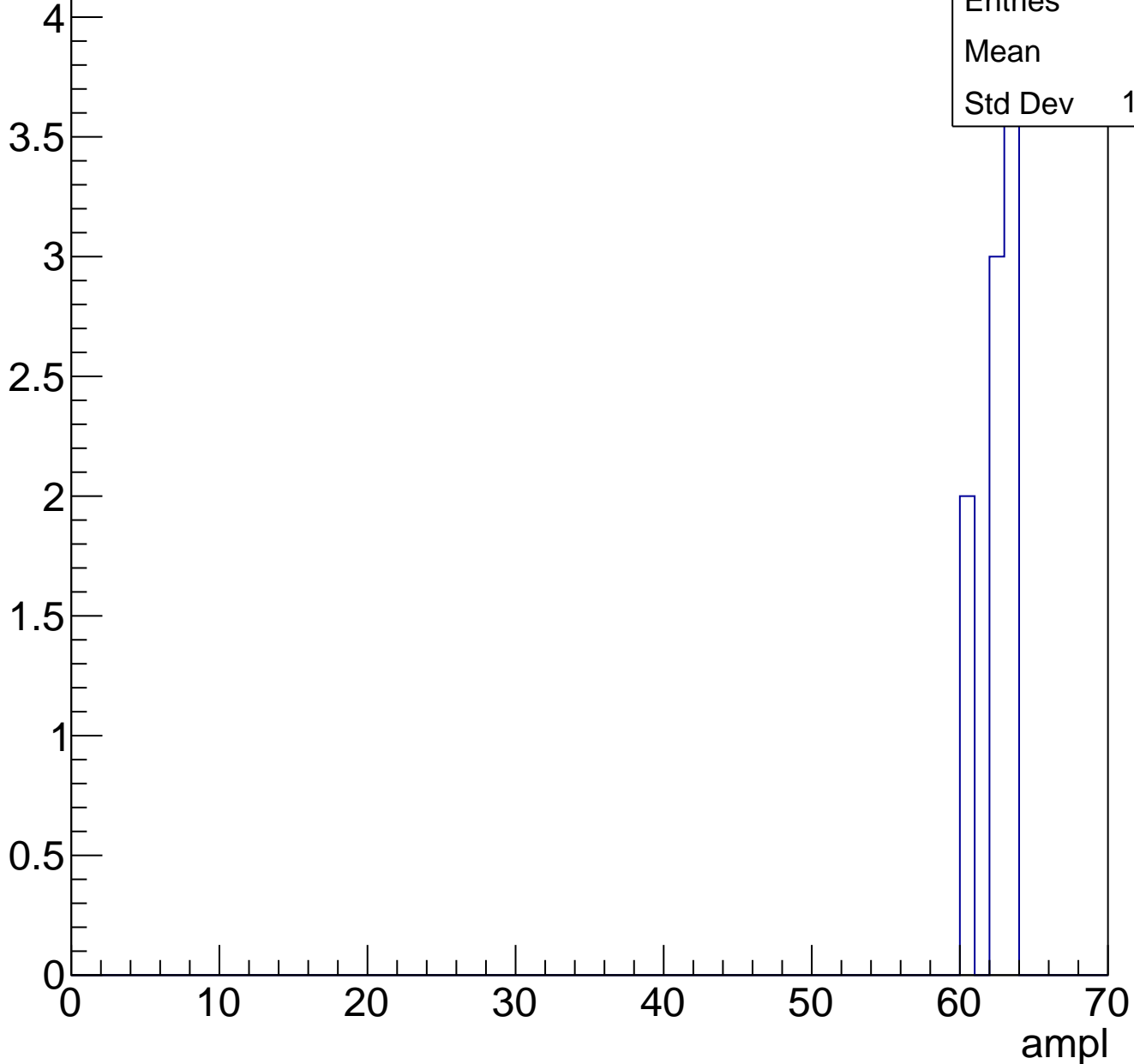
Entries	42
Mean	58.4
Std Dev	9.489



B1L102S, U26-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch91, adc0

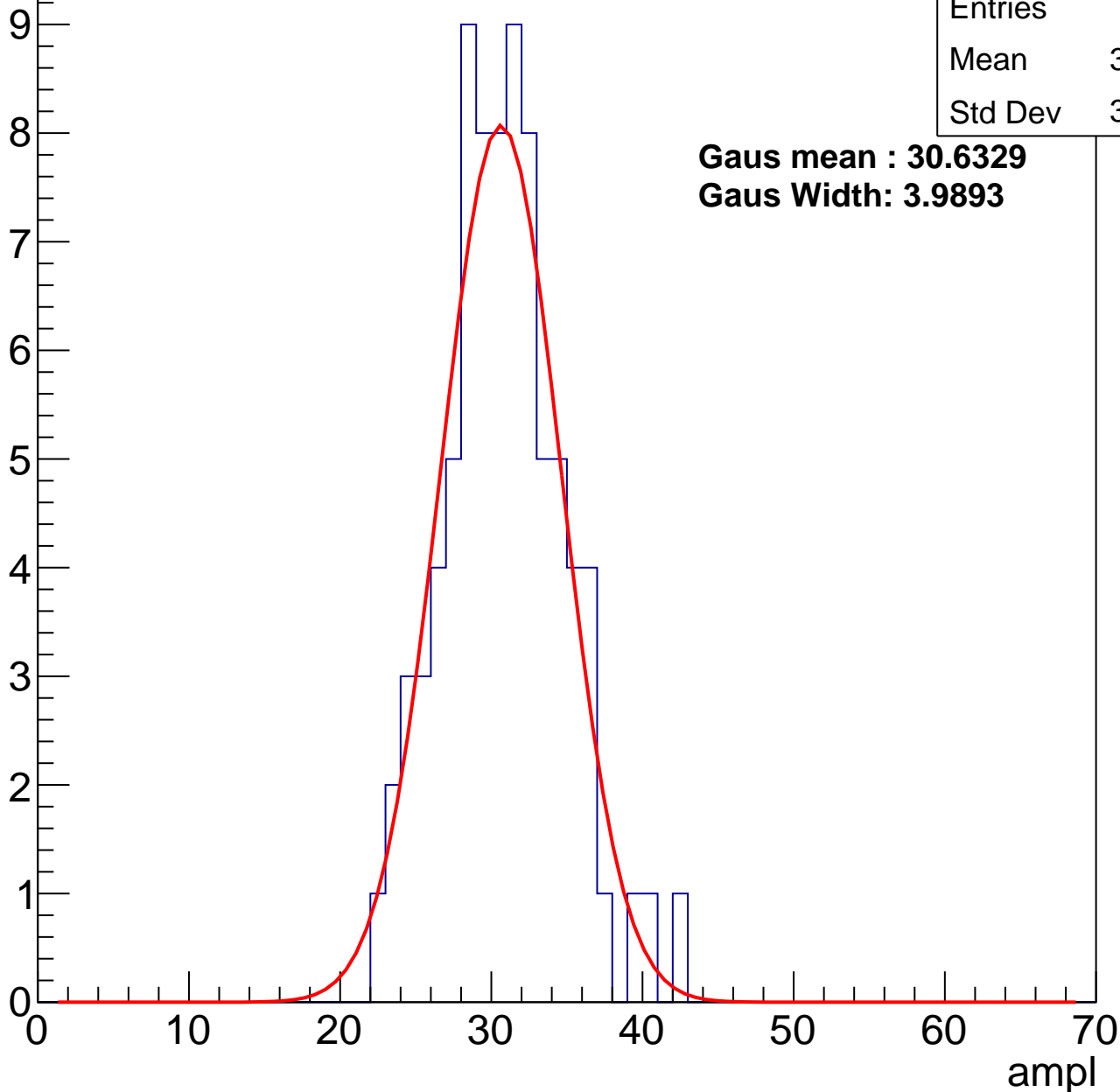
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	30.37
Std Dev	3.937

Gaus mean : 30.6329

Gaus Width: 3.9893



B1L102S, U26-ch91, adc1

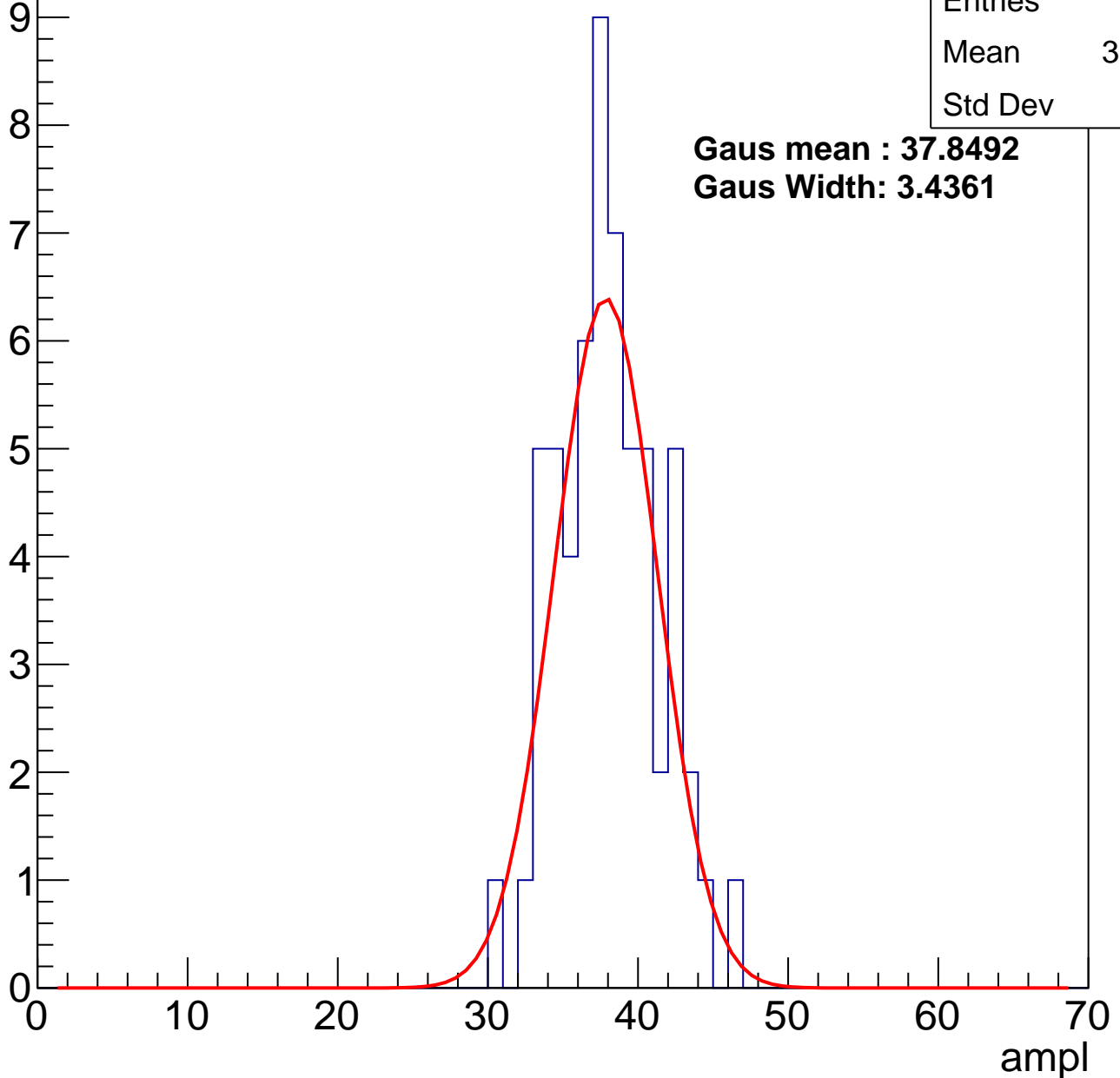
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	37.54
Std Dev	3.28

Gaus mean : 37.8492

Gaus Width: 3.4361



B1L102S, U26-ch91, adc2

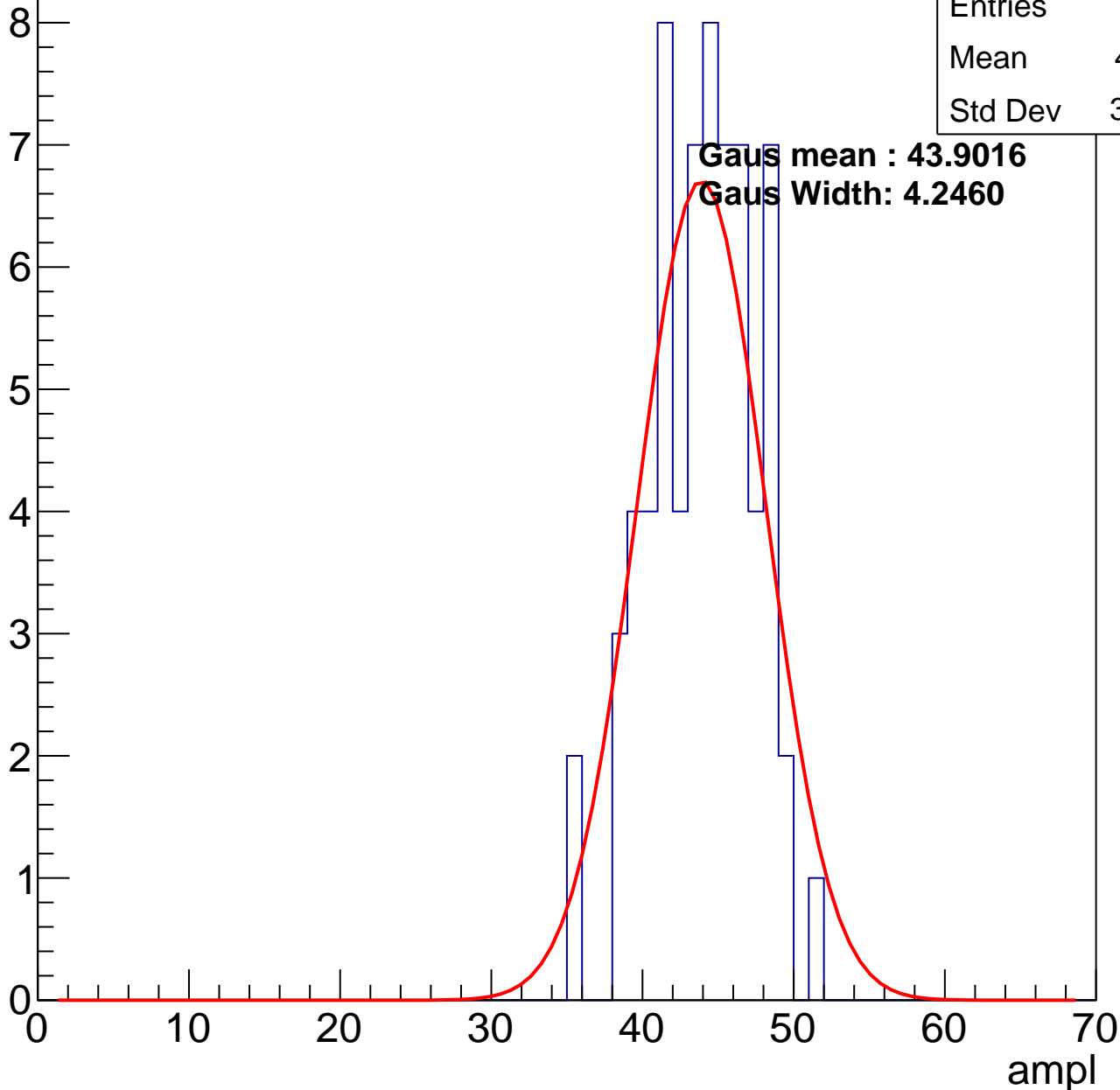
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.51
Std Dev	3.419

Gaus mean : 43.9016

Gaus Width: 4.2460

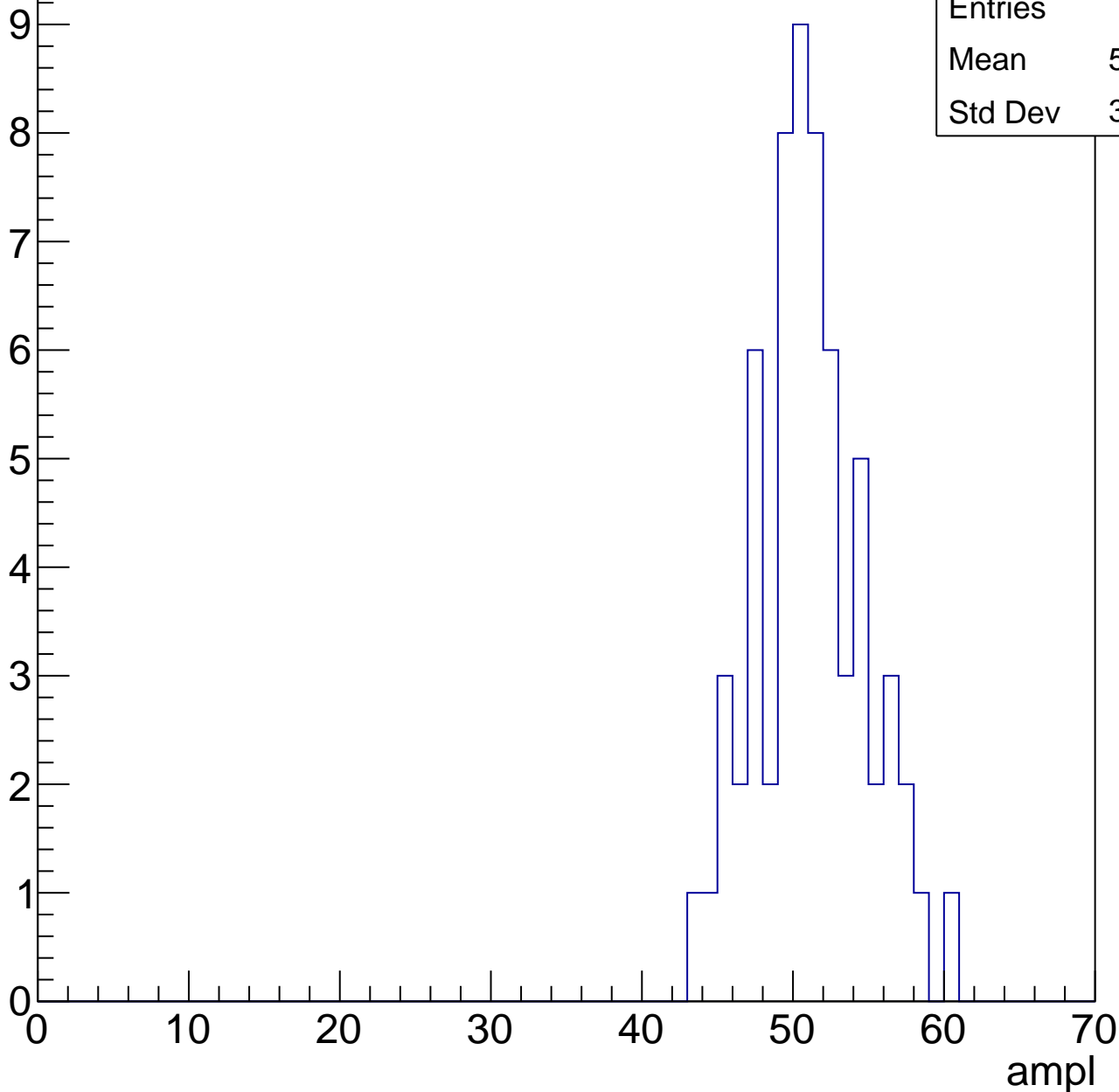


B1L102S, U26-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.68
Std Dev	3.545

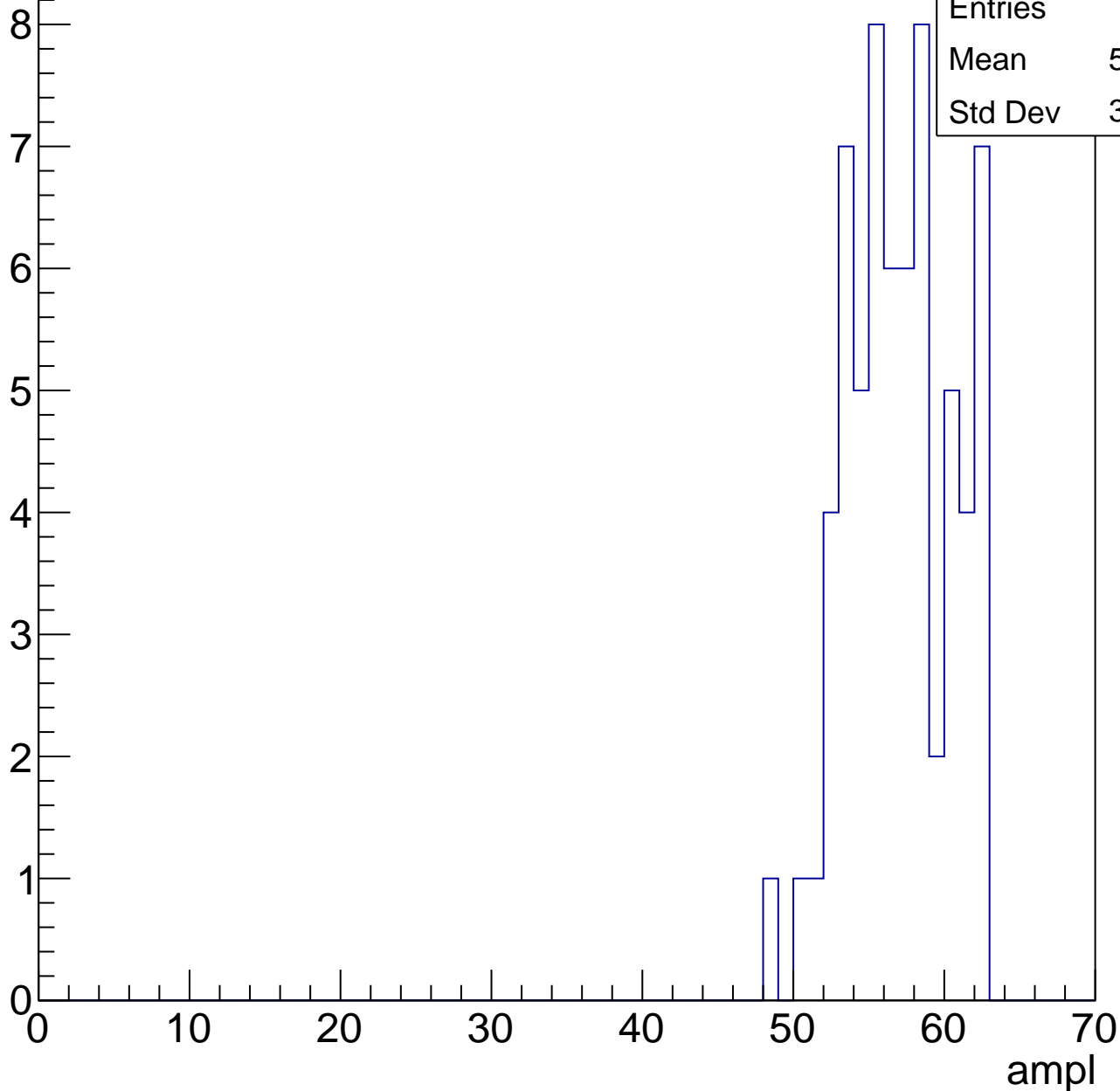


B1L102S, U26-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	56.55
Std Dev	3.392

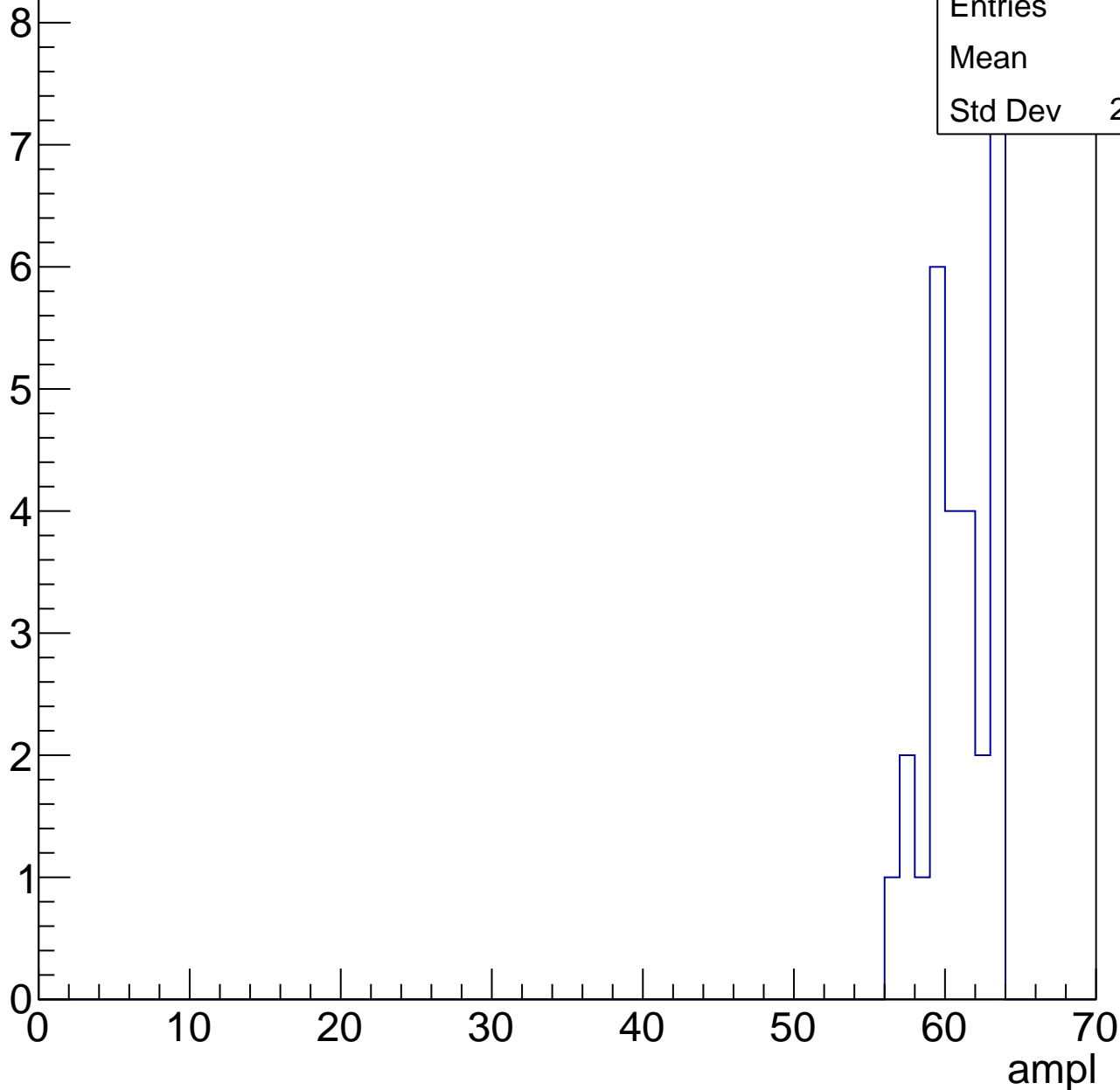


B1L102S, U26-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	60.5
Std Dev	2.079



B1L102S, U26-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

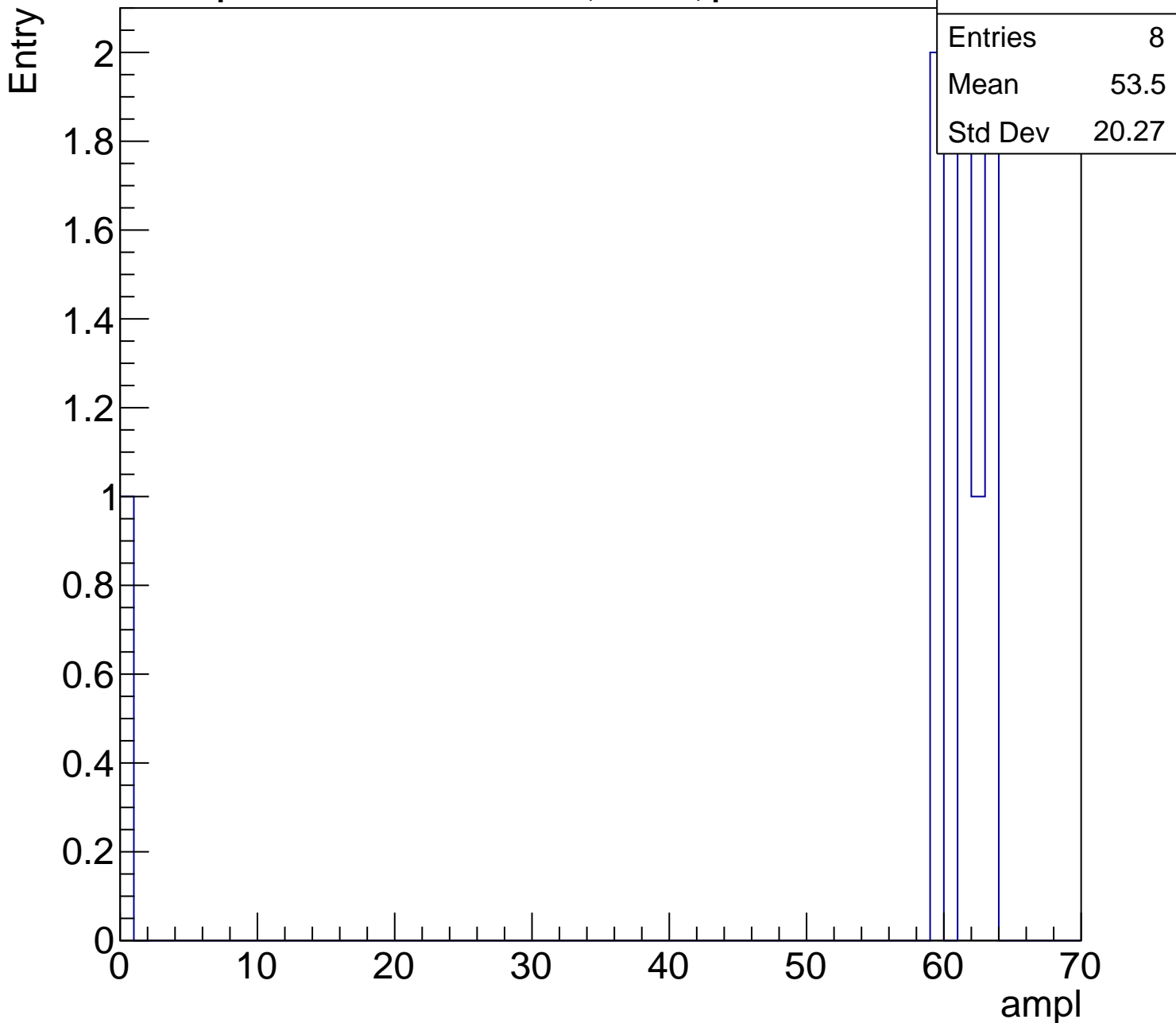
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.5
Std Dev	20.27

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch92, adc0

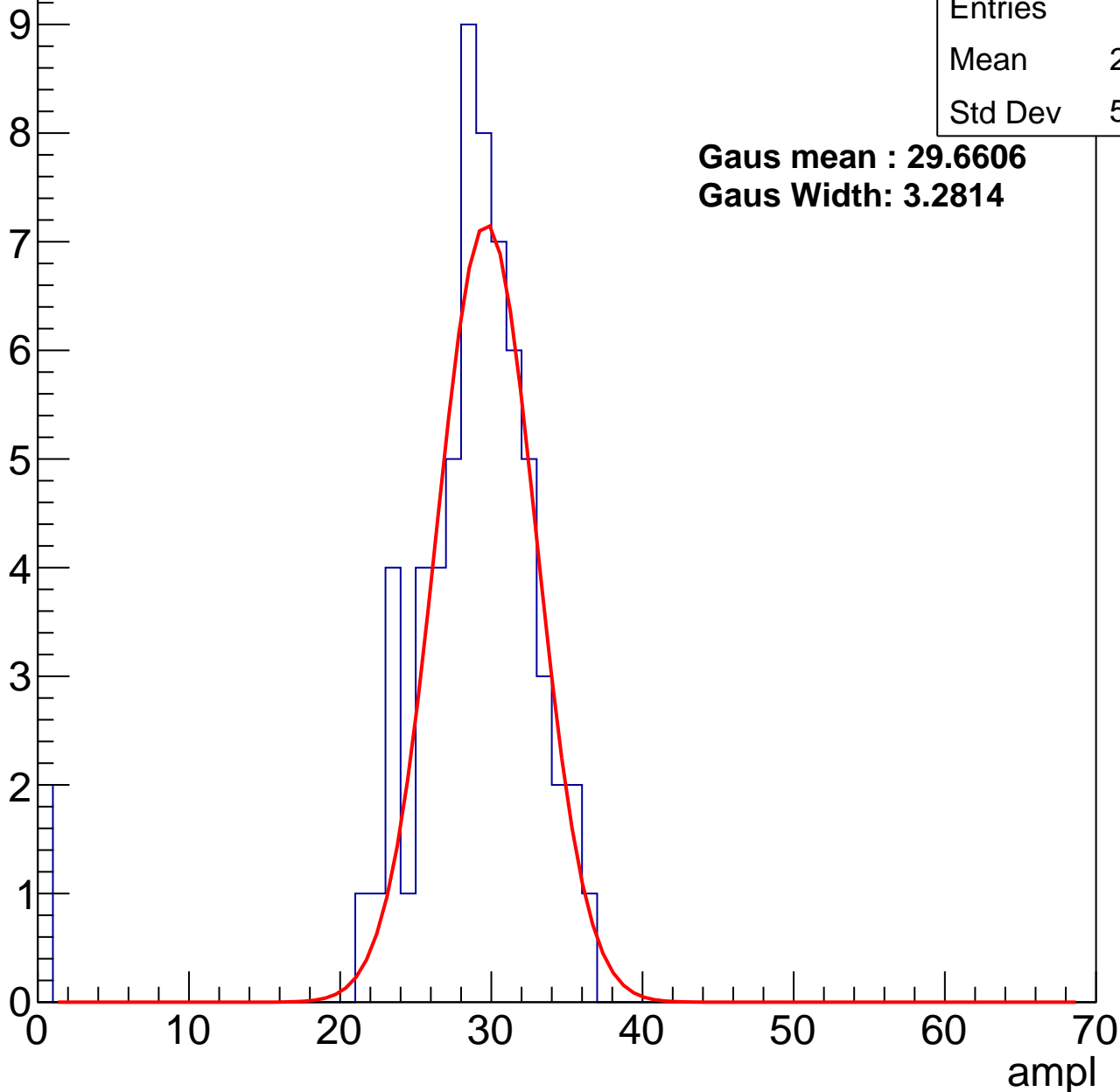
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.86
Std Dev	5.953

Gaus mean : 29.6606

Gaus Width: 3.2814



B1L102S, U26-ch92, adc1

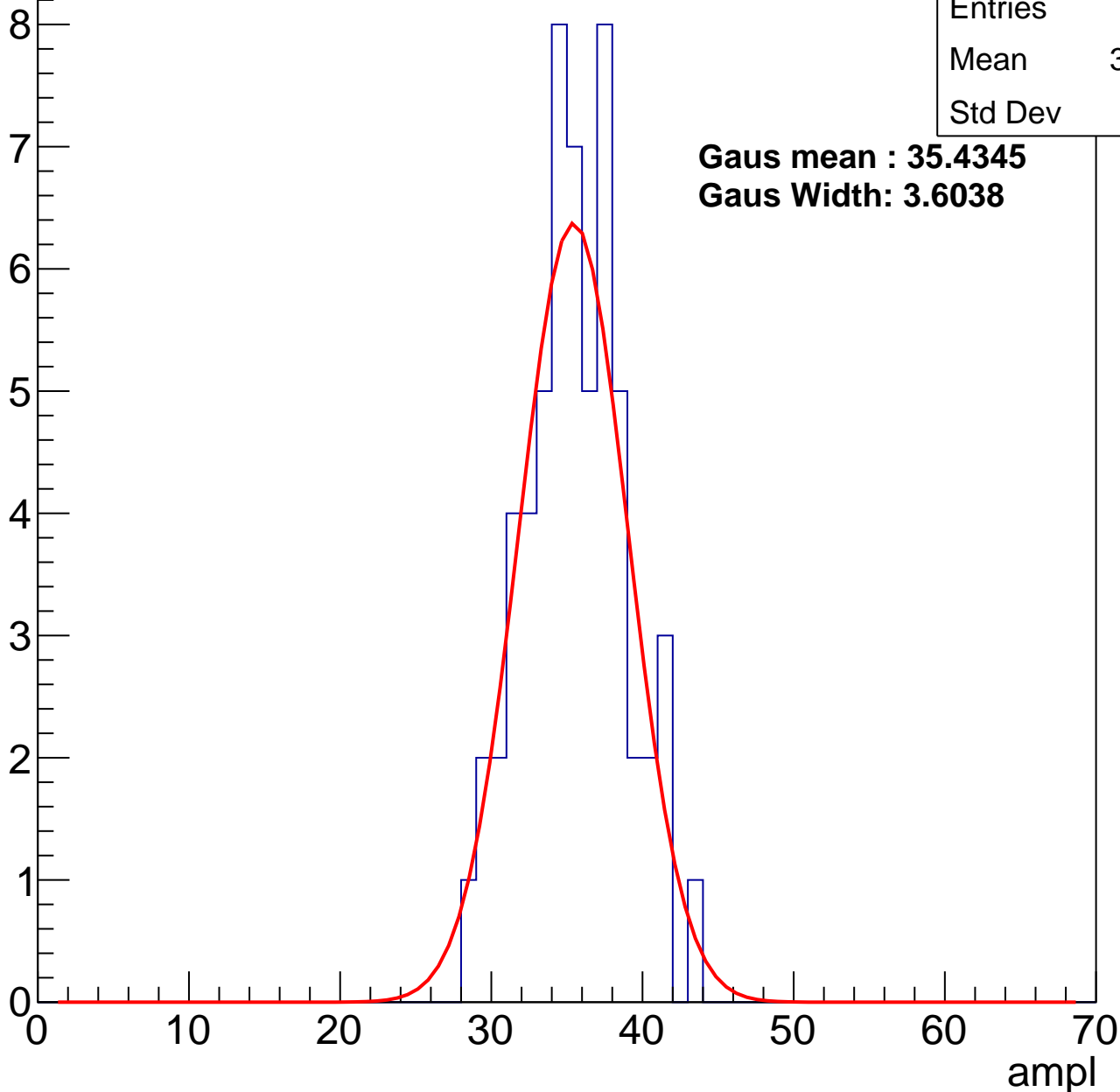
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.08
Std Dev	3.28

Gaus mean : 35.4345

Gaus Width: 3.6038



B1L102S, U26-ch92, adc2

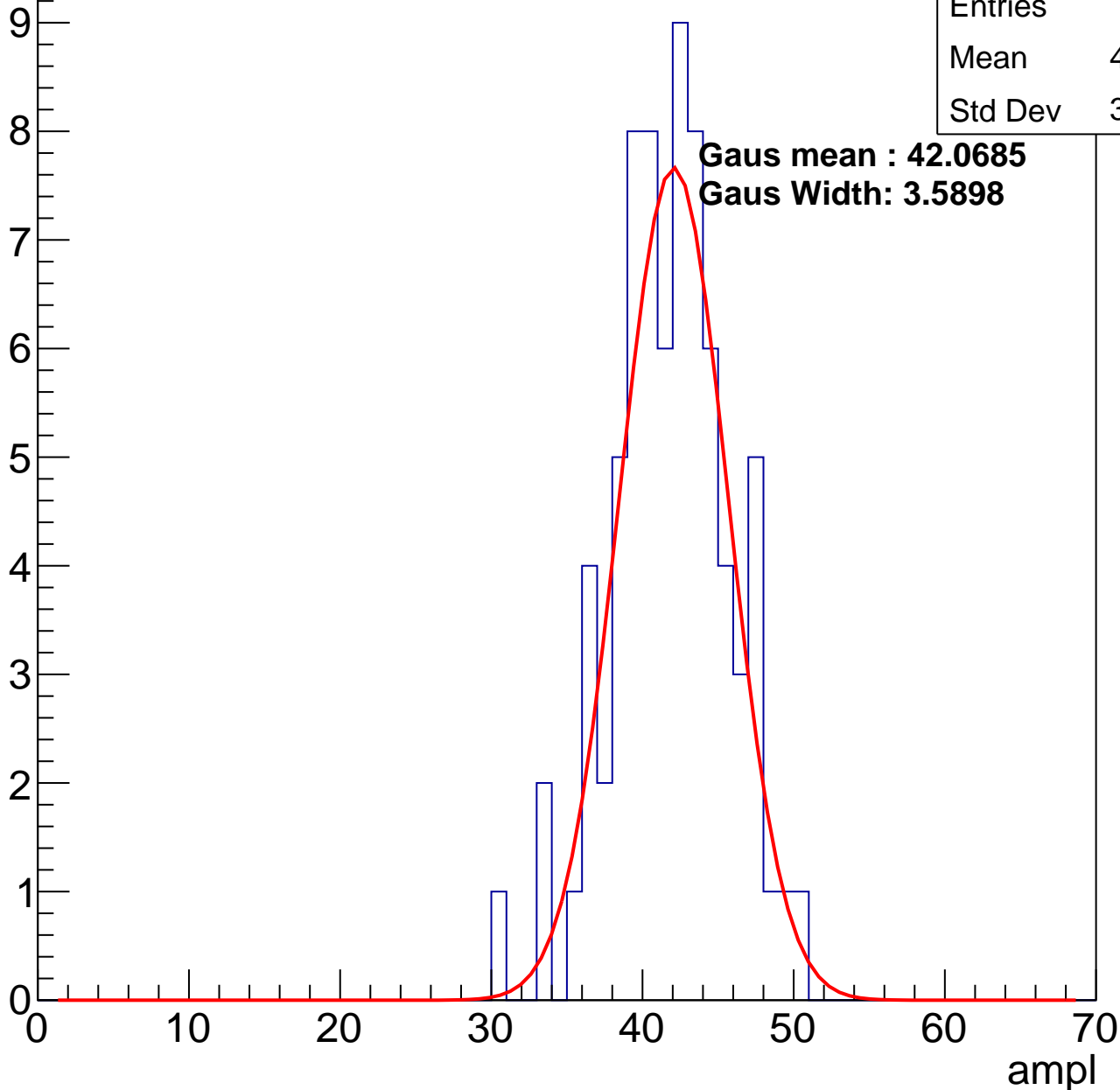
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.37
Std Dev	3.829

Gaus mean : 42.0685

Gaus Width: 3.5898

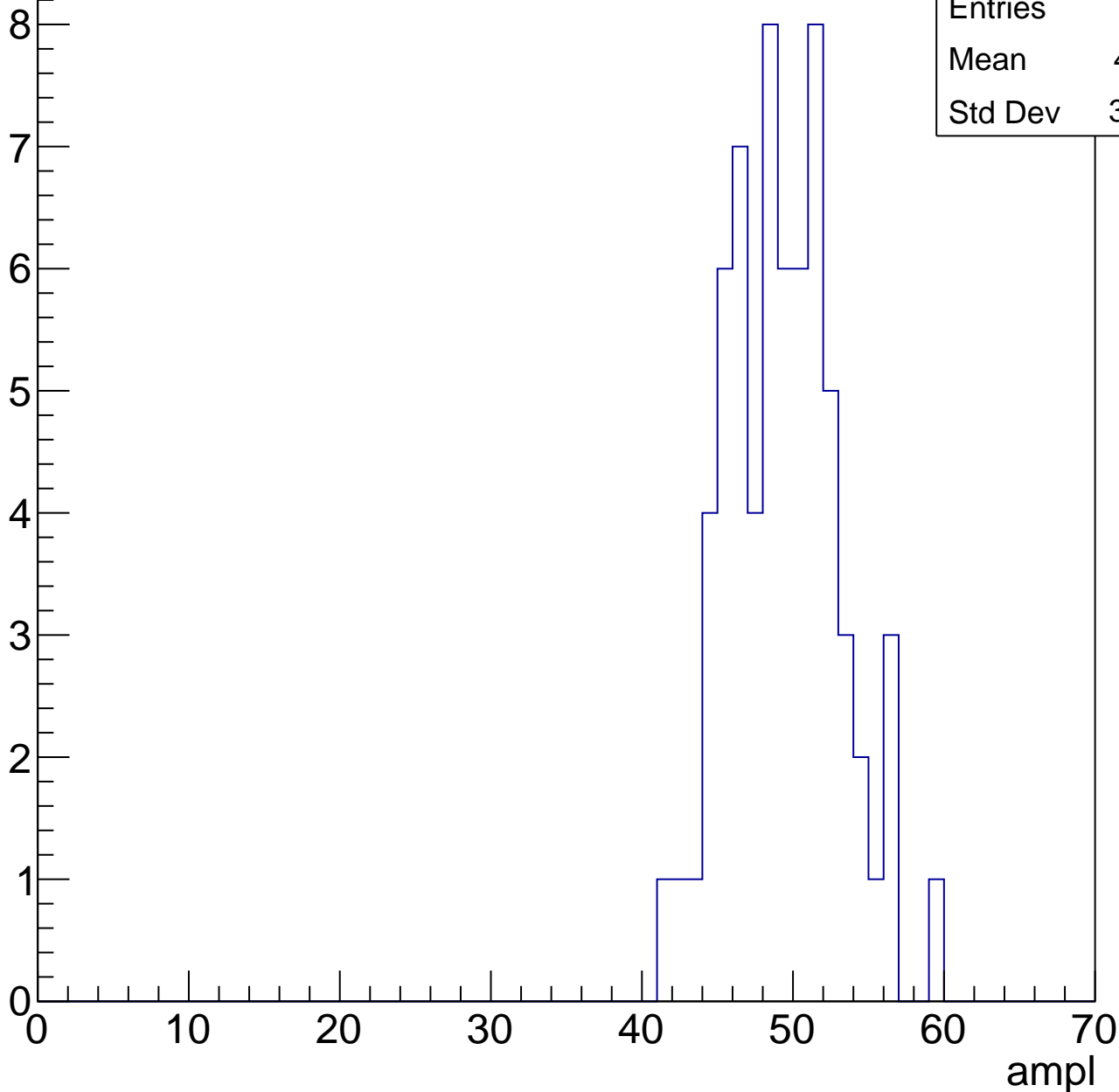


B1L102S, U26-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.91
Std Dev	3.656

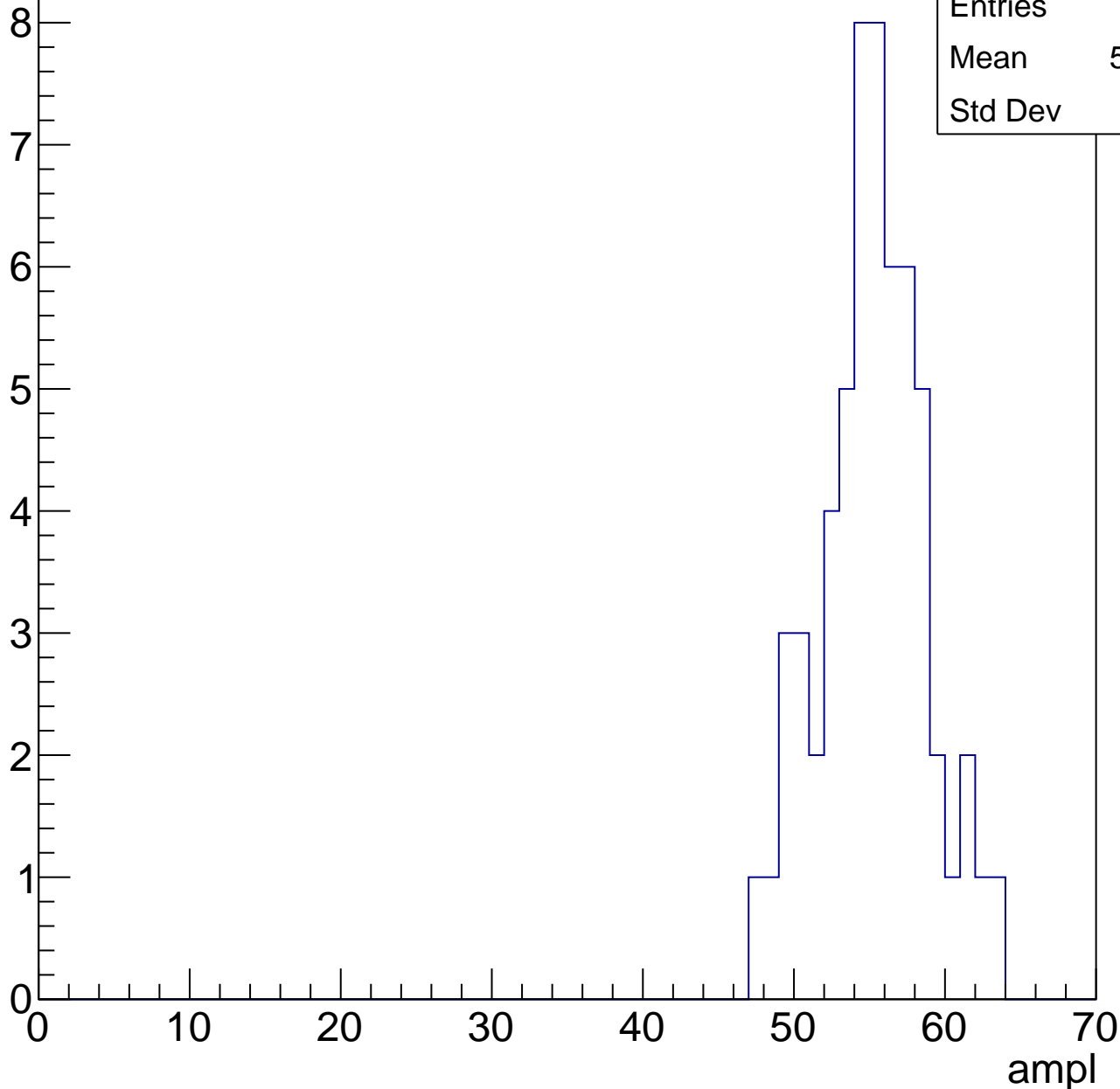


B1L102S, U26-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

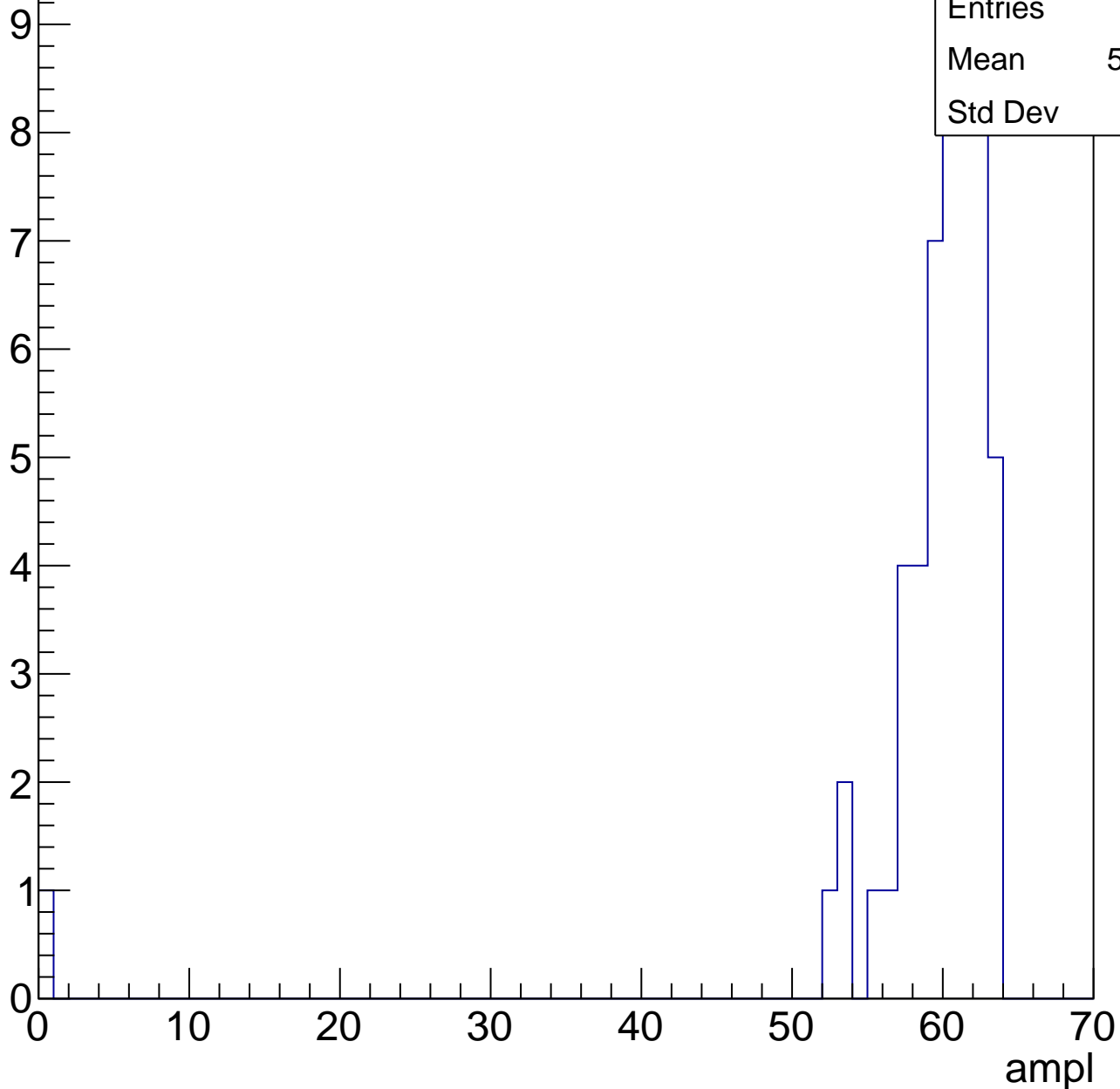
Entries	59
Mean	54.78
Std Dev	3.46



B1L102S, U26-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

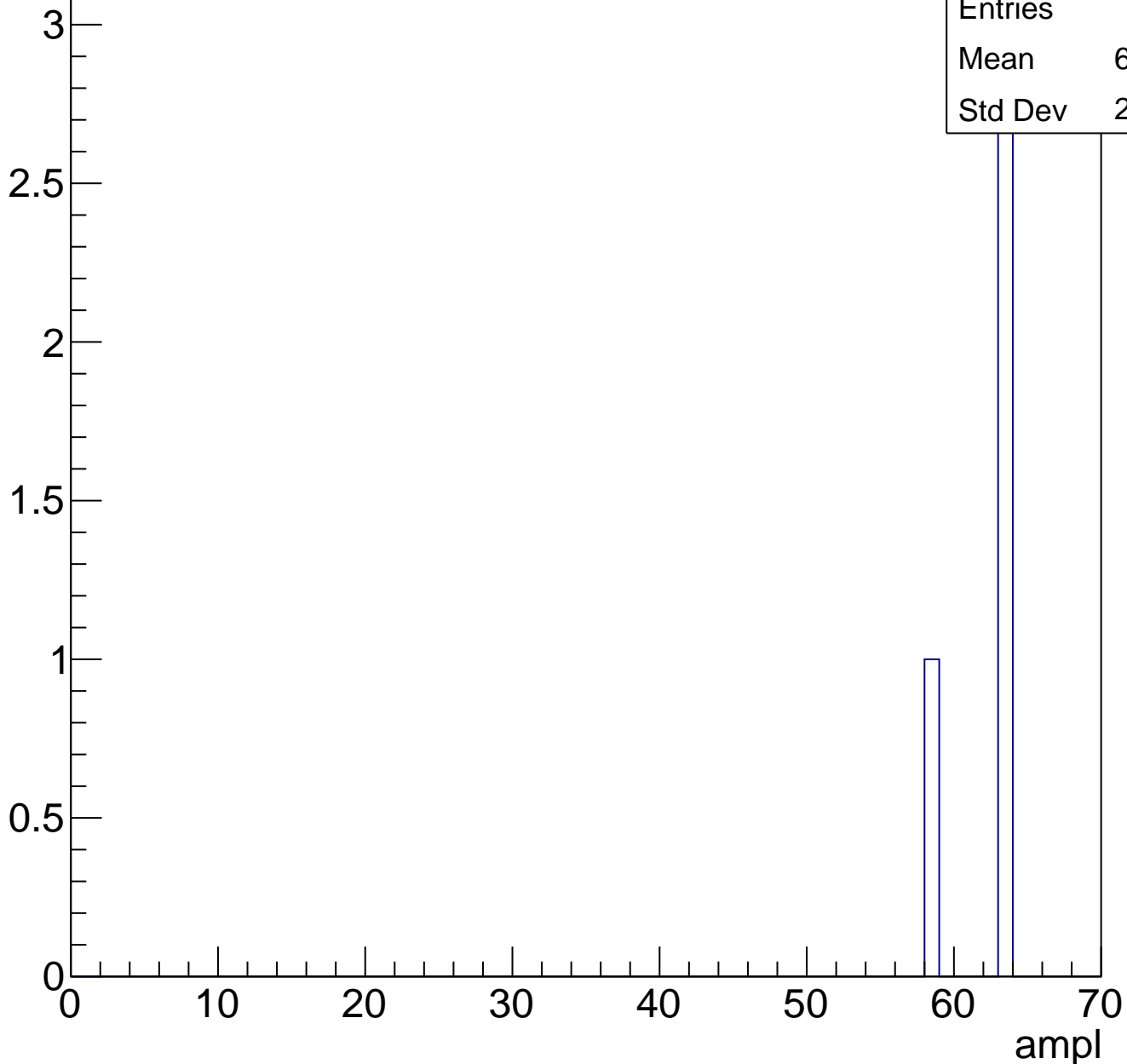
Entry



B1L102S, U26-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch93, adc0

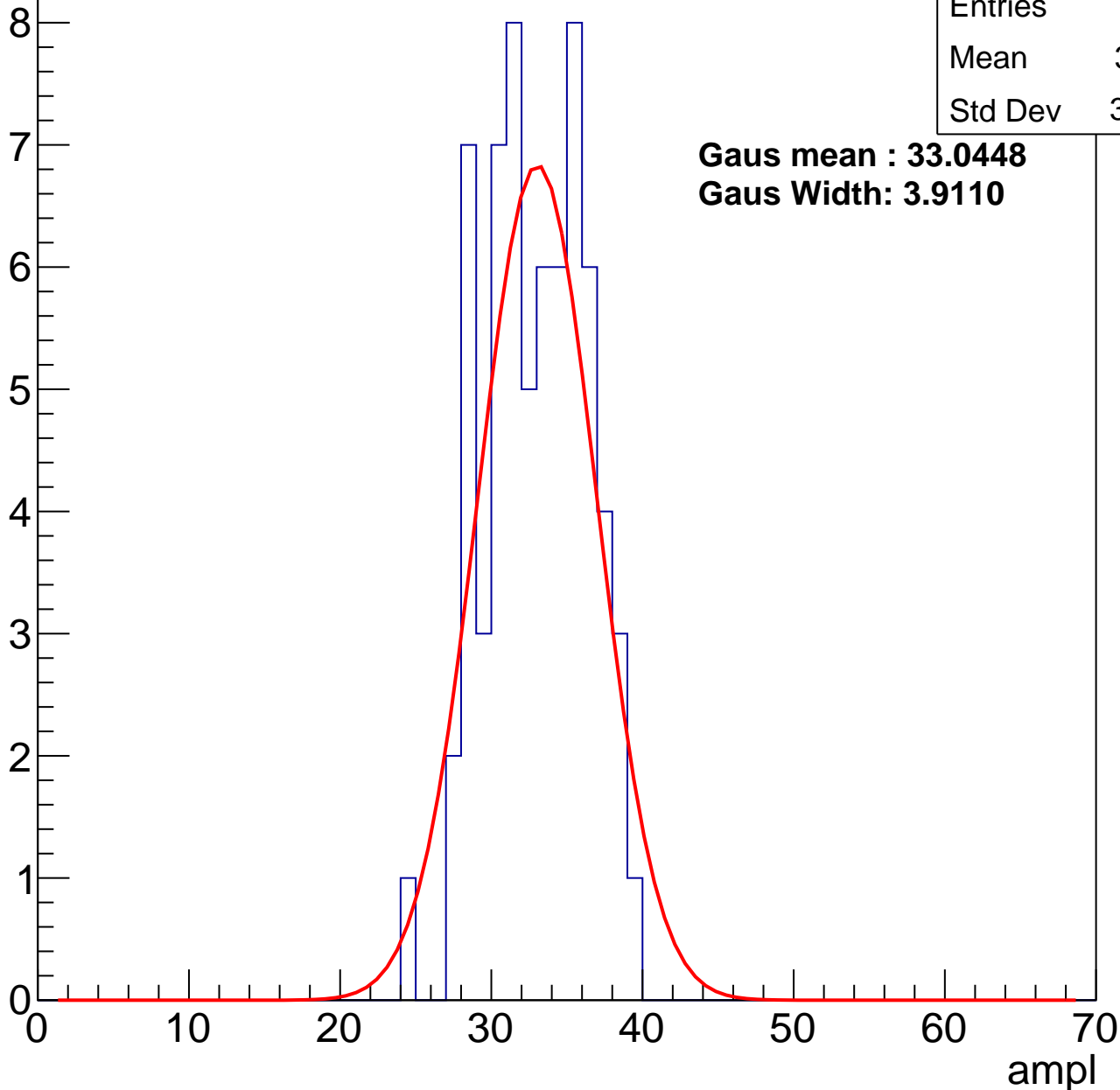
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	32.51
Std Dev	3.293

Gaus mean : 33.0448

Gaus Width: 3.9110



B1L102S, U26-ch93, adc1

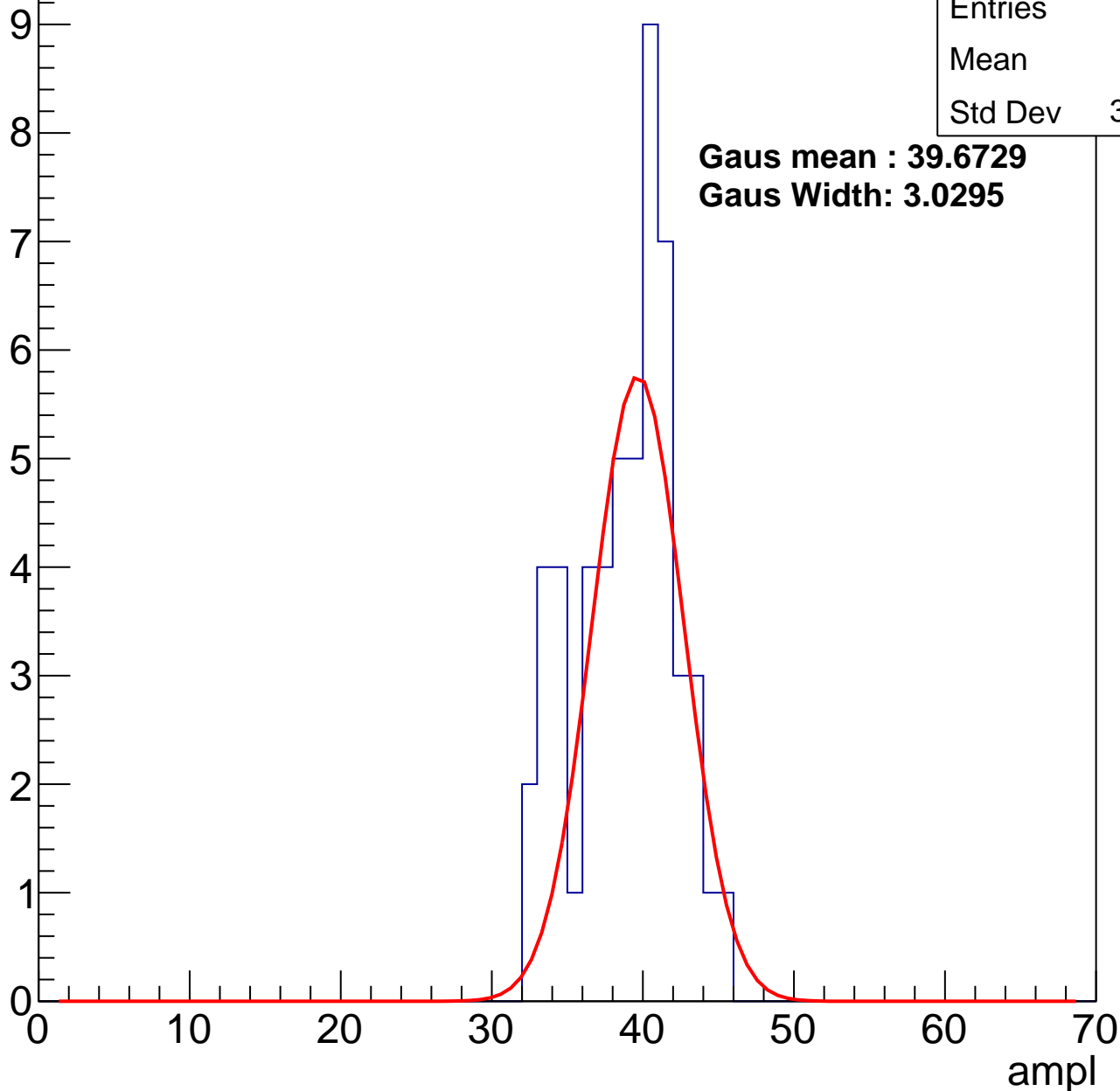
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	38.4
Std Dev	3.276

Gaus mean : 39.6729

Gaus Width: 3.0295



B1L102S, U26-ch93, adc2

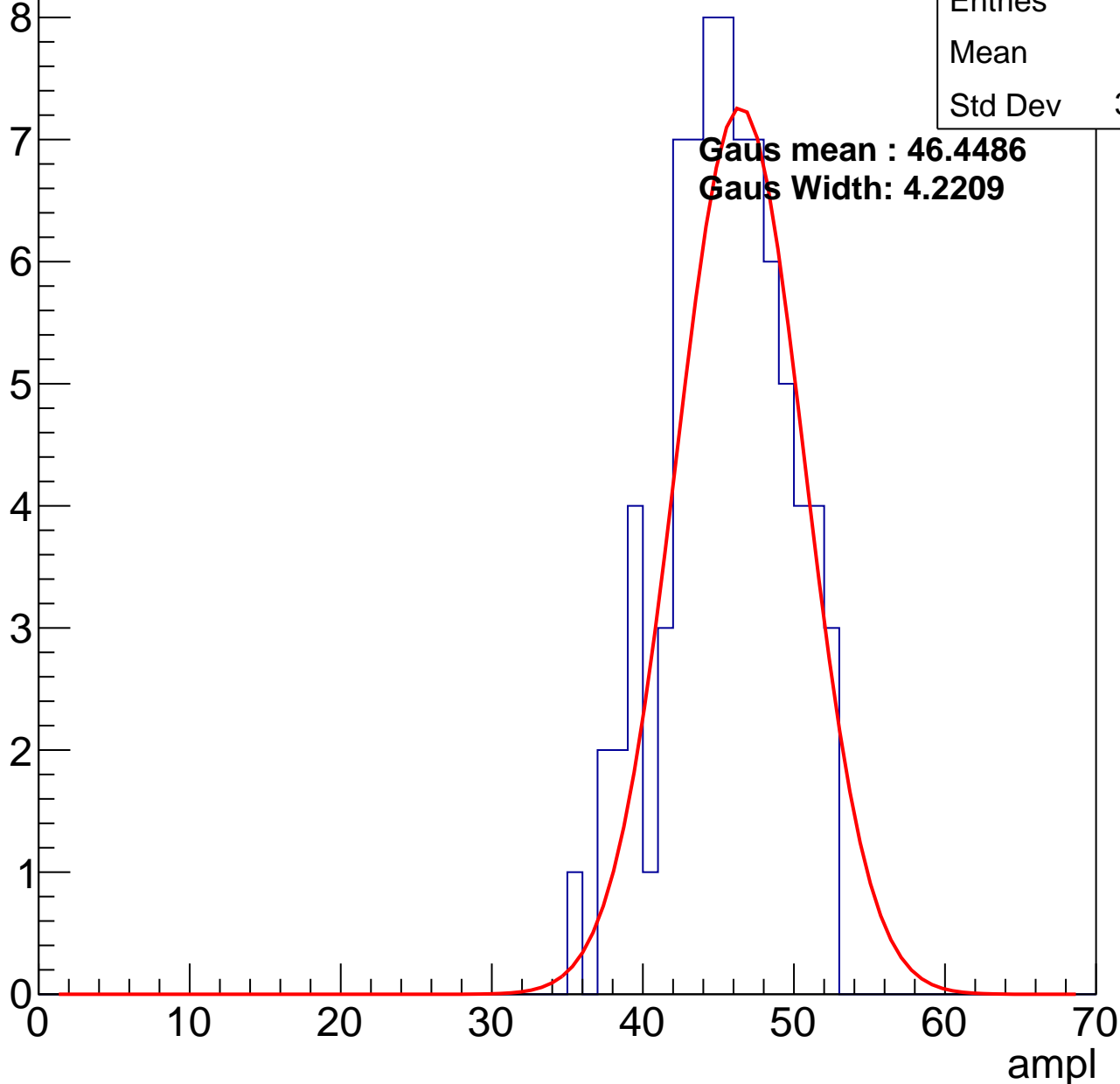
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	45
Std Dev	3.901

Gaus mean : 46.4486

Gaus Width: 4.2209



B1L102S, U26-ch93, adc3

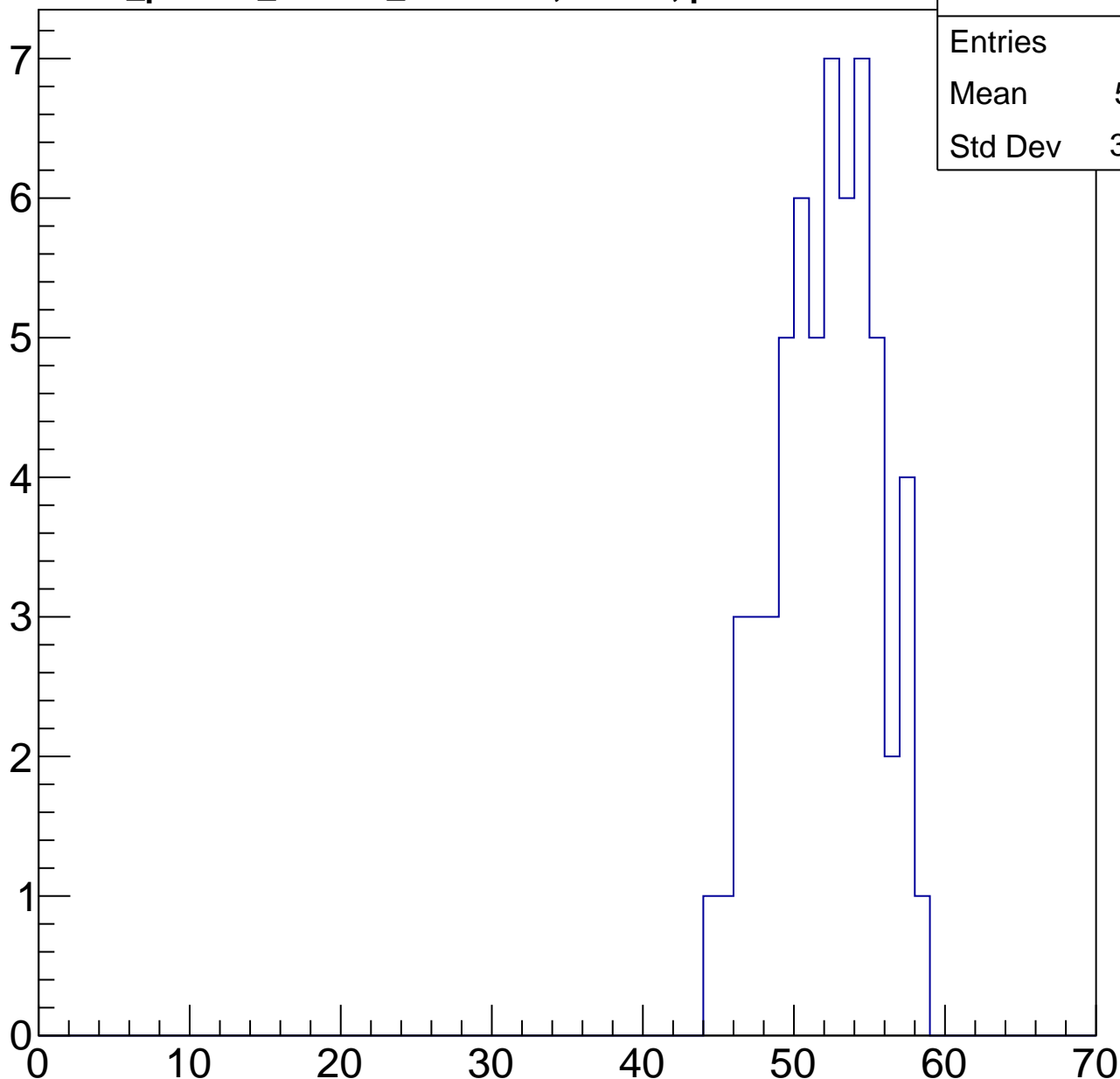
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	59
Mean	51.61
Std Dev	3.335

ampl

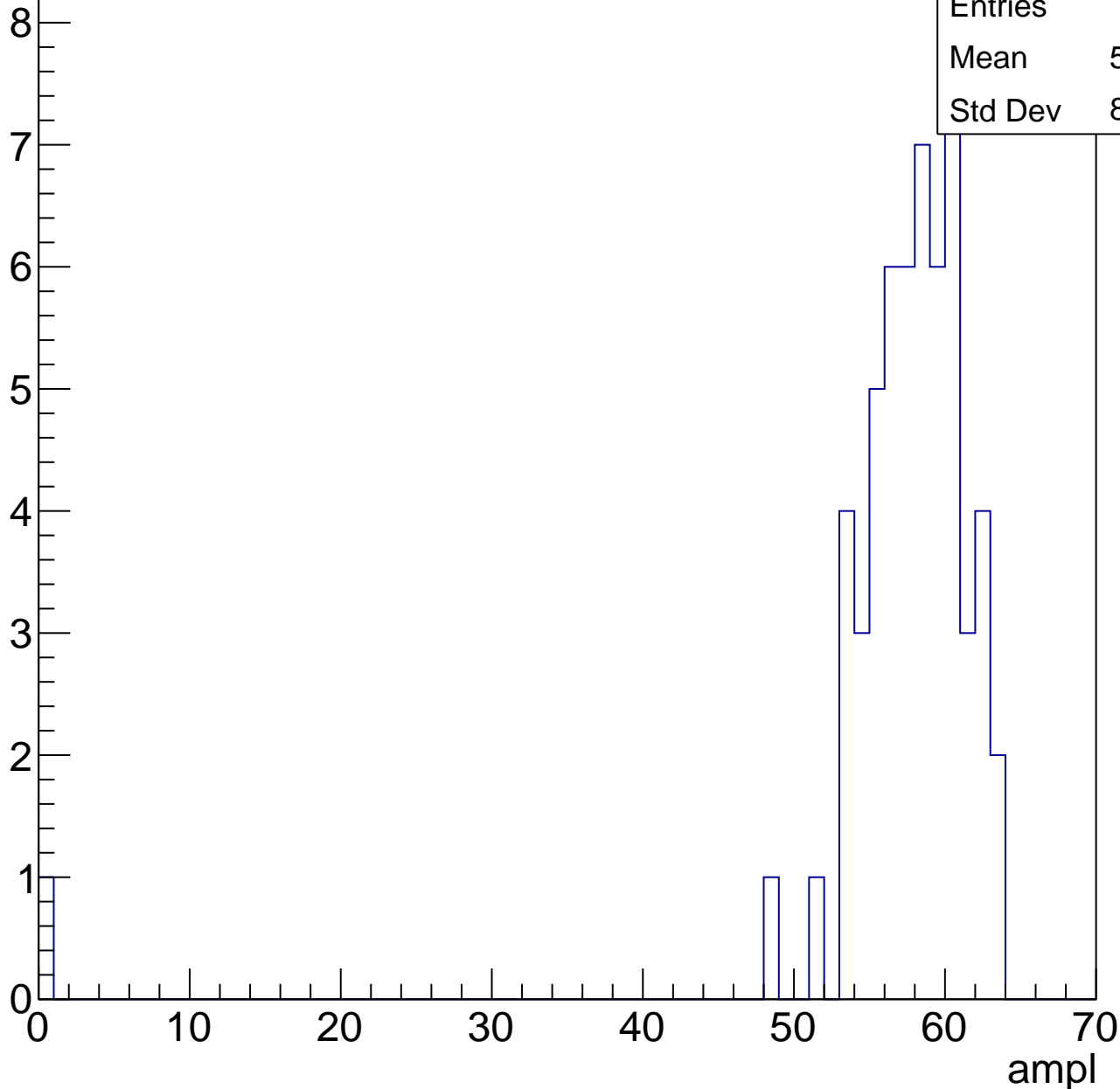


B1L102S, U26-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.54
Std Dev	8.159

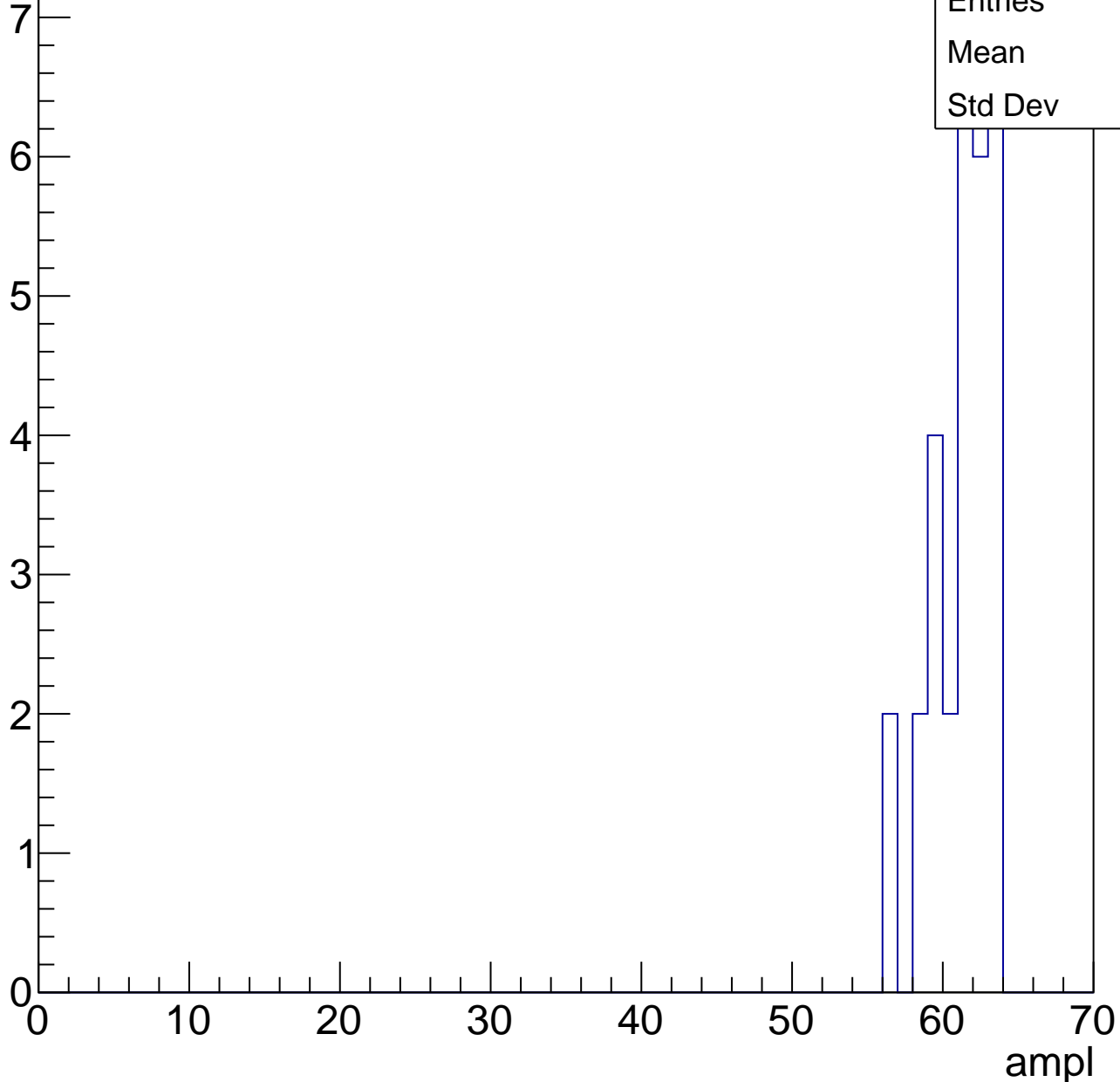


B1L102S, U26-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	60.8
Std Dev	1.99



B1L102S, U26-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch94, adc0

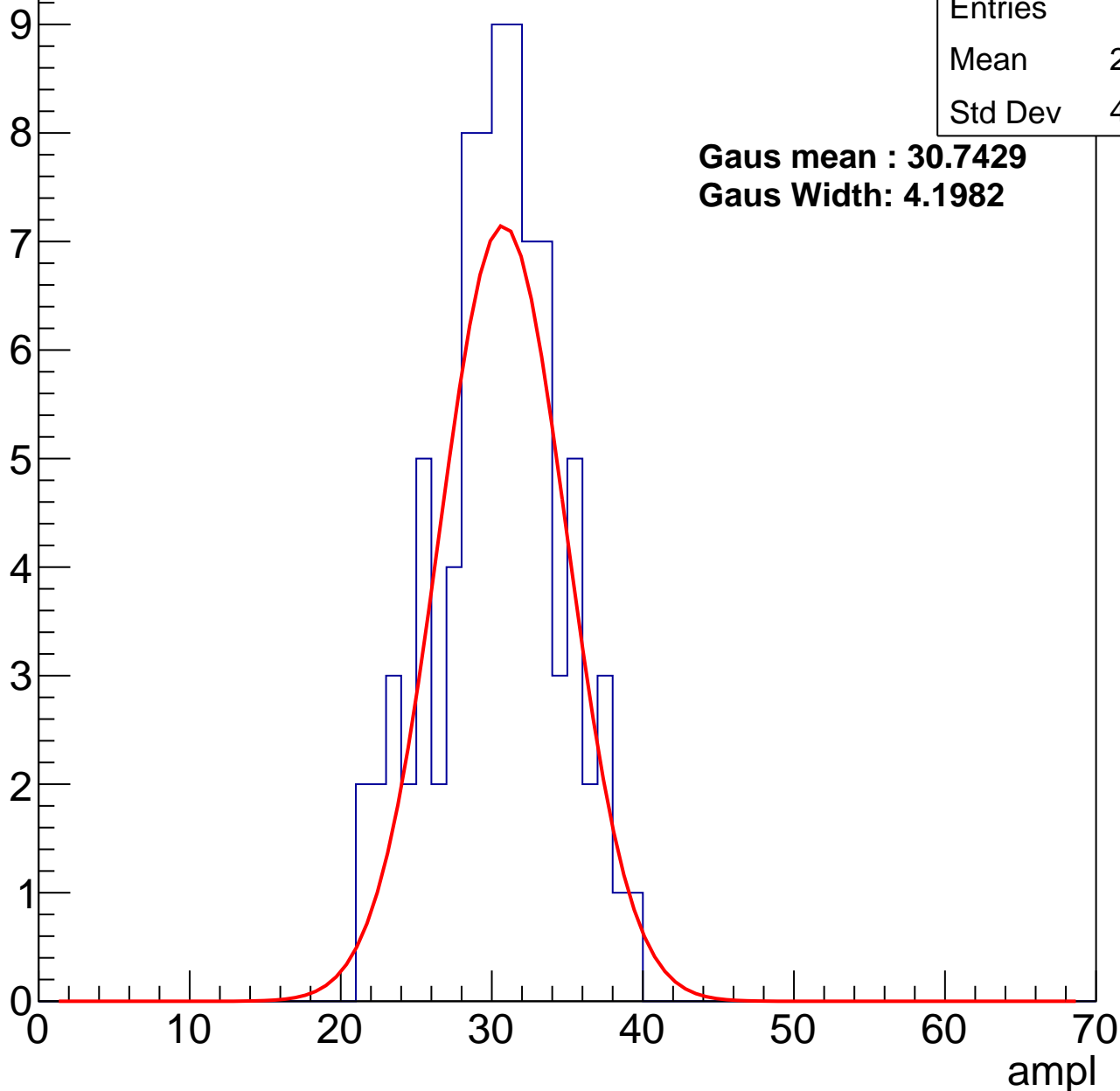
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.94
Std Dev	4.076

Gaus mean : 30.7429

Gaus Width: 4.1982



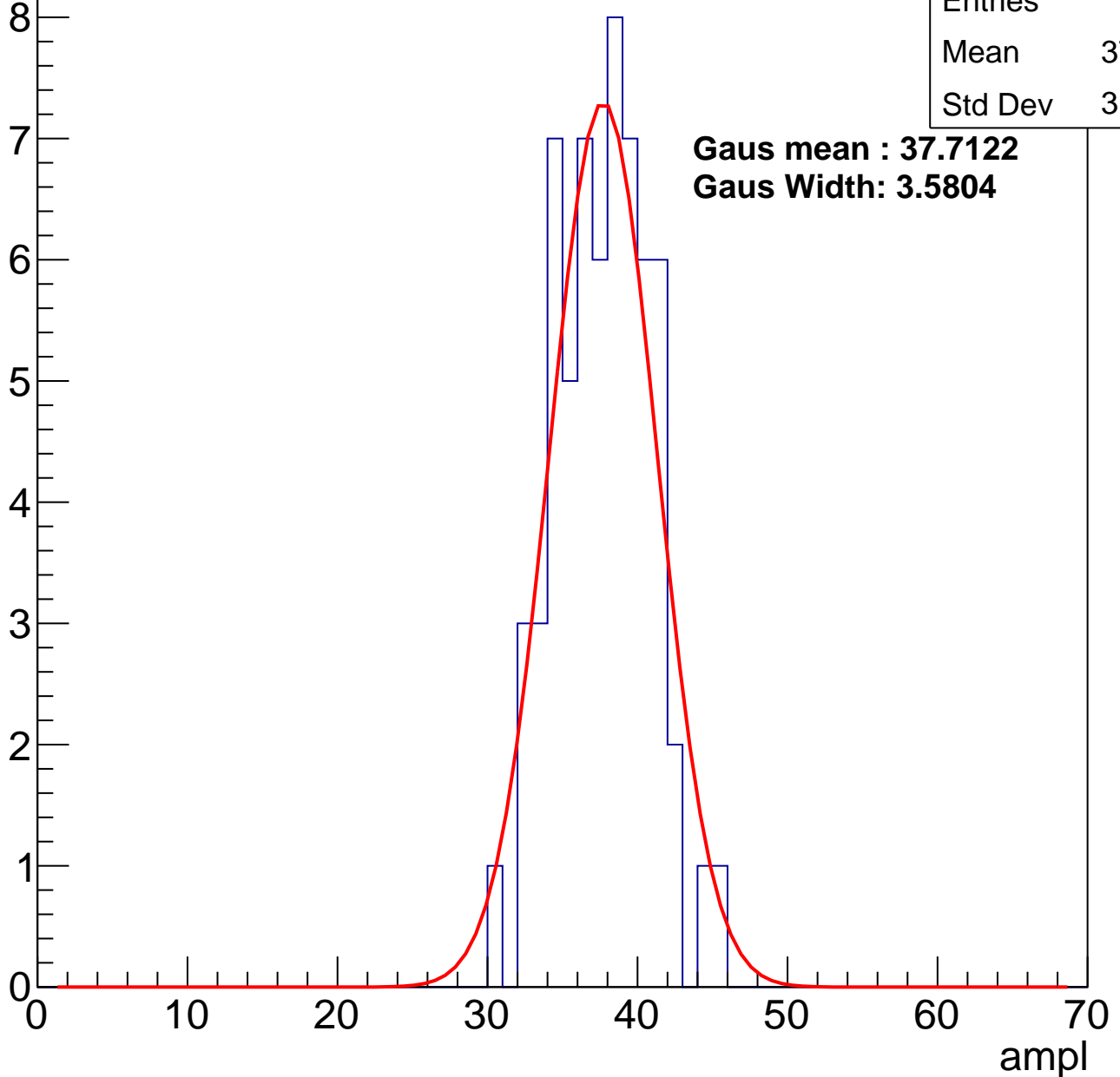
B1L102S, U26-ch94, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.27
Std Dev	3.103

Gaus mean : 37.7122
Gaus Width: 3.5804



B1L102S, U26-ch94, adc2

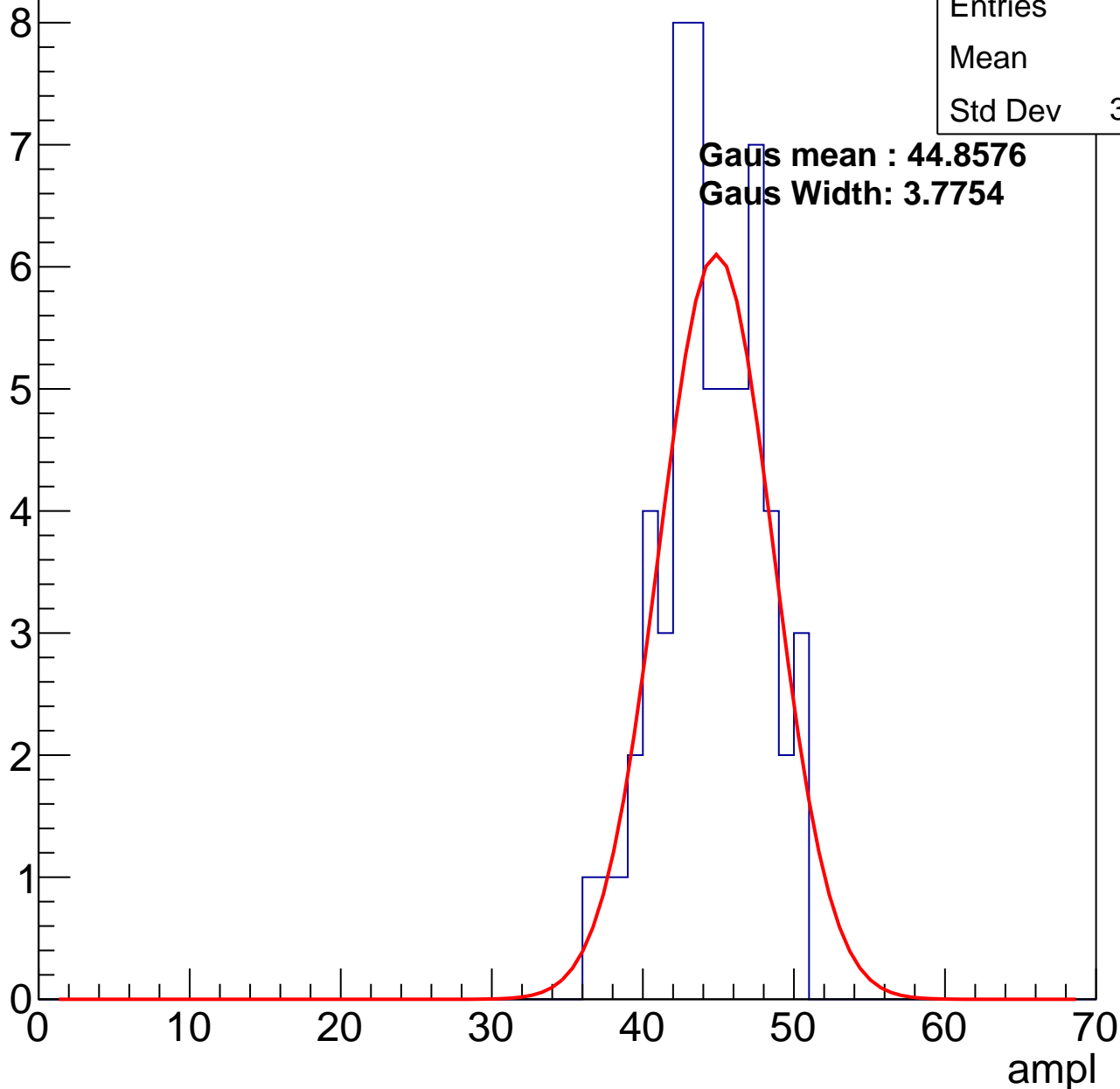
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44
Std Dev	3.294

Gaus mean : 44.8576

Gaus Width: 3.7754

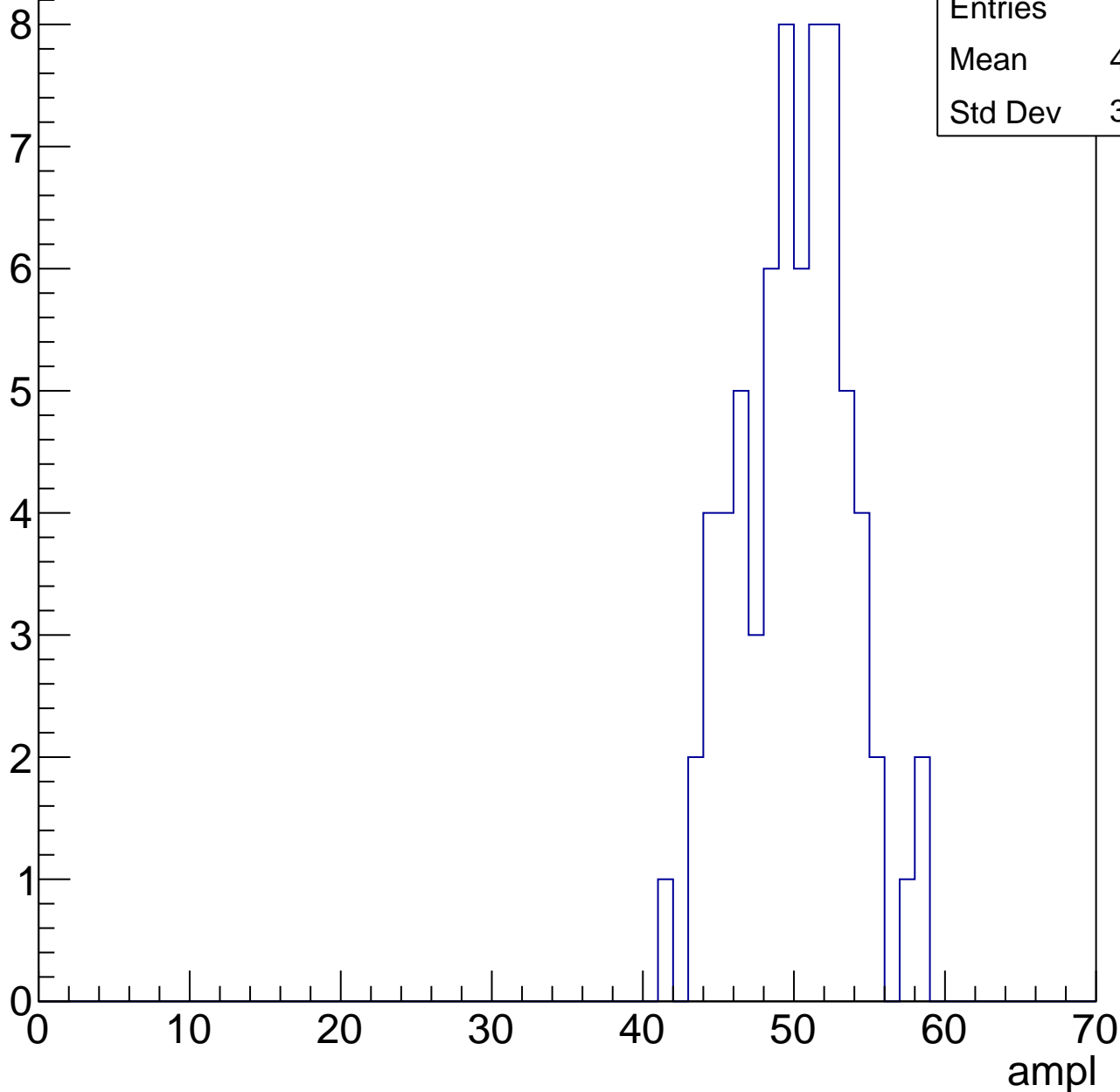


B1L102S, U26-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.59
Std Dev	3.665



B1L102S, U26-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

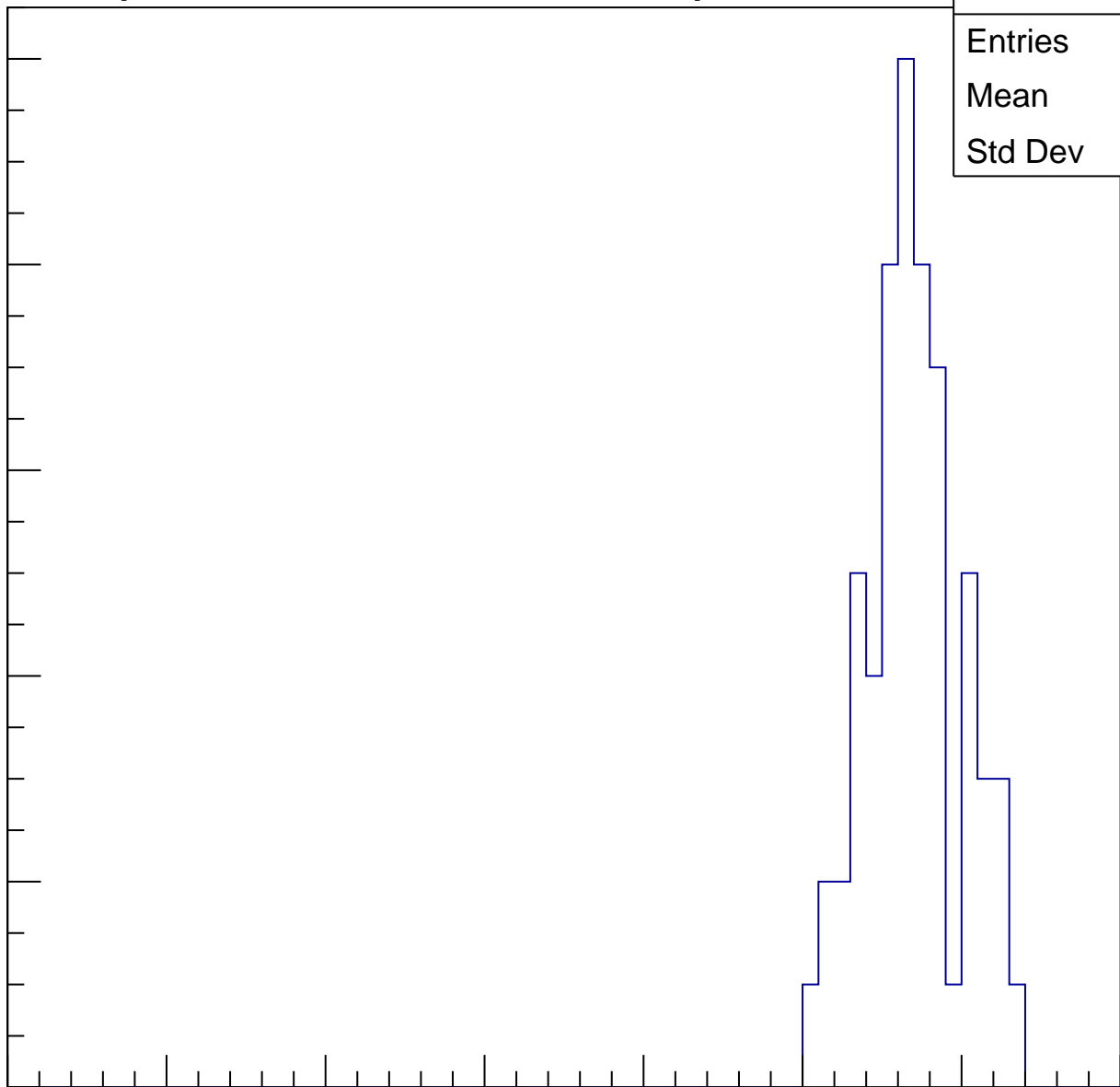
Entries	60
Mean	56.5
Std Dev	2.969

Entry

10
8
6
4
2
0

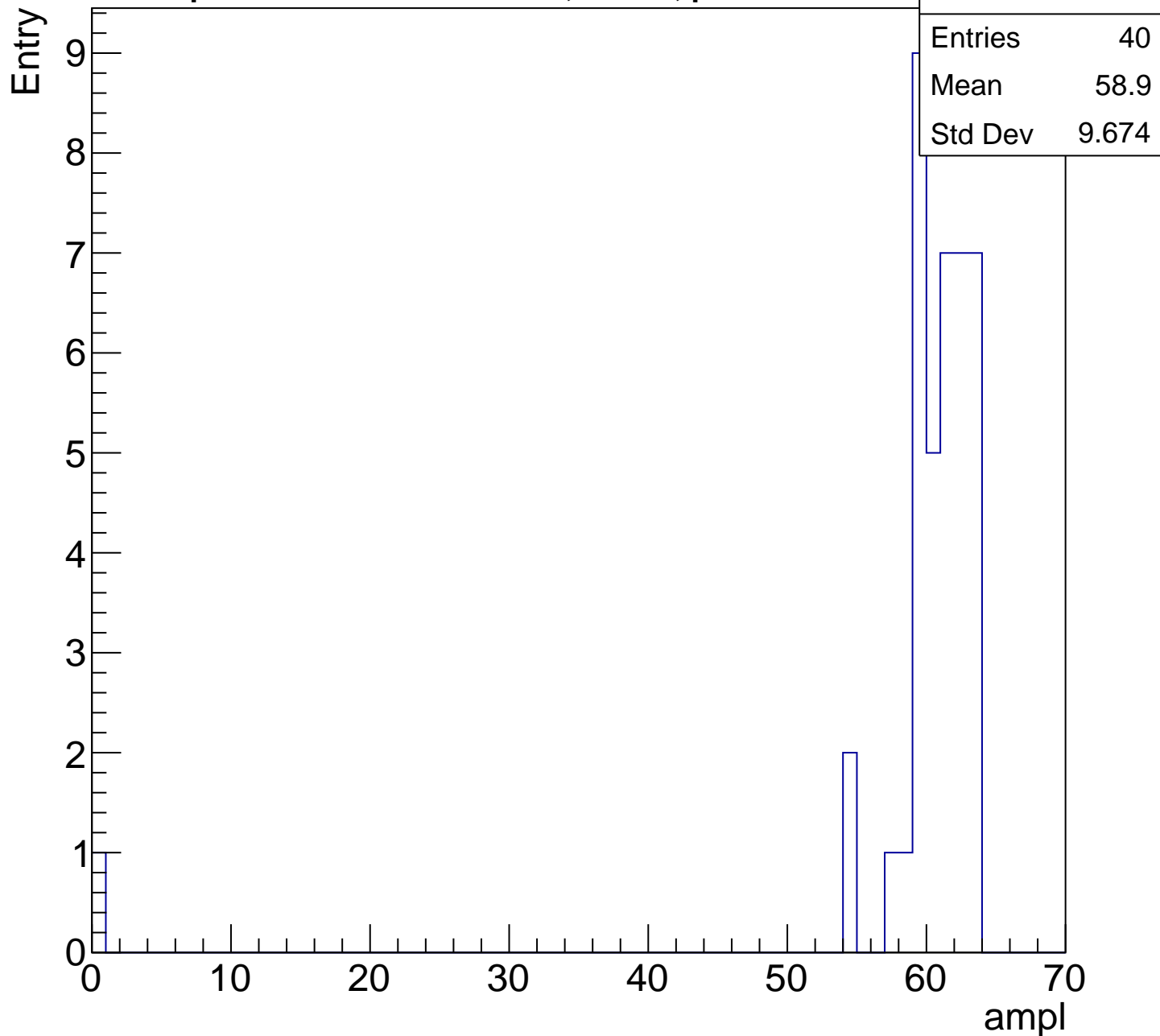
0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U26-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L102S, U26-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch95, adc0

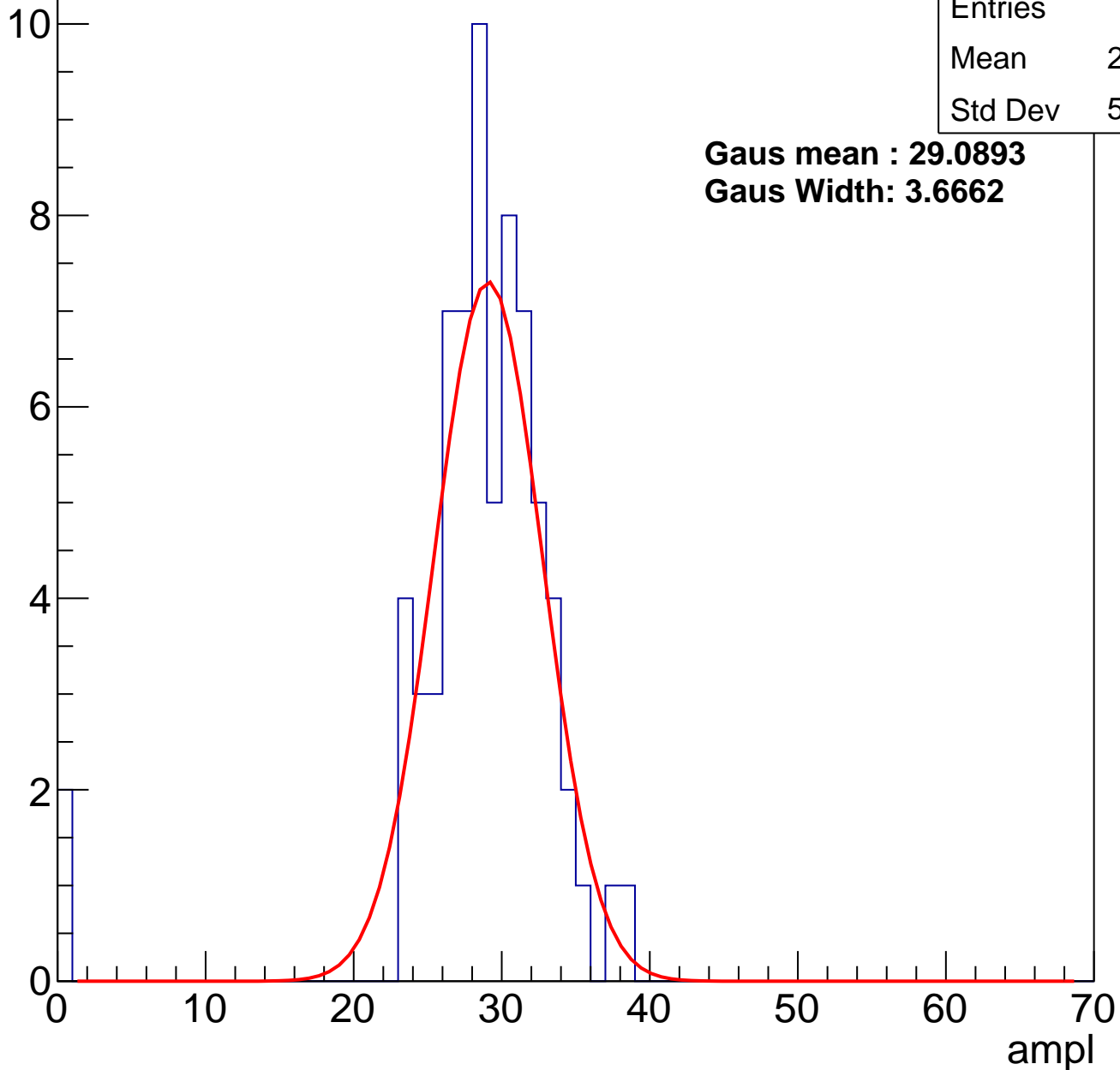
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	28.03
Std Dev	5.806

Gaus mean : 29.0893

Gaus Width: 3.6662

Entry



B1L102S, U26-ch95, adc1

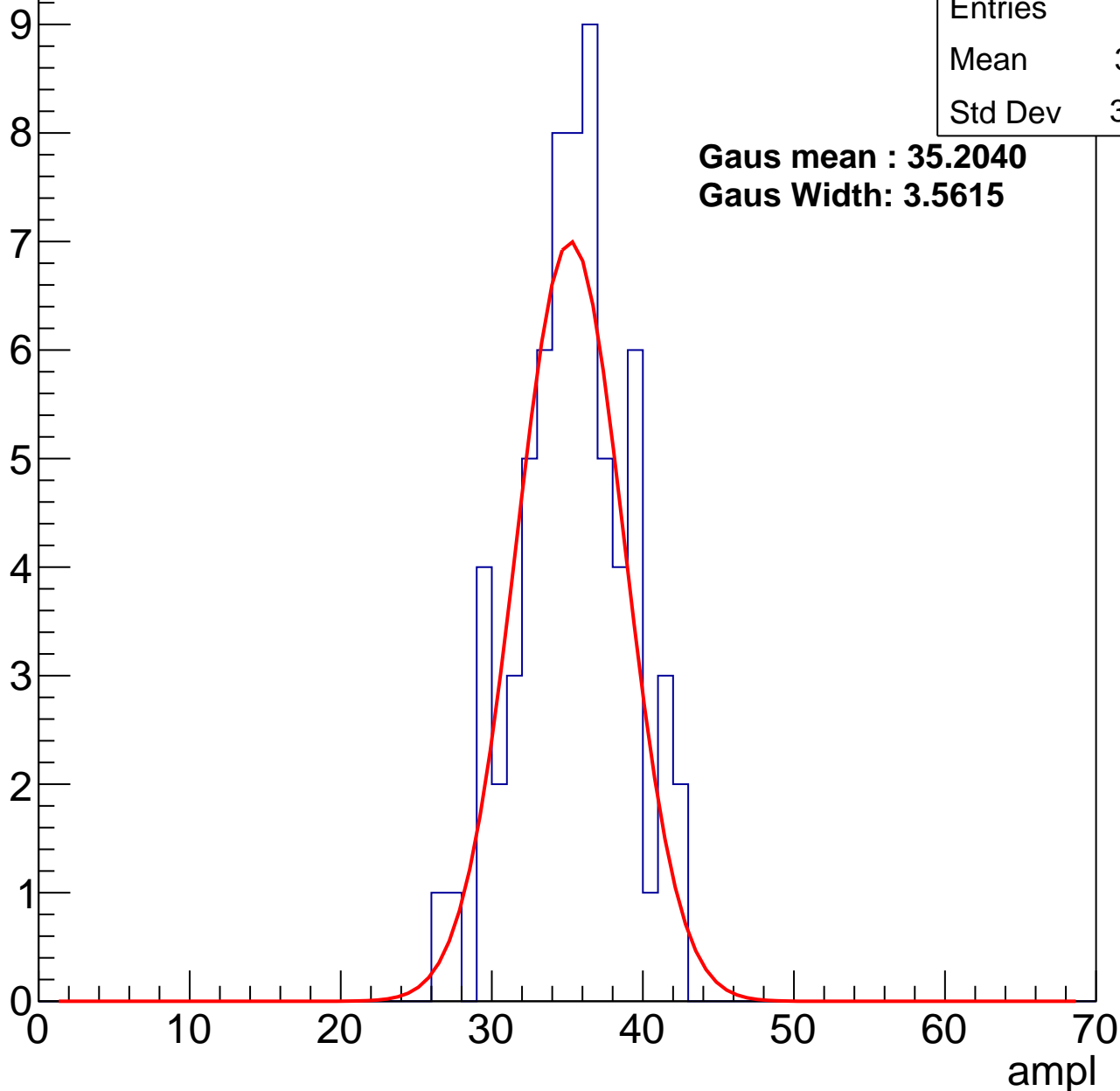
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.91
Std Dev	3.547

Gaus mean : 35.2040

Gaus Width: 3.5615



B1L102S, U26-ch95, adc2

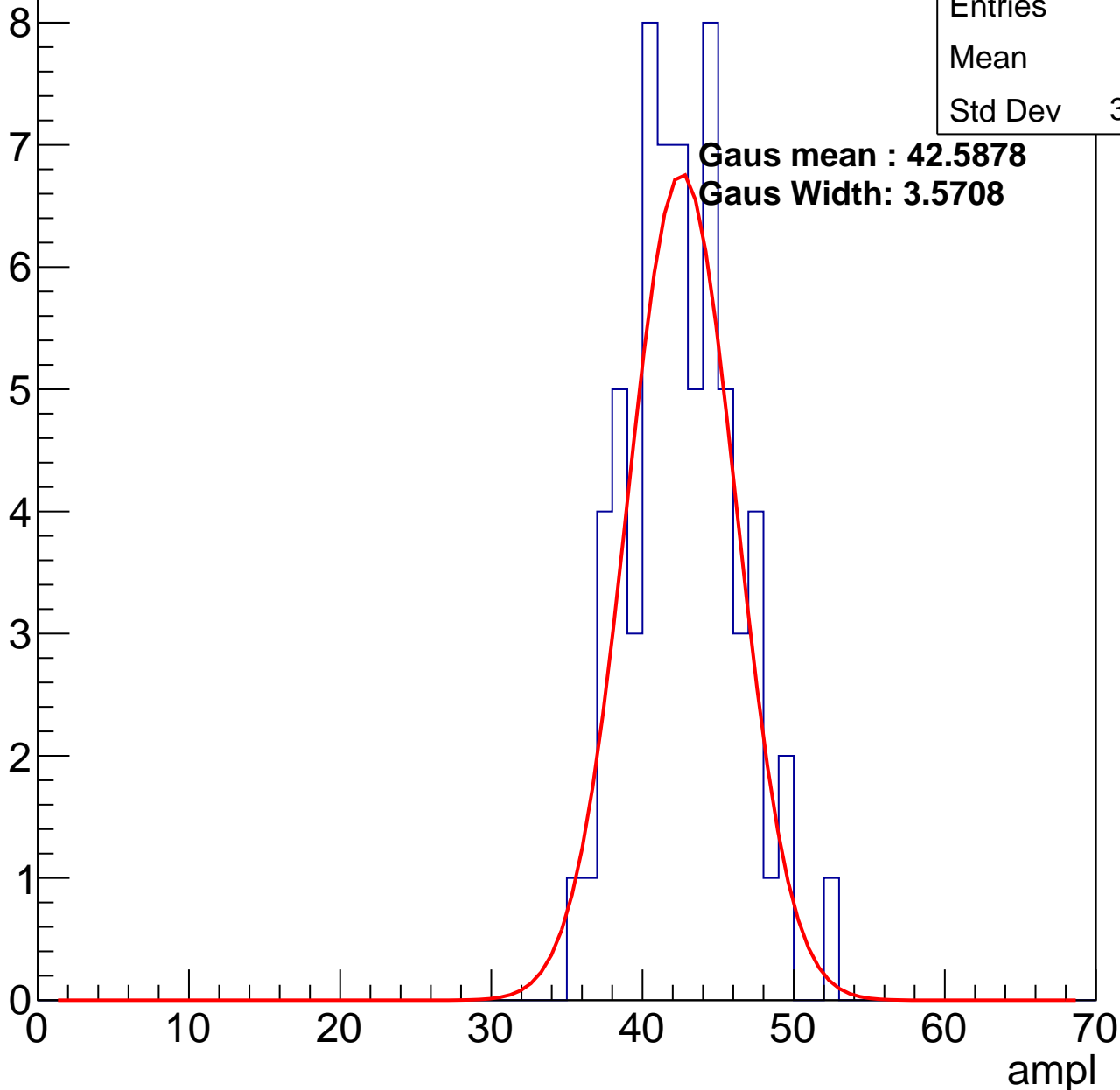
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.2
Std Dev	3.492

Gaus mean : 42.5878

Gaus Width: 3.5708

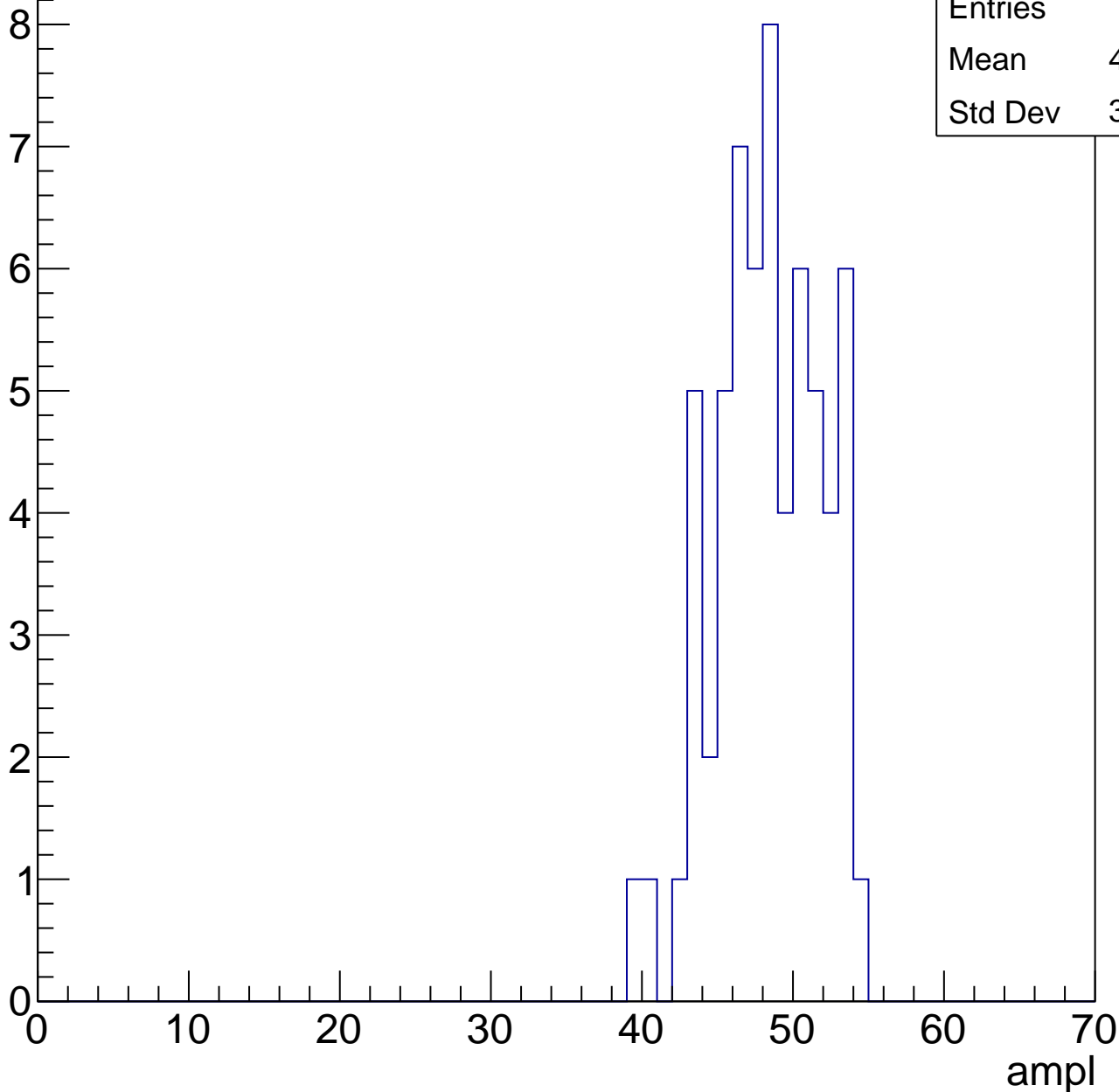


B1L102S, U26-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.87
Std Dev	3.452

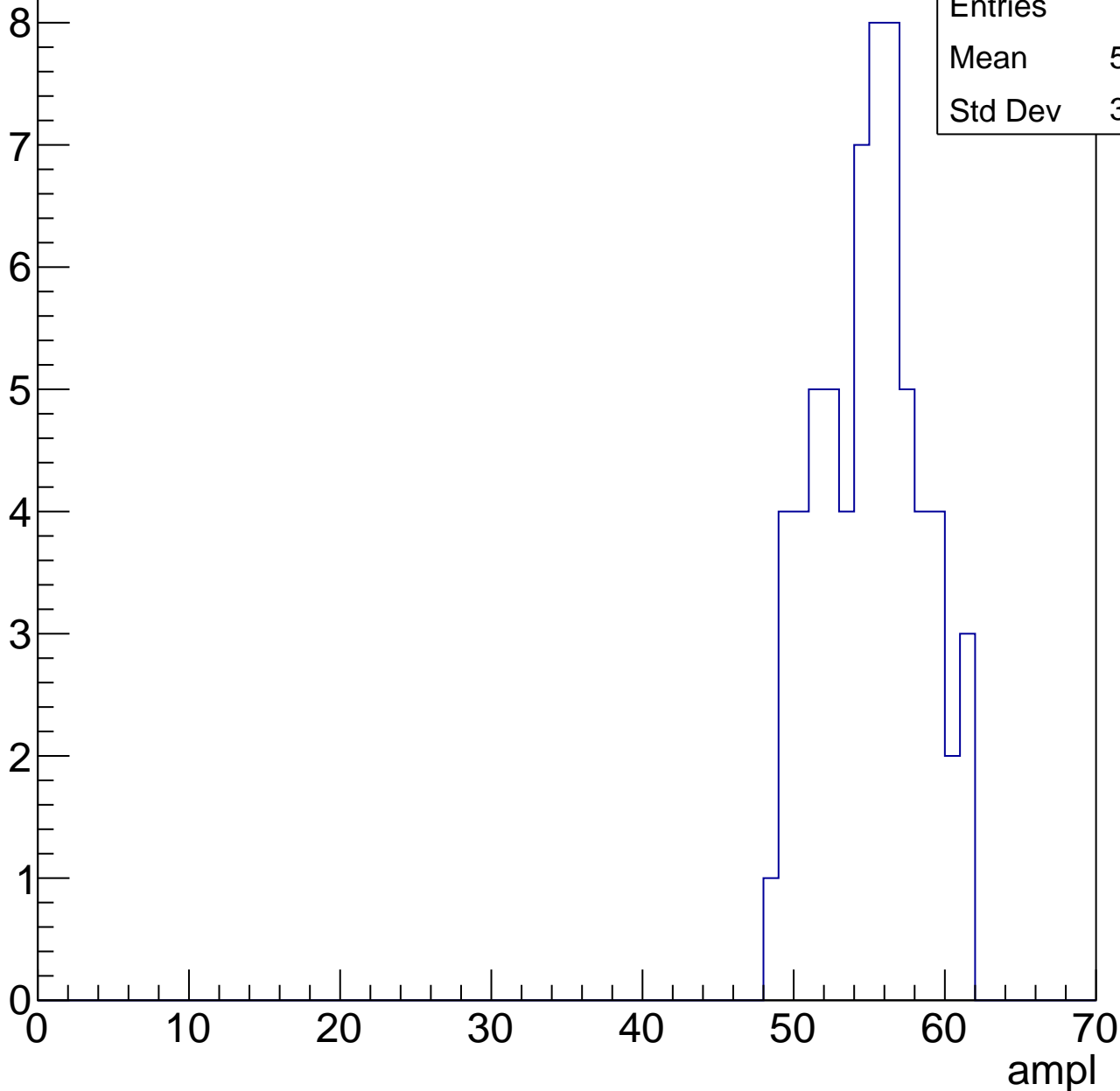


B1L102S, U26-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.58
Std Dev	3.344

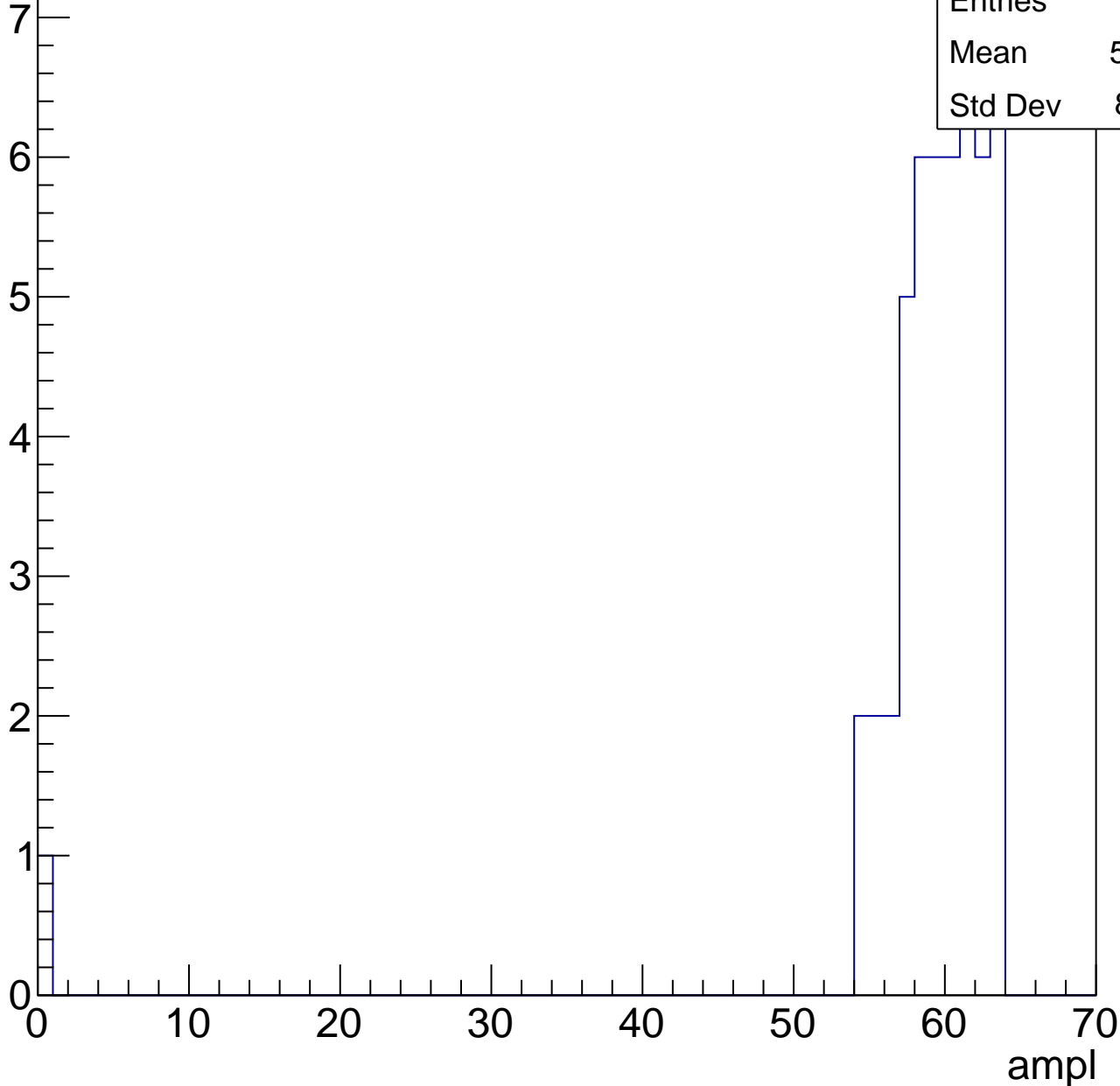


B1L102S, U26-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

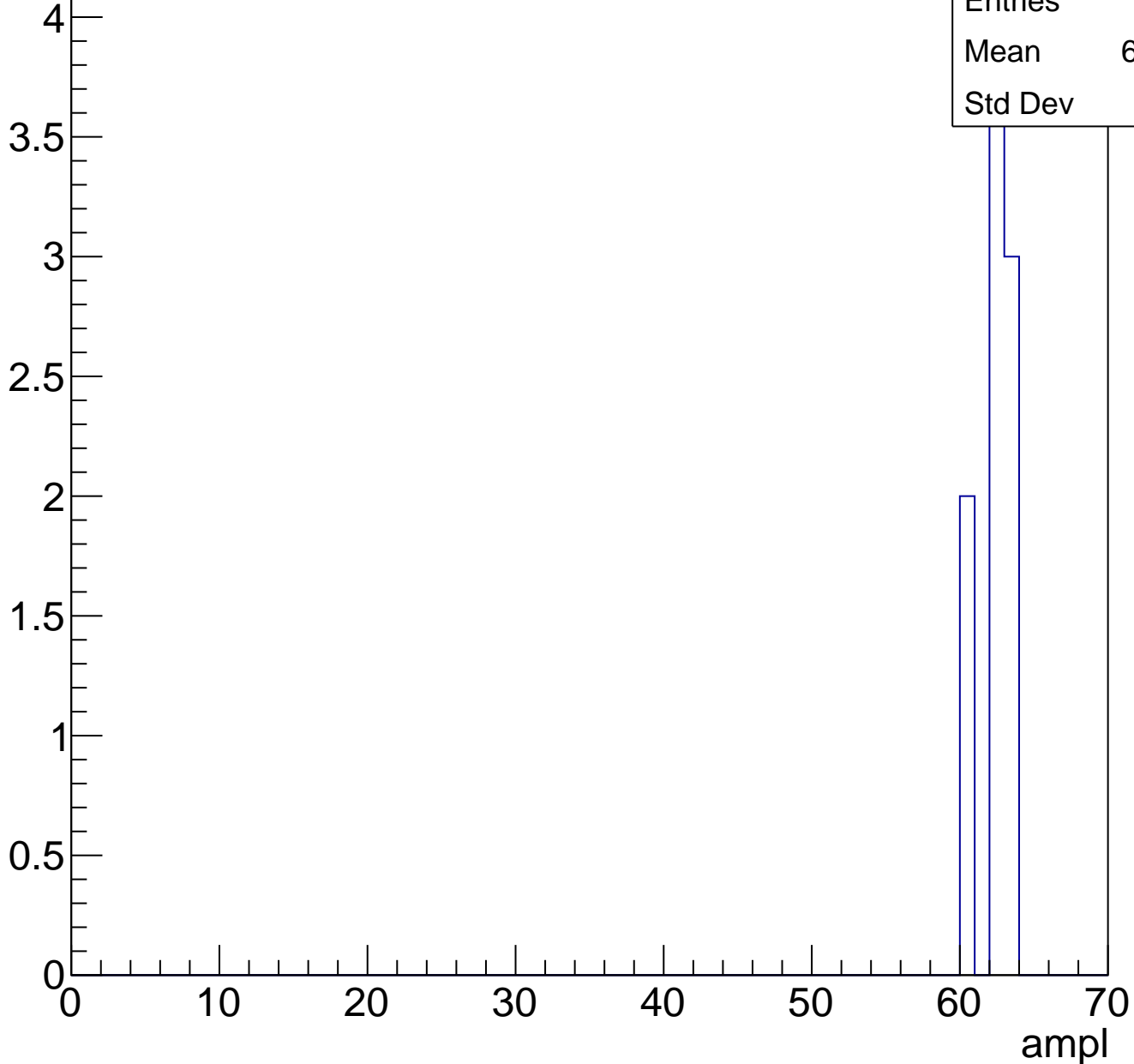
Entries	50
Mean	58.34
Std Dev	8.701



B1L102S, U26-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch96, adc0

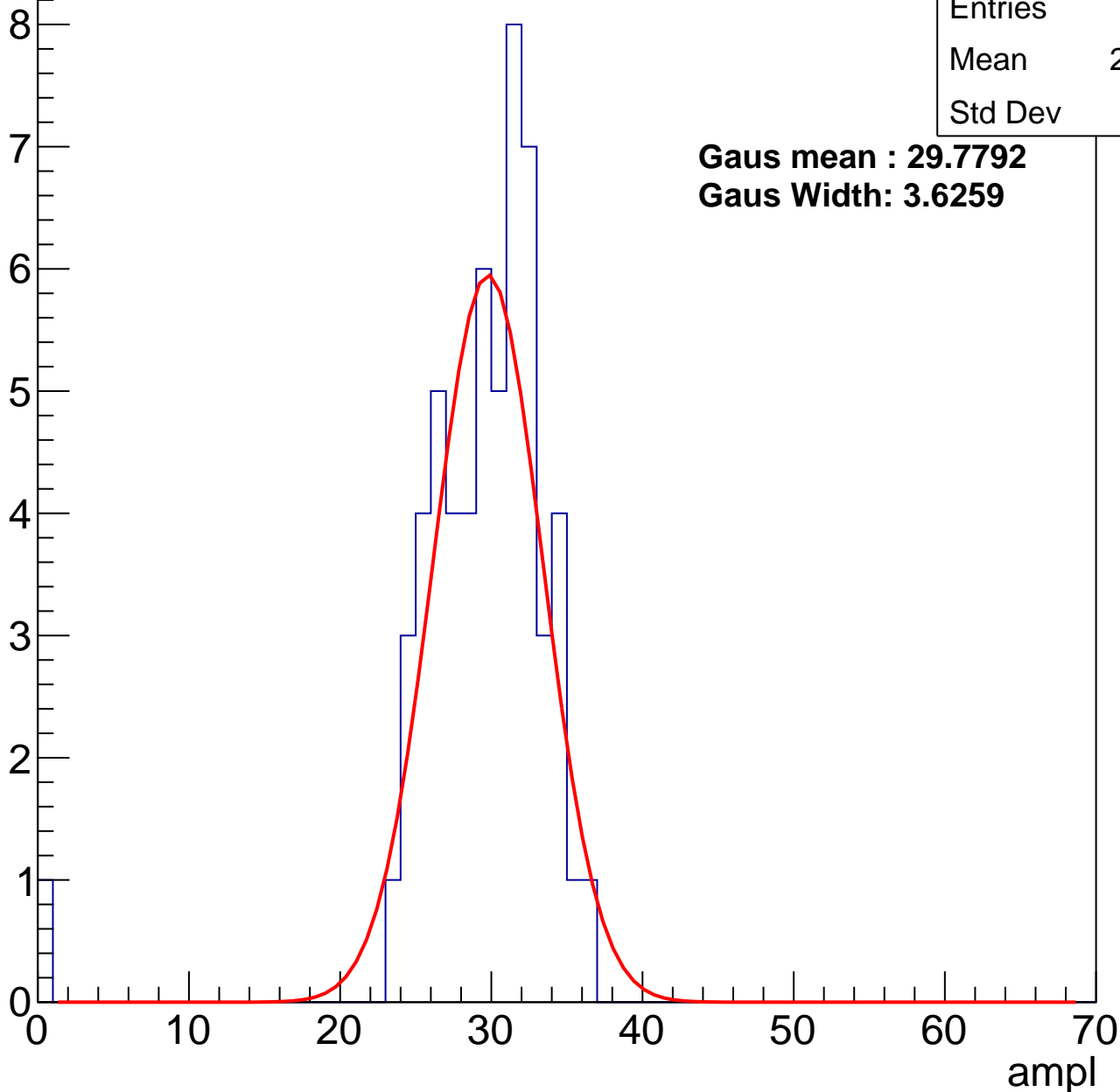
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	28.89
Std Dev	4.98

Gaus mean : 29.7792

Gaus Width: 3.6259



B1L102S, U26-ch96, adc1

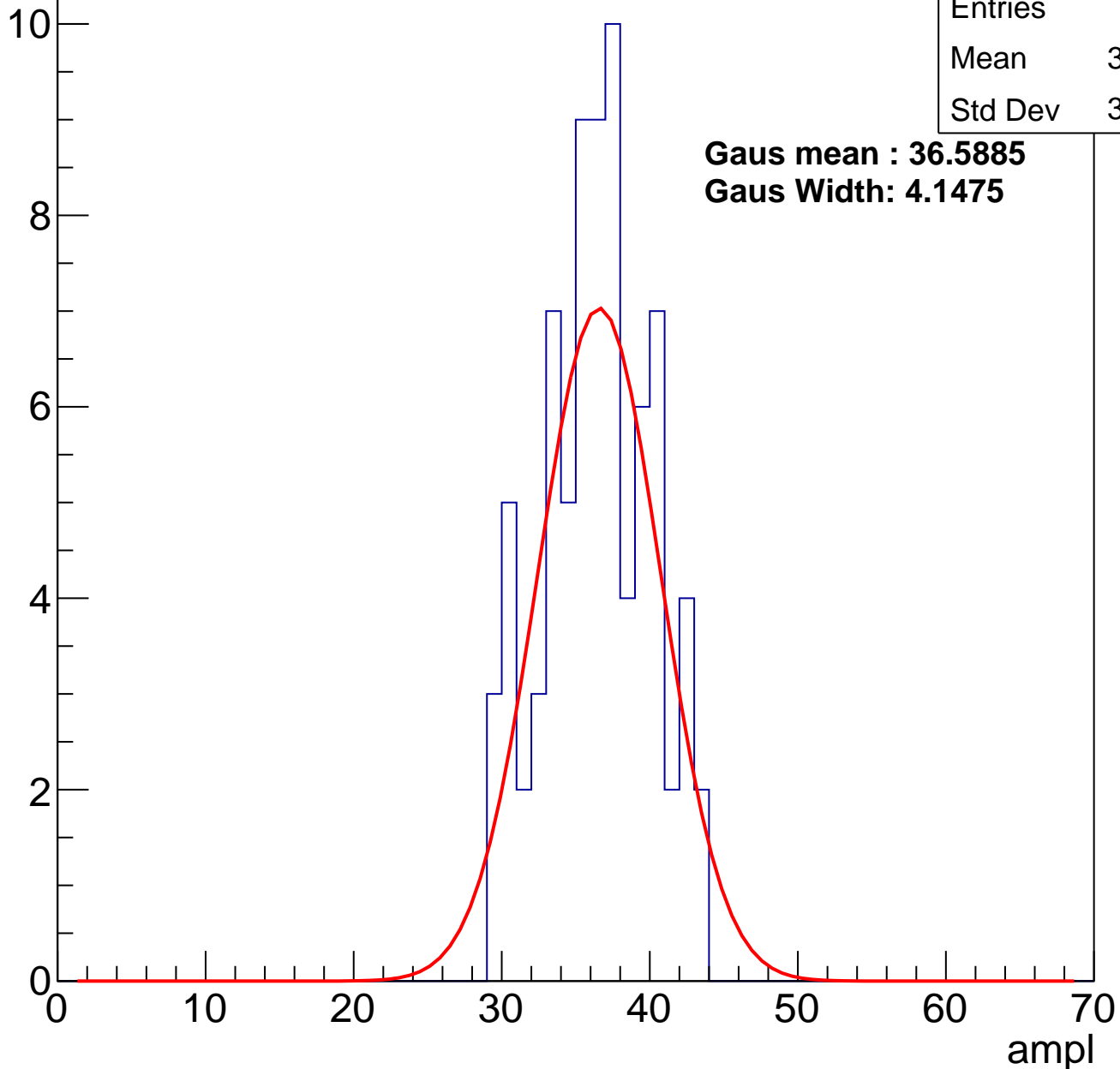
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	35.99
Std Dev	3.582

Gaus mean : 36.5885

Gaus Width: 4.1475

Entry



B1L102S, U26-ch96, adc2

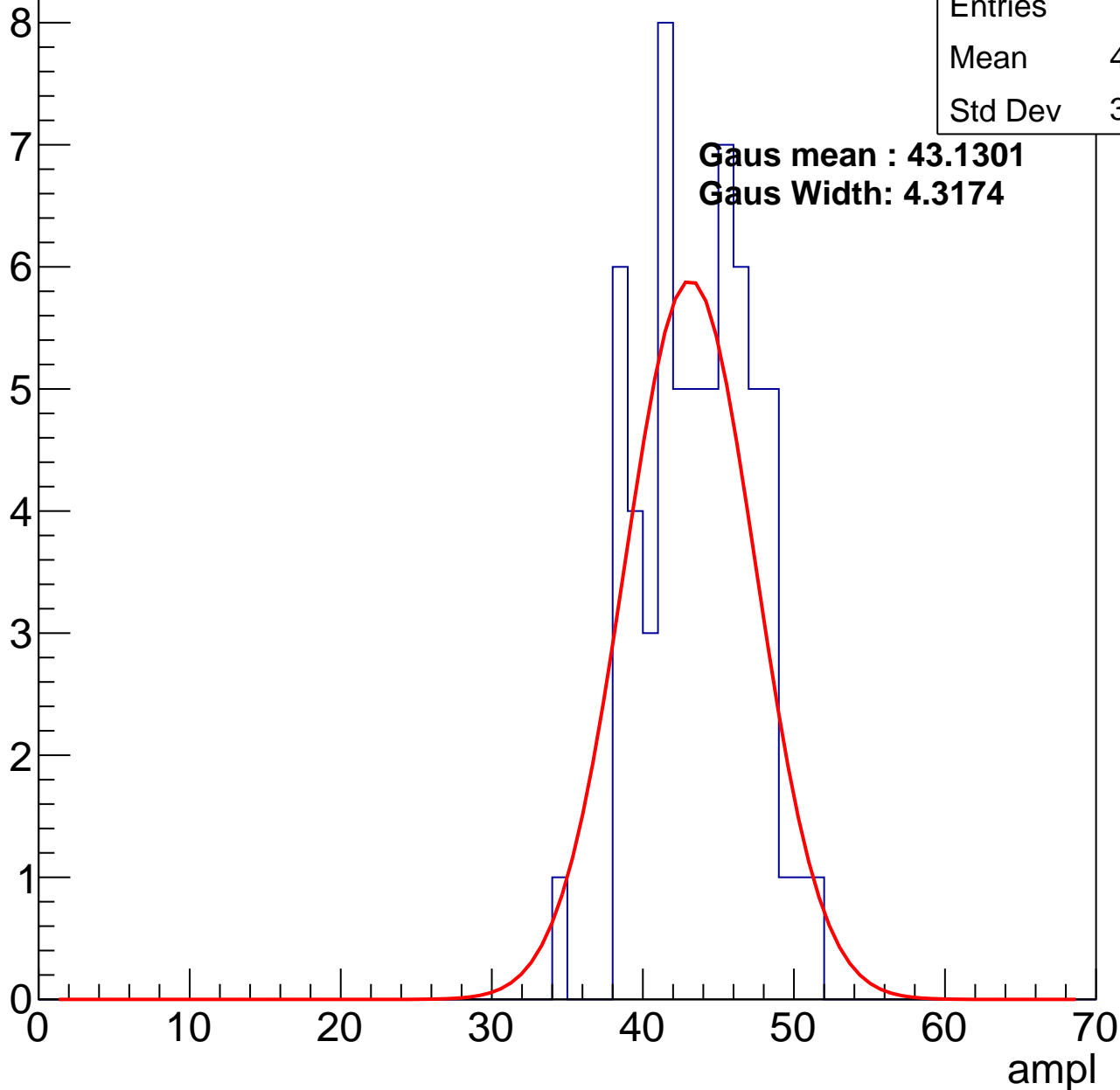
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.29
Std Dev	3.552

Gaus mean : 43.1301

Gaus Width: 4.3174

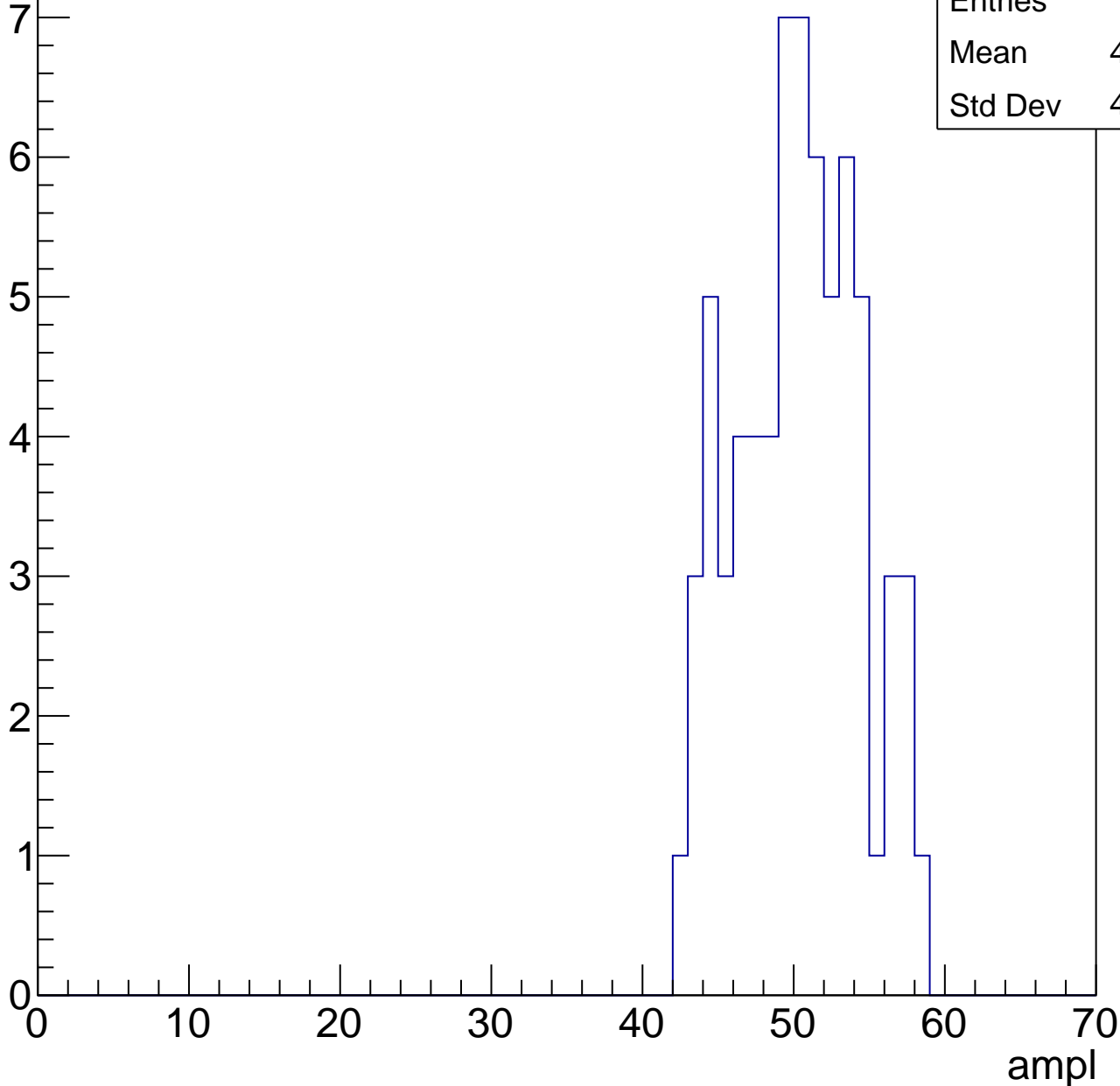


B1L102S, U26-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

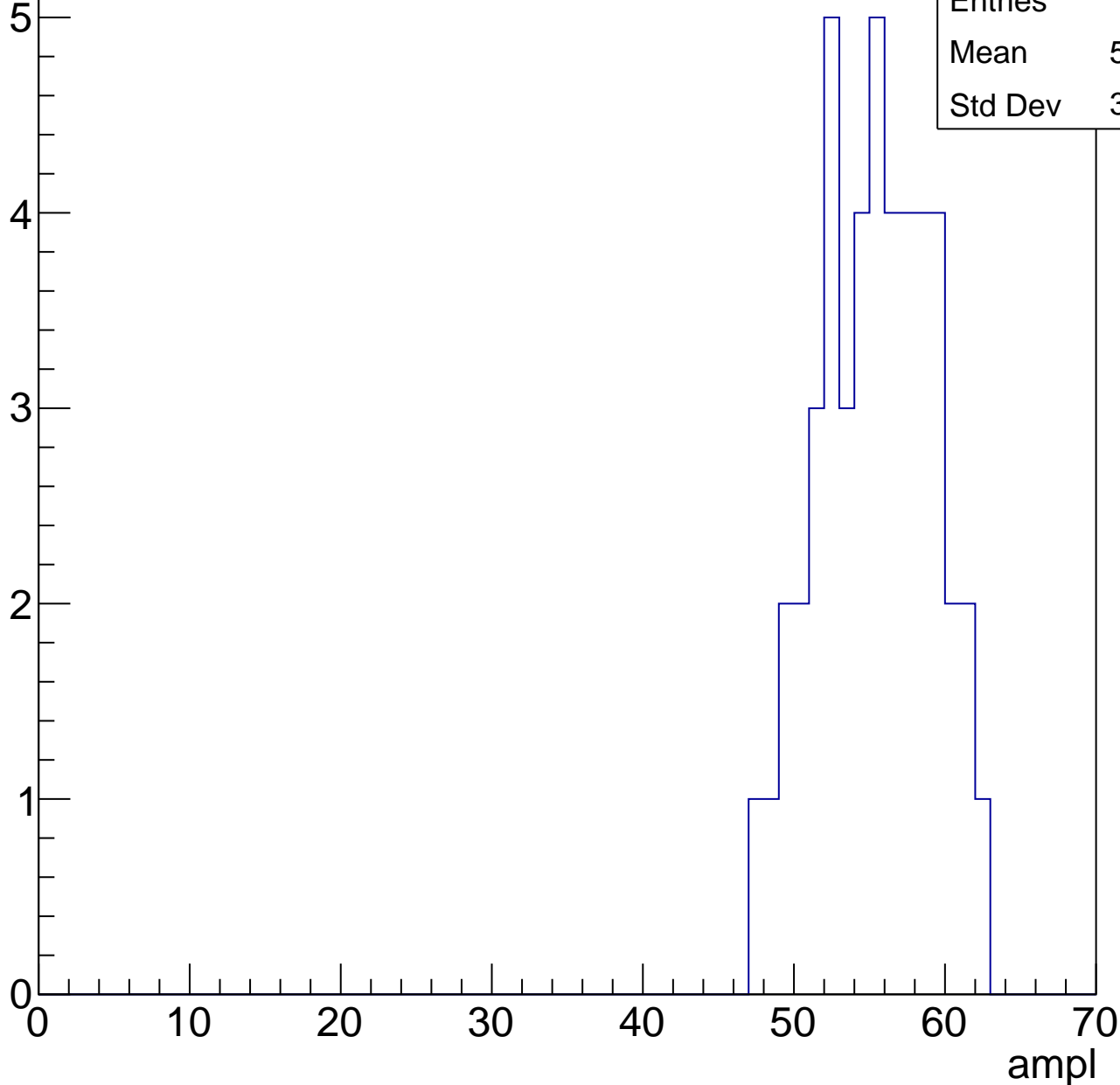
Entries	68
Mean	49.84
Std Dev	4.006



B1L102S, U26-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

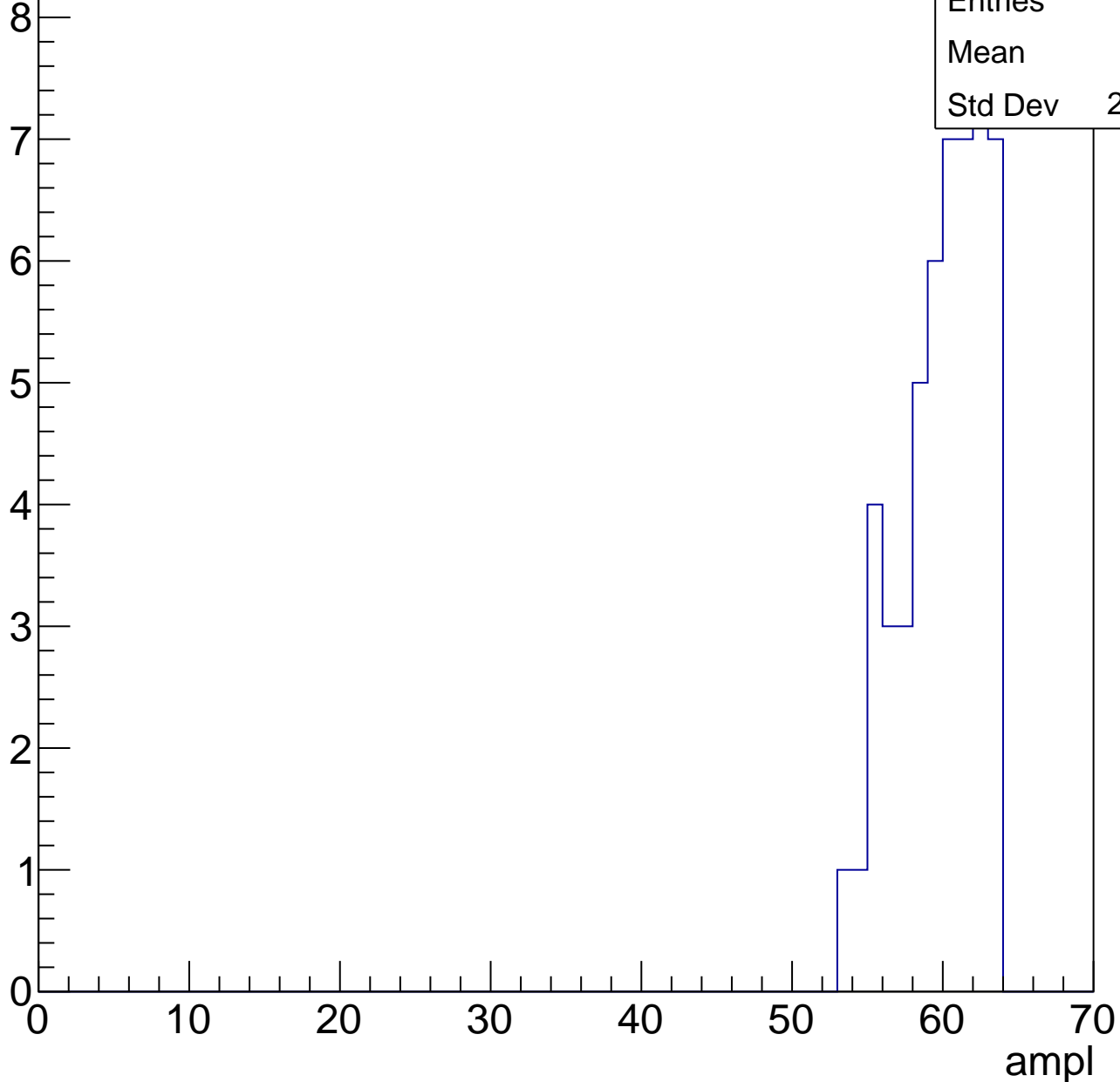
Entry



B1L102S, U26-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

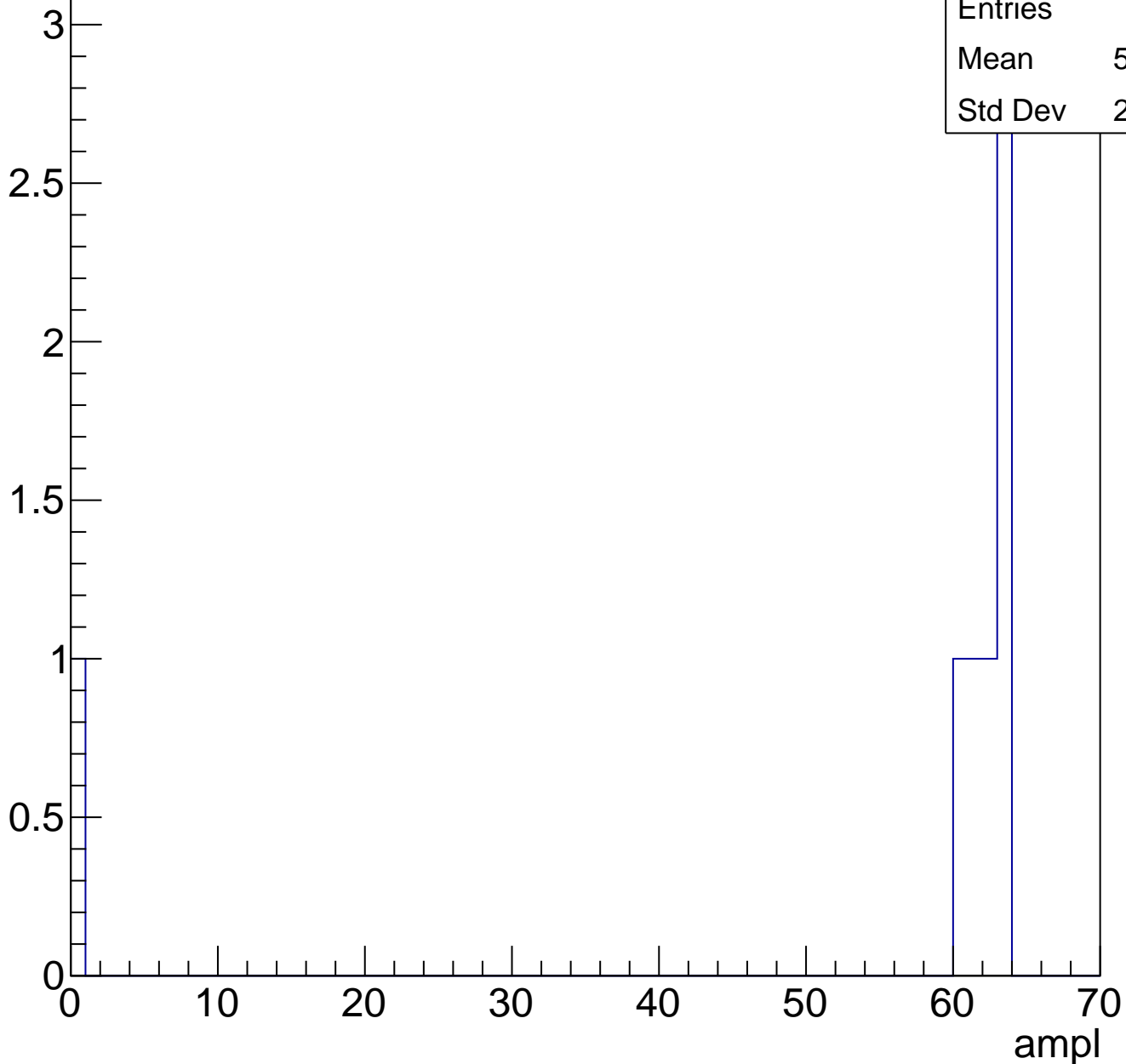
Entry



B1L102S, U26-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch97, adc0

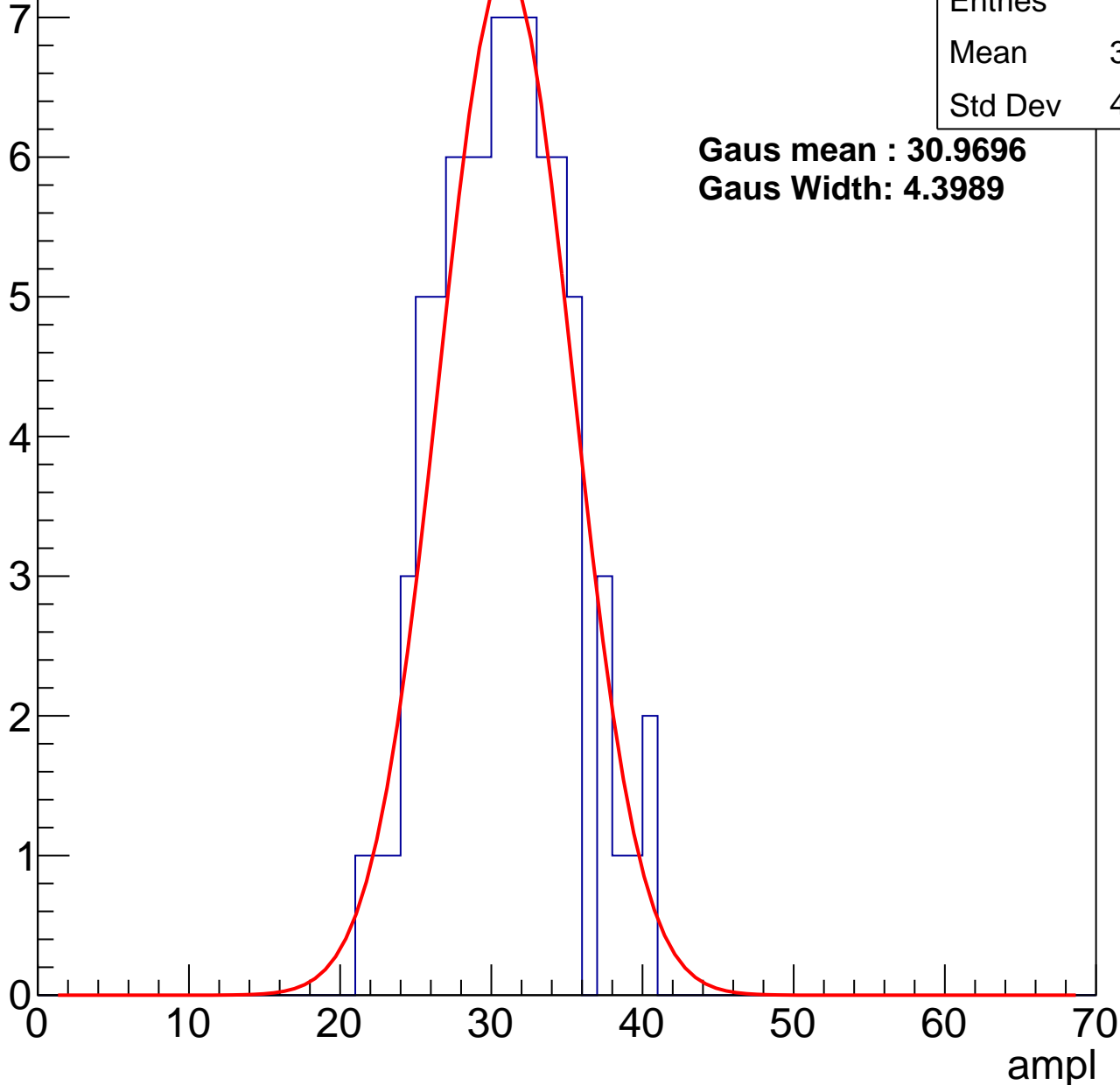
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	30.29
Std Dev	4.189

Gaus mean : 30.9696

Gaus Width: 4.3989



B1L102S, U26-ch97, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	37.45
Std Dev	3.755

Gaus mean : 37.9068

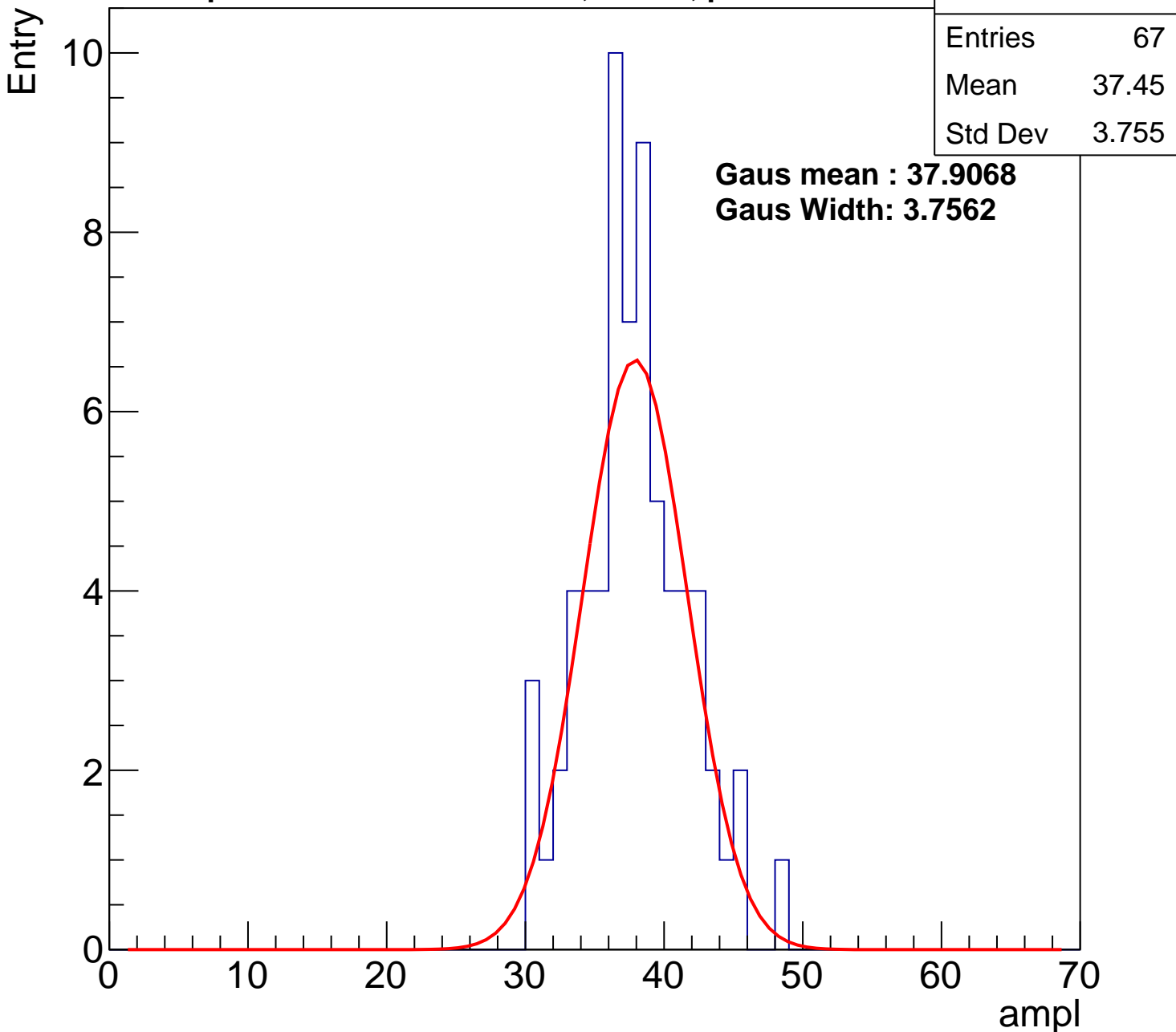
Gaus Width: 3.7562

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch97, adc2

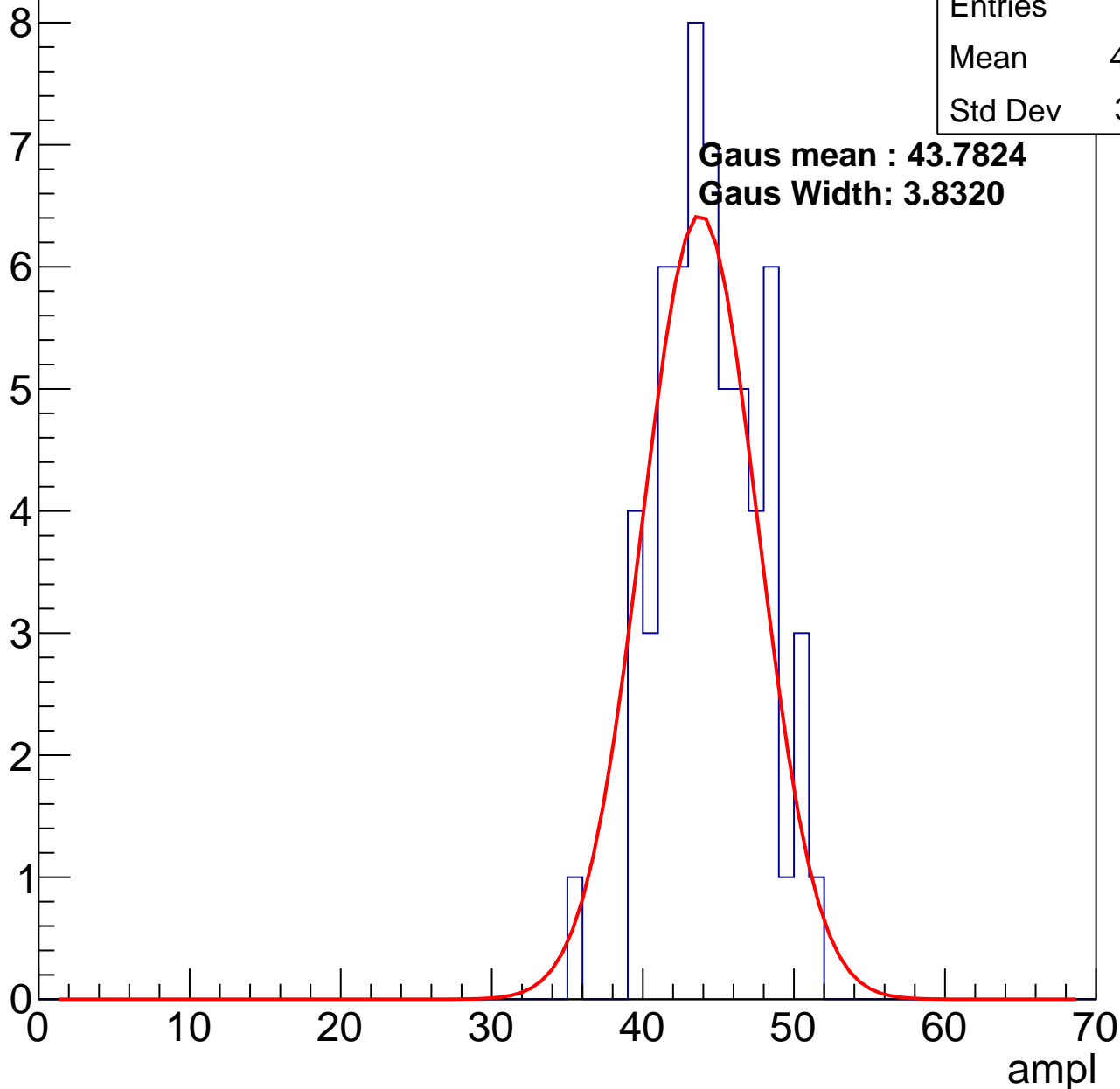
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	44.03
Std Dev	3.301

Gaus mean : 43.7824

Gaus Width: 3.8320

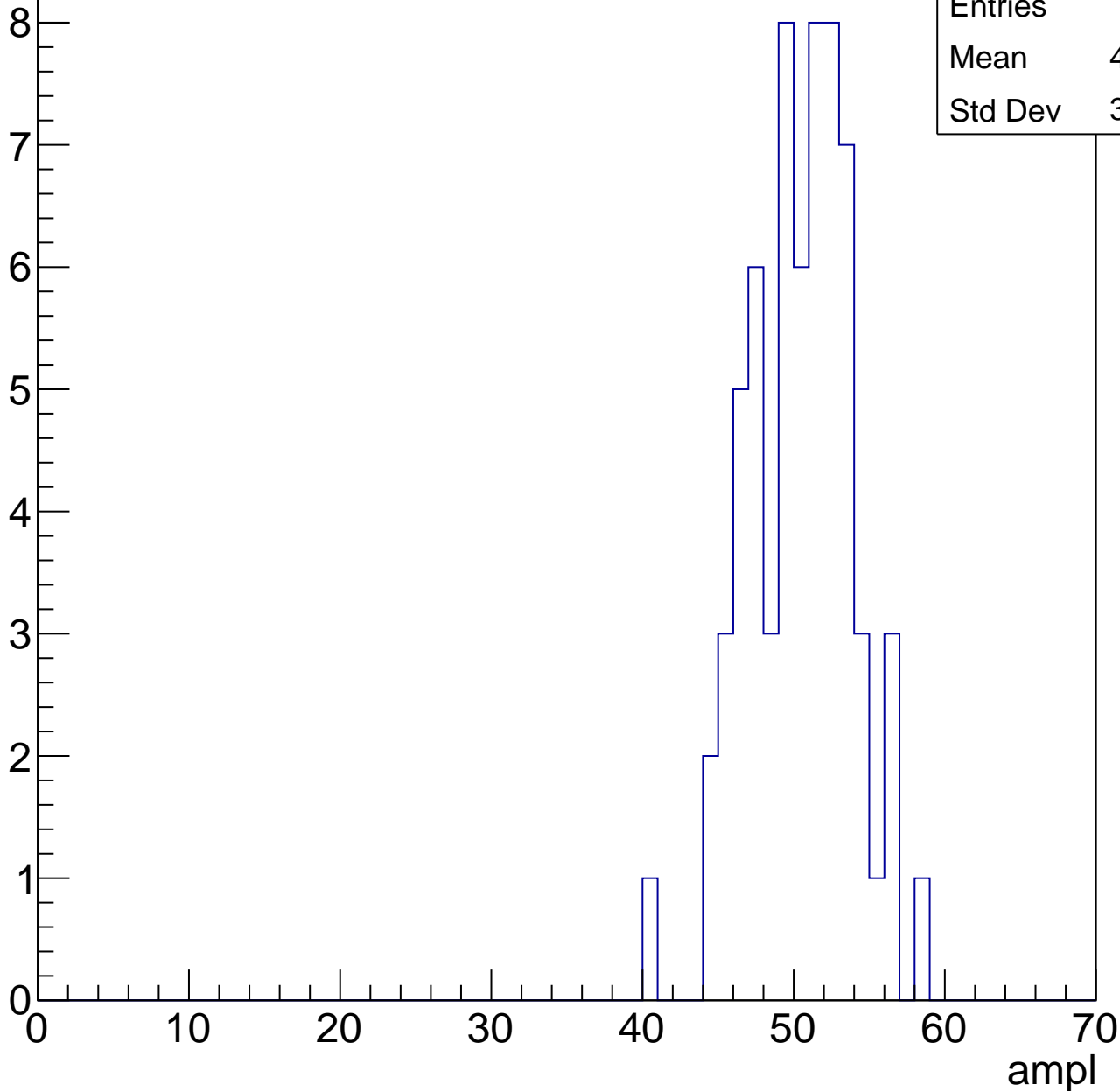


B1L102S, U26-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.98
Std Dev	3.395

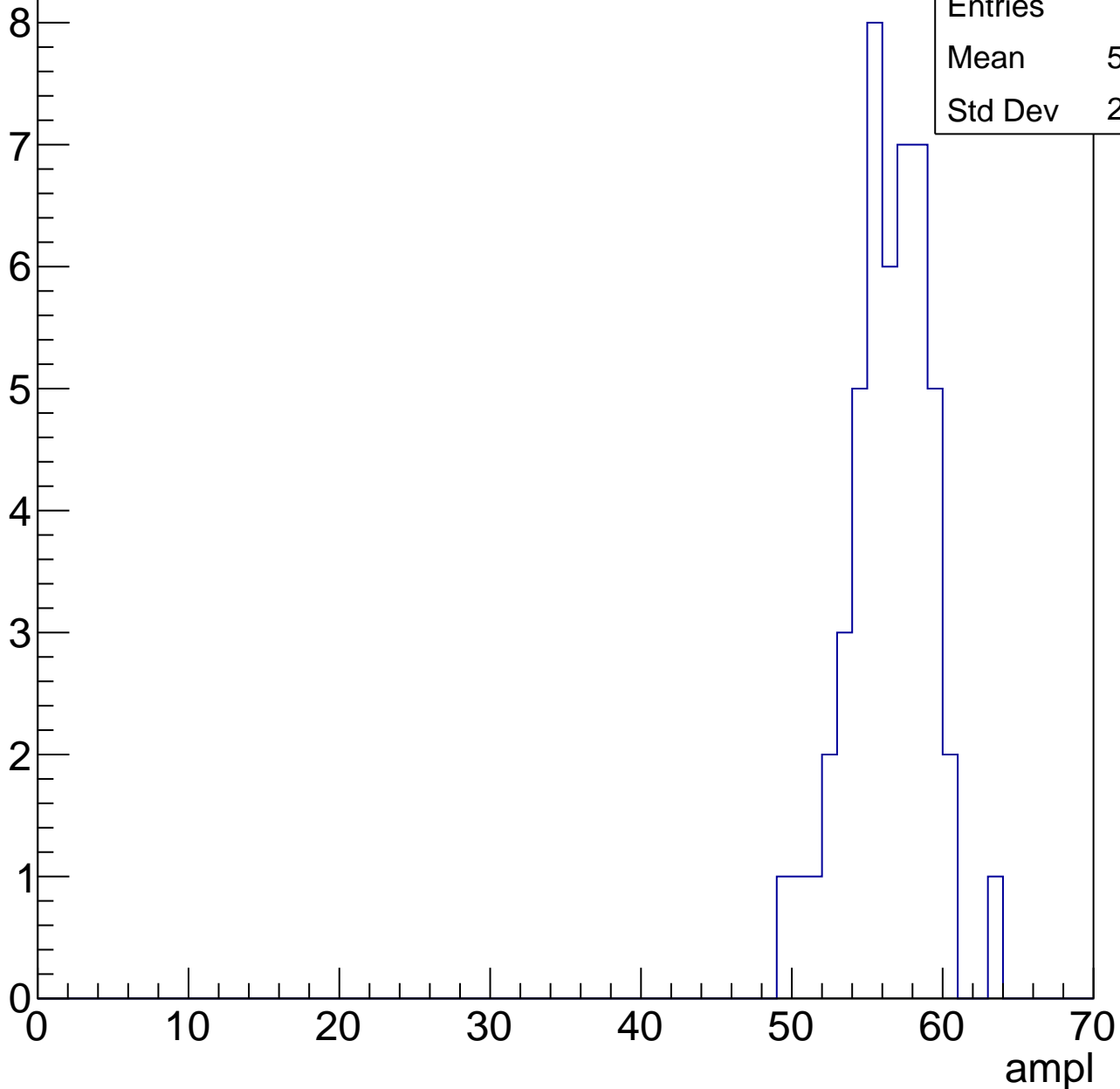


B1L102S, U26-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.96
Std Dev	2.703

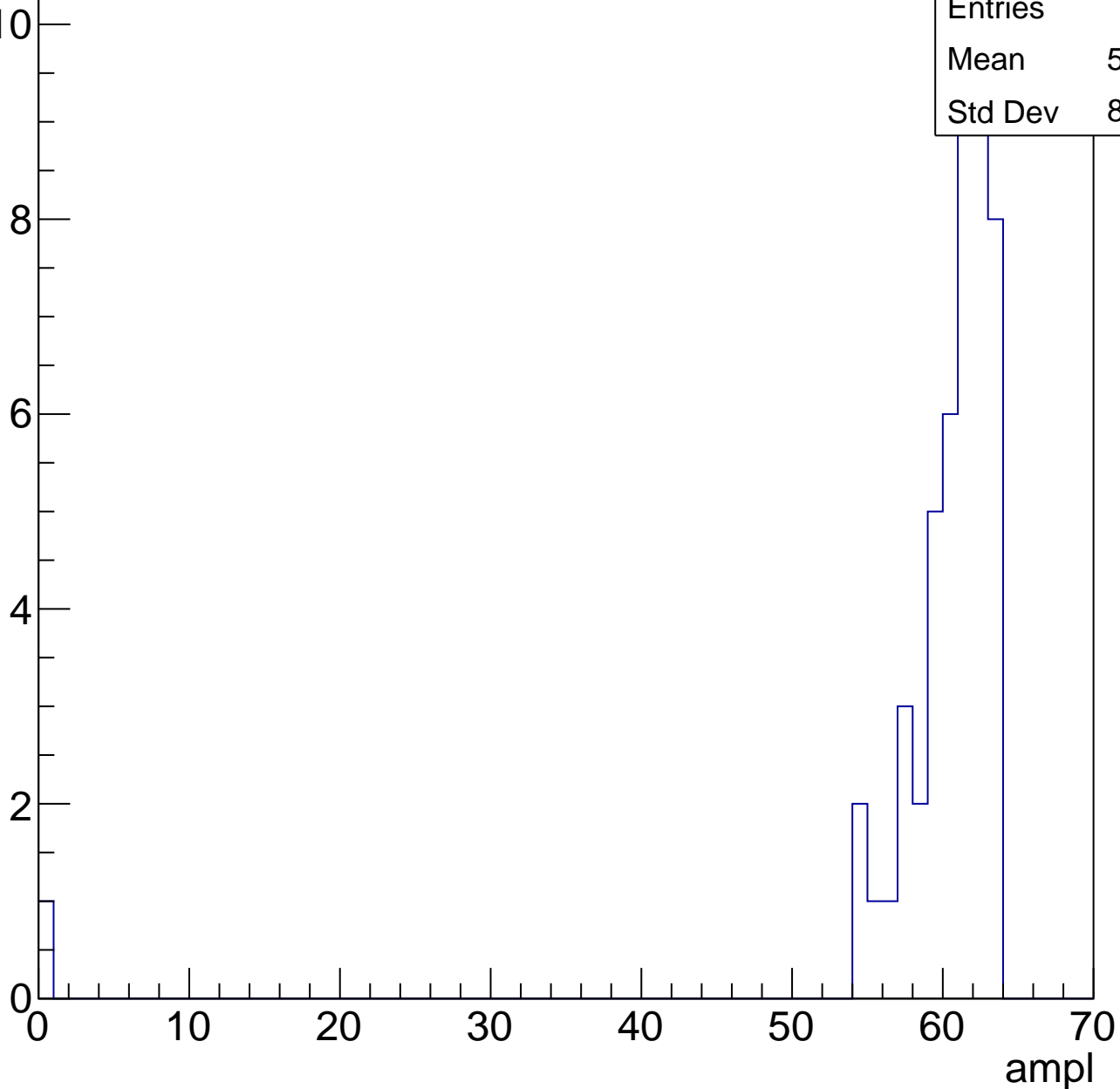


B1L102S, U26-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

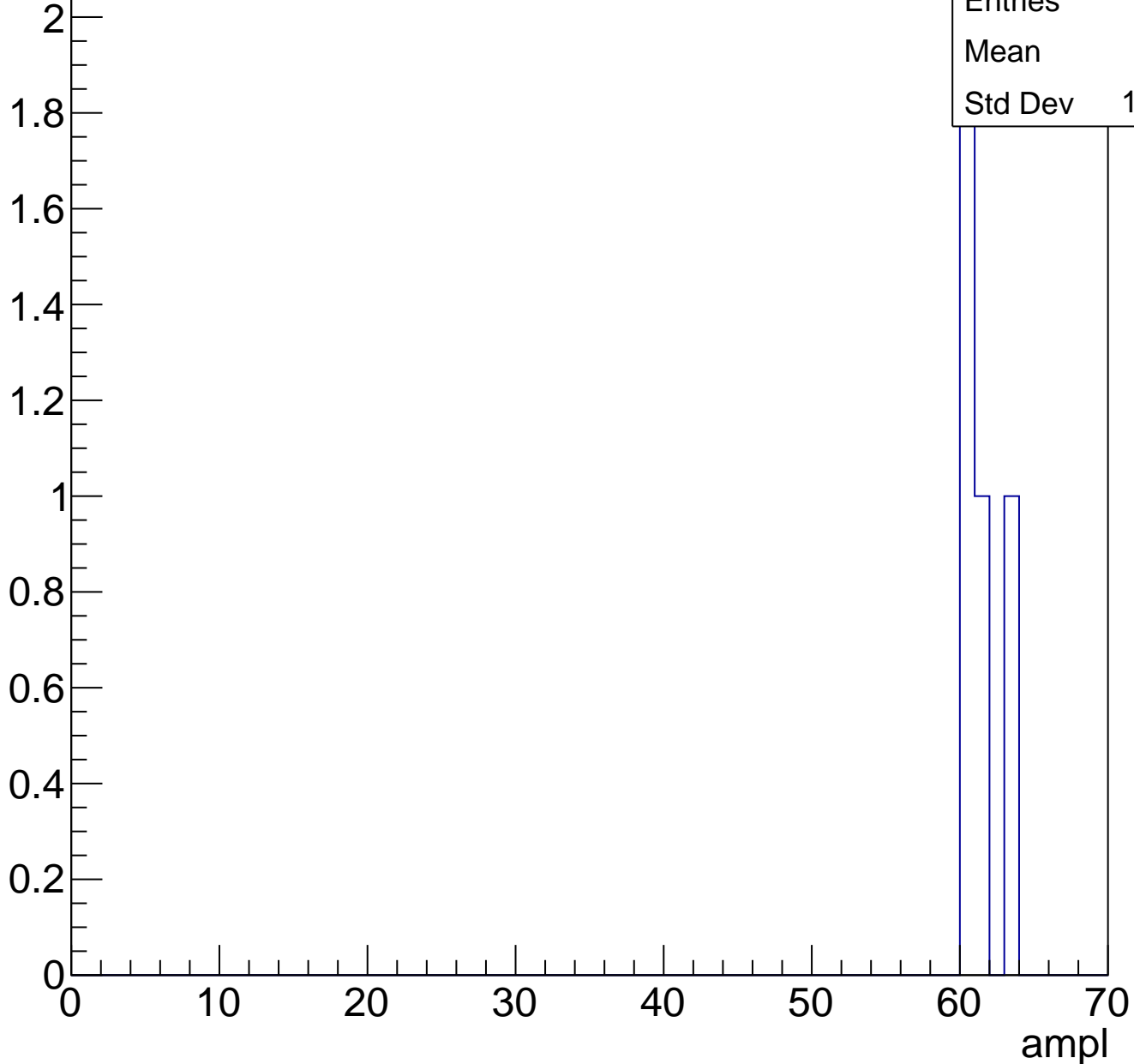
Entries	48
Mean	59.04
Std Dev	8.933



B1L102S, U26-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch98, adc0

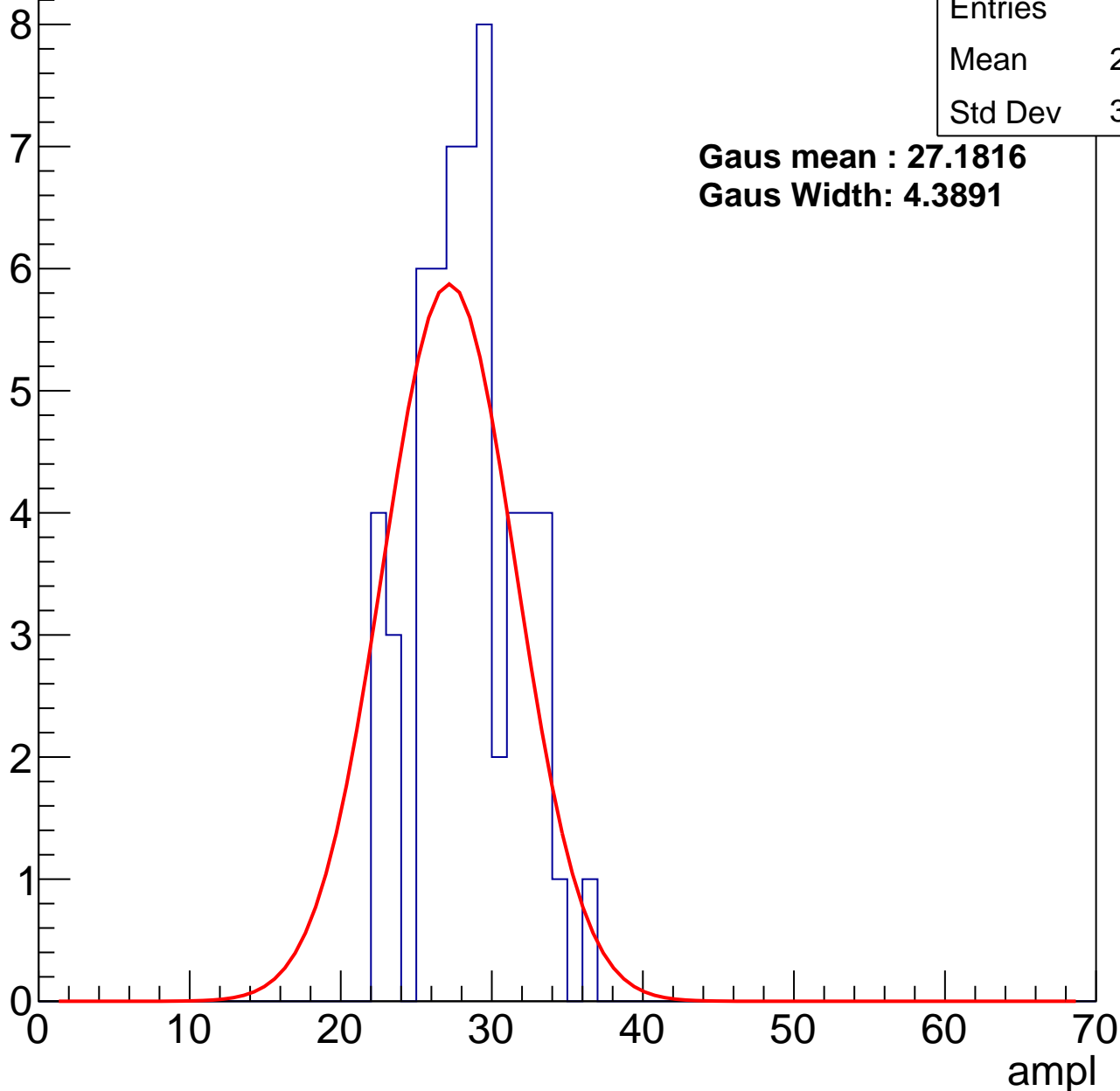
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.96
Std Dev	3.298

Gaus mean : 27.1816

Gaus Width: 4.3891



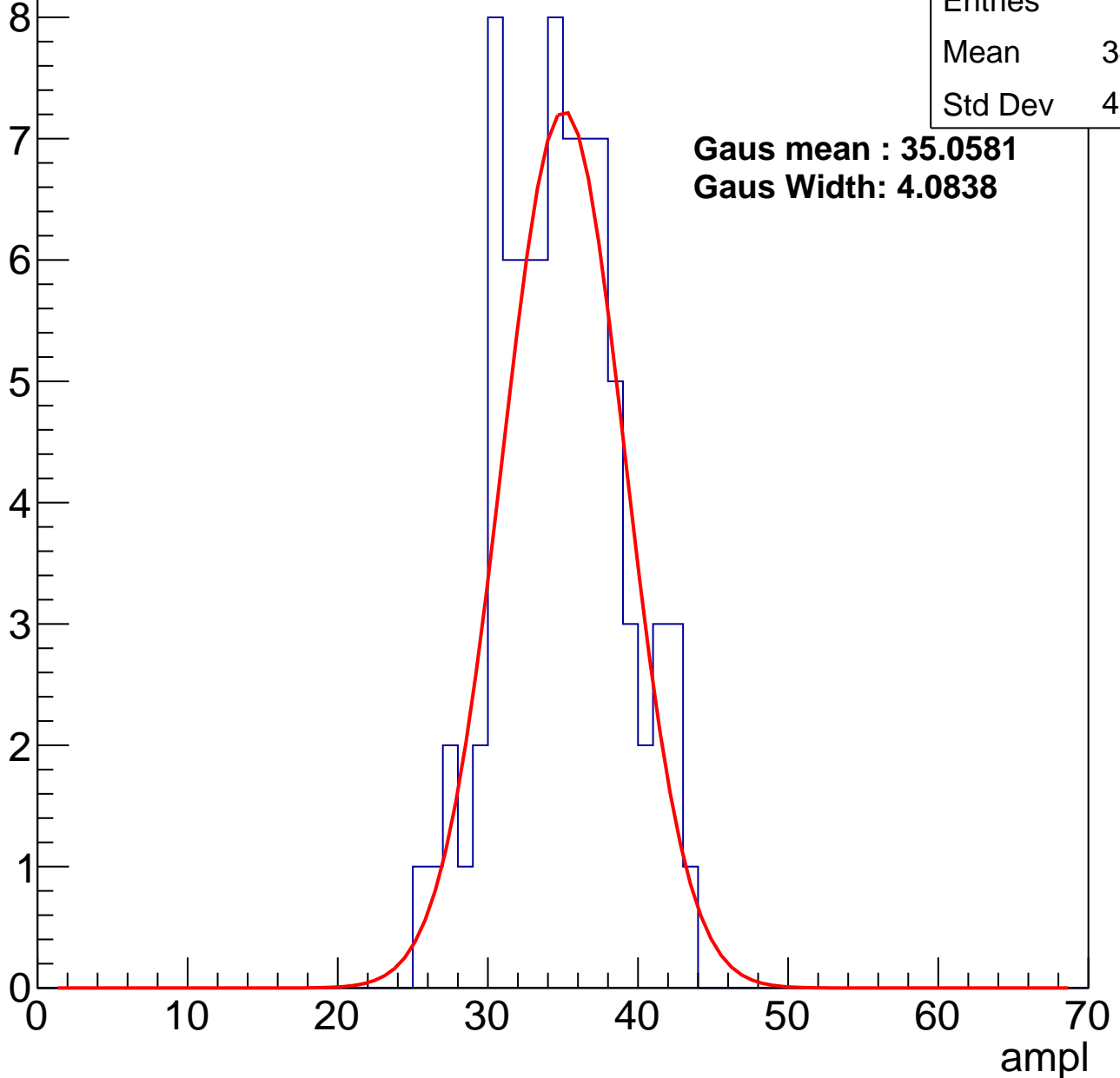
B1L102S, U26-ch98, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	34.35
Std Dev	4.019

Gaus mean : 35.0581
Gaus Width: 4.0838



B1L102S, U26-ch98, adc2

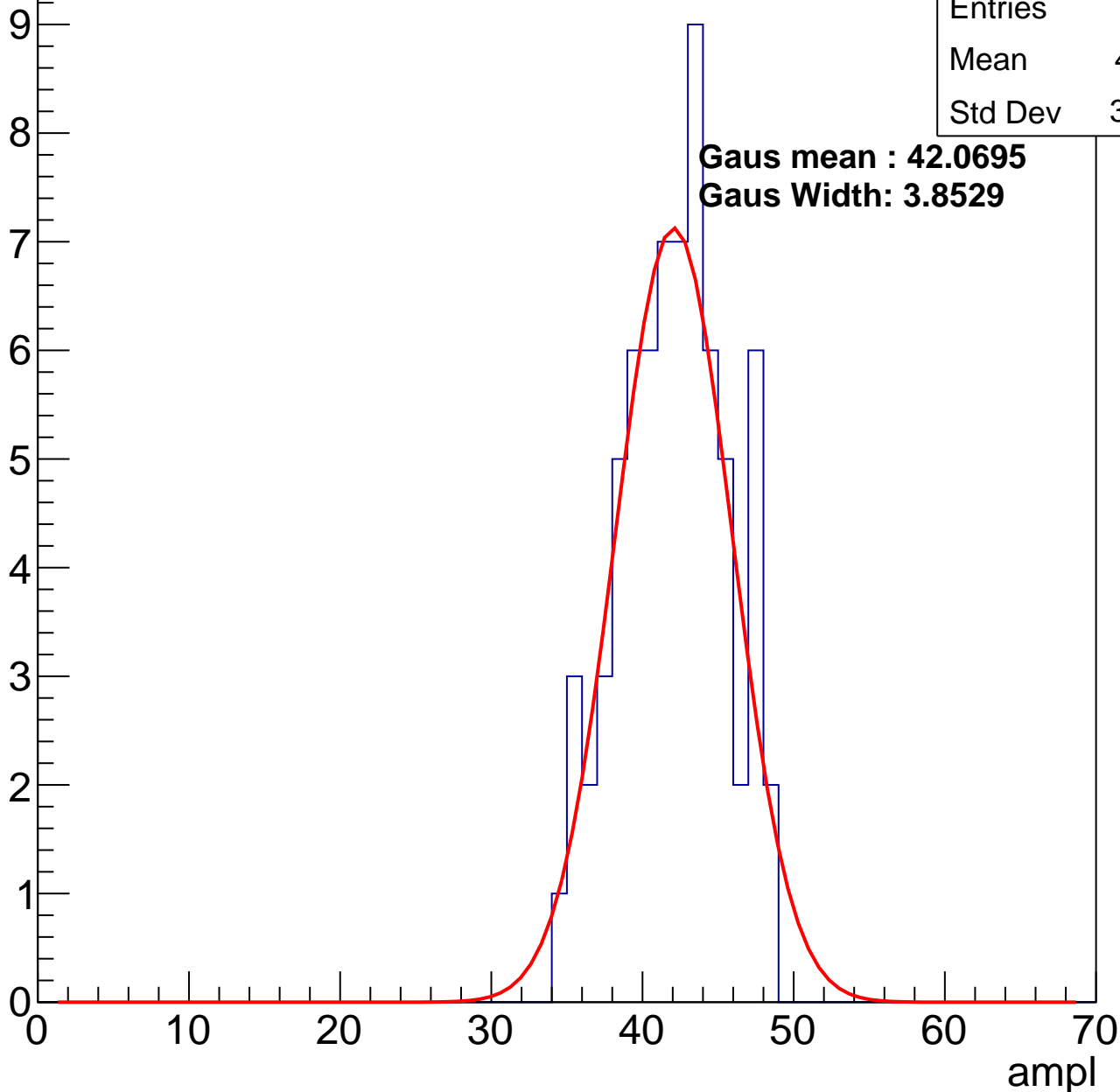
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.61
Std Dev	3.474

Gaus mean : 42.0695

Gaus Width: 3.8529

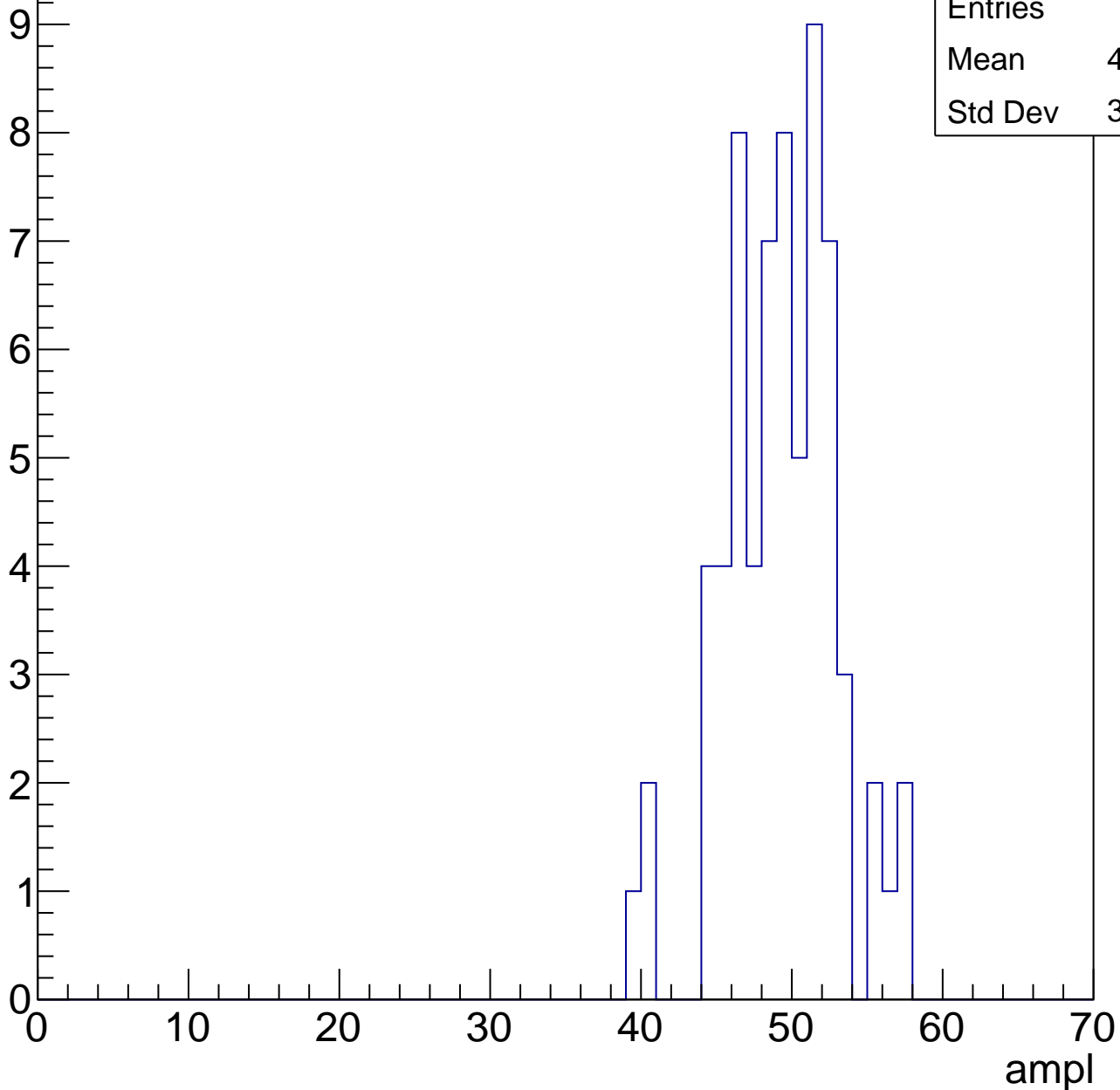


B1L102S, U26-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.82
Std Dev	3.705

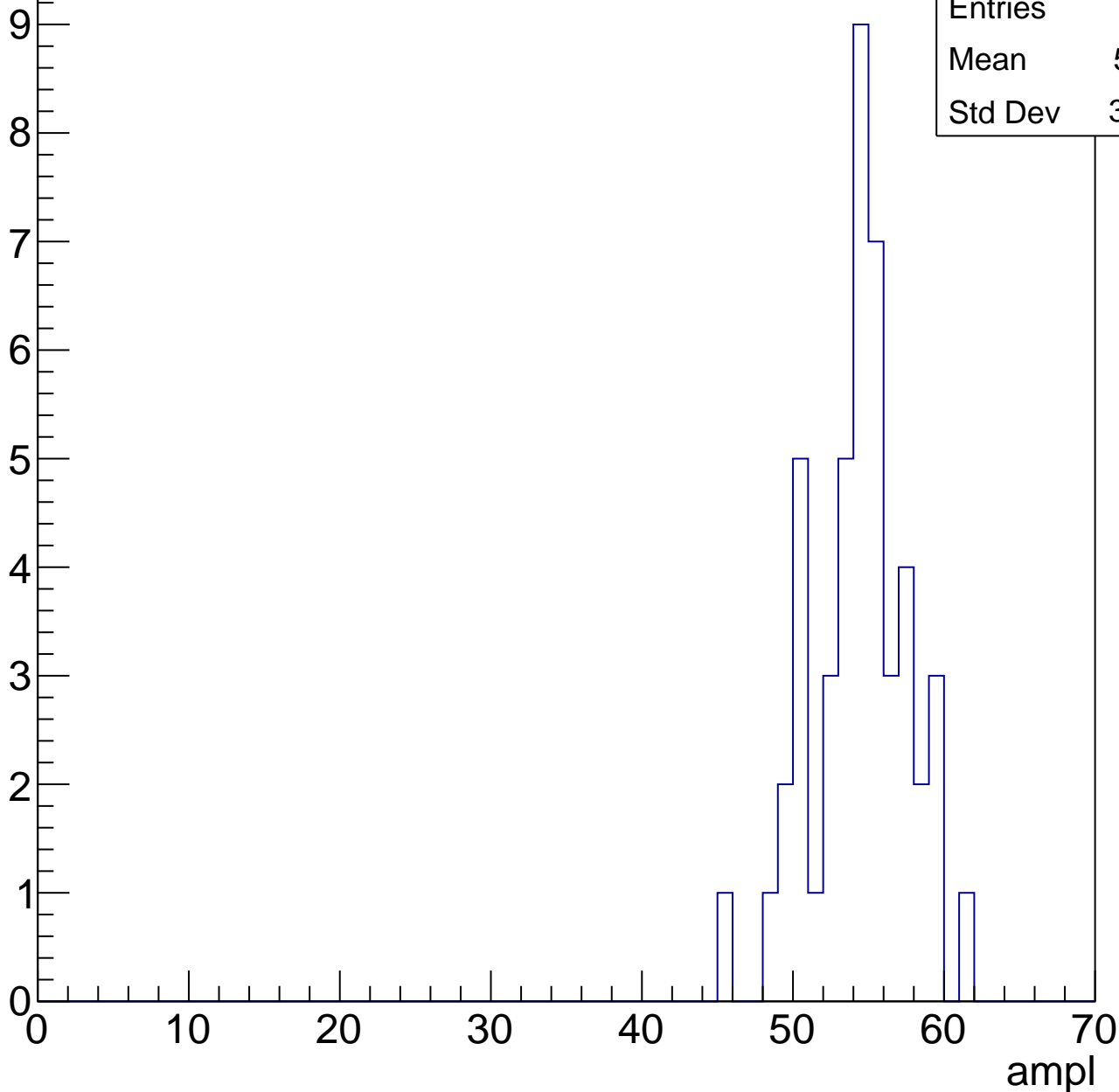


B1L102S, U26-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.91
Std Dev	3.208

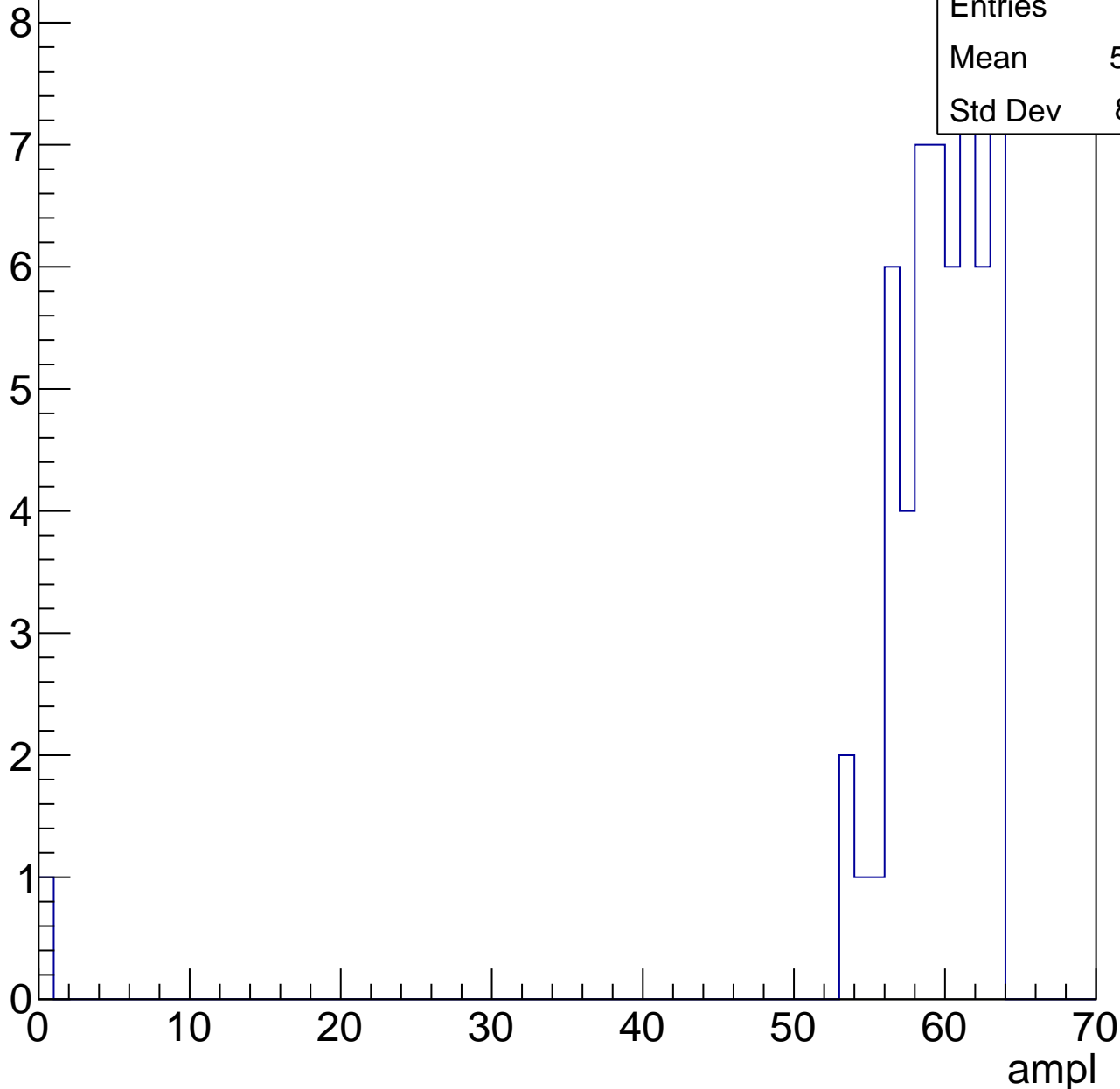


B1L102S, U26-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

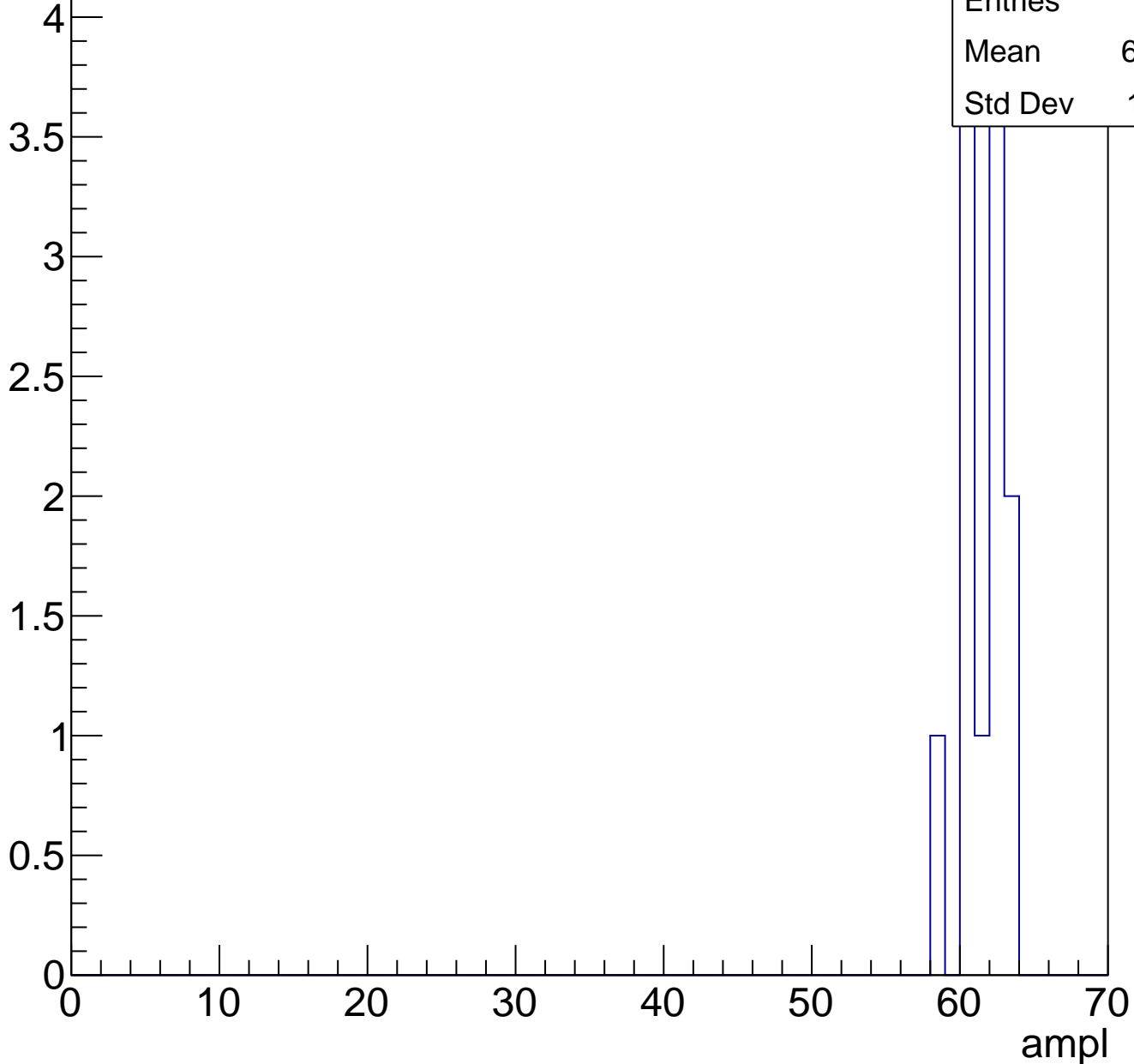
Entries	57
Mean	58.28
Std Dev	8.231



B1L102S, U26-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U26-ch99, adc0

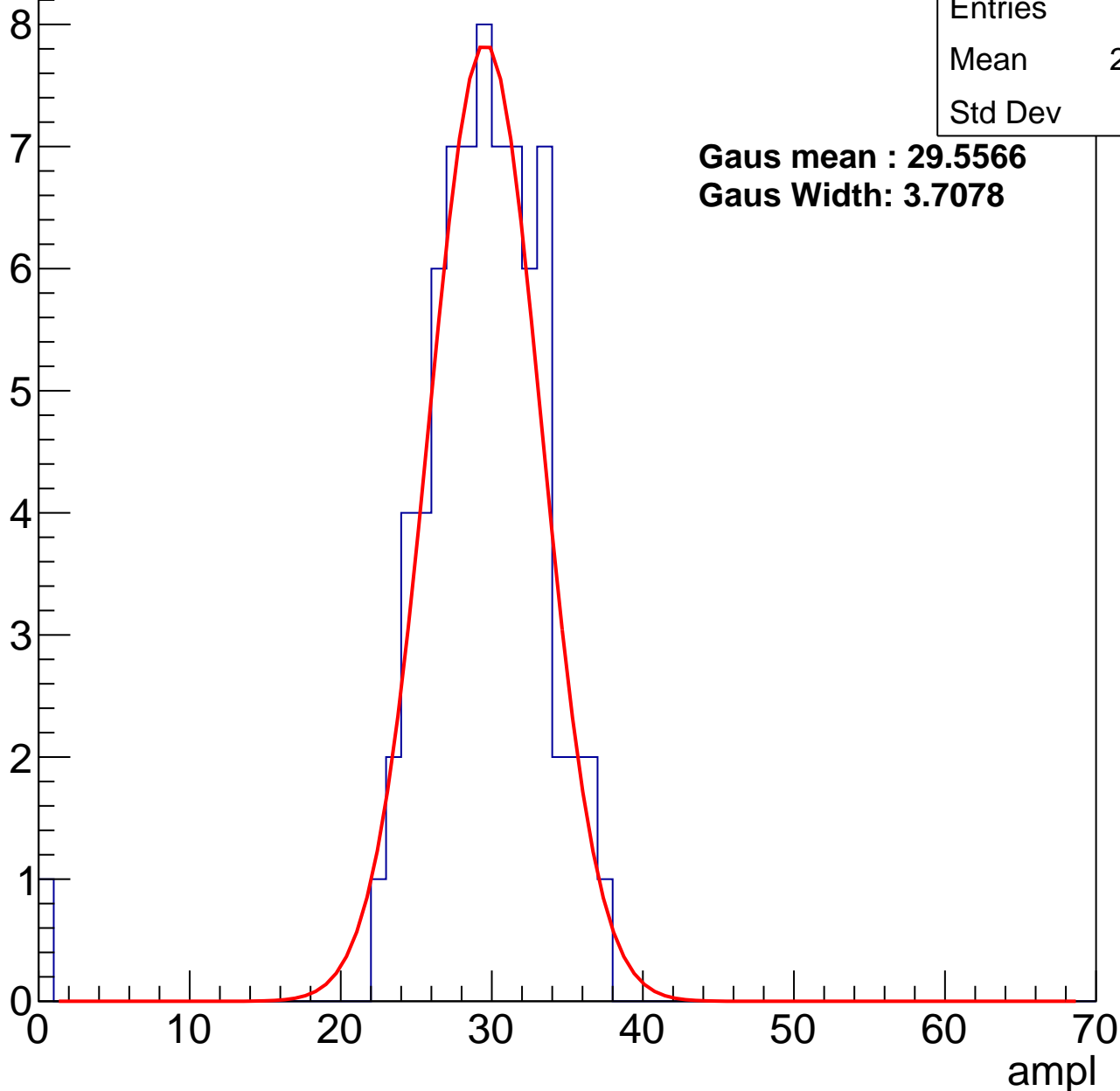
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.84
Std Dev	4.79

Gaus mean : 29.5566

Gaus Width: 3.7078



B1L102S, U26-ch99, adc1

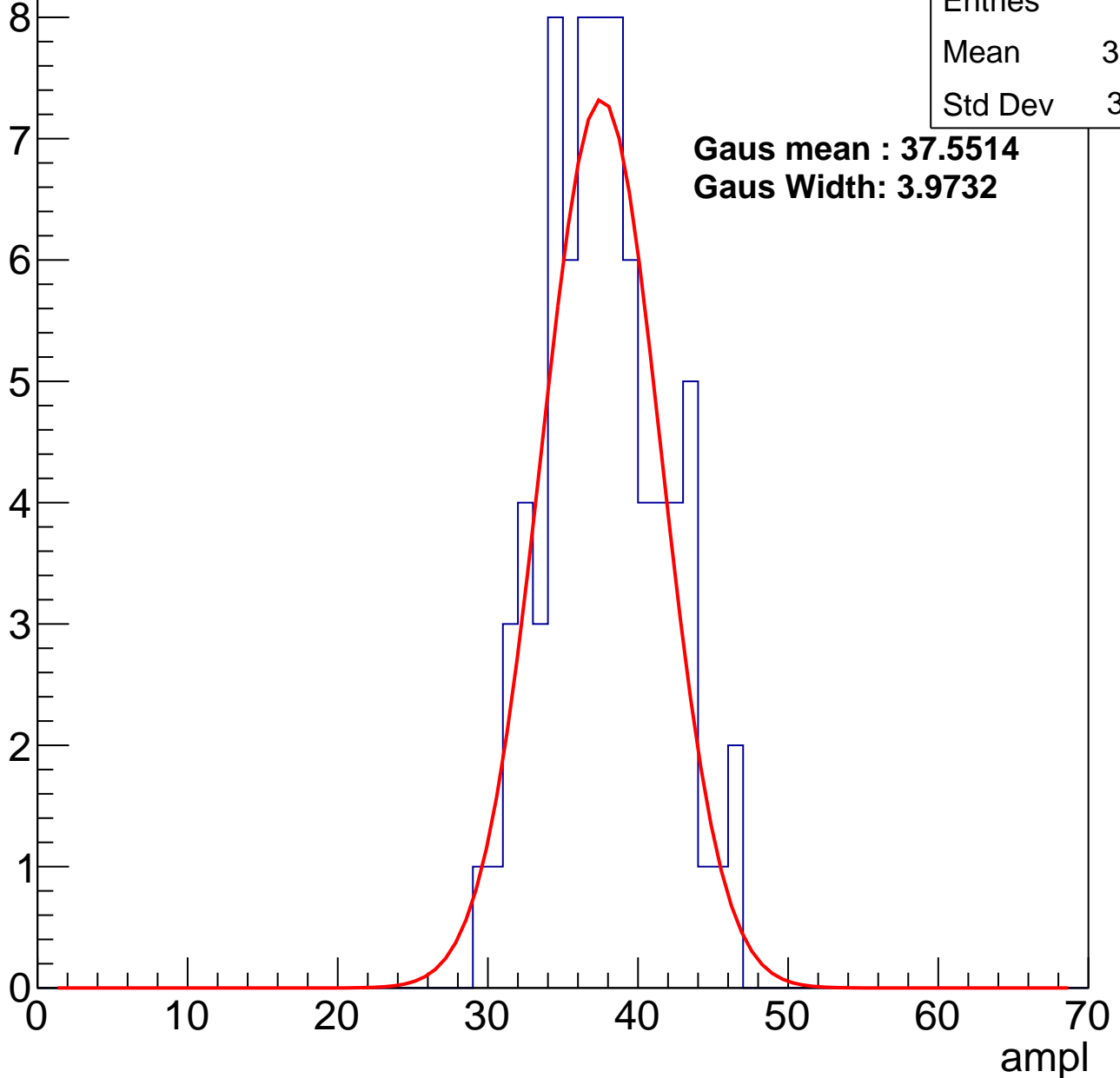
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	37.29
Std Dev	3.871

Gaus mean : 37.5514

Gaus Width: 3.9732



B1L102S, U26-ch99, adc2

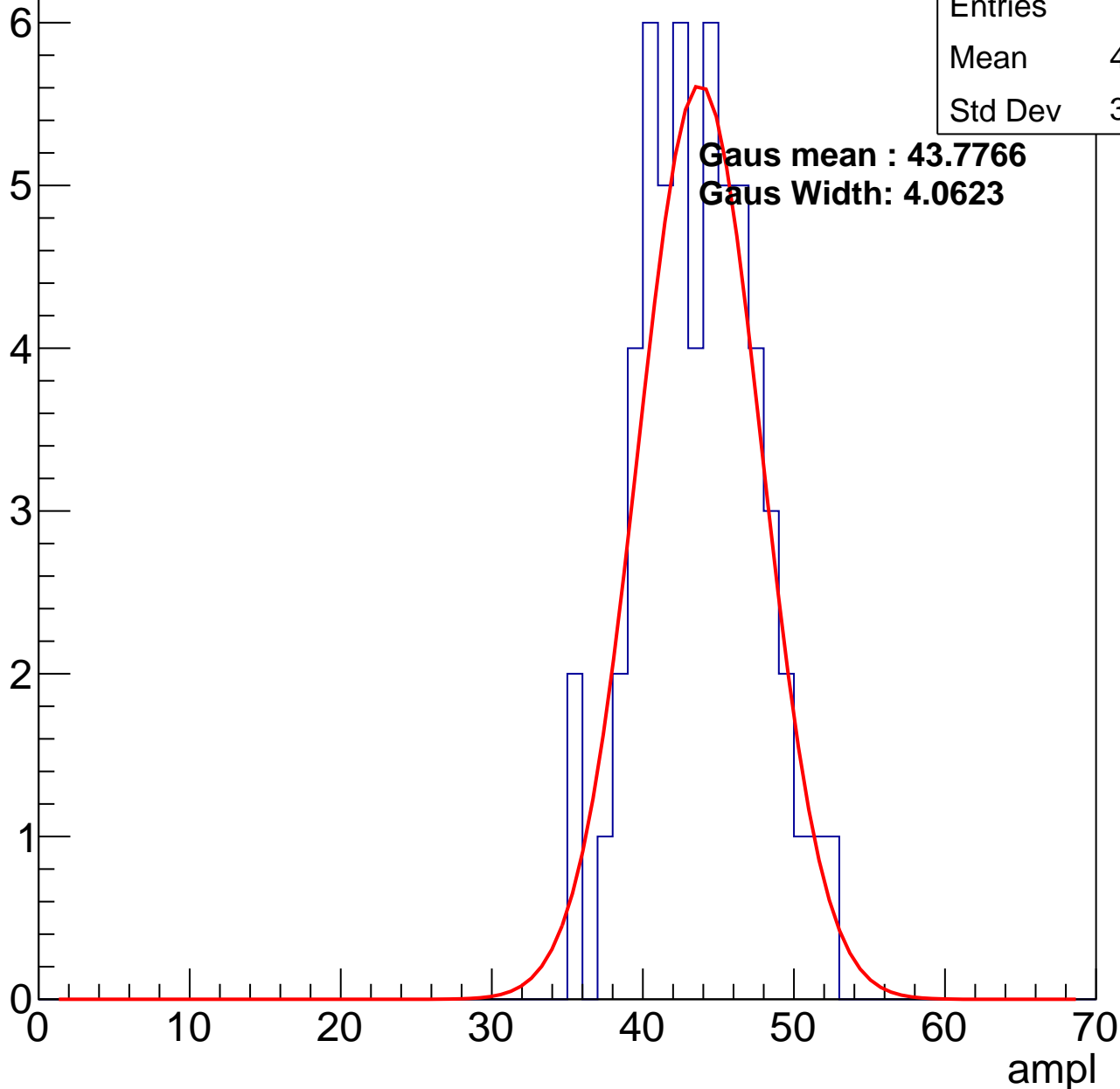
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.28
Std Dev	3.782

Gaus mean : 43.7766

Gaus Width: 4.0623

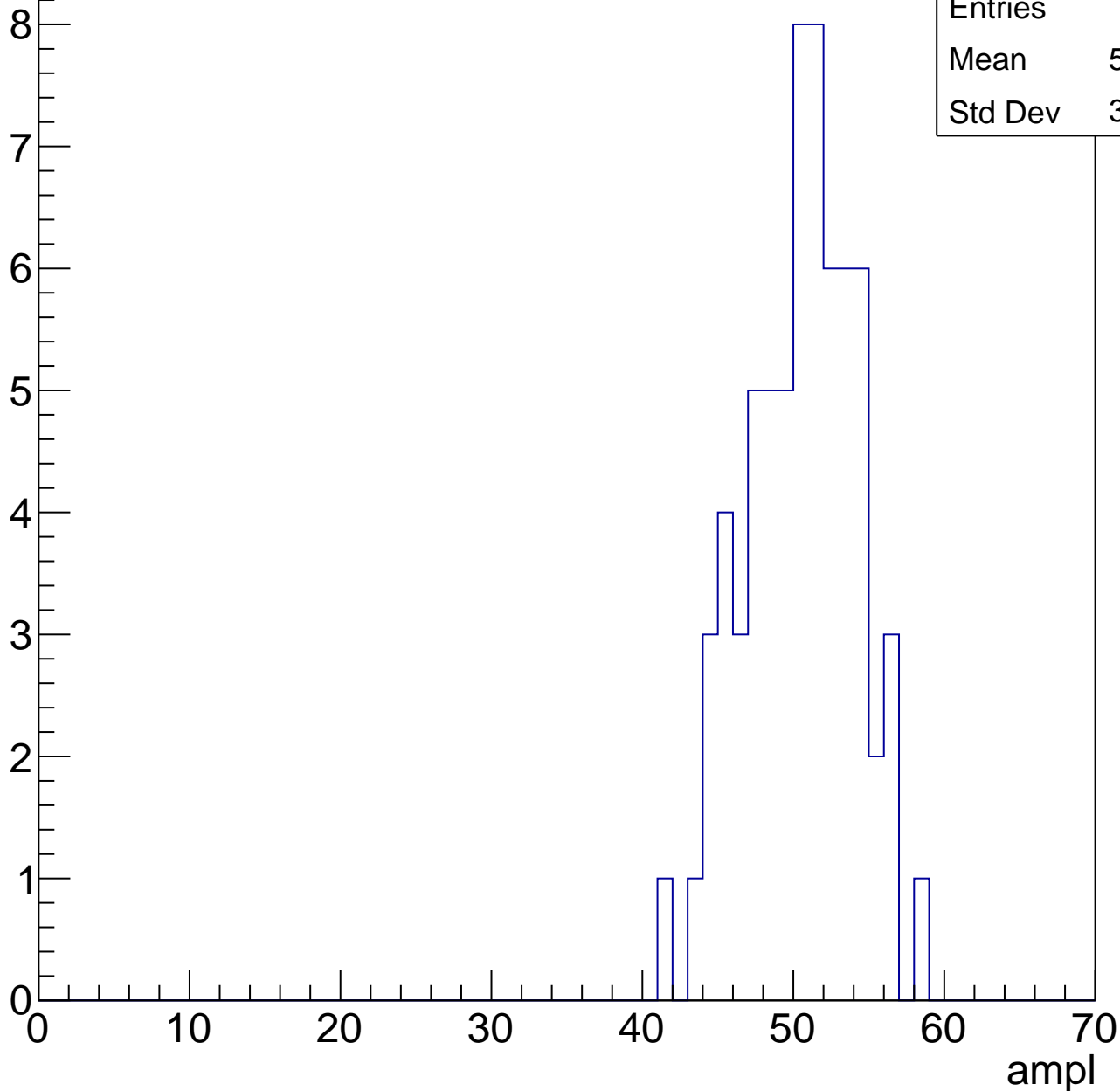


B1L102S, U26-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.03
Std Dev	3.583

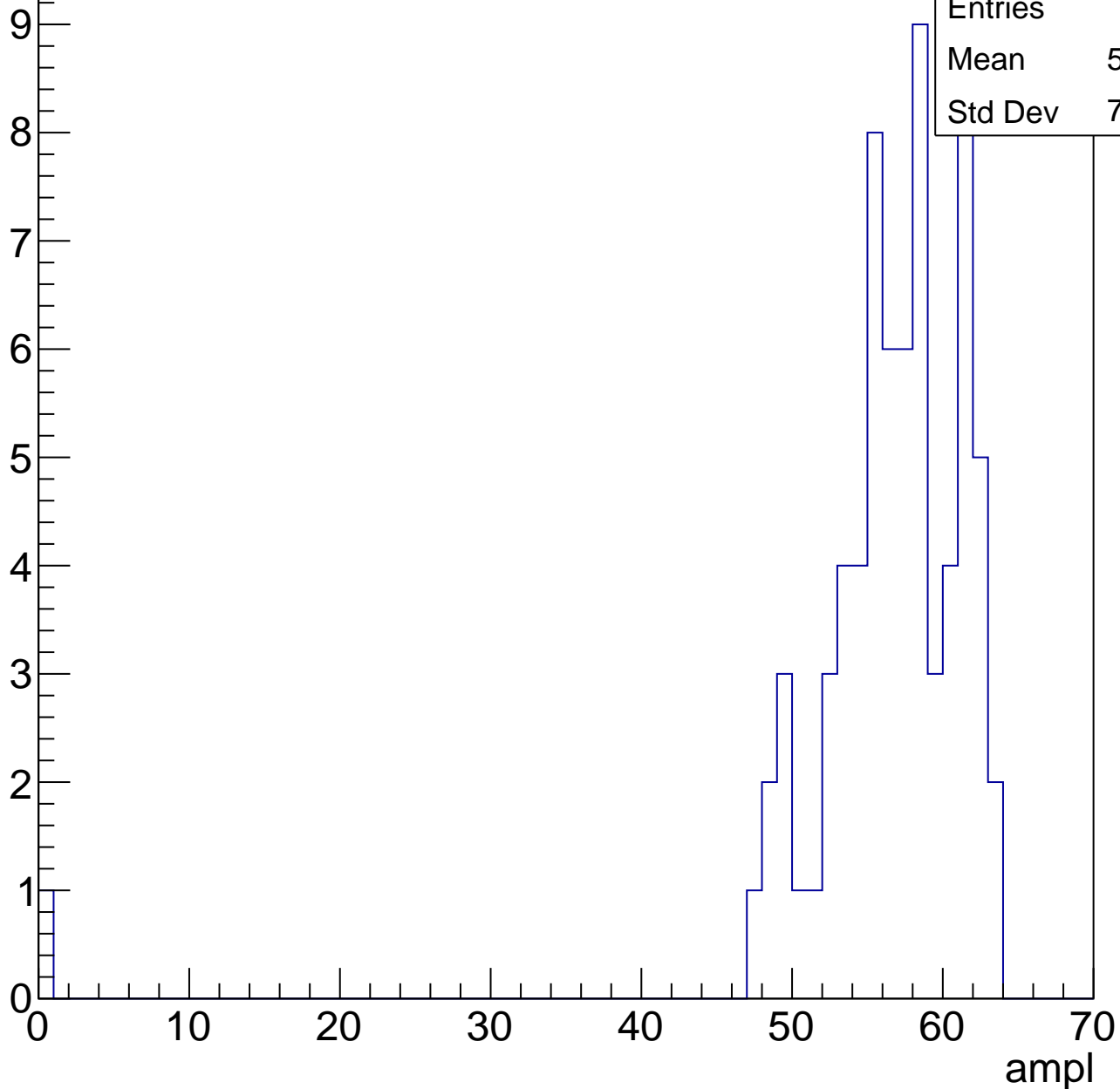


B1L102S, U26-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55.72
Std Dev	7.752



B1L102S, U26-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	32
Mean	60.31
Std Dev	2.098

0

10

20

30

40

50

60

70

ampl

B1L102S, U26-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch100, adc0

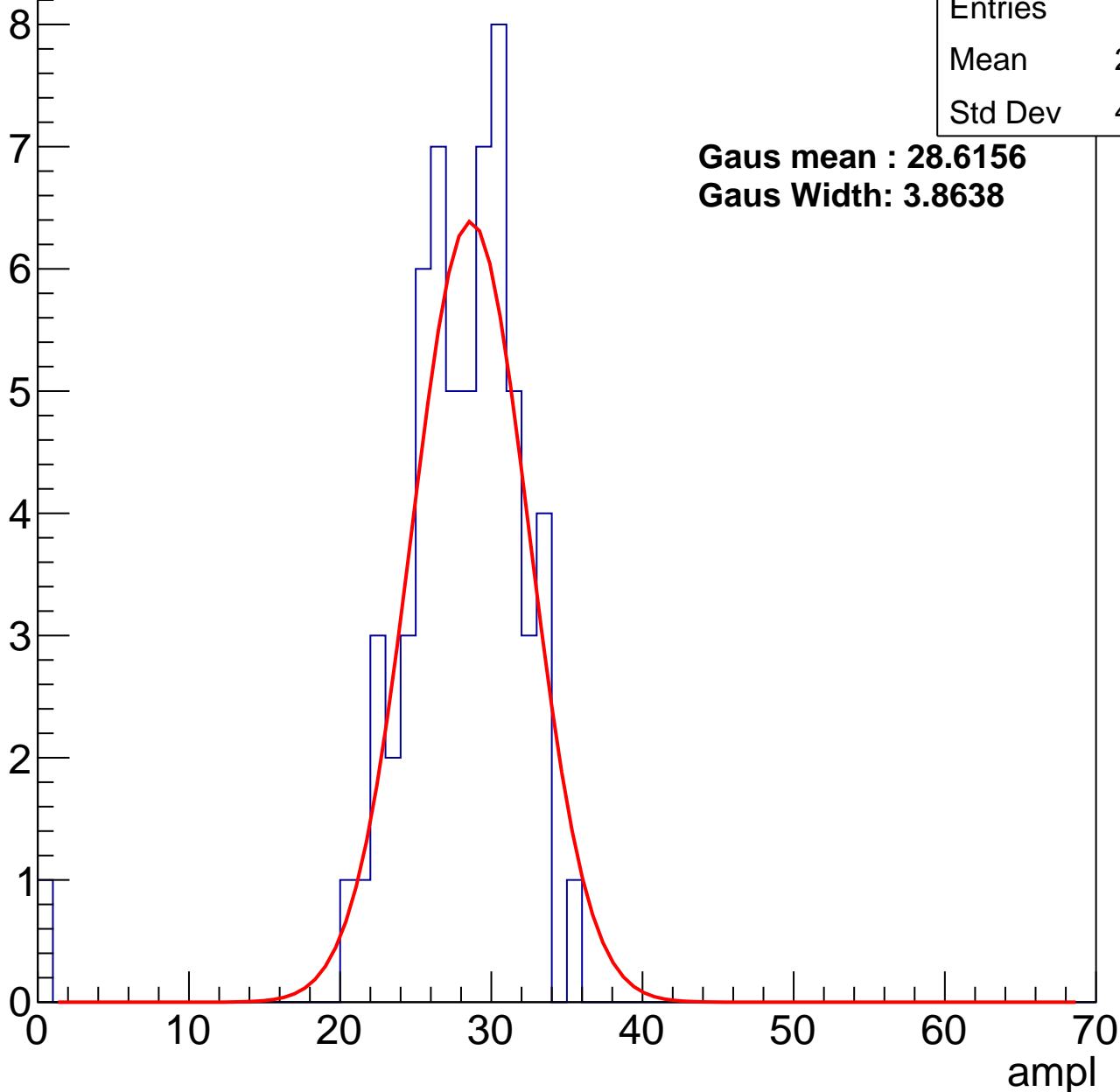
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.31
Std Dev	4.821

Gaus mean : 28.6156

Gaus Width: 3.8638



B1L102S, U26-ch100, adc1

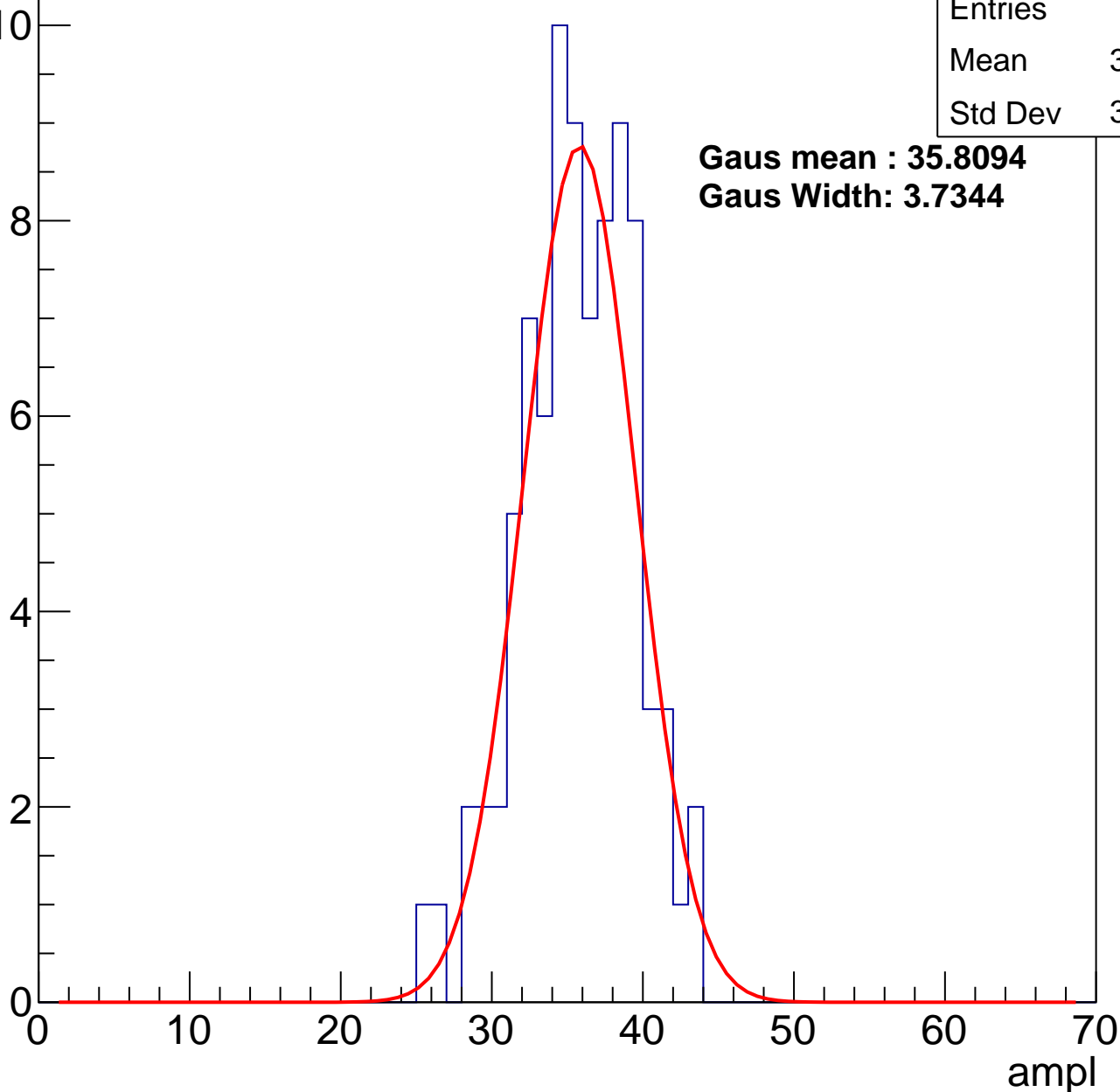
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	35.23
Std Dev	3.706

Gaus mean : 35.8094

Gaus Width: 3.7344



B1L102S, U26-ch100, adc2

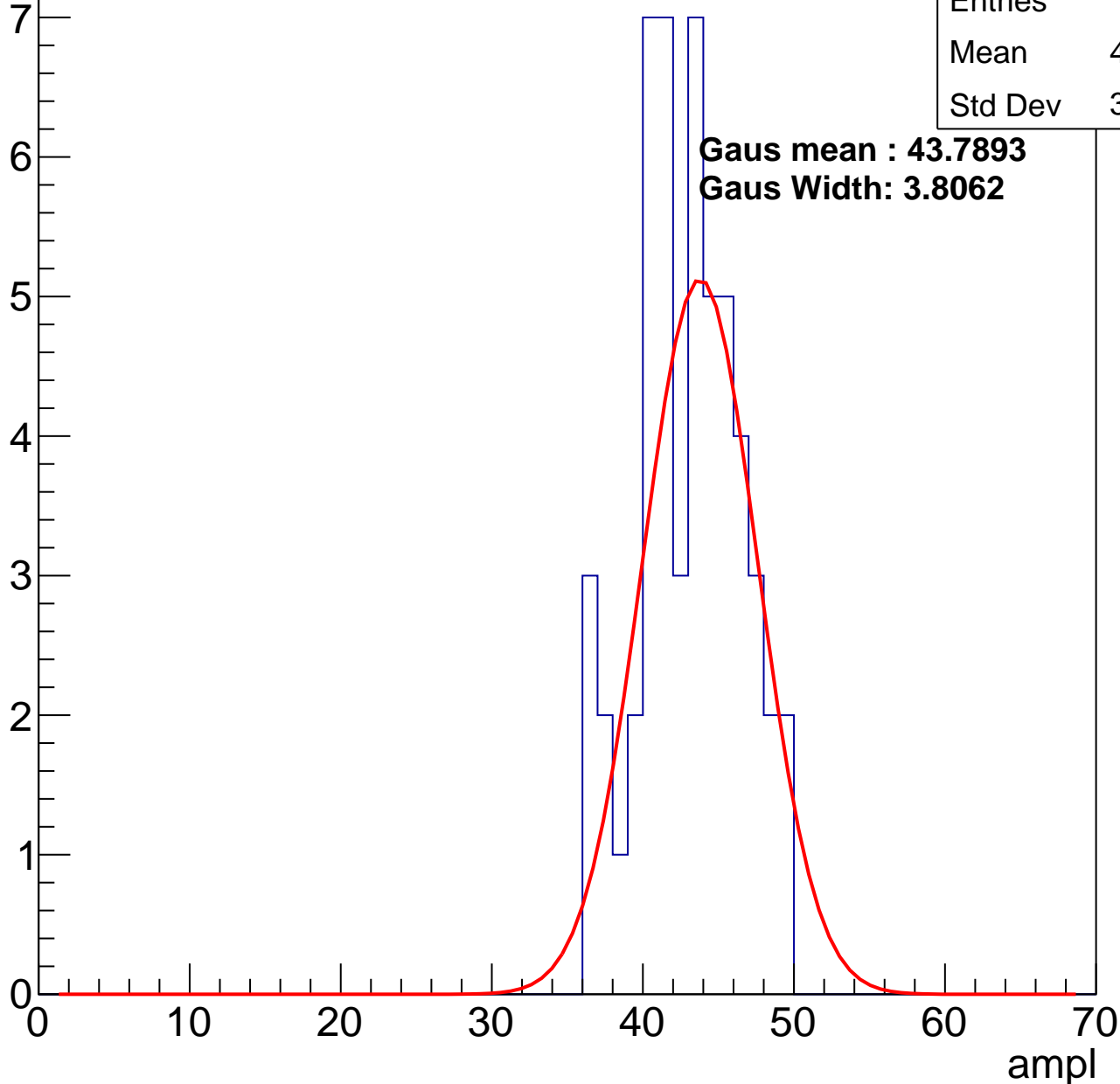
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.57
Std Dev	3.339

Gaus mean : 43.7893

Gaus Width: 3.8062

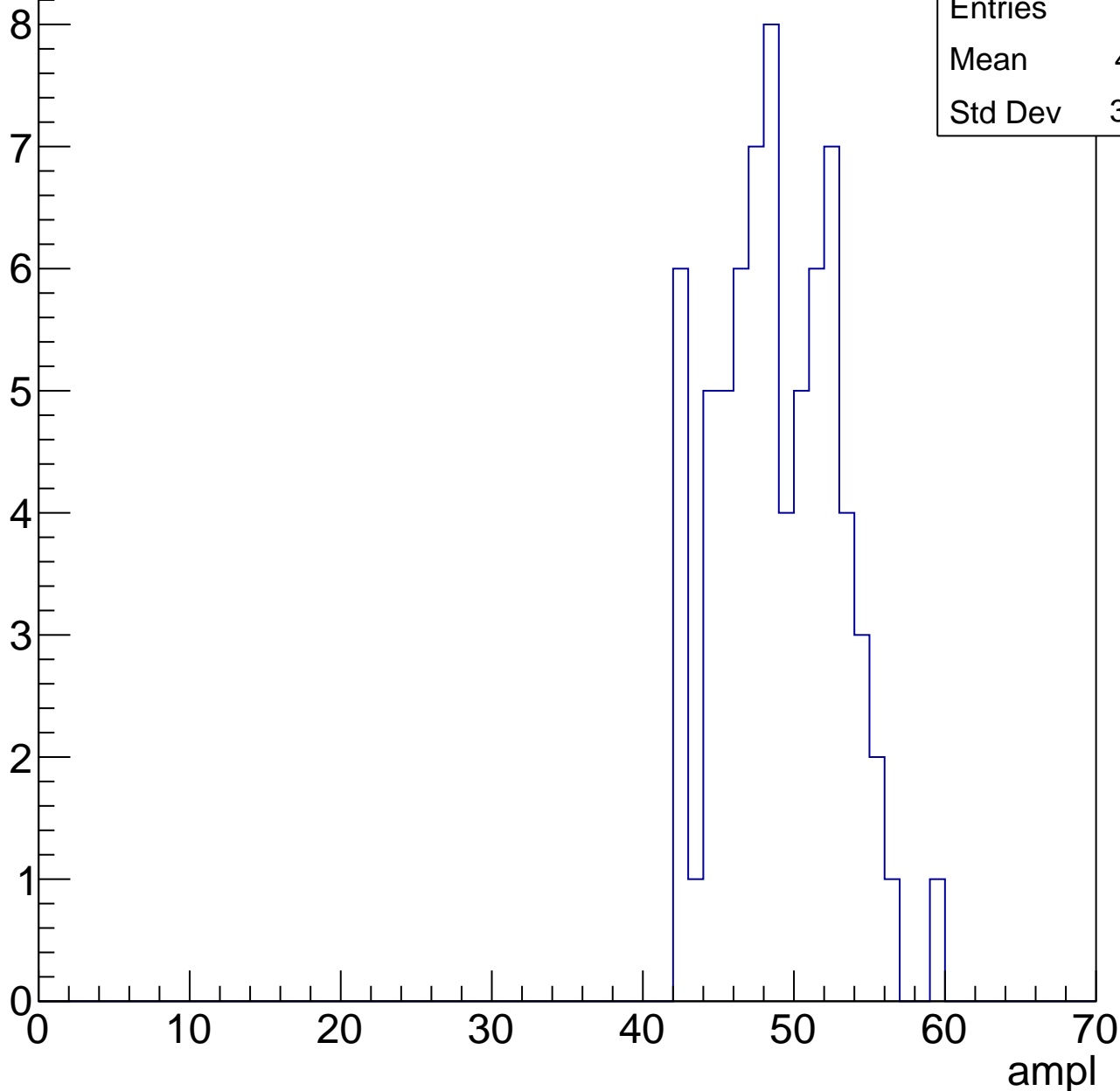


B1L102S, U26-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.51
Std Dev	3.863

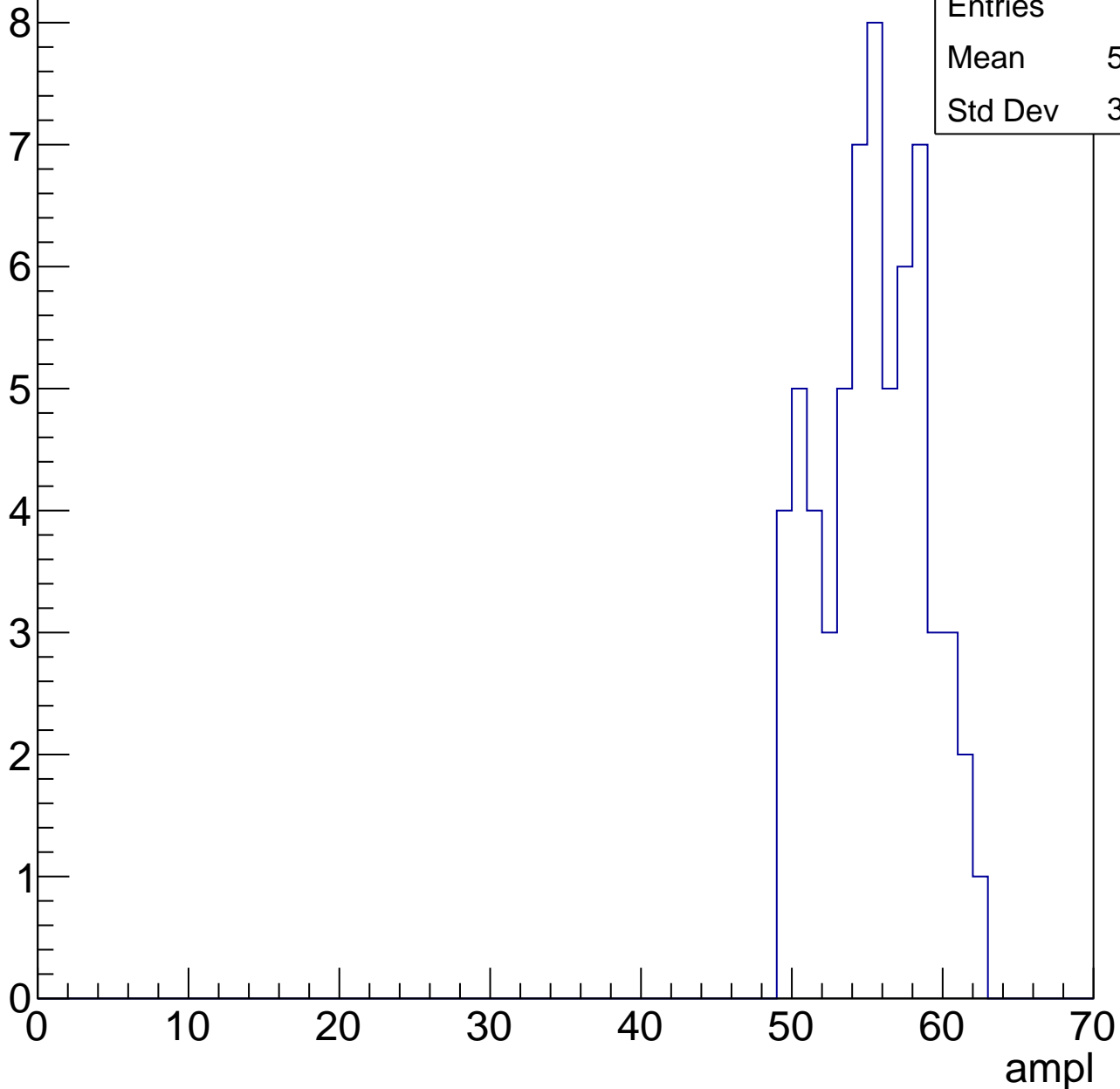


B1L102S, U26-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.89
Std Dev	3.386

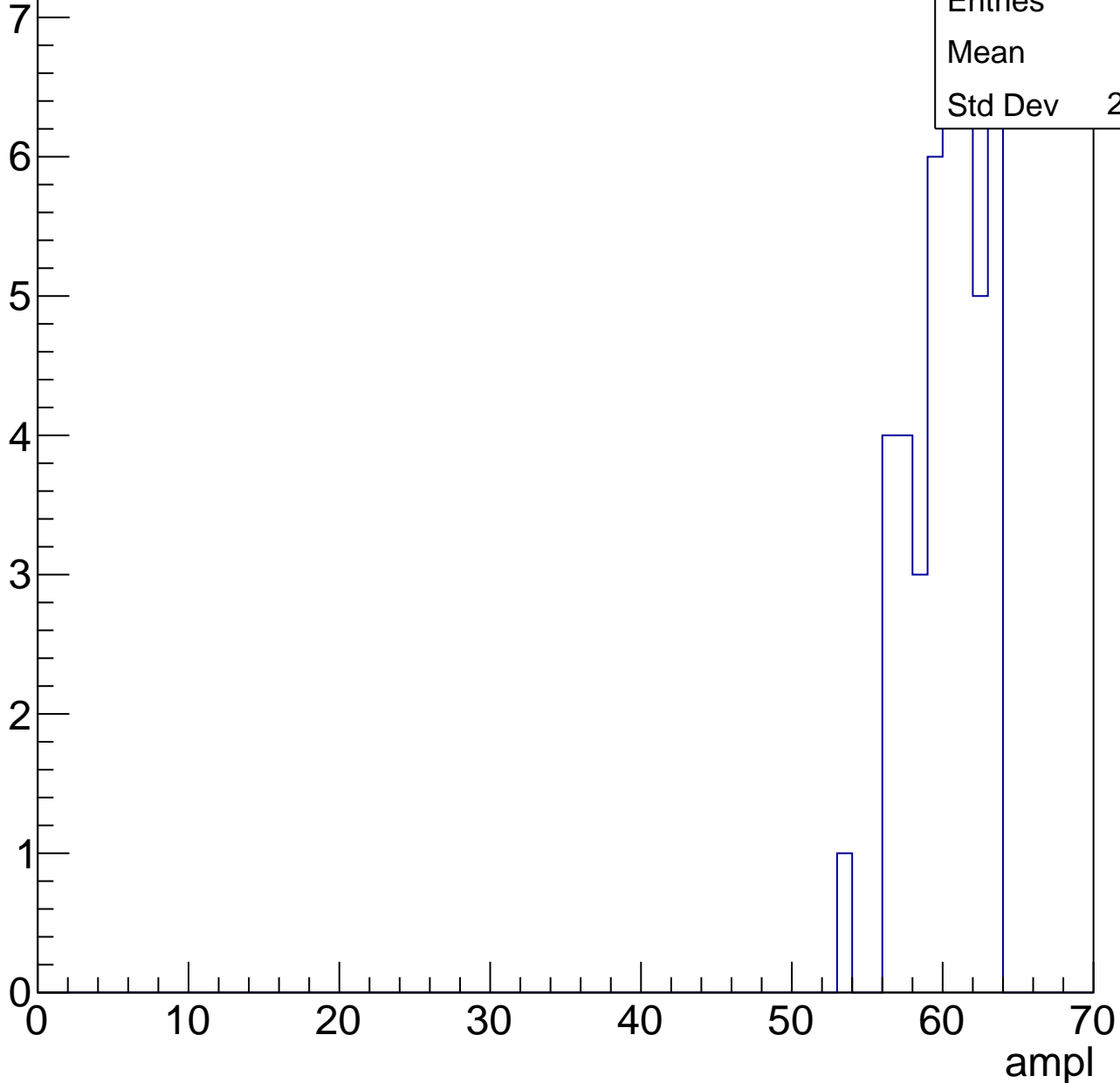


B1L102S, U26-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.8
Std Dev	2.408



B1L102S, U26-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

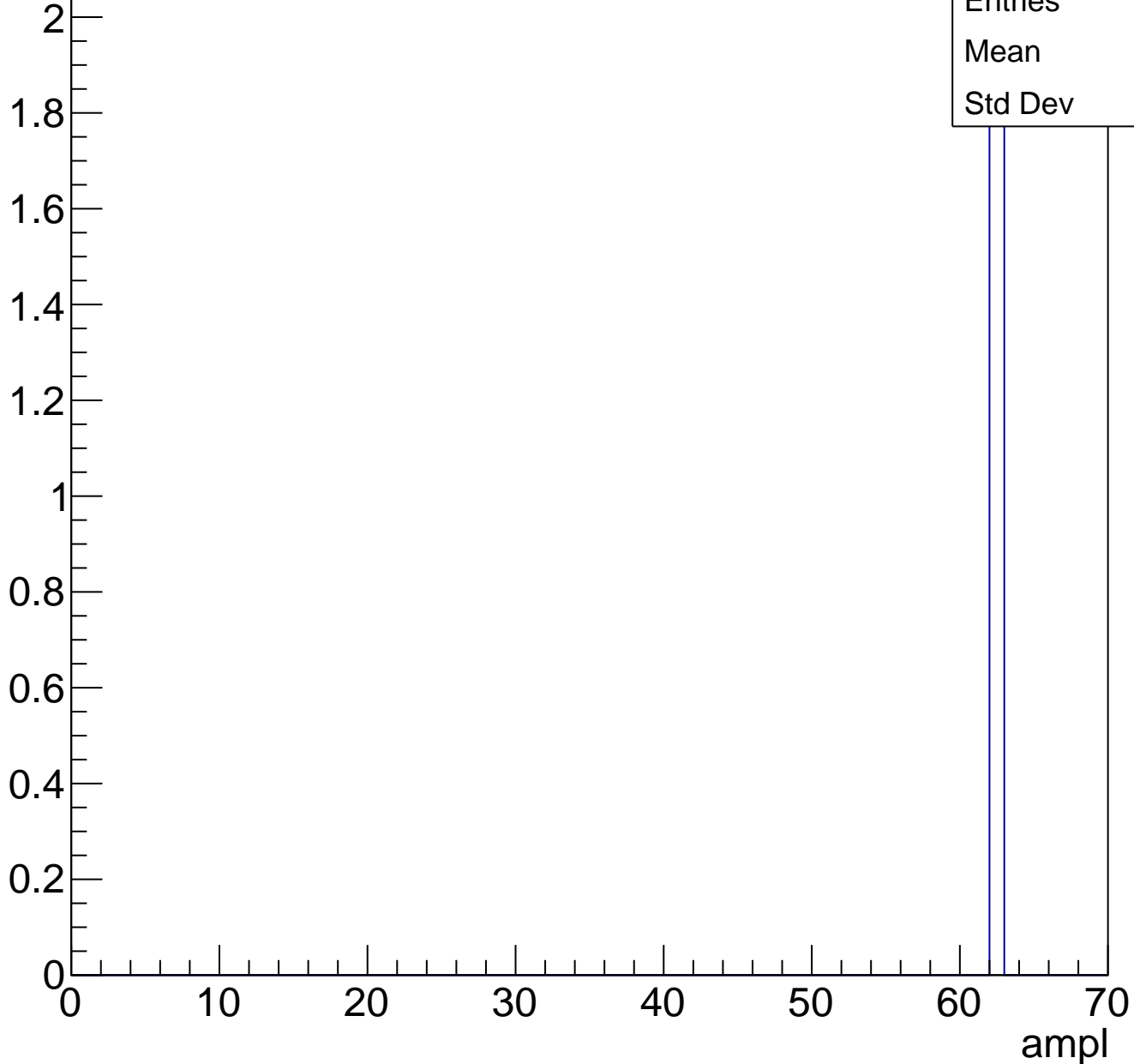


Entries	7
Mean	53.43
Std Dev	21.82

B1L102S, U26-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch101, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	28.88
Std Dev	4.966

Gaus mean : 29.9203

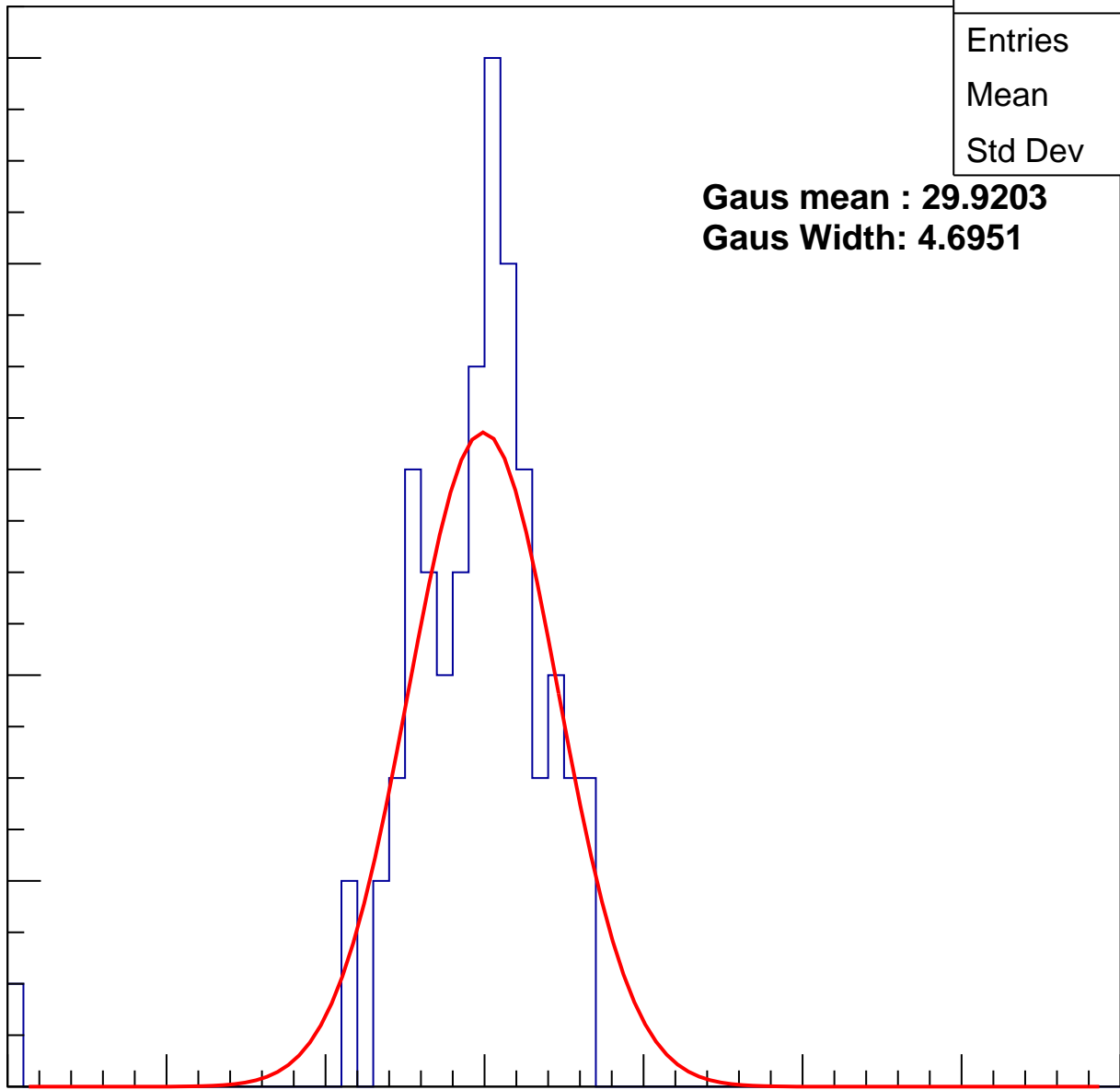
Gaus Width: 4.6951

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch101, adc1

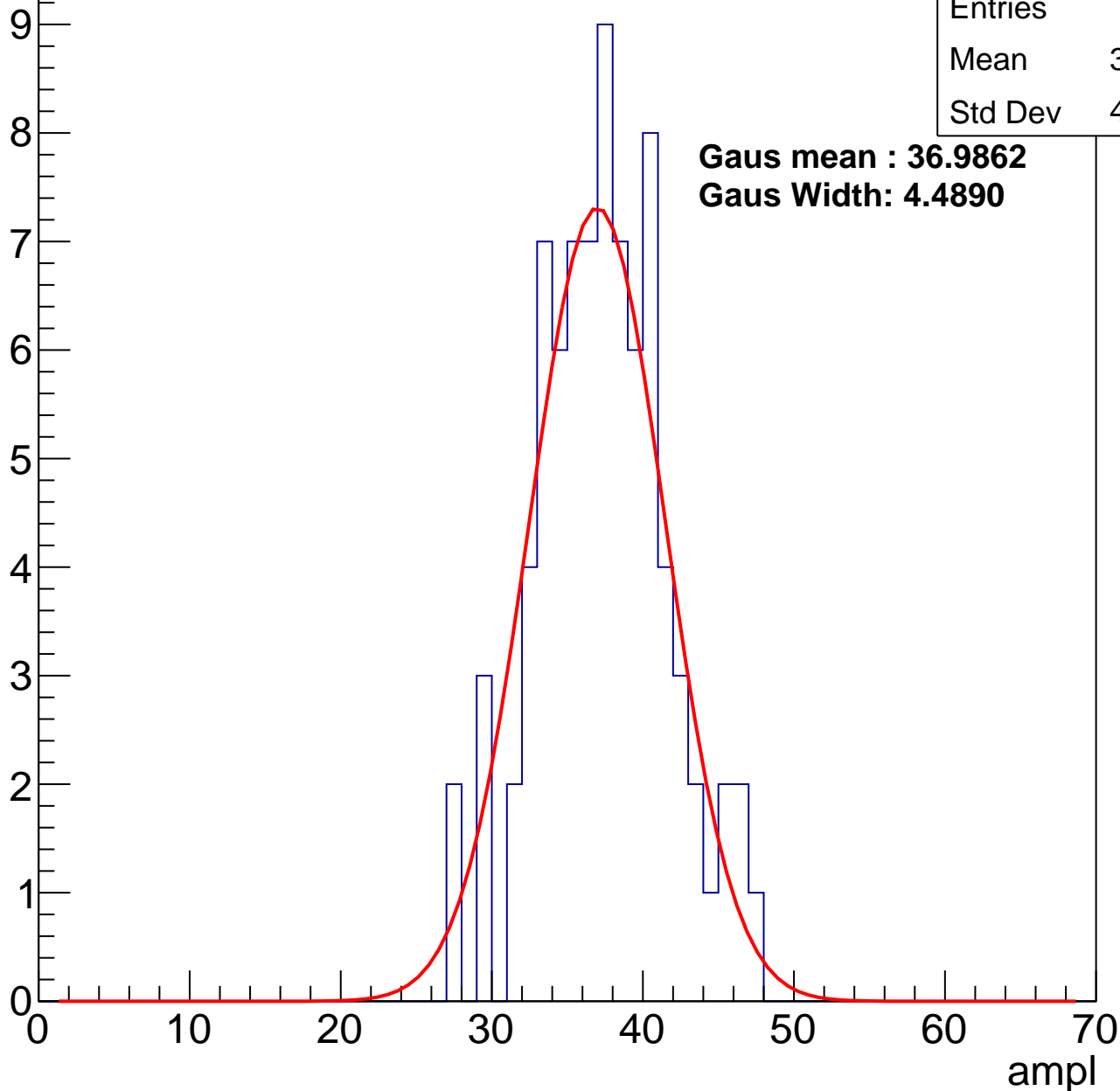
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.93
Std Dev	4.276

Gaus mean : 36.9862

Gaus Width: 4.4890



B1L102S, U26-ch101, adc2

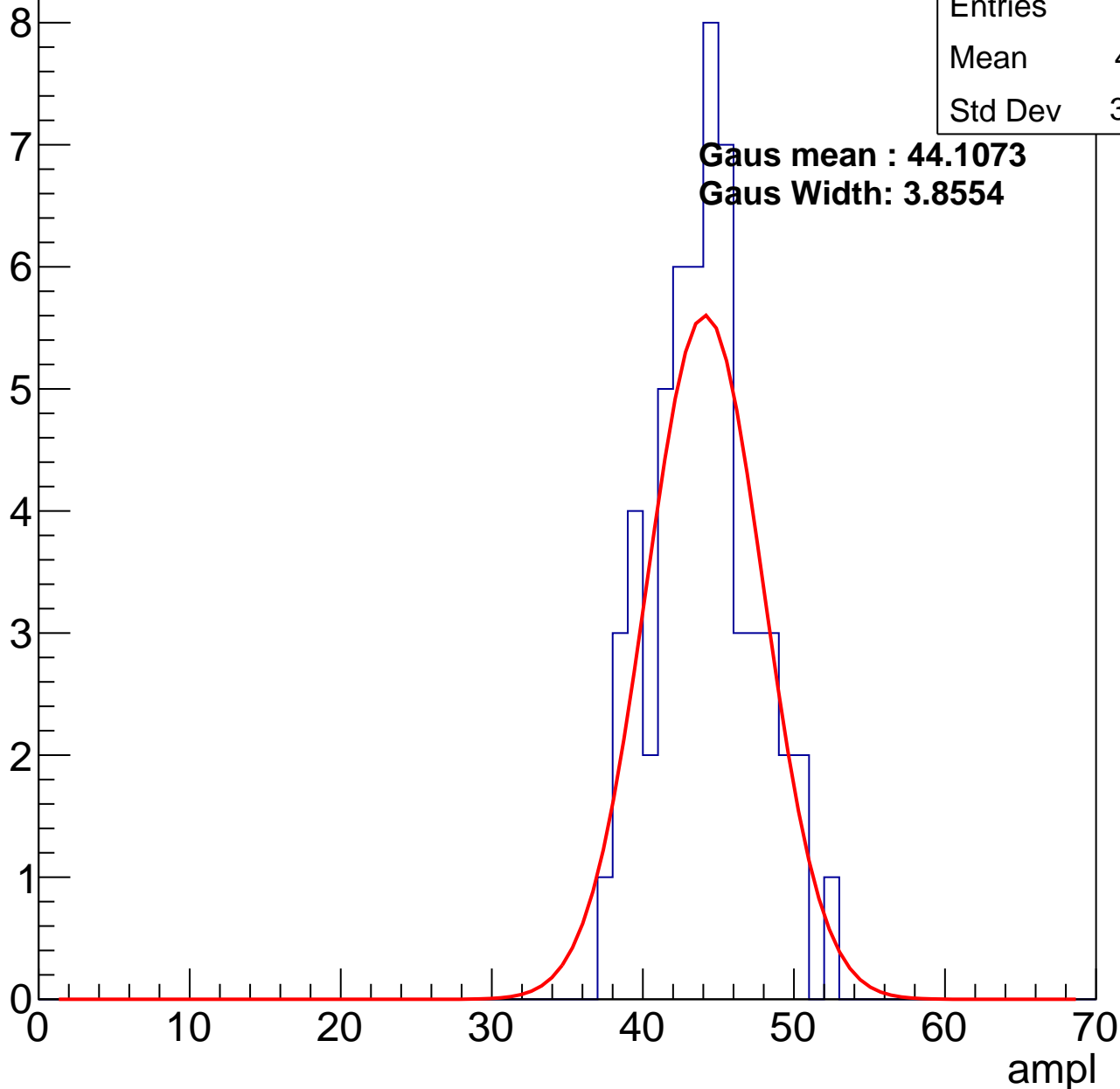
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.61
Std Dev	3.368

Gaus mean : 44.1073

Gaus Width: 3.8554

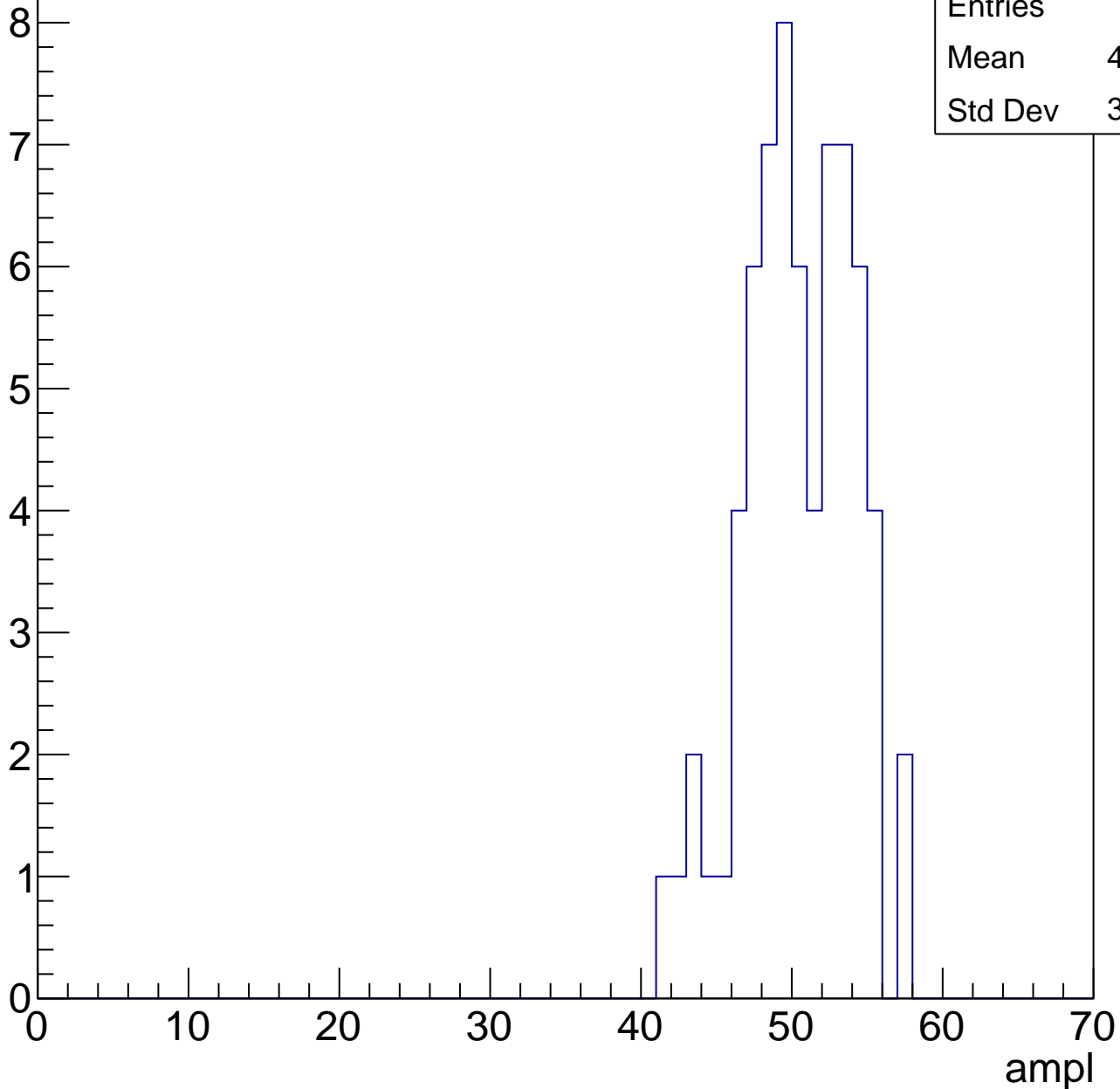


B1L102S, U26-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.99
Std Dev	3.555



B1L102S, U26-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

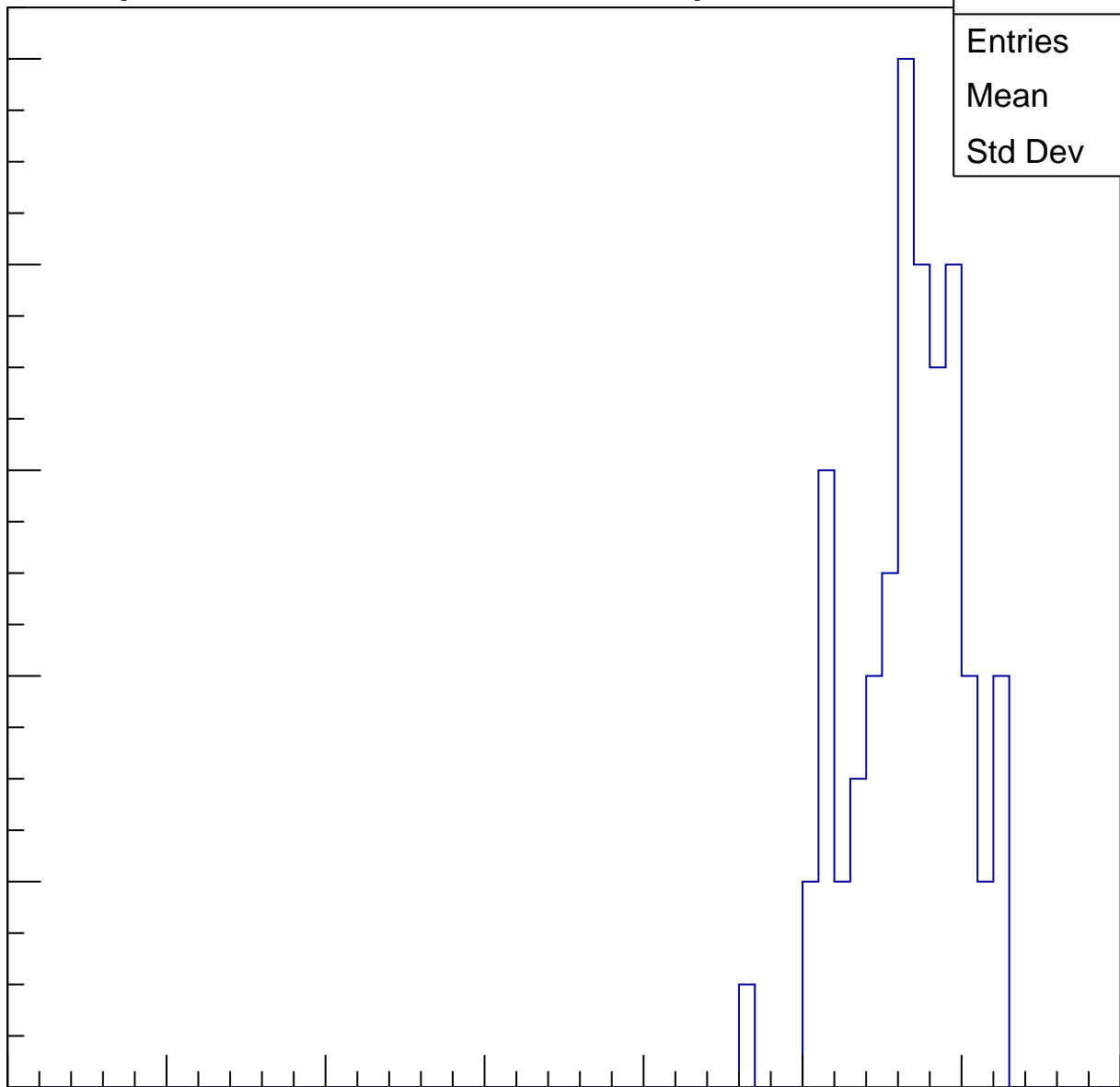
Entries	66
Mean	56.21
Std Dev	3.405

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	37
Mean	59.38
Std Dev	10.06

Entry

10

8

6

4

2

0

0

10

20

30

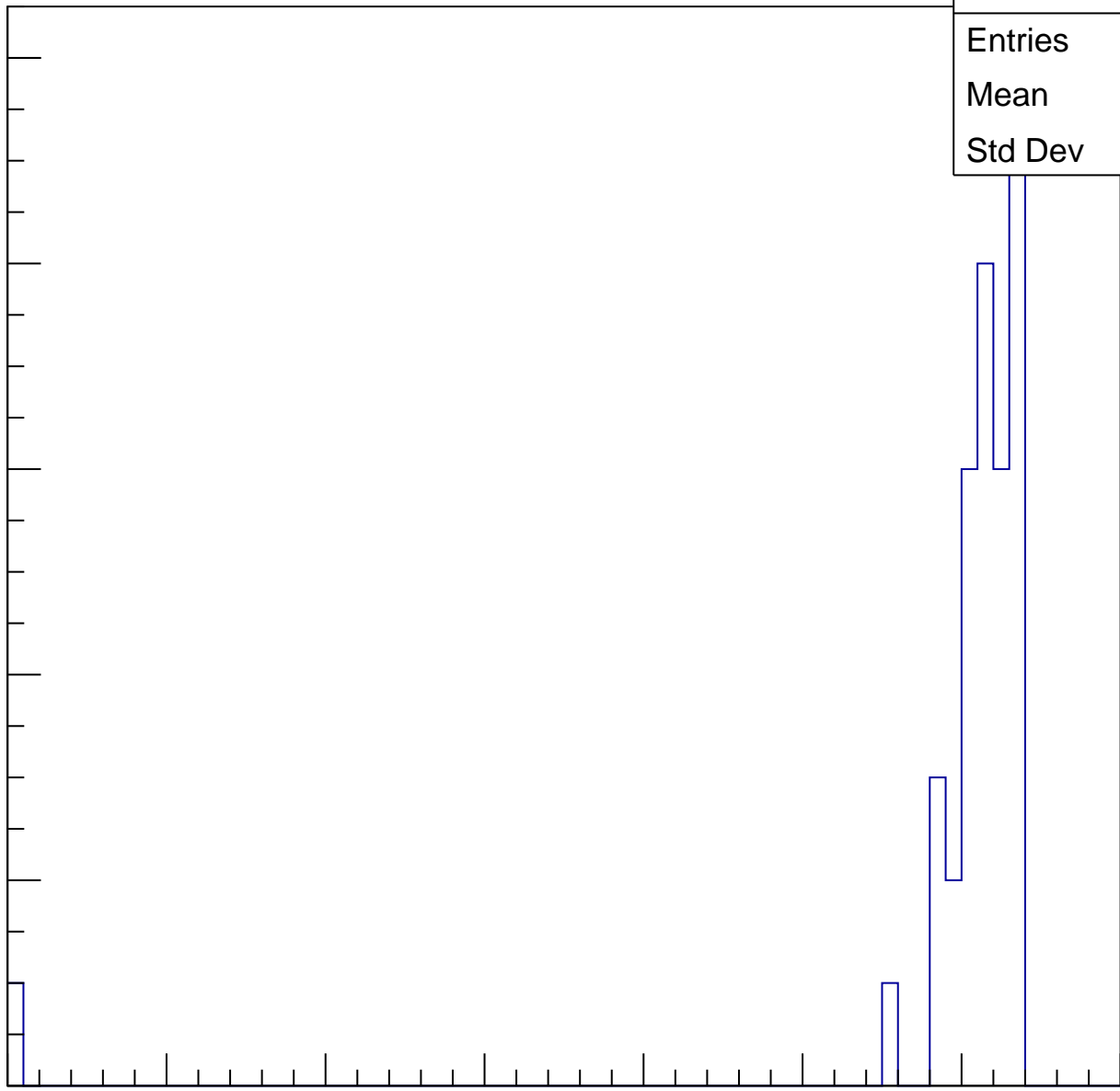
40

50

60

70

ampl



B1L102S, U26-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch102, adc0

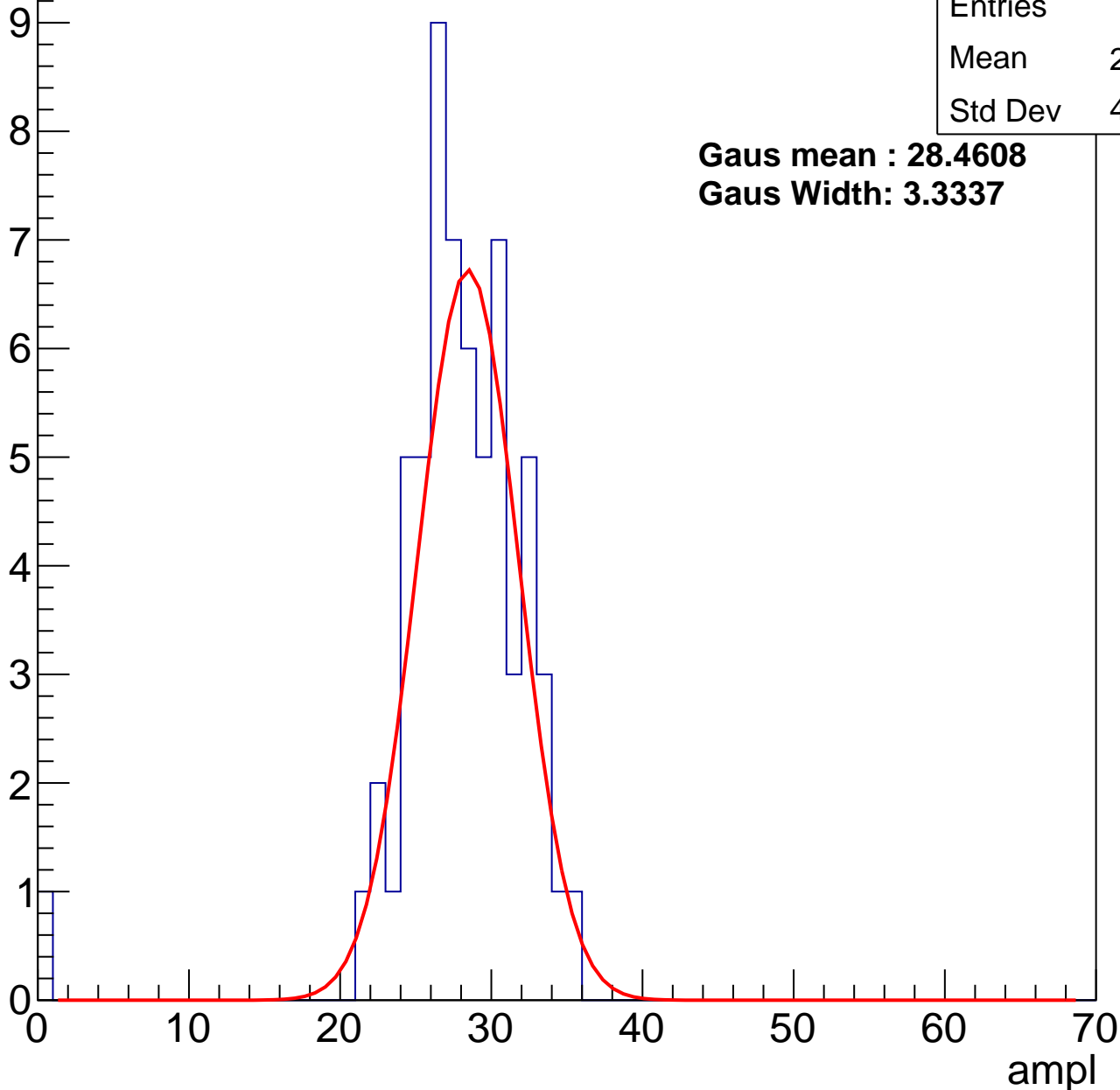
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.42
Std Dev	4.713

Gaus mean : 28.4608

Gaus Width: 3.3337



B1L102S, U26-ch102, adc1

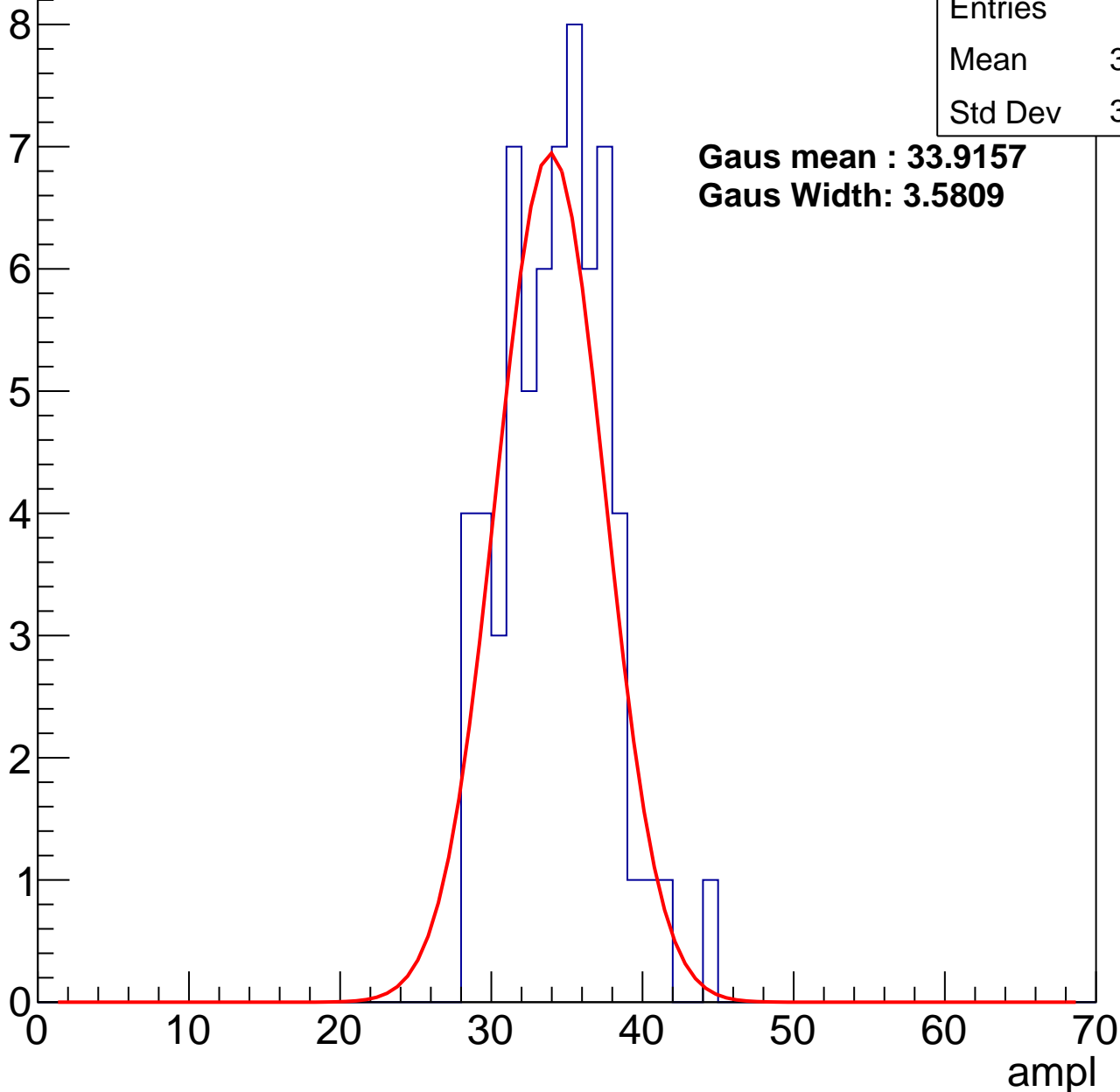
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	33.88
Std Dev	3.395

Gaus mean : 33.9157

Gaus Width: 3.5809



B1L102S, U26-ch102, adc2

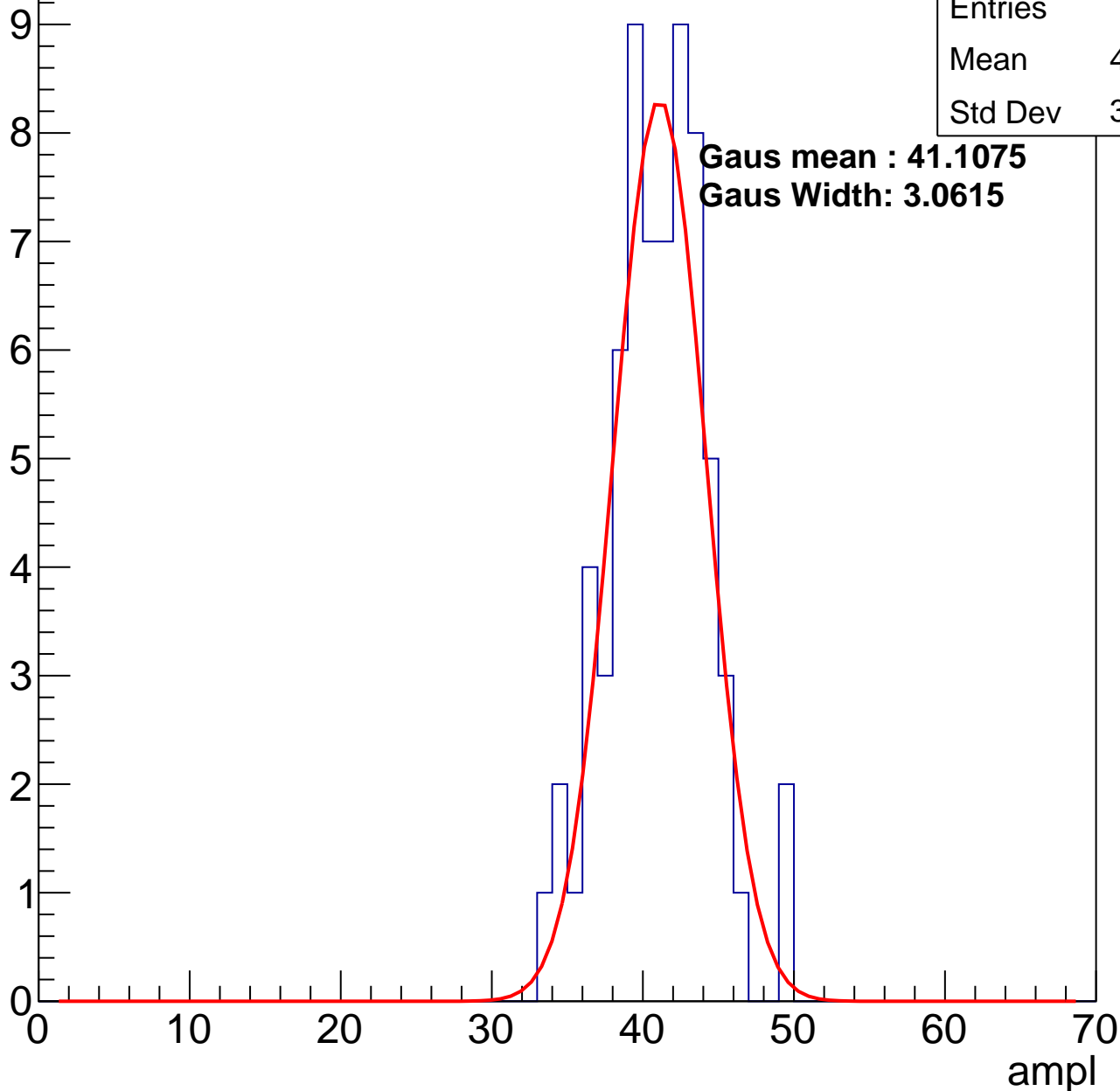
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.56
Std Dev	3.247

Gaus mean : 41.1075

Gaus Width: 3.0615



B1L102S, U26-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

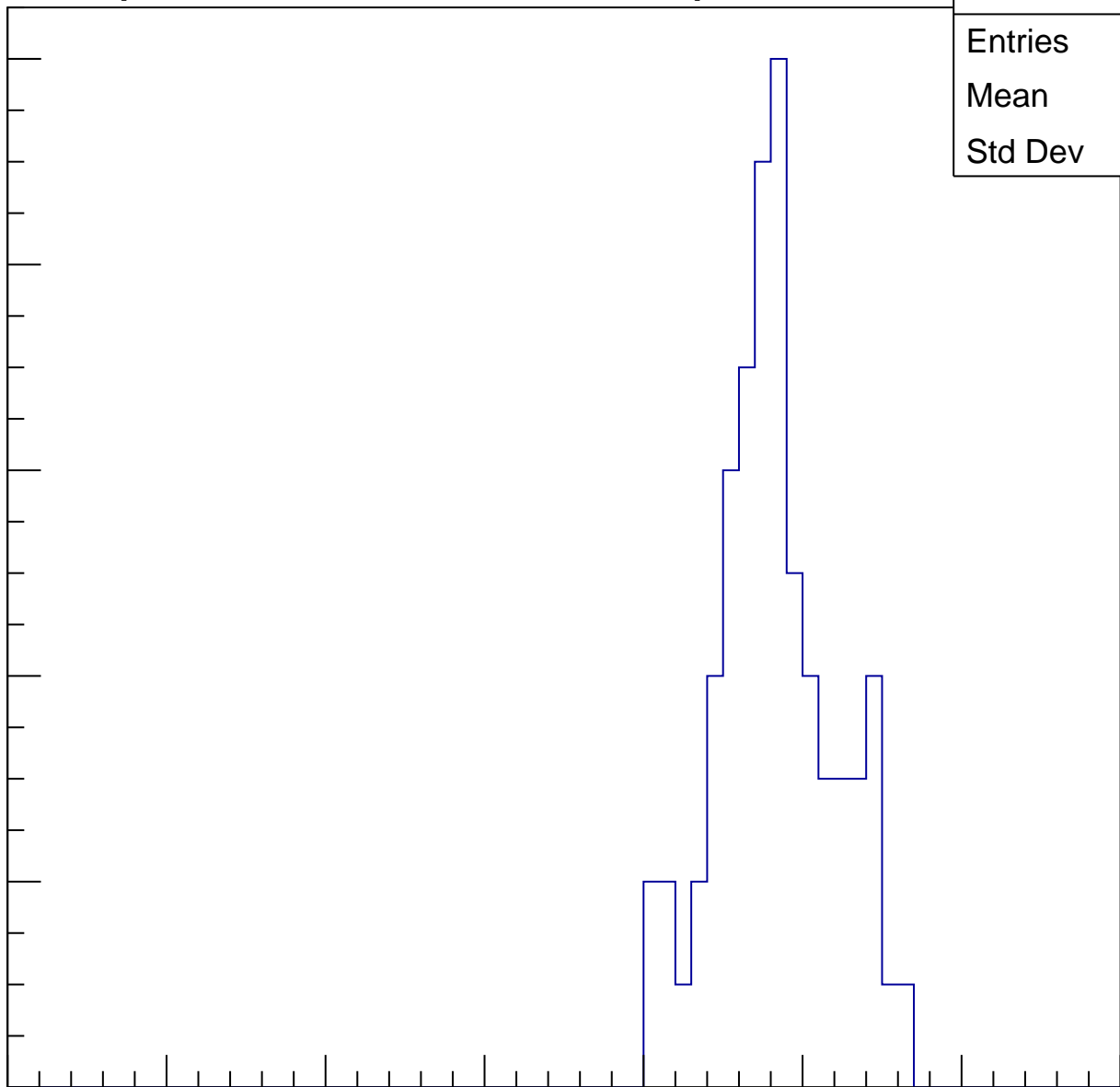
Entries	67
Mean	47.78
Std Dev	3.648

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

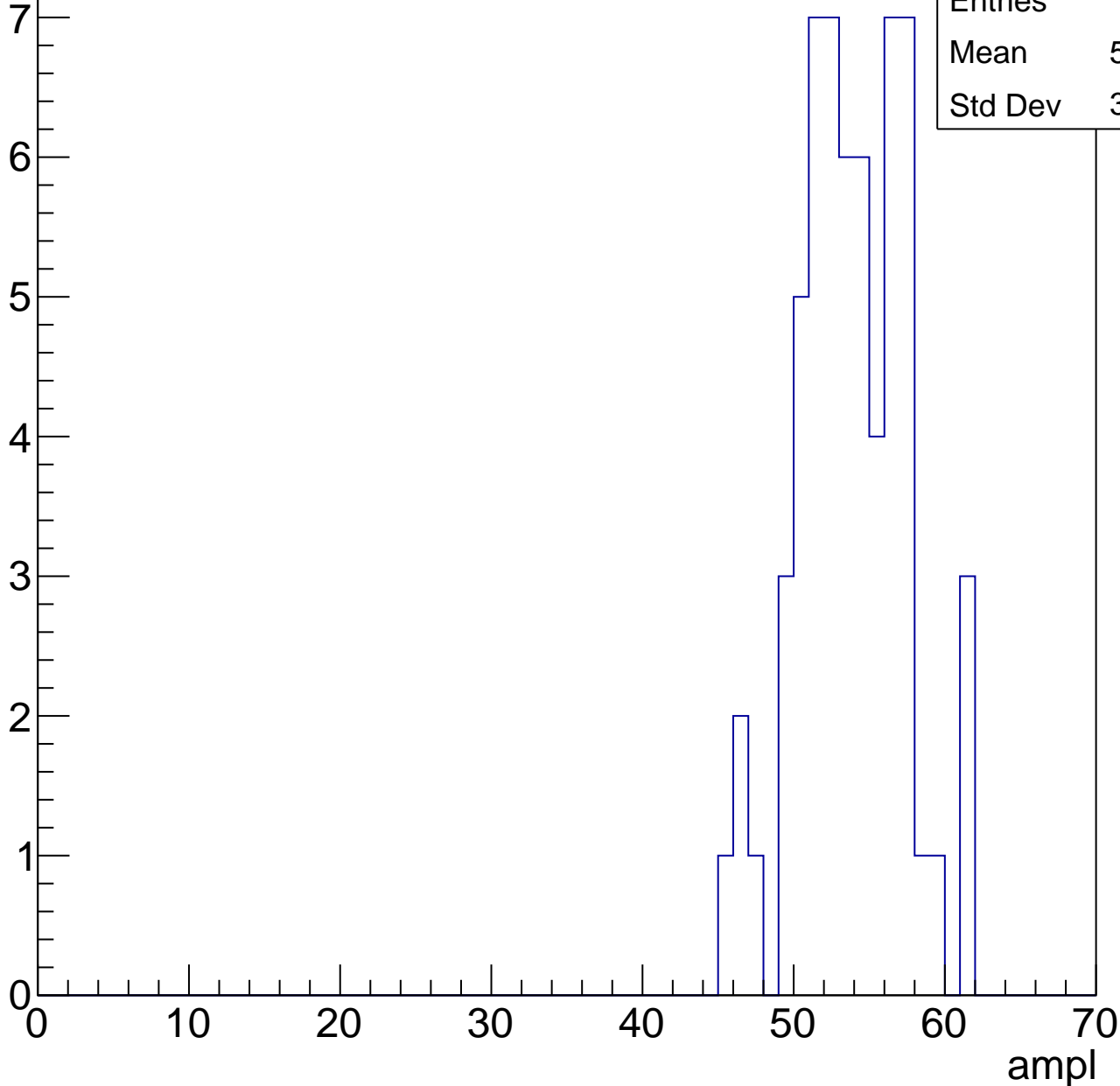


B1L102S, U26-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.36
Std Dev	3.548

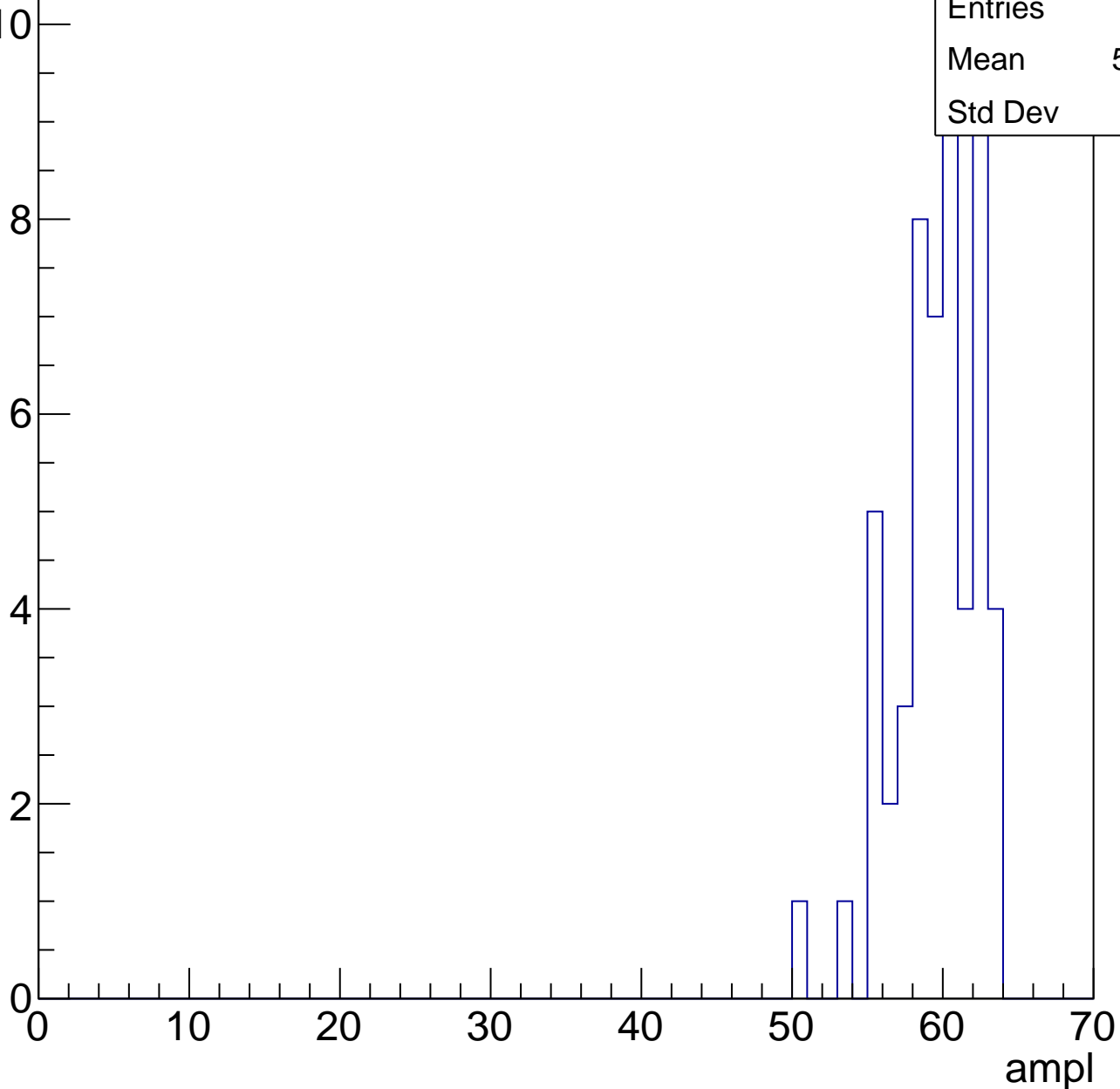


B1L102S, U26-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	59.11
Std Dev	2.74

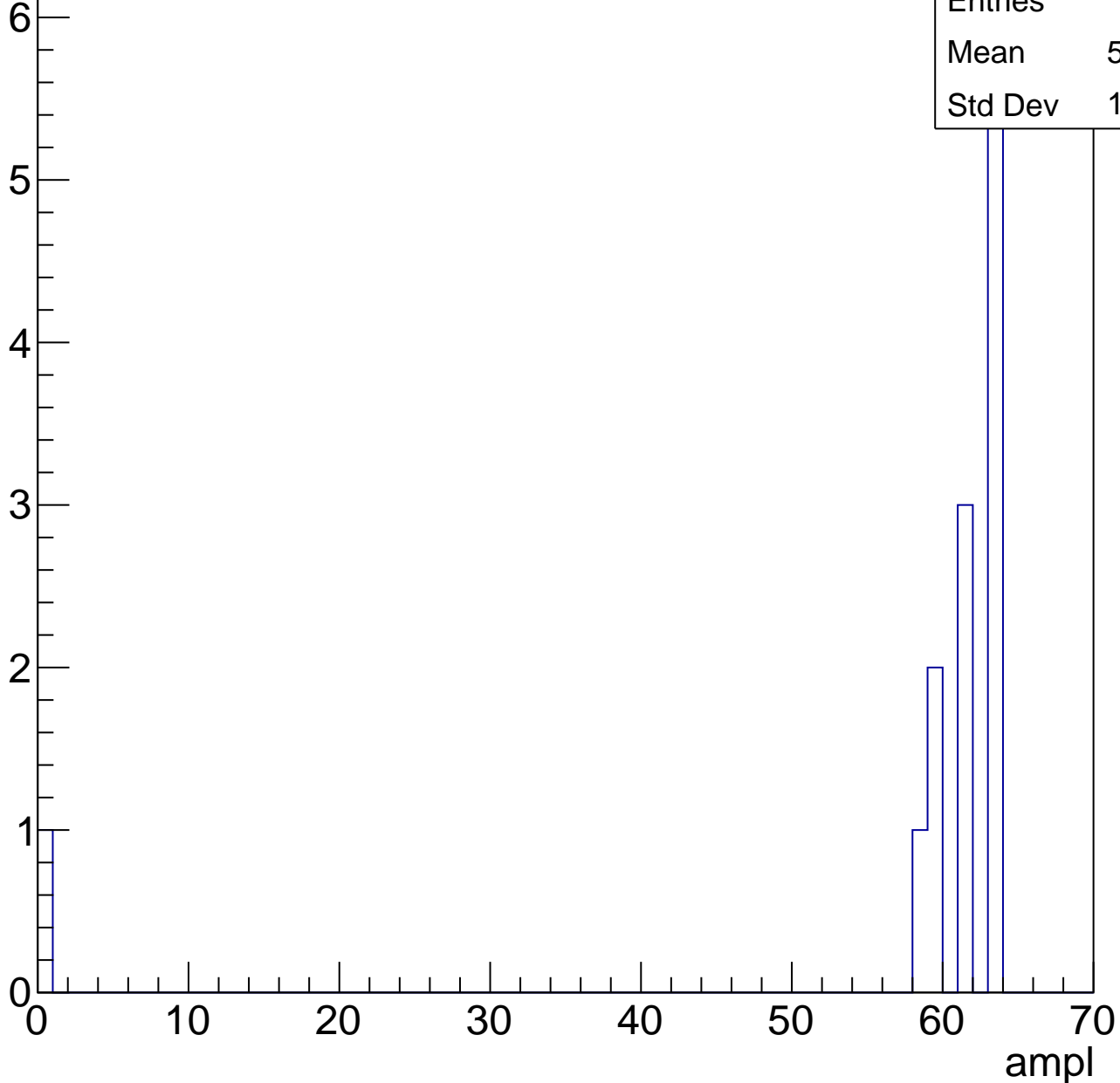


B1L102S, U26-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.69
Std Dev	16.46



B1L102S, U26-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch103, adc0

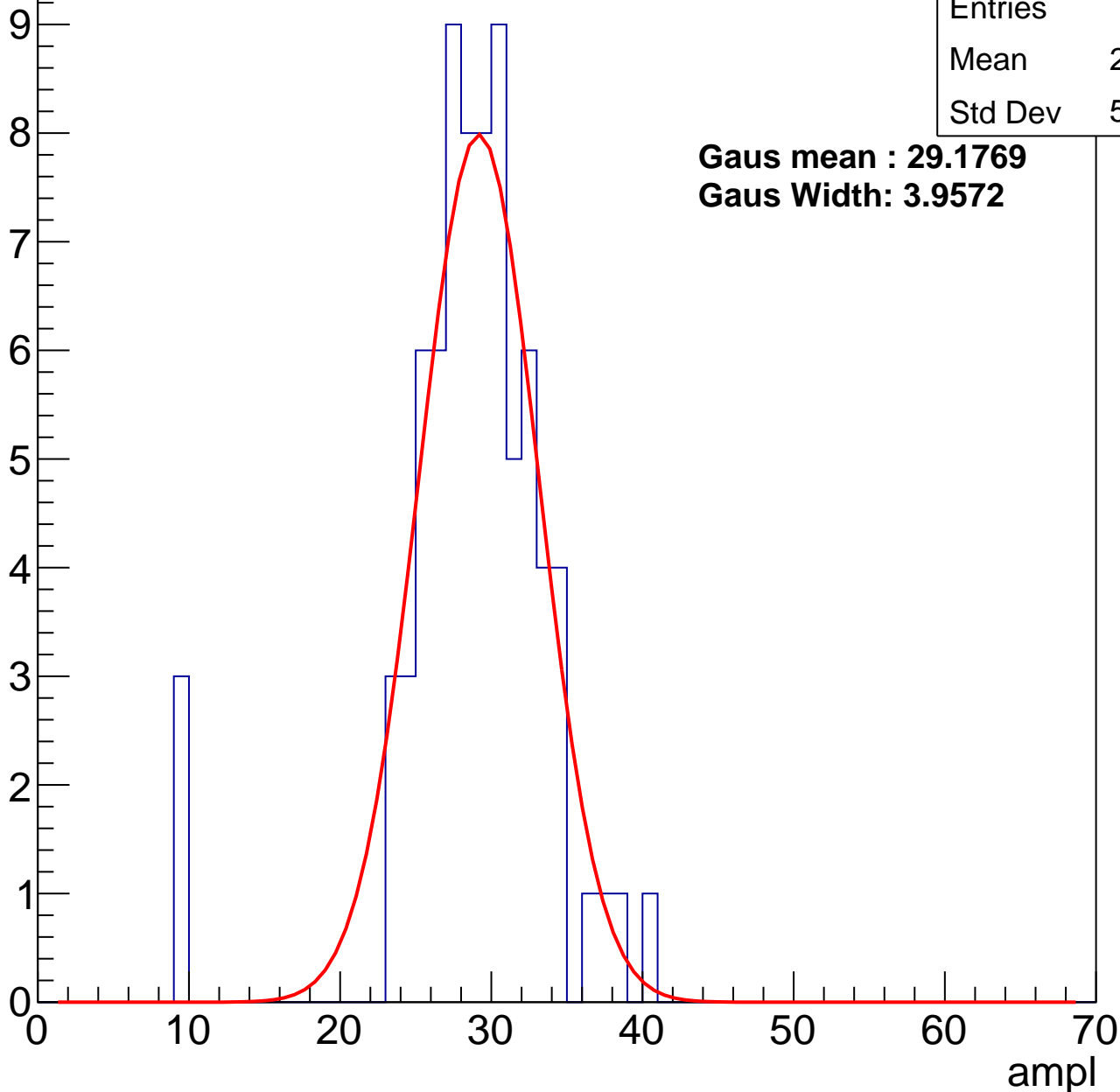
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.32
Std Dev	5.192

Gaus mean : 29.1769

Gaus Width: 3.9572



B1L102S, U26-ch103, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	36.16
Std Dev	3.317

Gaus mean : 36.6356

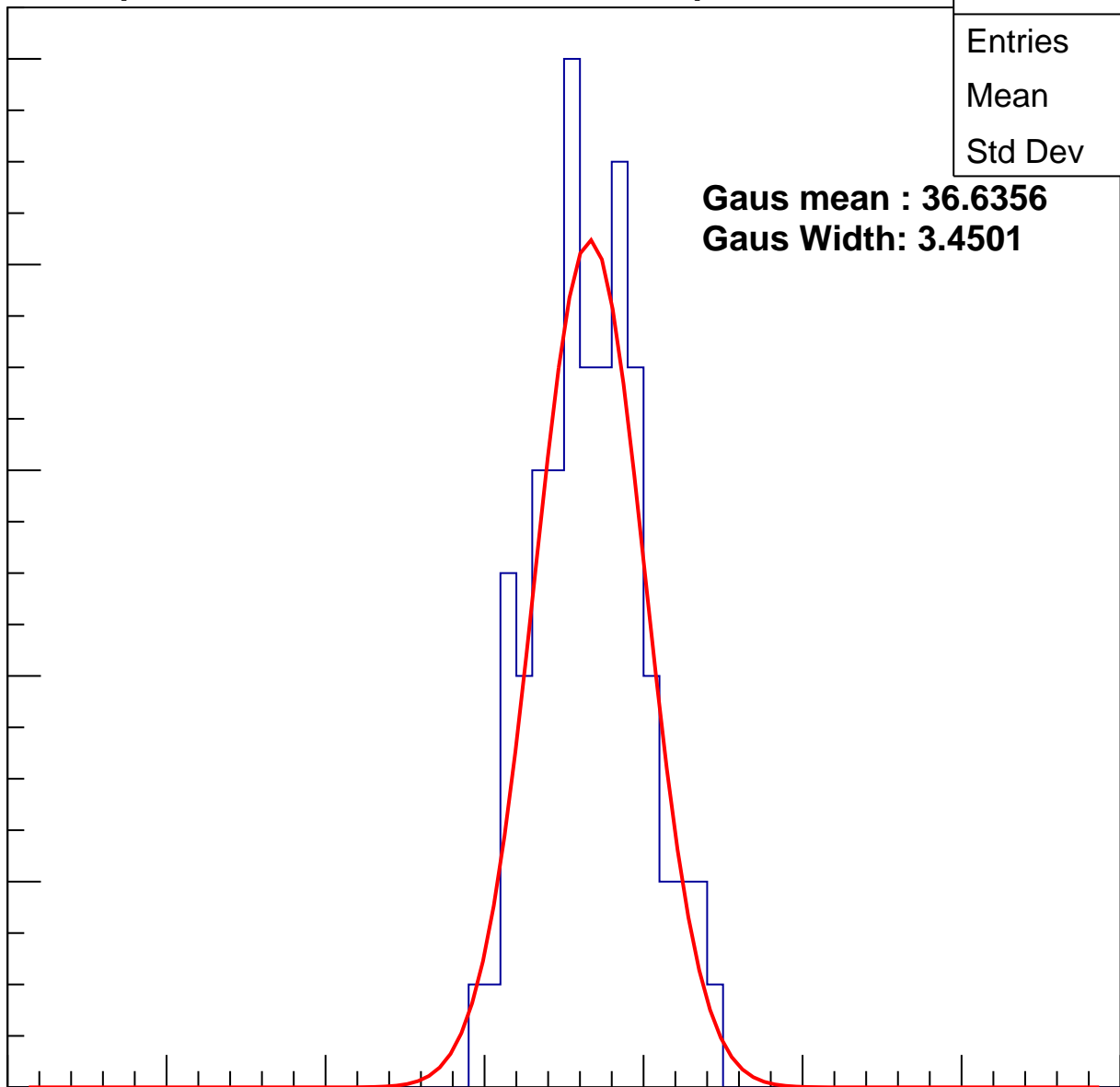
Gaus Width: 3.4501

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch103, adc2

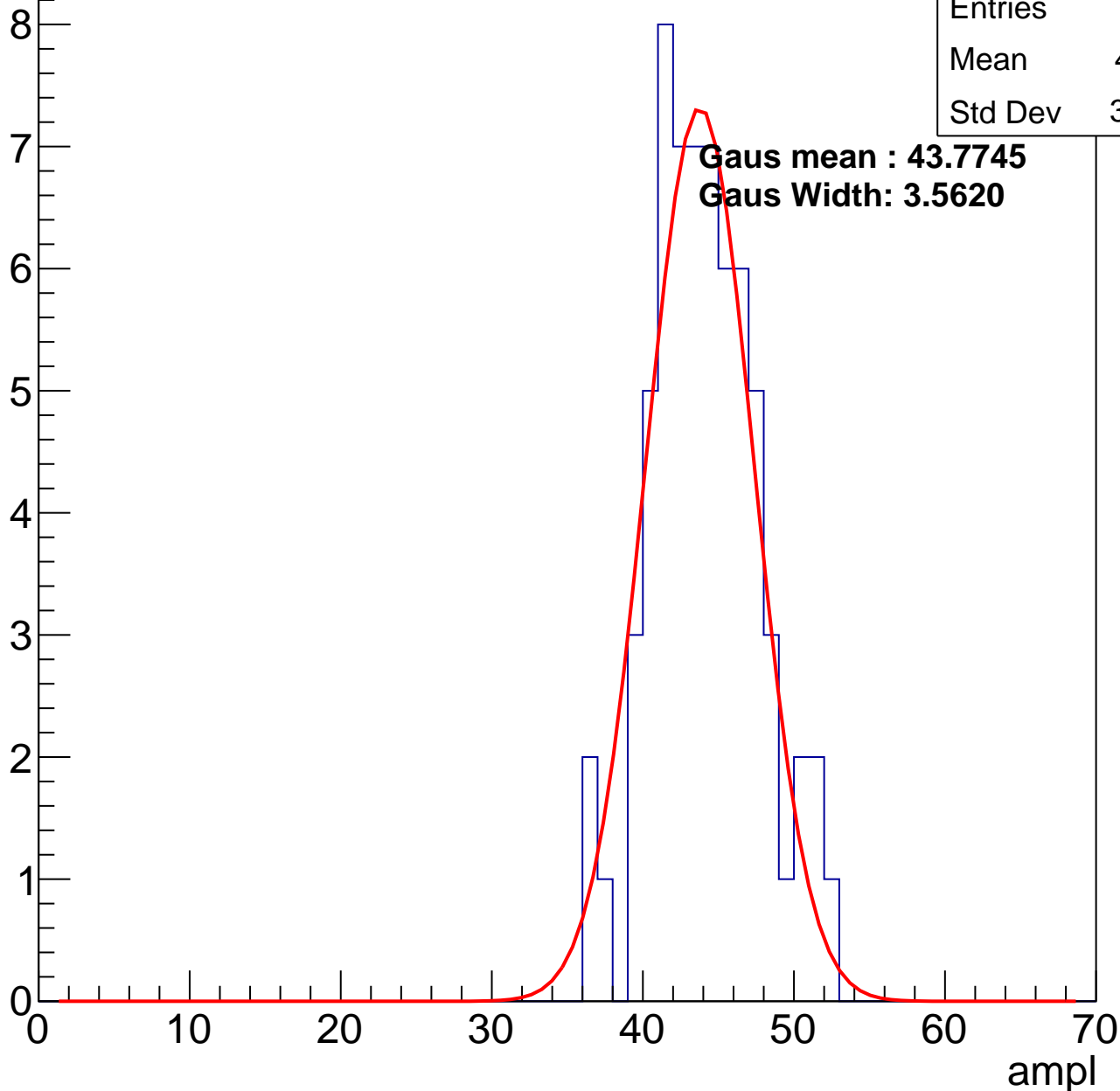
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.71
Std Dev	3.515

Gaus mean : 43.7745

Gaus Width: 3.5620

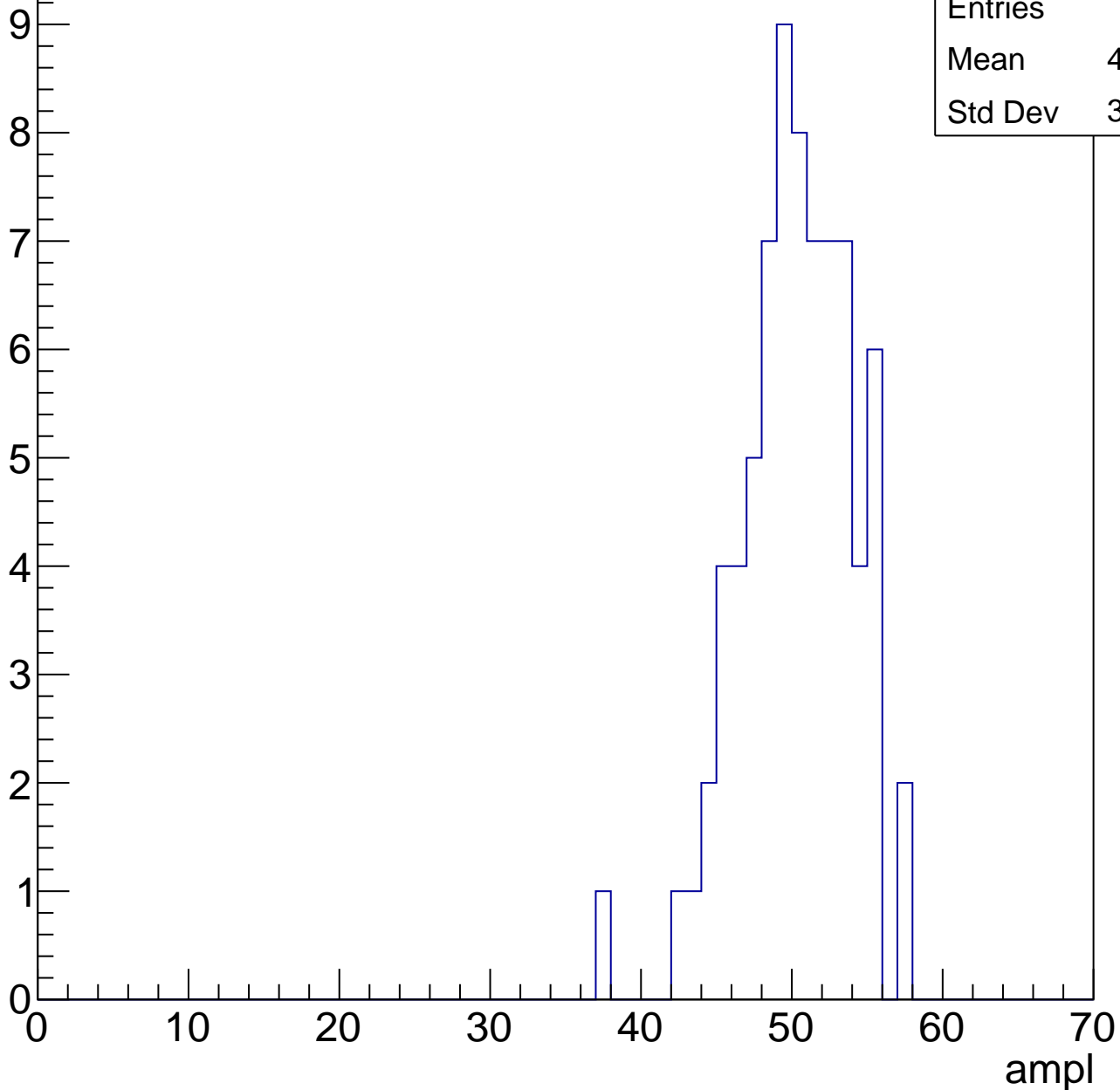


B1L102S, U26-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	49.84
Std Dev	3.666

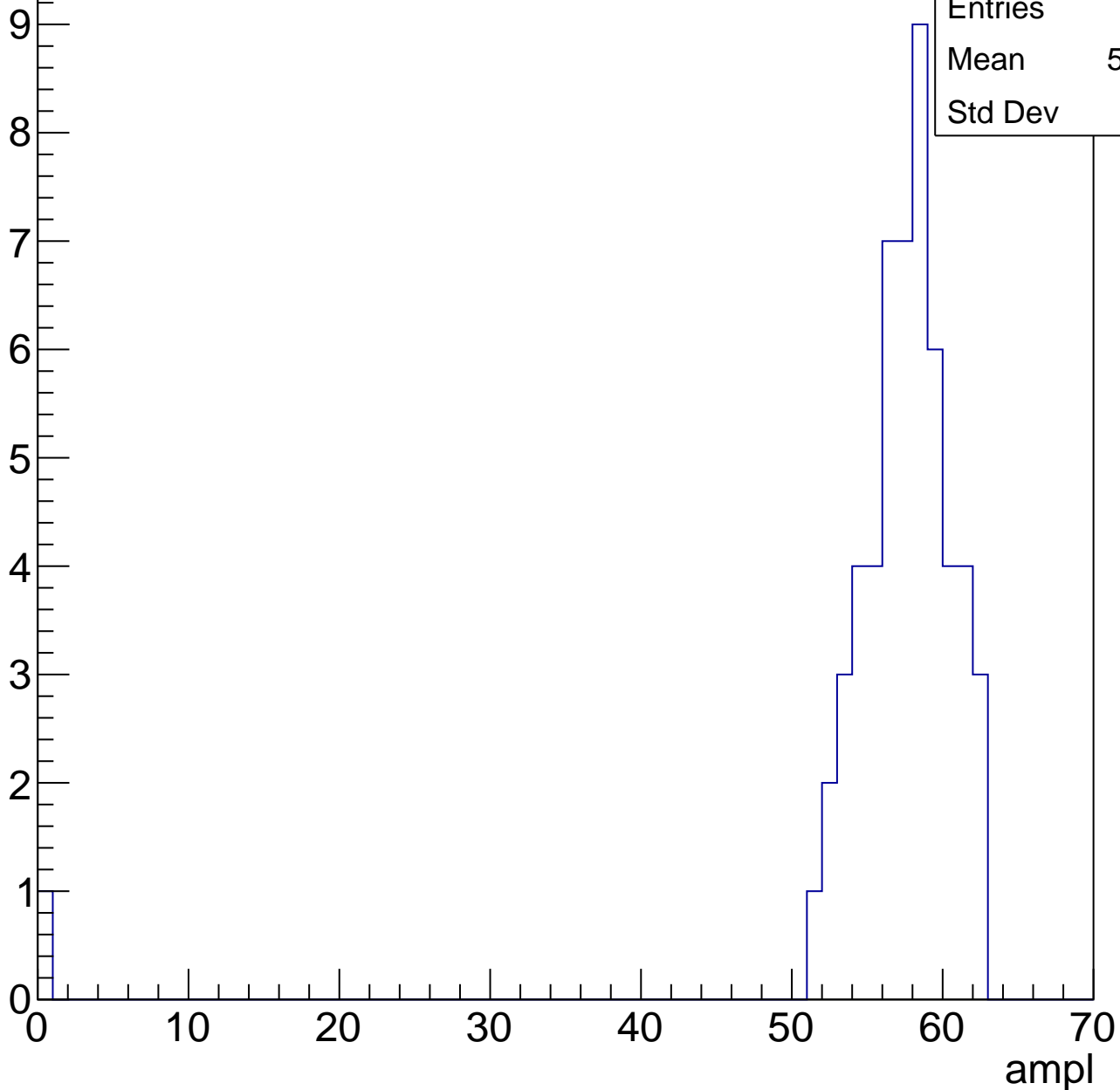


B1L102S, U26-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.13
Std Dev	8.1

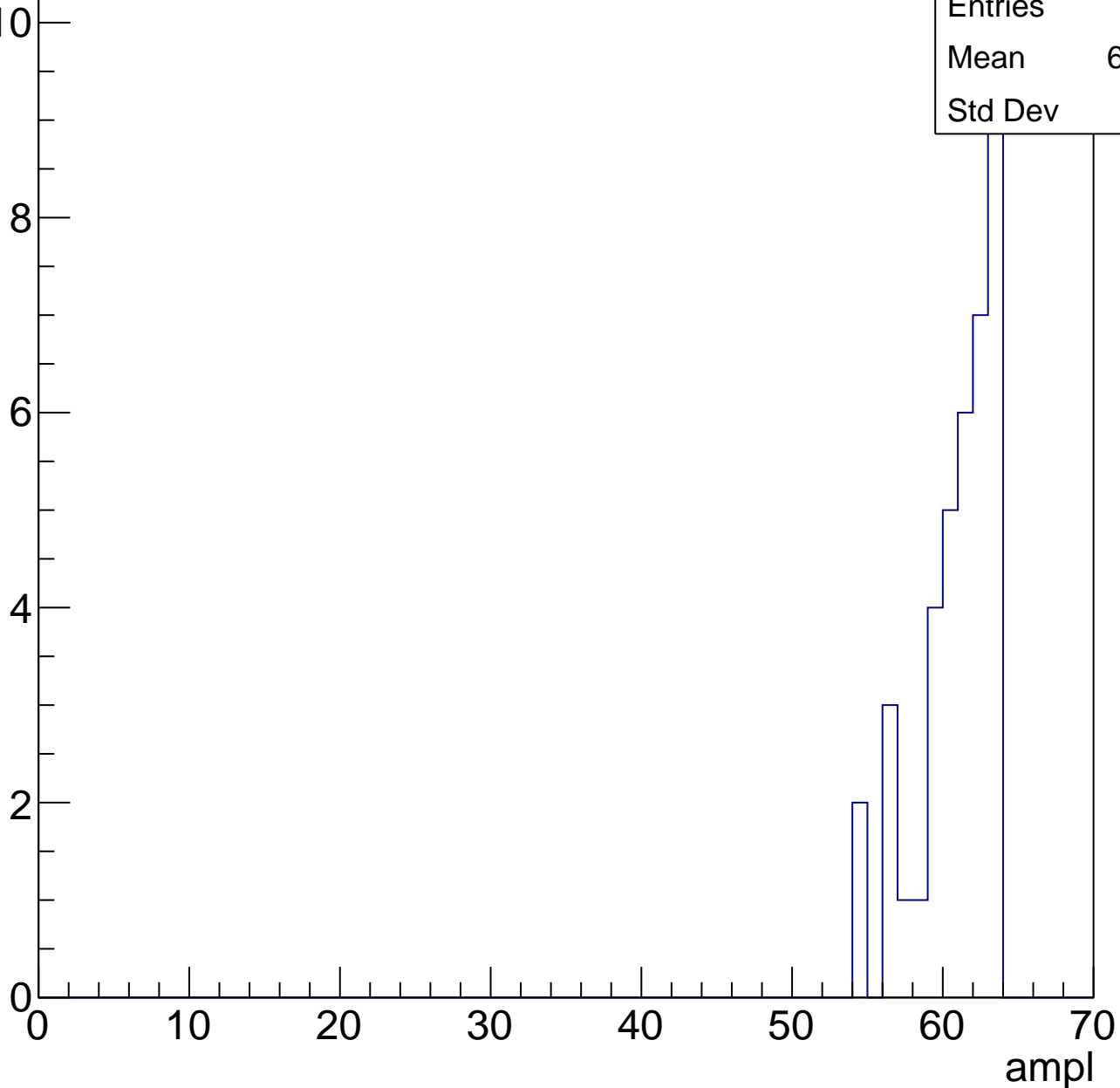


B1L102S, U26-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	60.44
Std Dev	2.55



B1L102S, U26-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L102S, U26-ch104, adc0

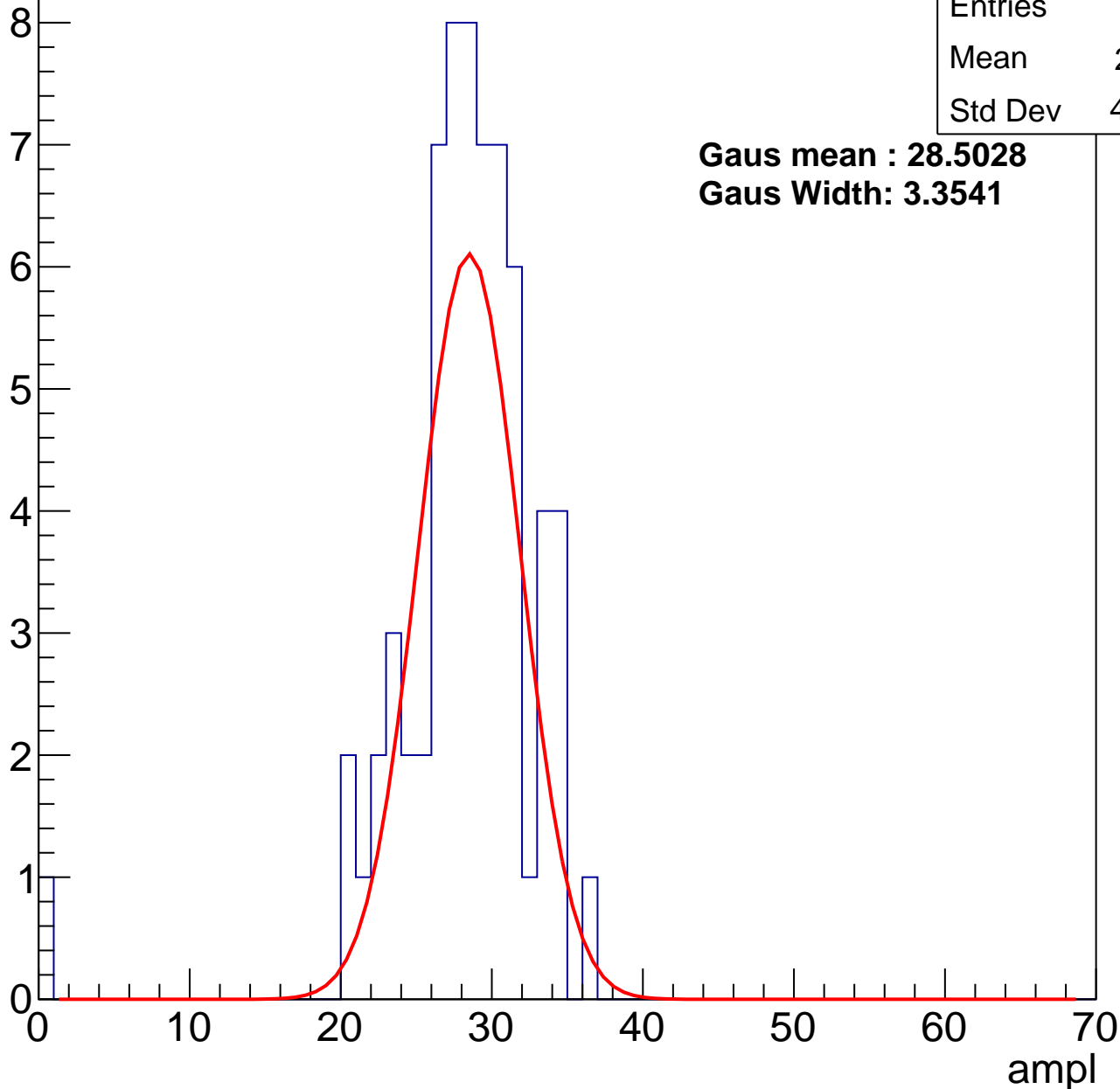
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.71
Std Dev	4.926

Gaus mean : 28.5028

Gaus Width: 3.3541



B1L102S, U26-ch104, adc1

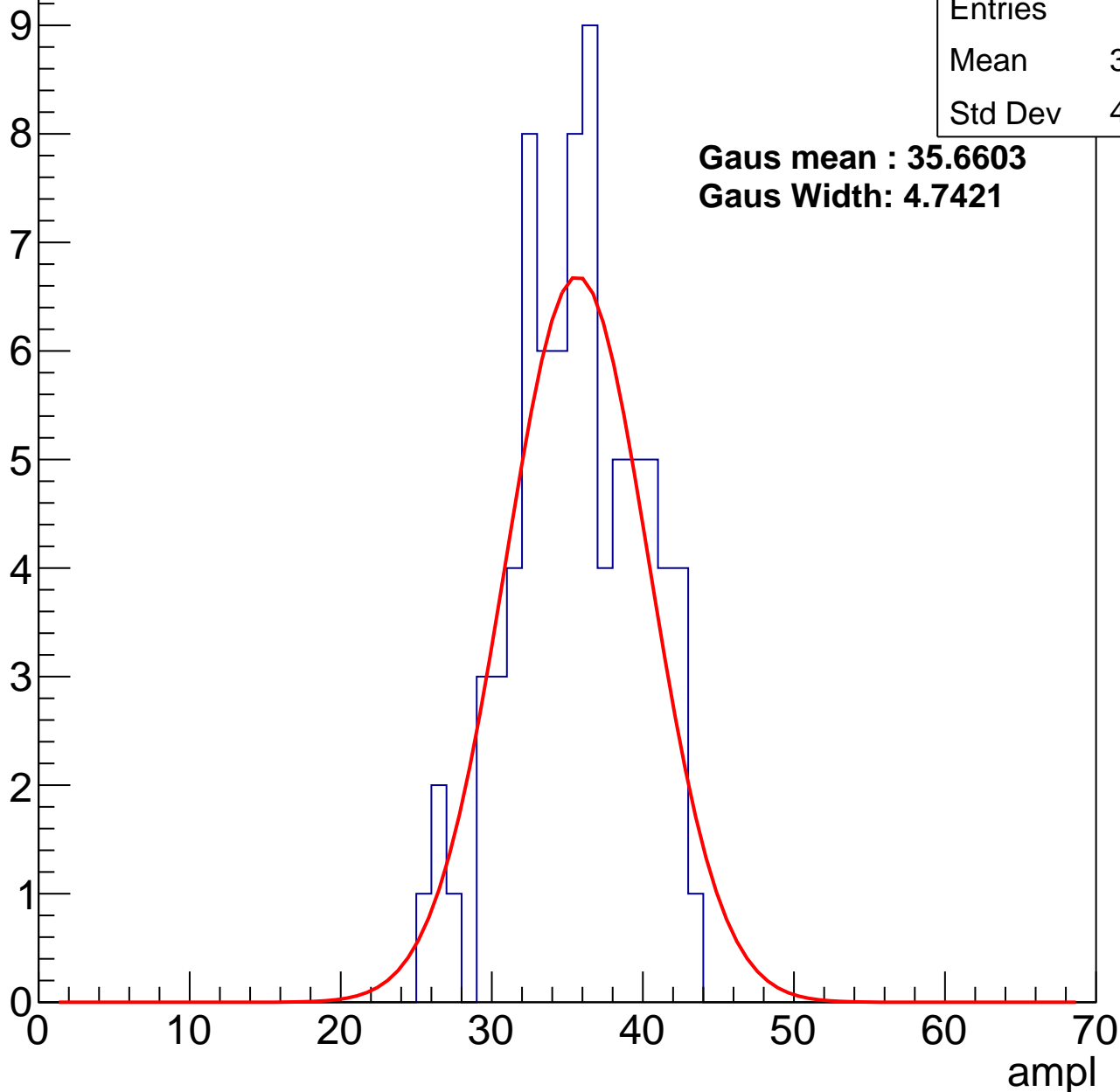
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.13
Std Dev	4.138

Gaus mean : 35.6603

Gaus Width: 4.7421



B1L102S, U26-ch104, adc2

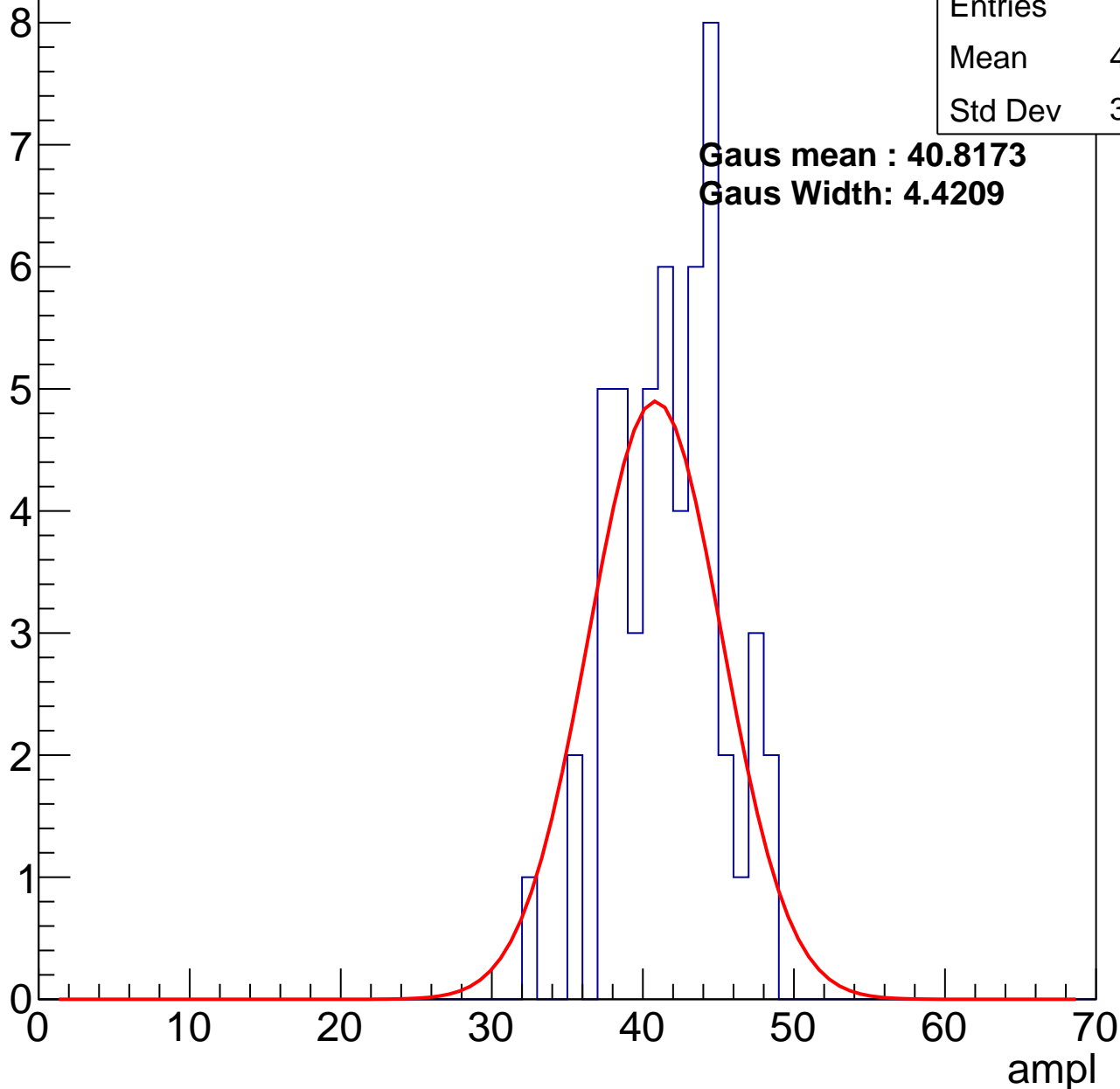
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.34
Std Dev	3.507

Gaus mean : 40.8173

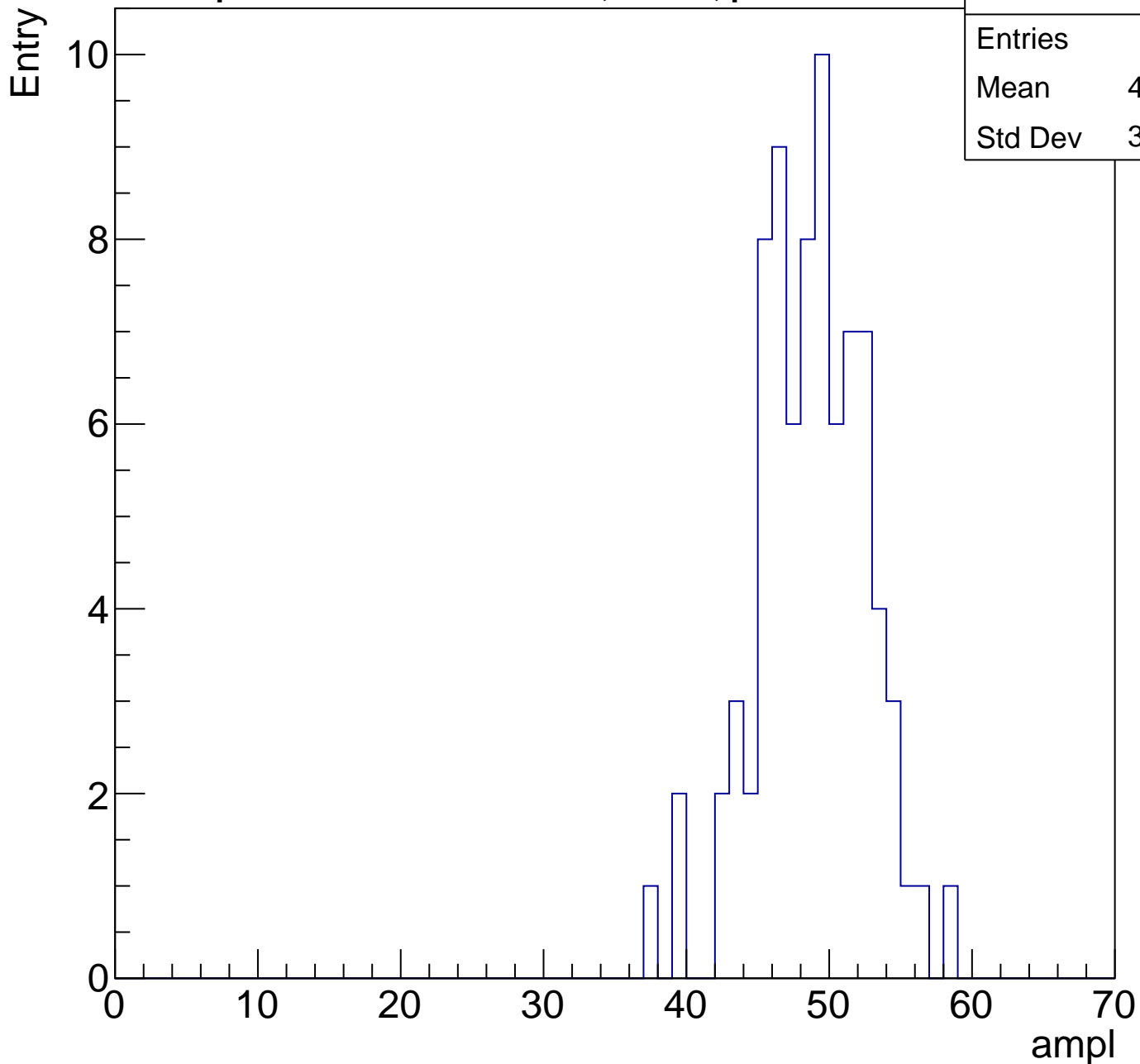
Gaus Width: 4.4209



B1L102S, U26-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	48.27
Std Dev	3.849

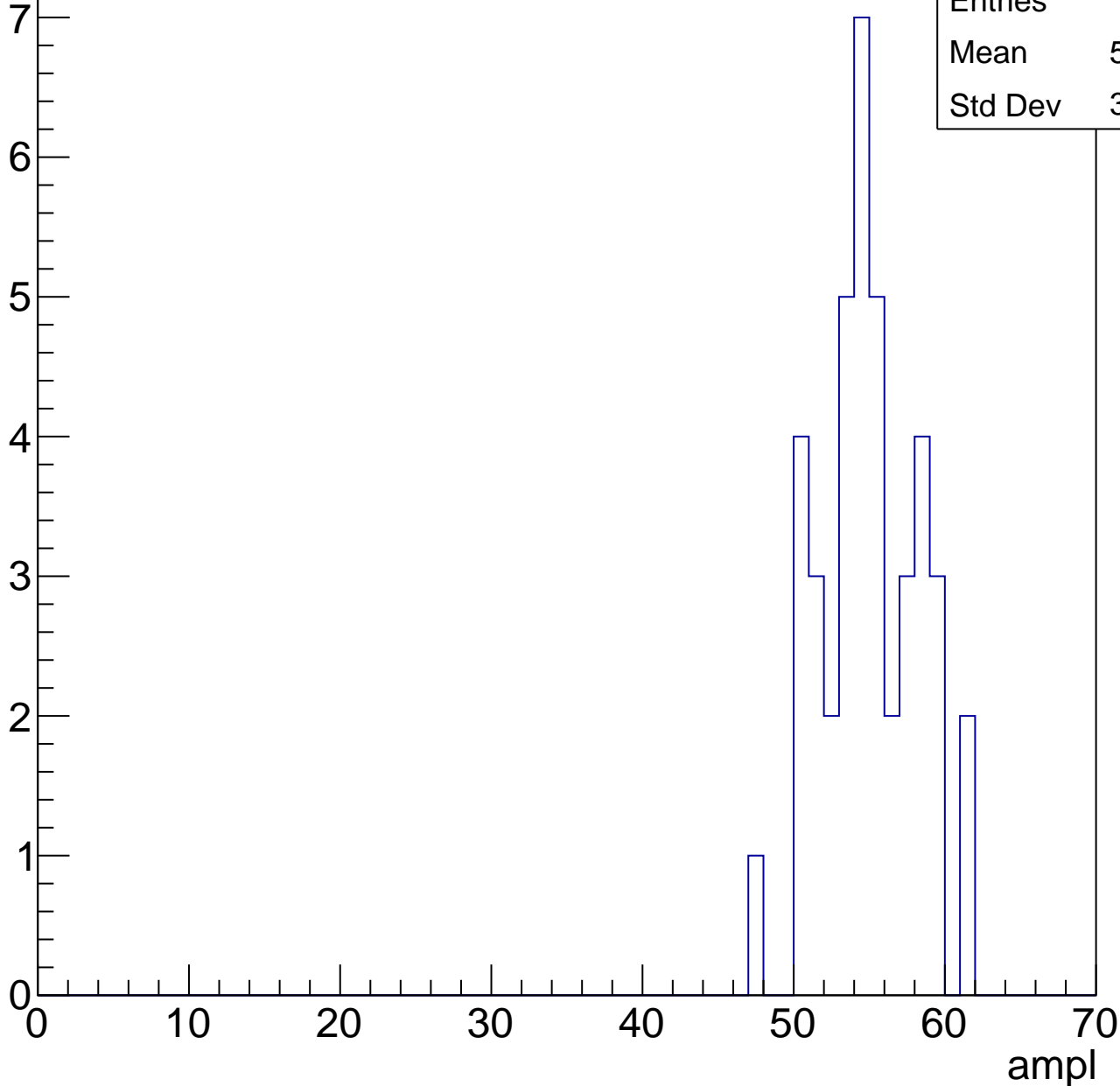


B1L102S, U26-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	54.54
Std Dev	3.194

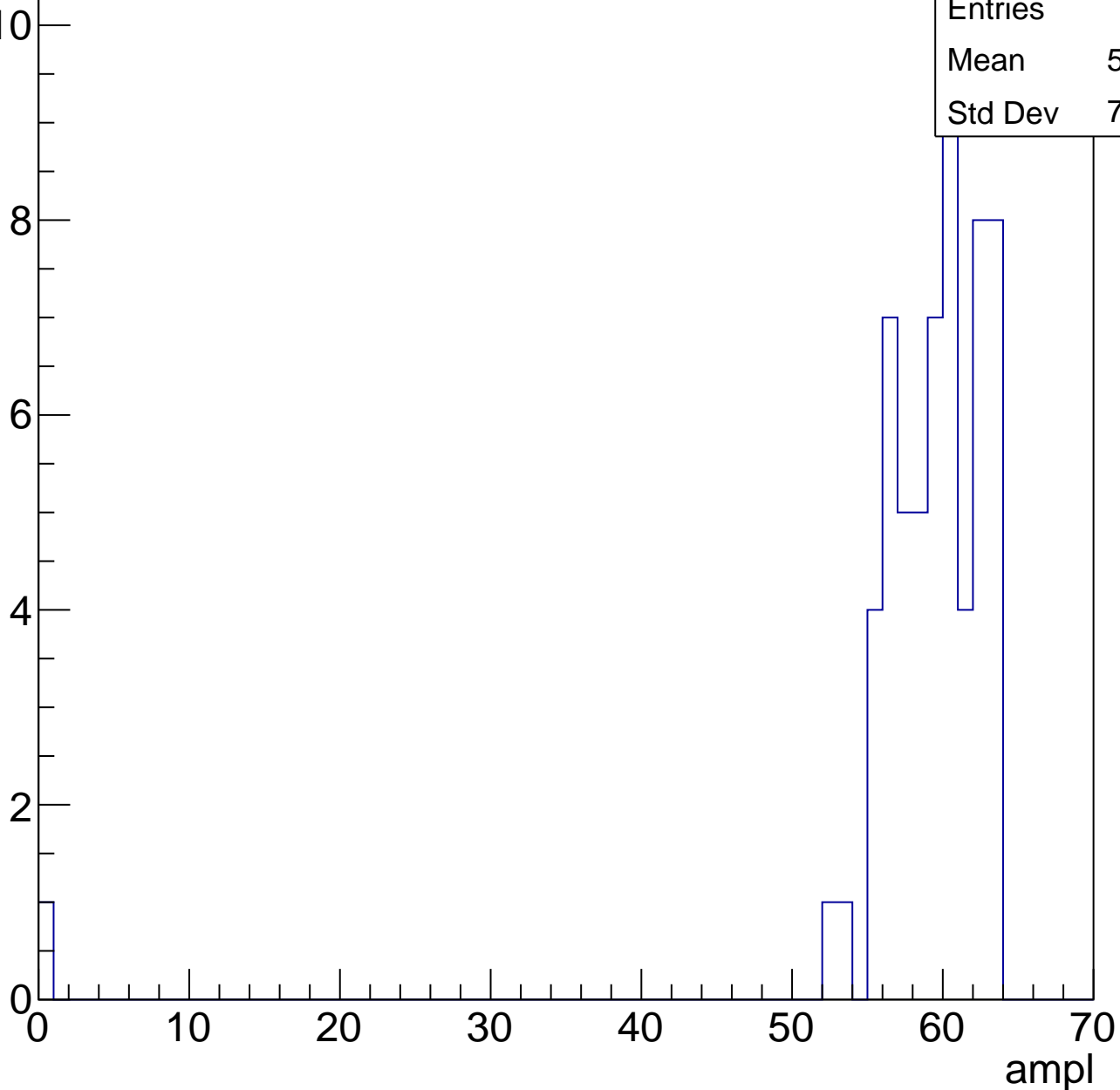


B1L102S, U26-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

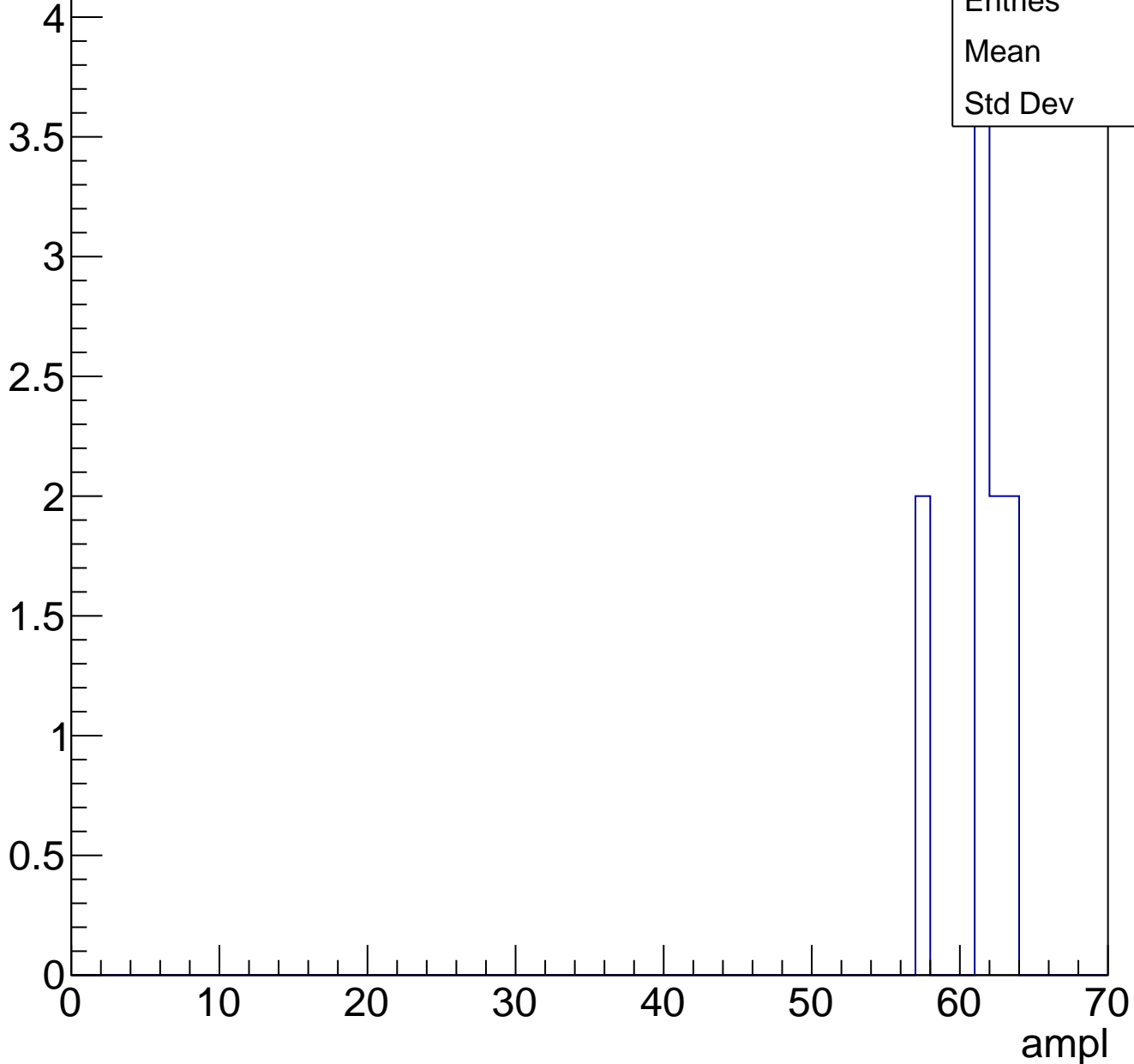
Entries	61
Mean	58.18
Std Dev	7.999



B1L102S, U26-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U26-ch105, adc0

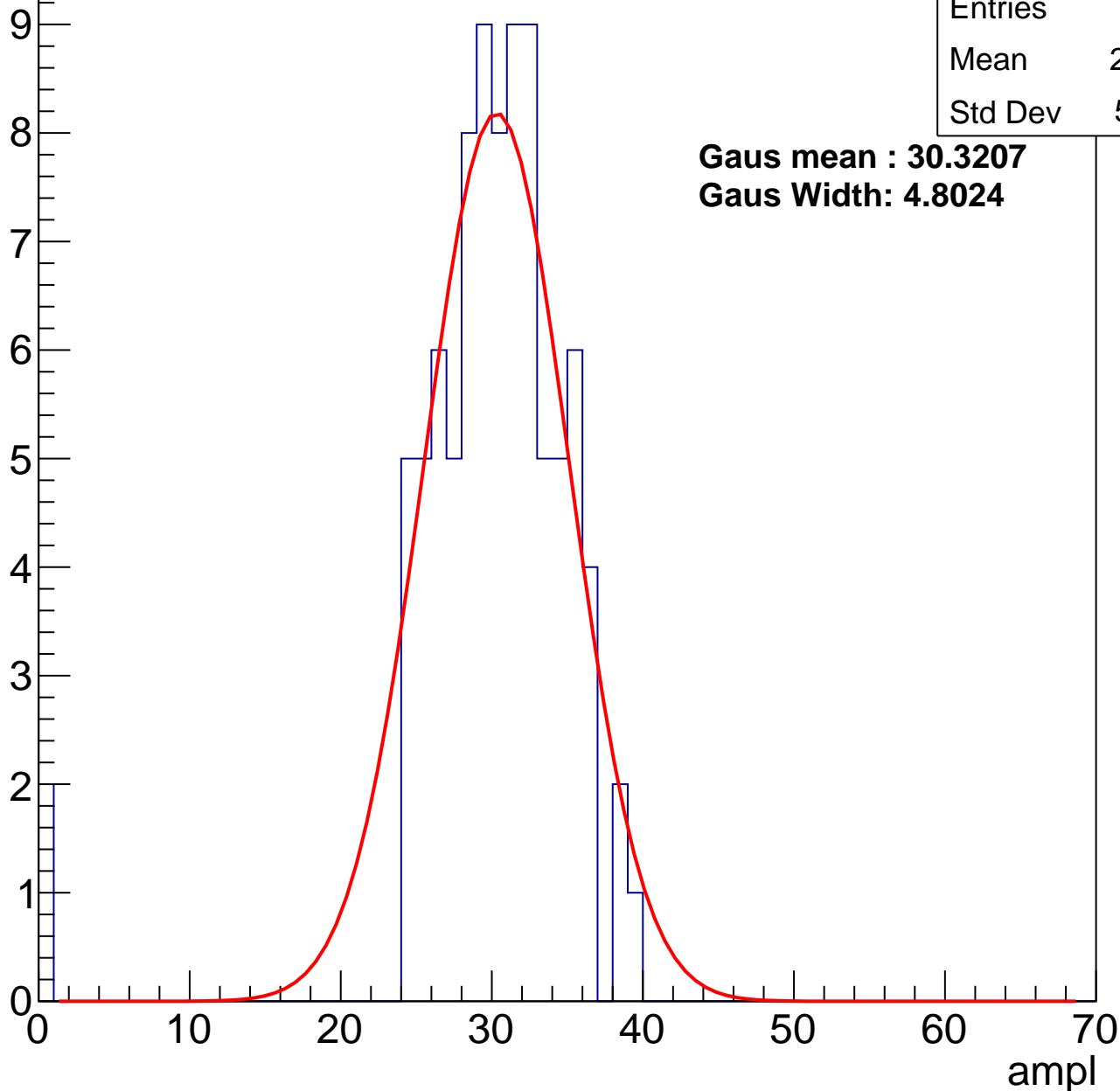
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	29.57
Std Dev	5.751

Gaus mean : 30.3207

Gaus Width: 4.8024



B1L102S, U26-ch105, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	37.47
Std Dev	3.425

Gaus mean : 38.1272

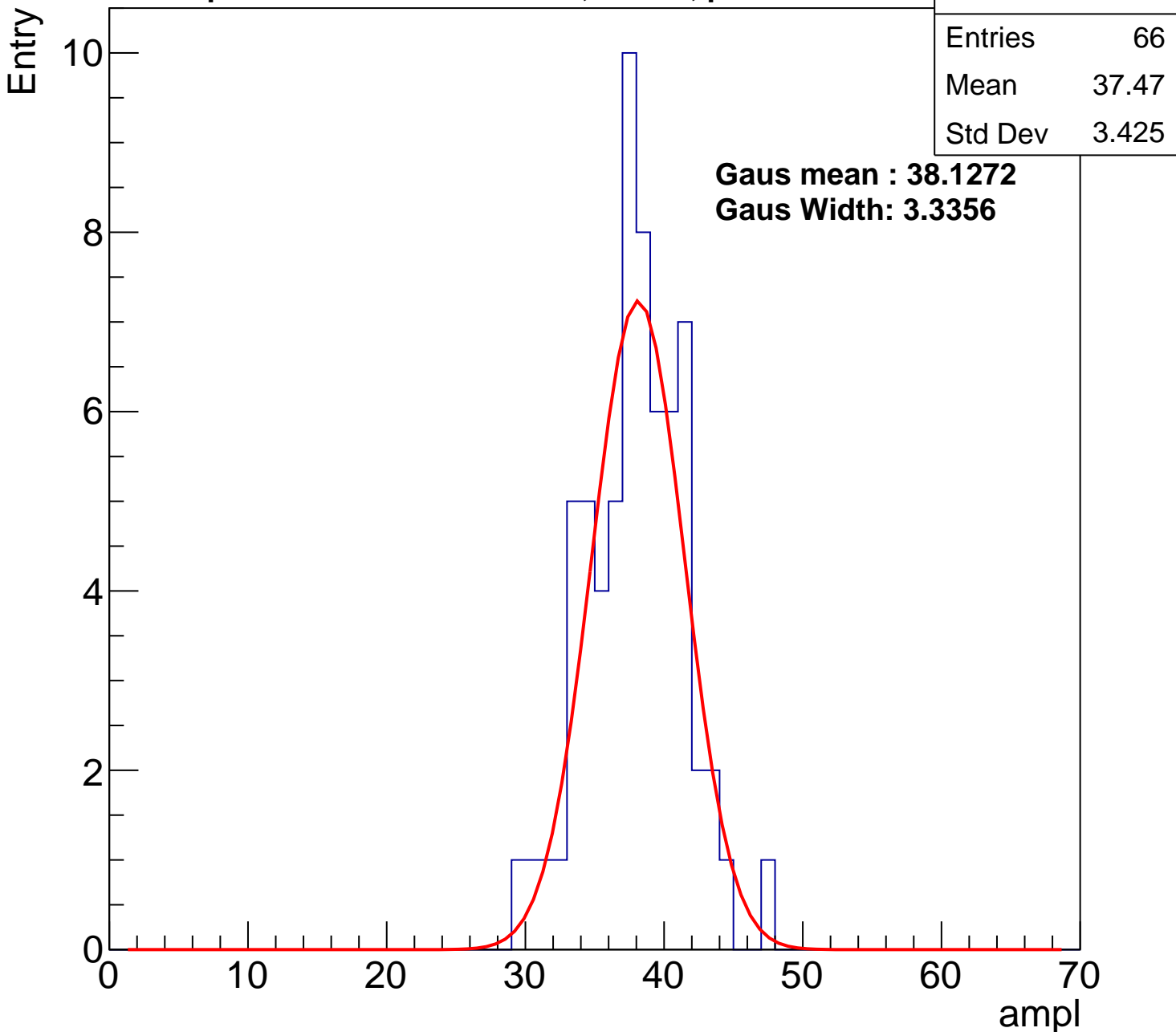
Gaus Width: 3.3356

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U26-ch105, adc2

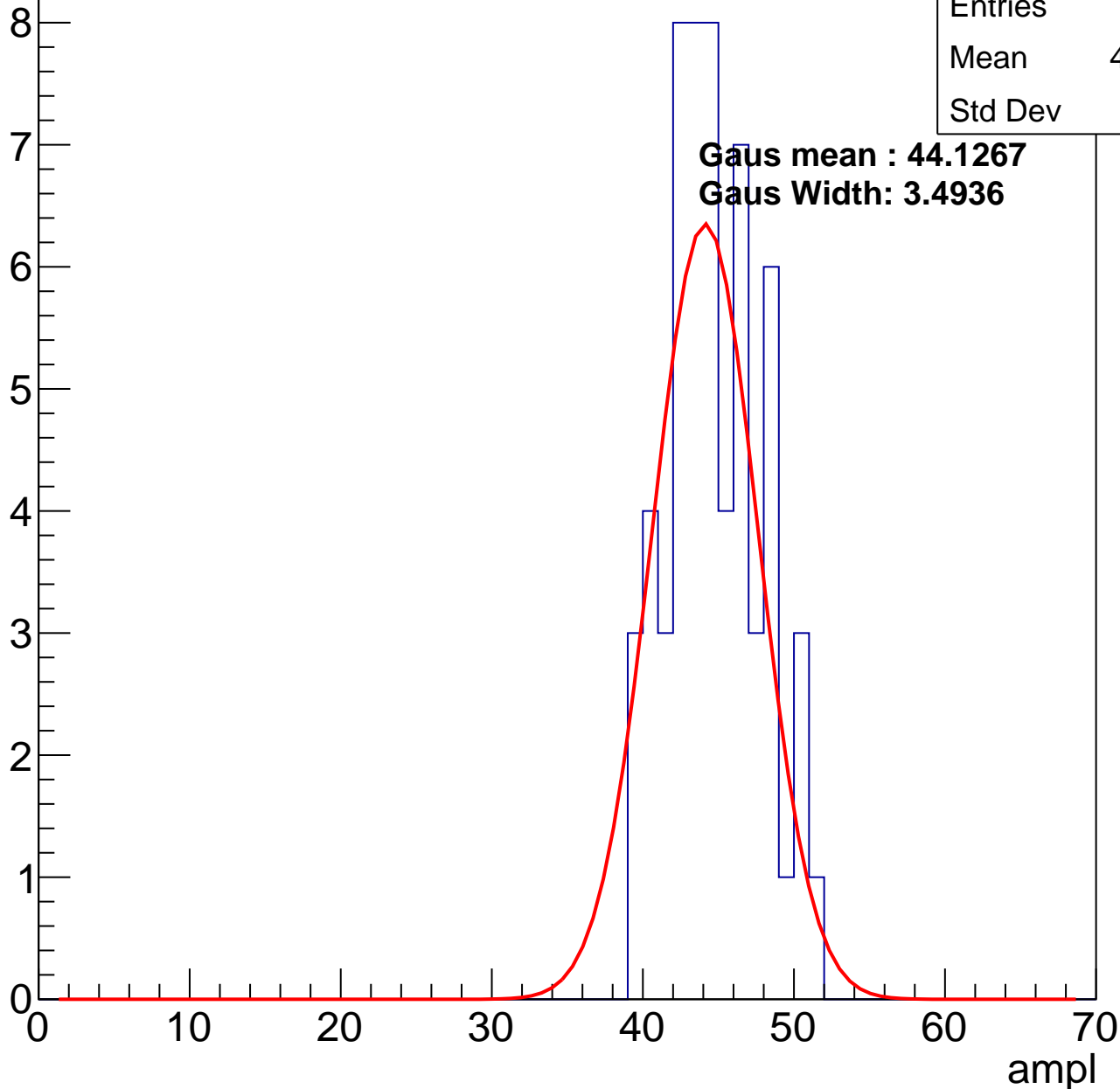
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.29
Std Dev	3.02

Gaus mean : 44.1267

Gaus Width: 3.4936

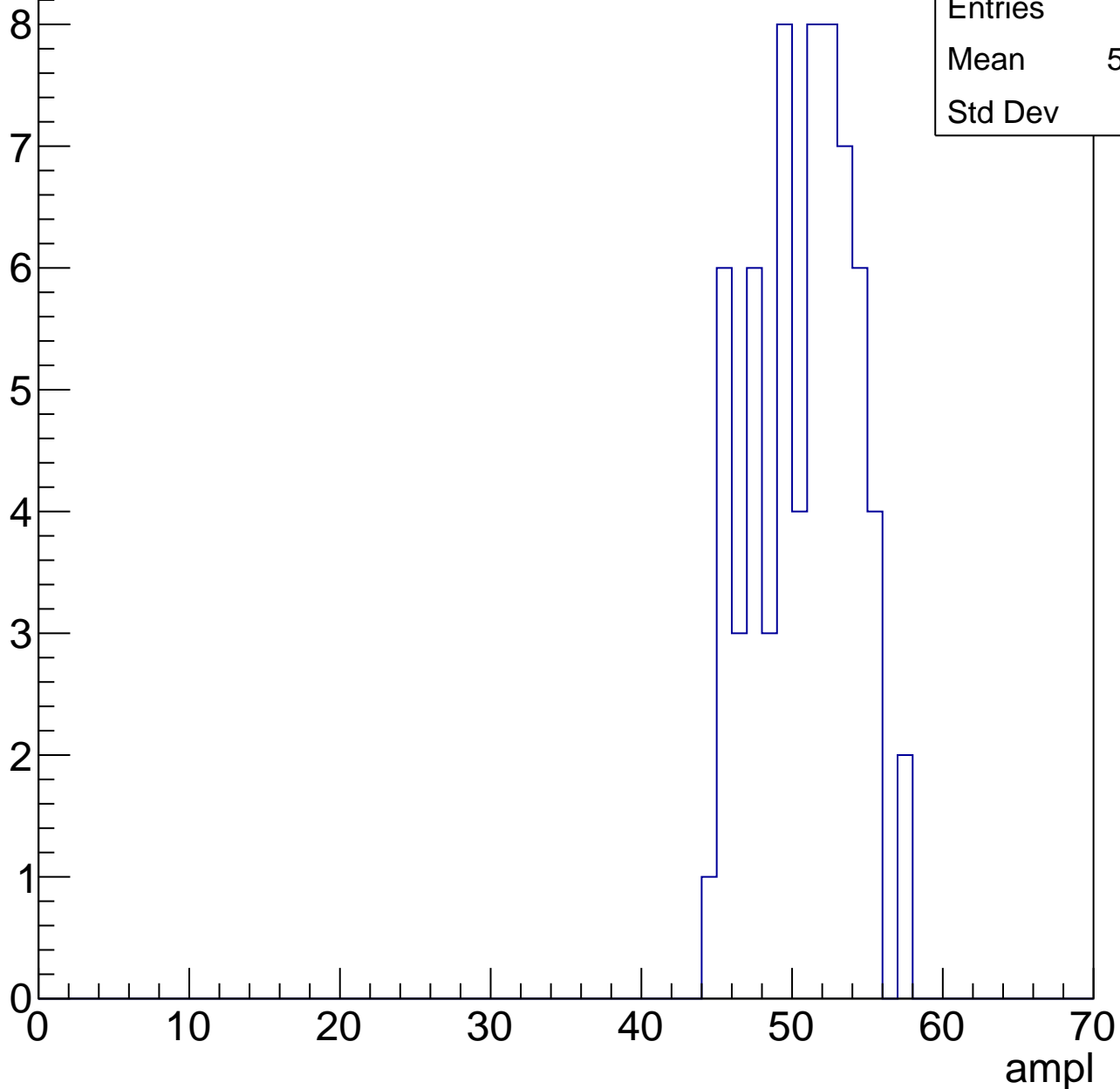


B1L102S, U26-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.35
Std Dev	3.25

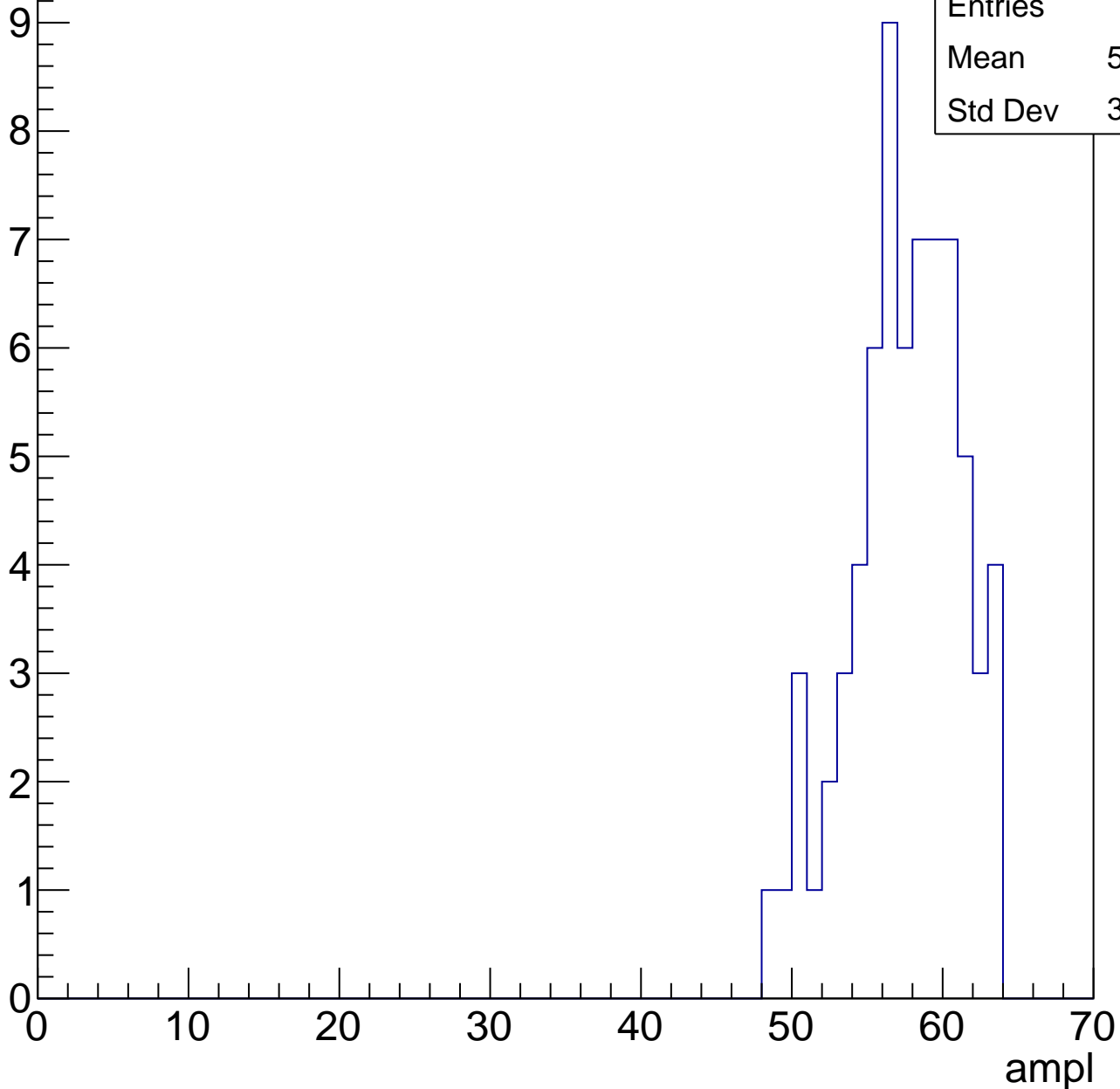


B1L102S, U26-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	57.03
Std Dev	3.595

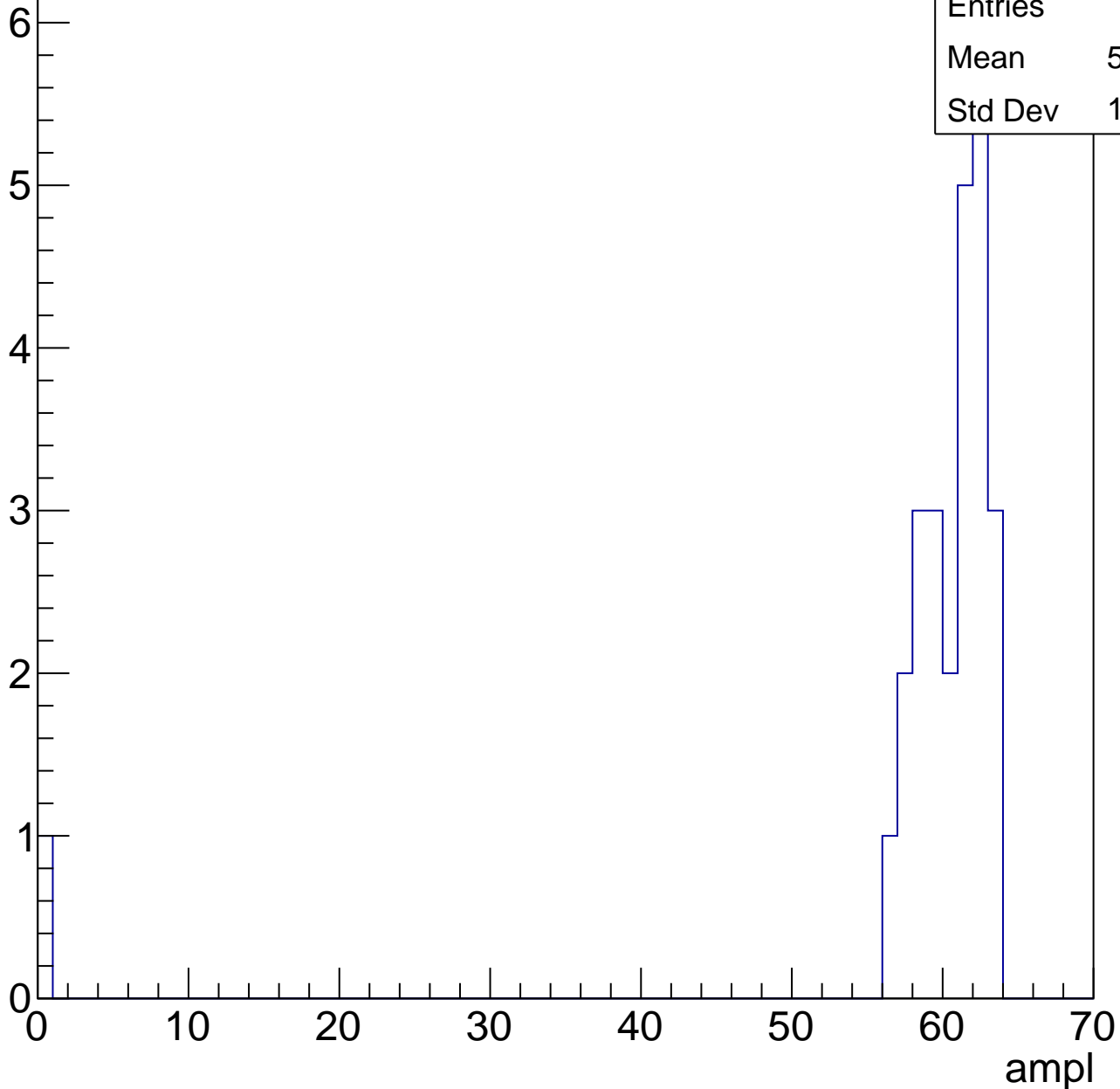


B1L102S, U26-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	57.96
Std Dev	11.76



B1L102S, U26-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	62.2
Std Dev	1.166

B1L102S, U26-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



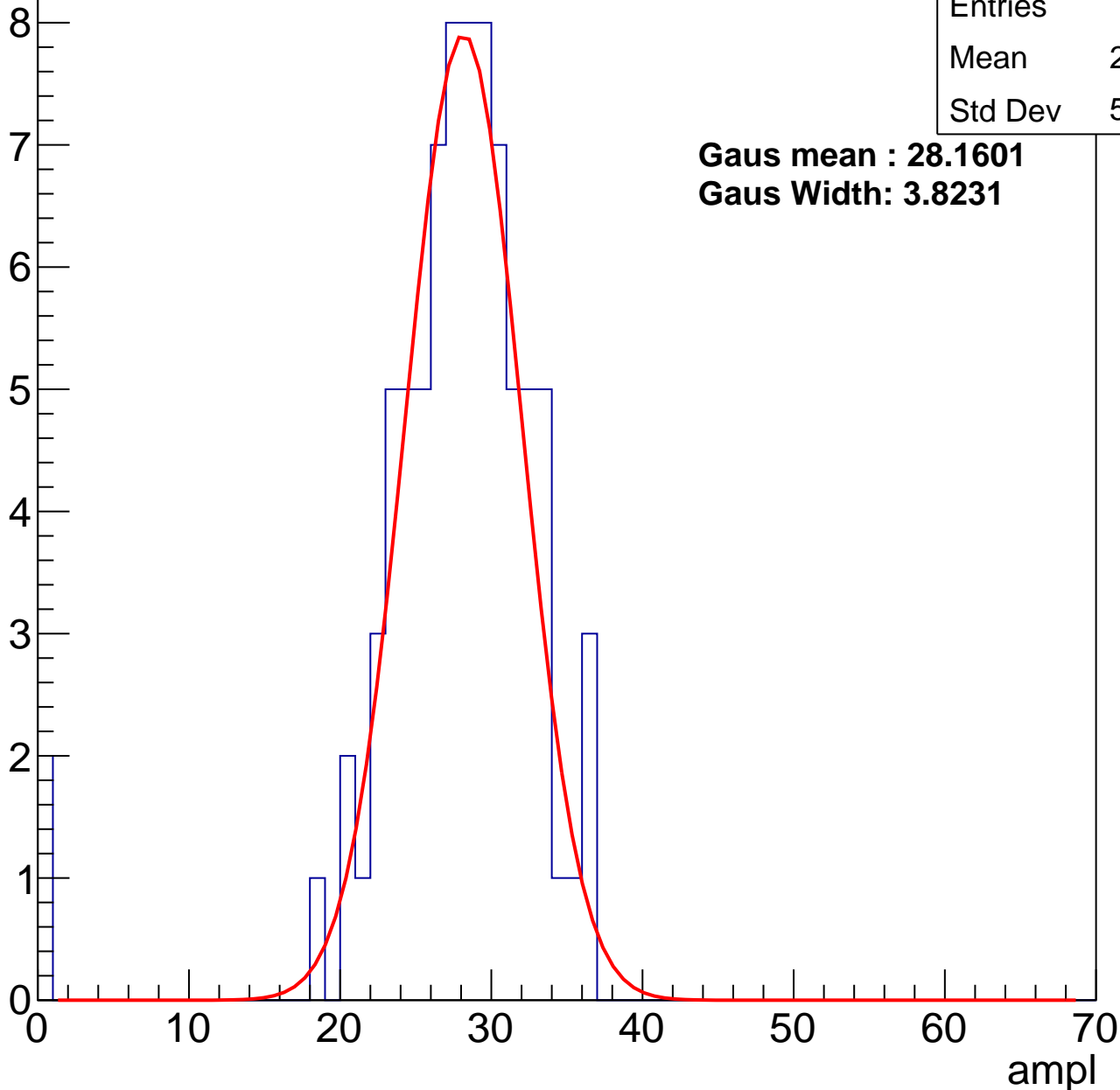
B1L102S, U26-ch106, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	27.15
Std Dev	5.783

Gaus mean : 28.1601
Gaus Width: 3.8231



B1L102S, U26-ch106, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	34.92
Std Dev	5.56

Gaus mean : 35.6665

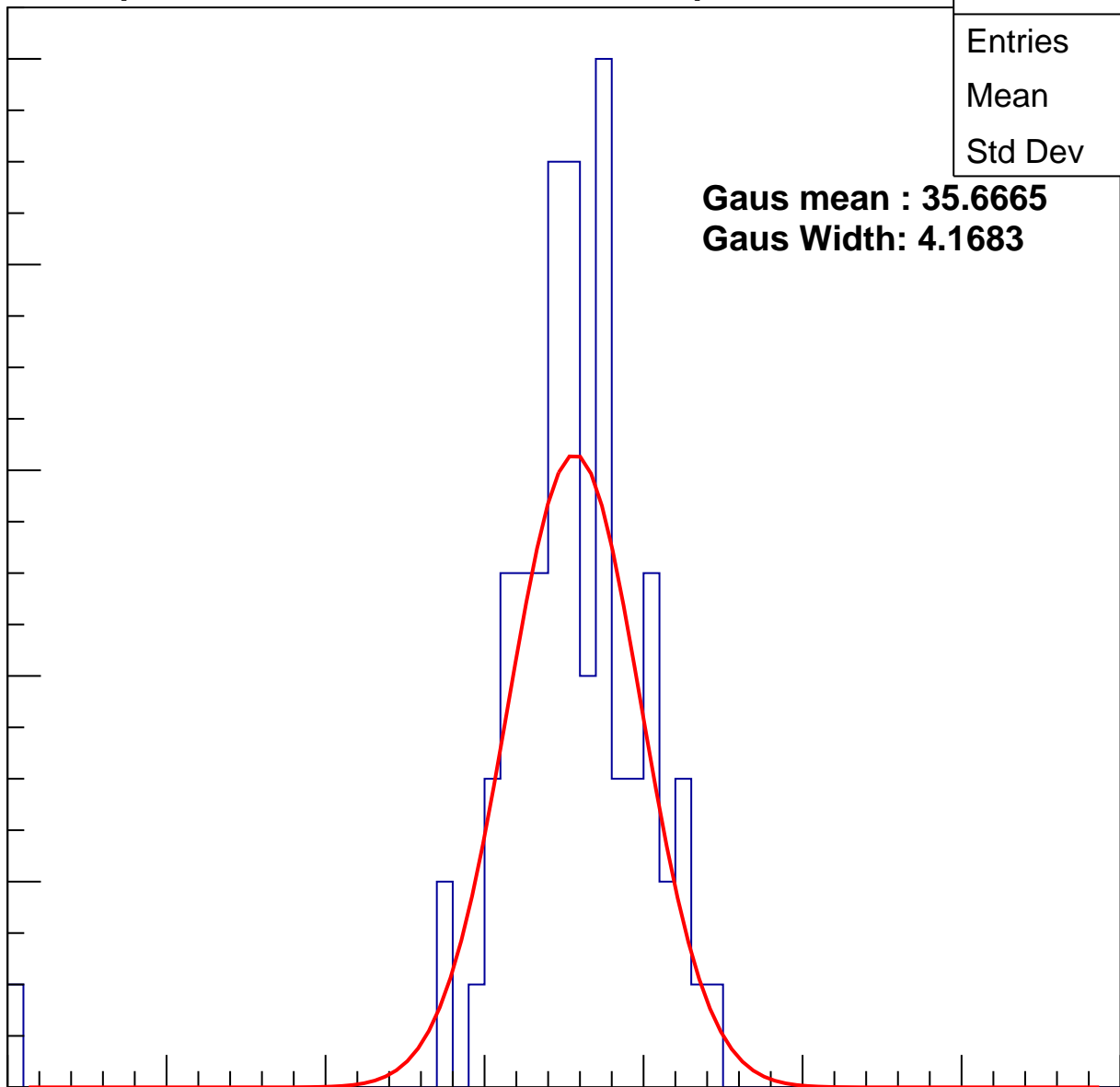
Gaus Width: 4.1683

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



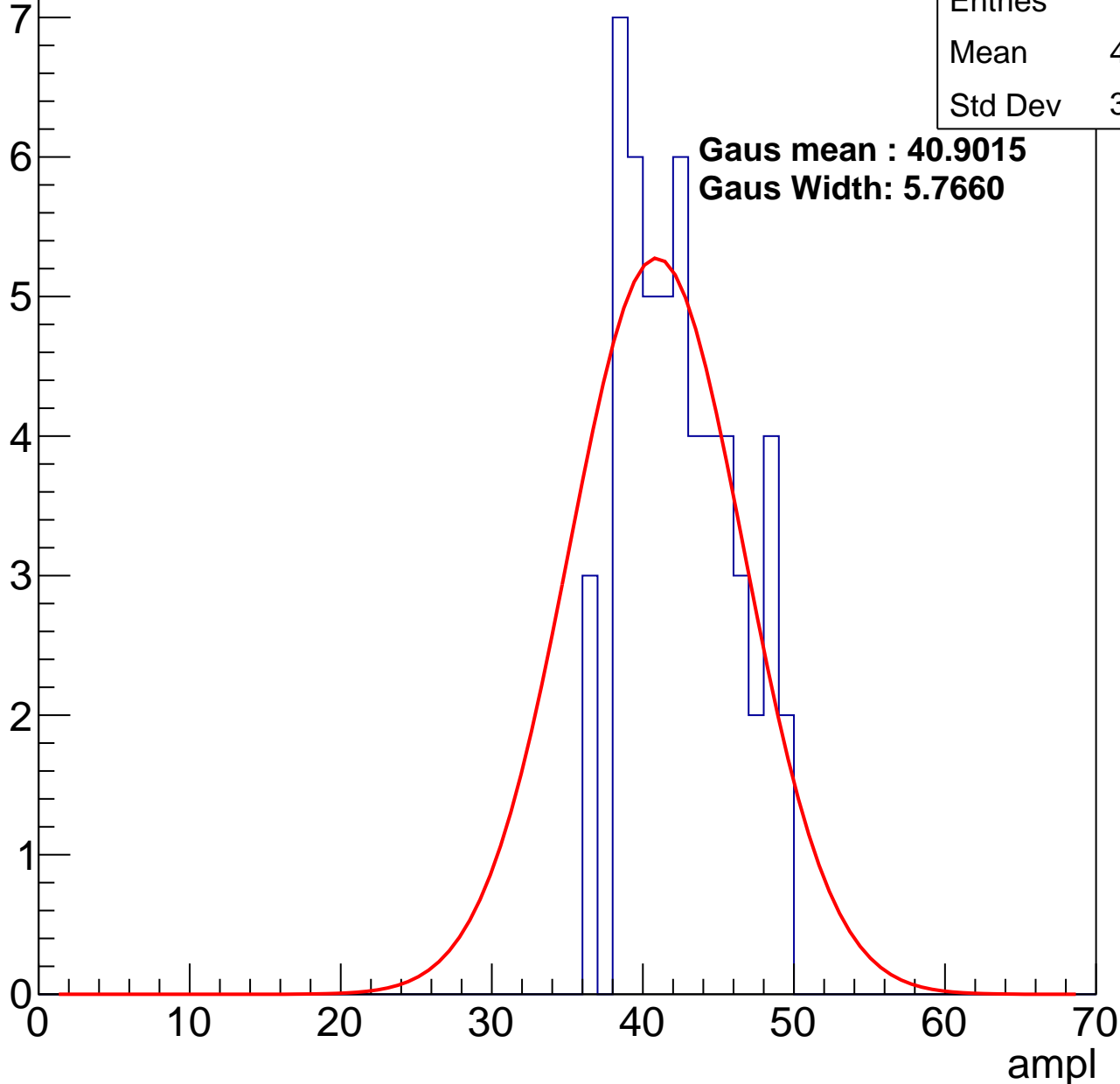
B1L102S, U26-ch106, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.09
Std Dev	3.554

Gaus mean : 40.9015
Gaus Width: 5.7660

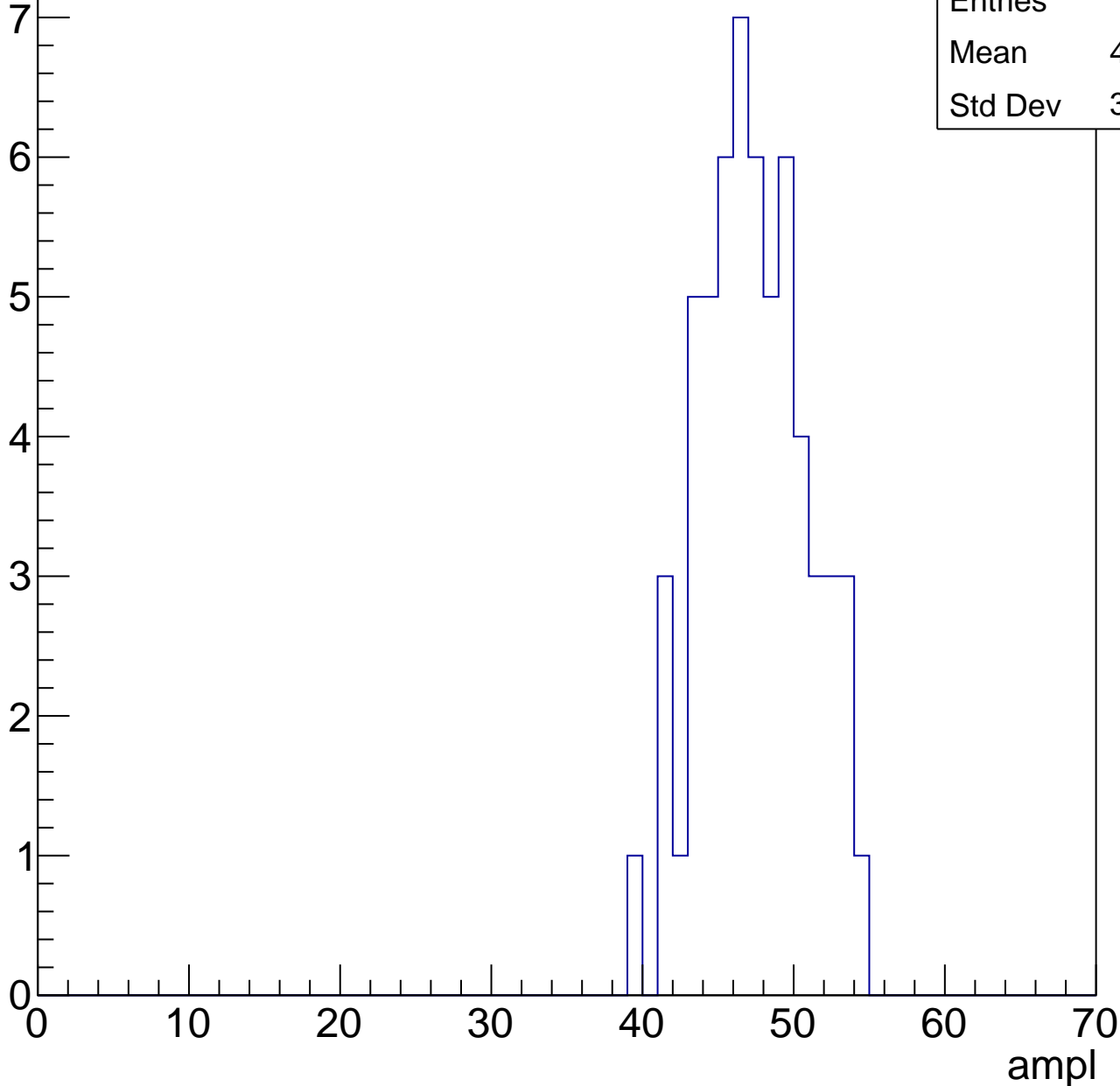


B1L102S, U26-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	46.93
Std Dev	3.439

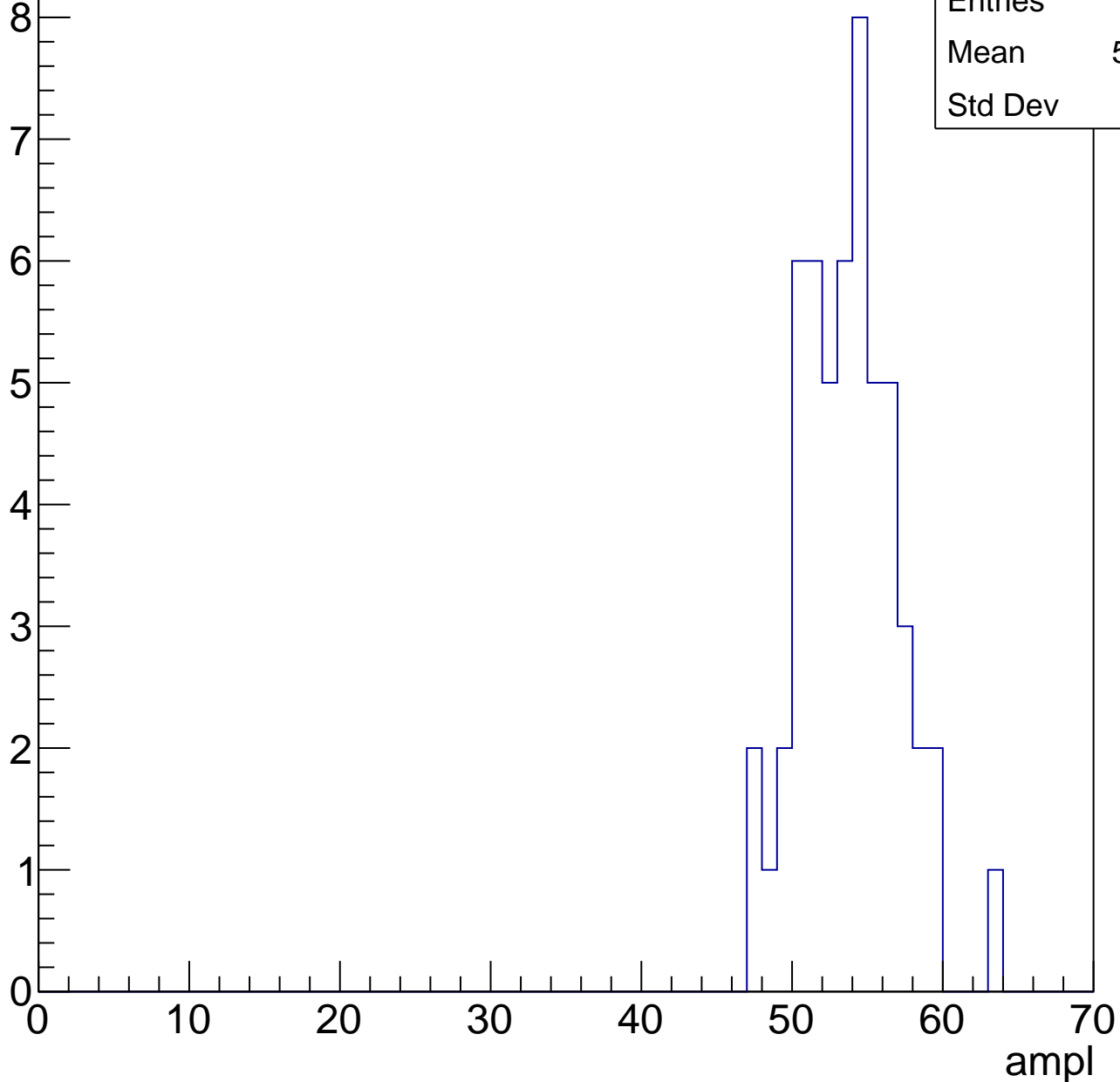


B1L102S, U26-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

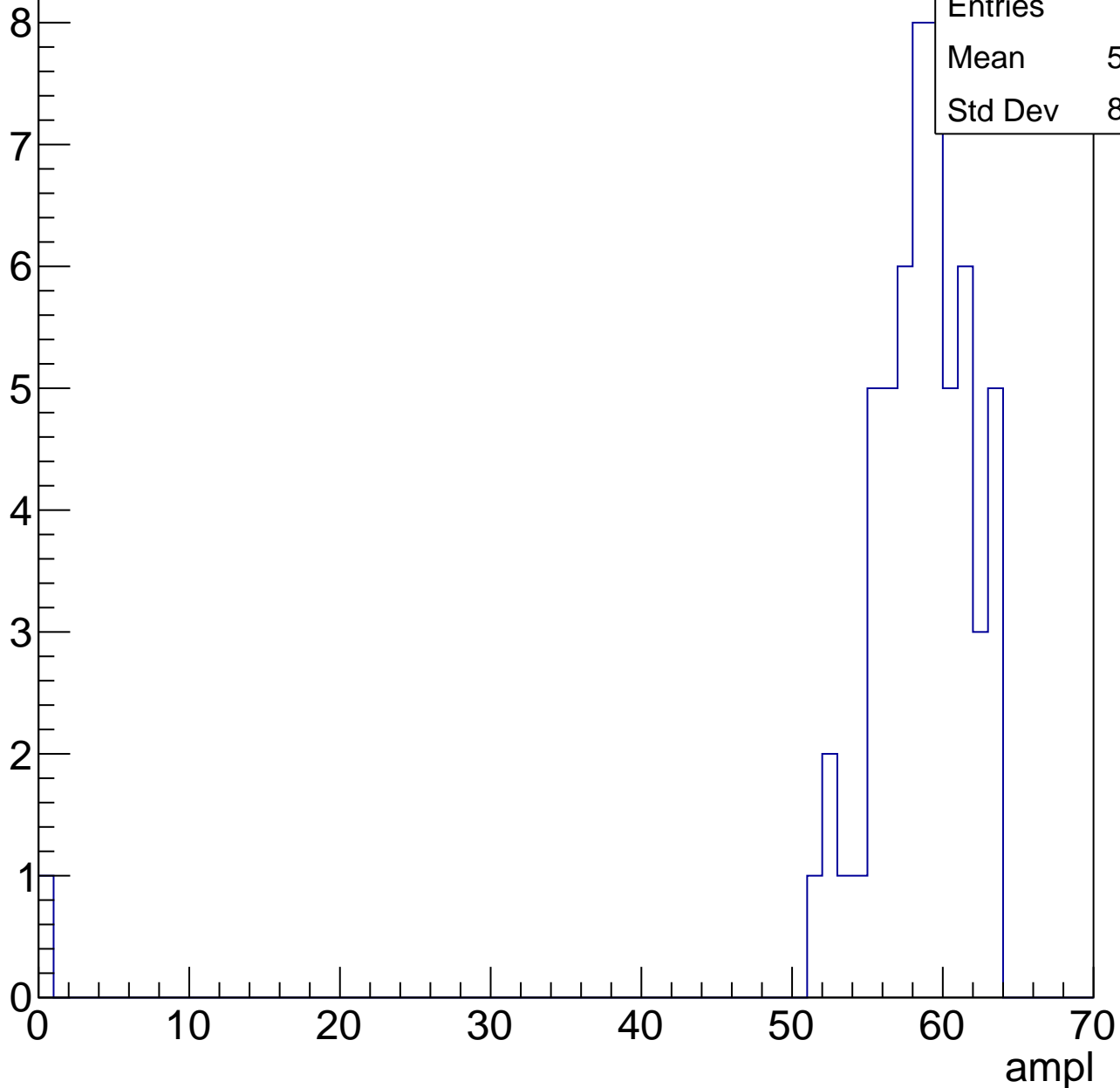
Entries	54
Mean	53.31
Std Dev	3.19



B1L102S, U26-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

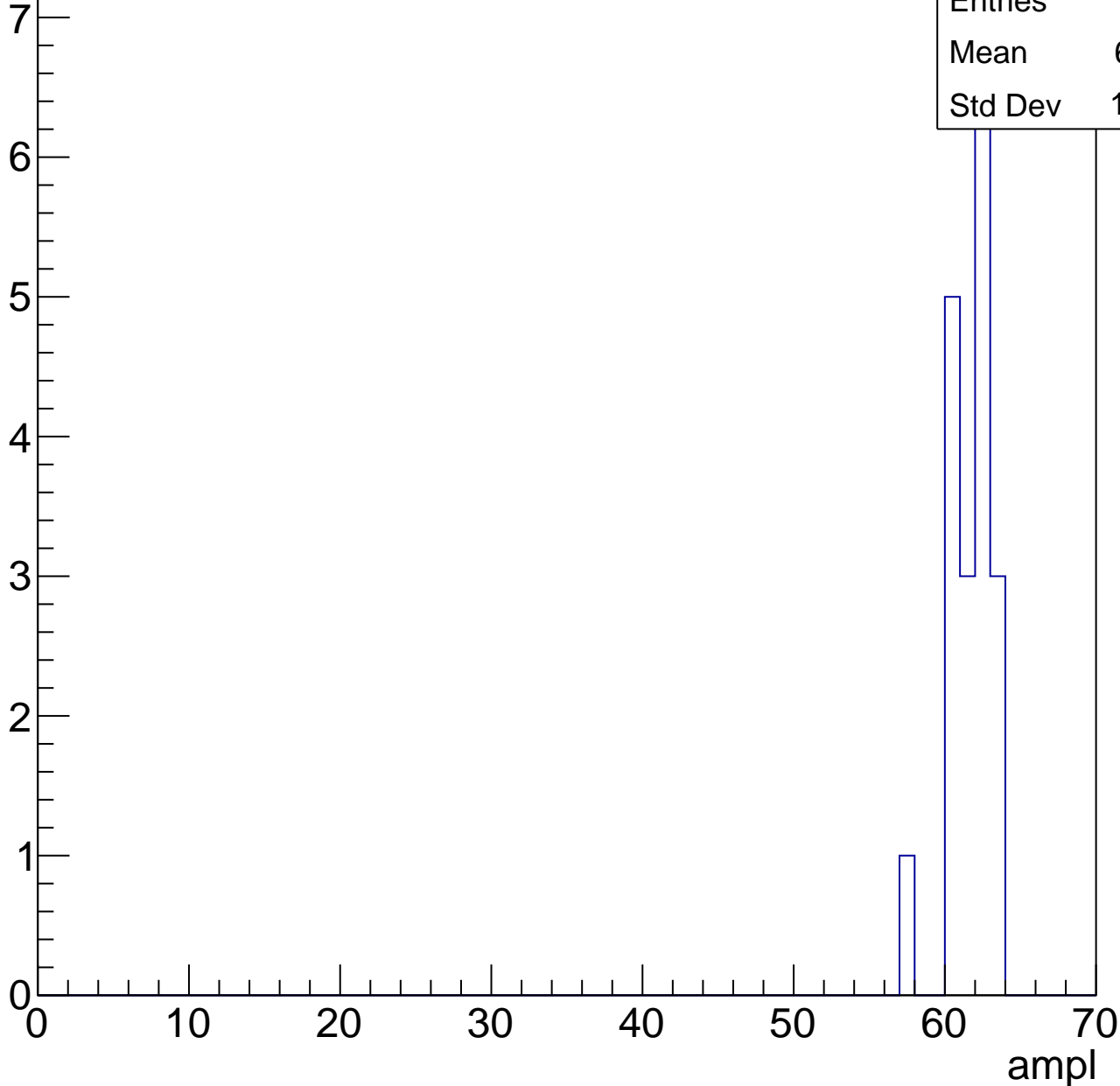


B1L102S, U26-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.21
Std Dev	1.436



B1L102S, U26-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch107, adc0

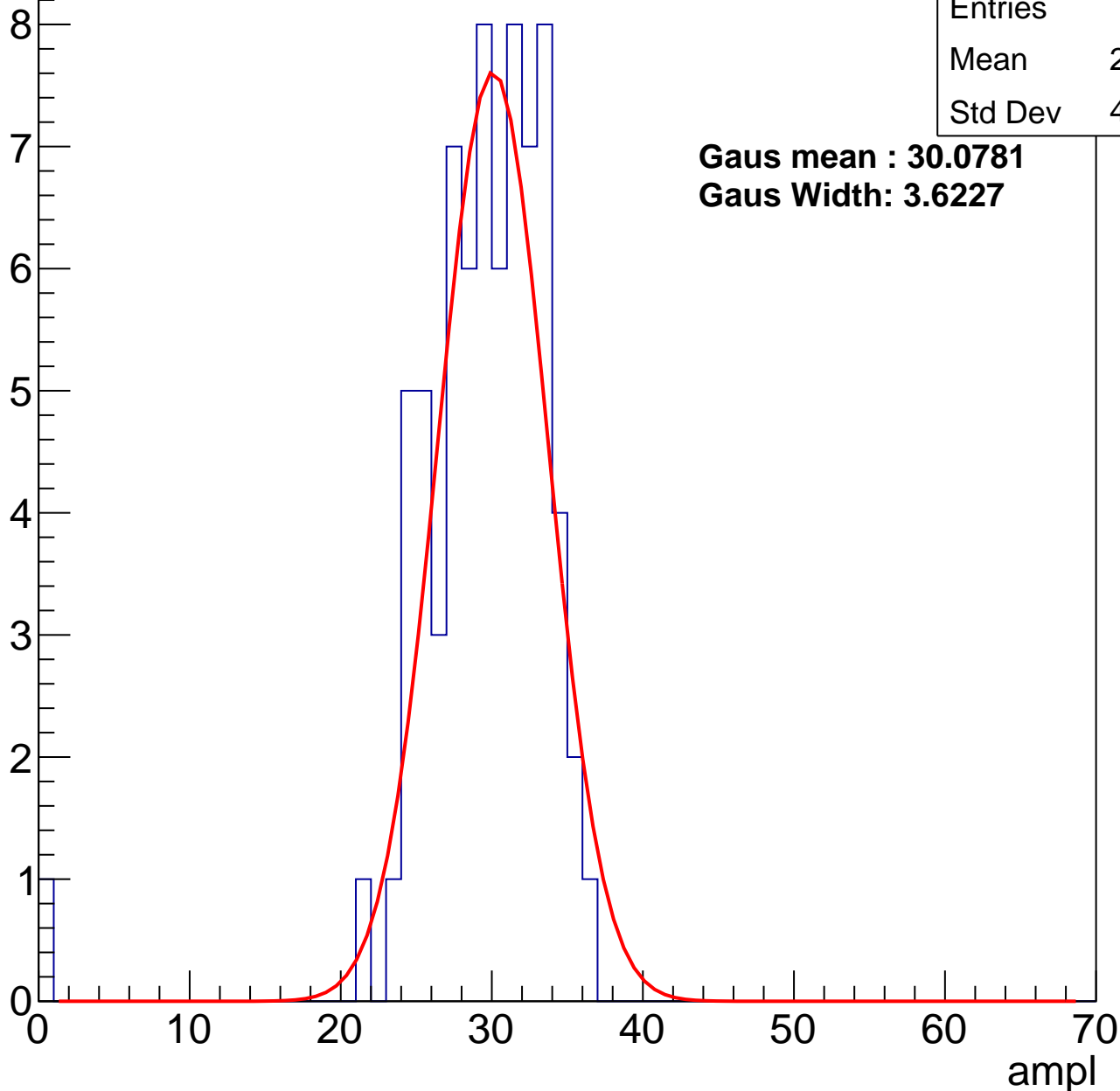
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.96
Std Dev	4.764

Gaus mean : 30.0781

Gaus Width: 3.6227



B1L102S, U26-ch107, adc1

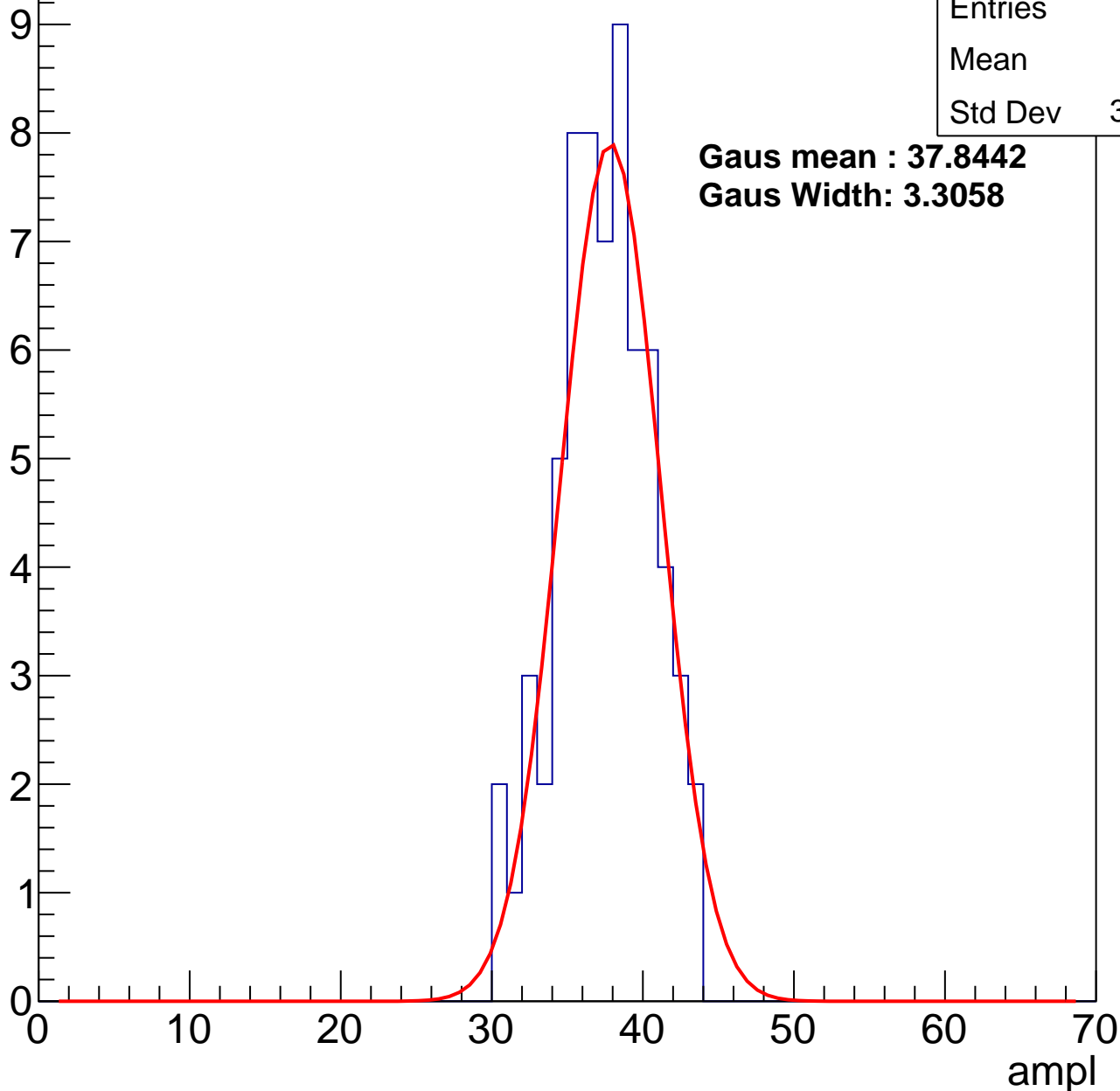
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37
Std Dev	3.075

Gaus mean : 37.8442

Gaus Width: 3.3058



B1L102S, U26-ch107, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.85
Std Dev	3.744

Gaus mean : 44.8874

Gaus Width: 3.9440

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

0

10

20

30

40

50

60

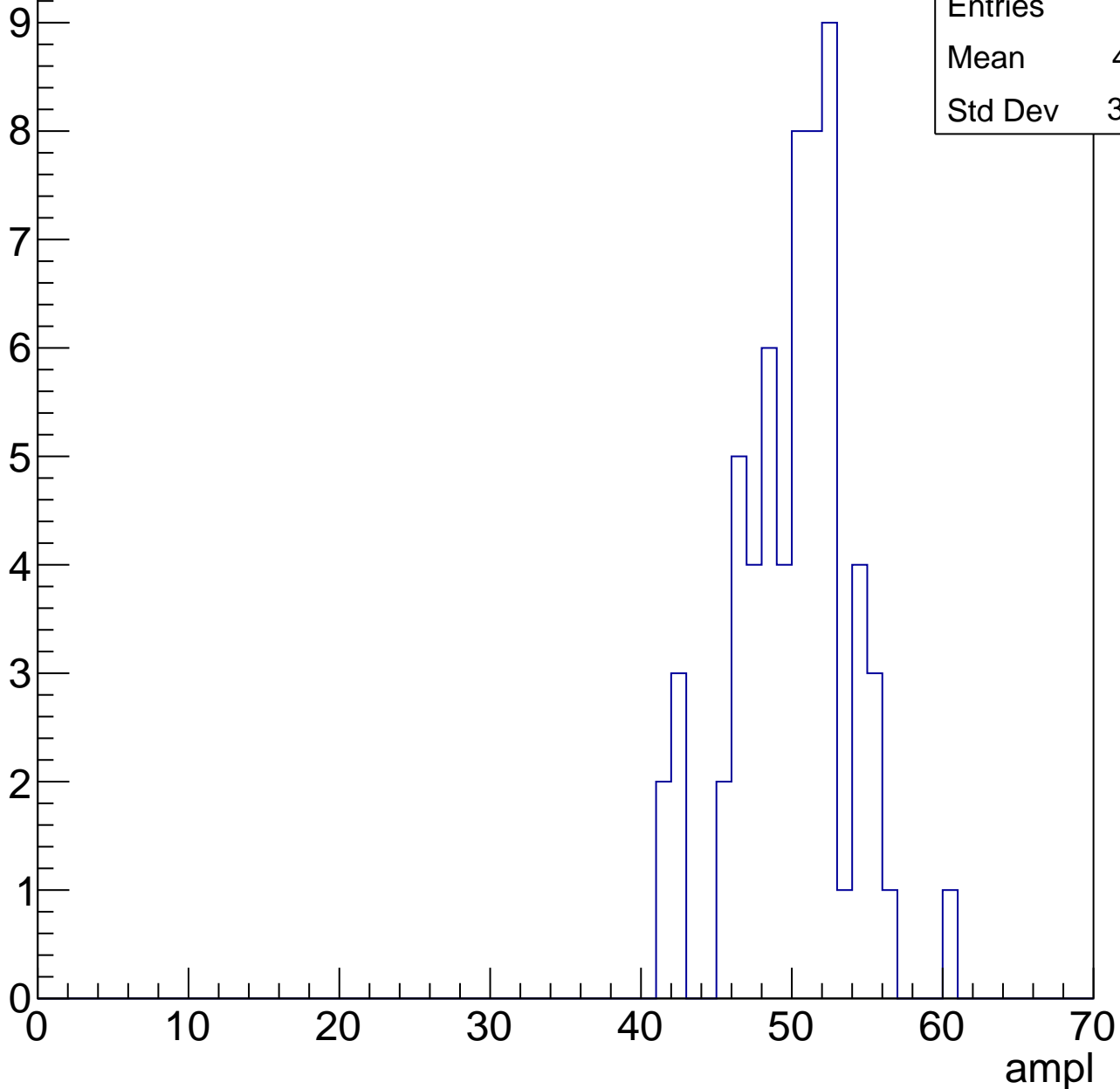
70

B1L102S, U26-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.61
Std Dev	3.769

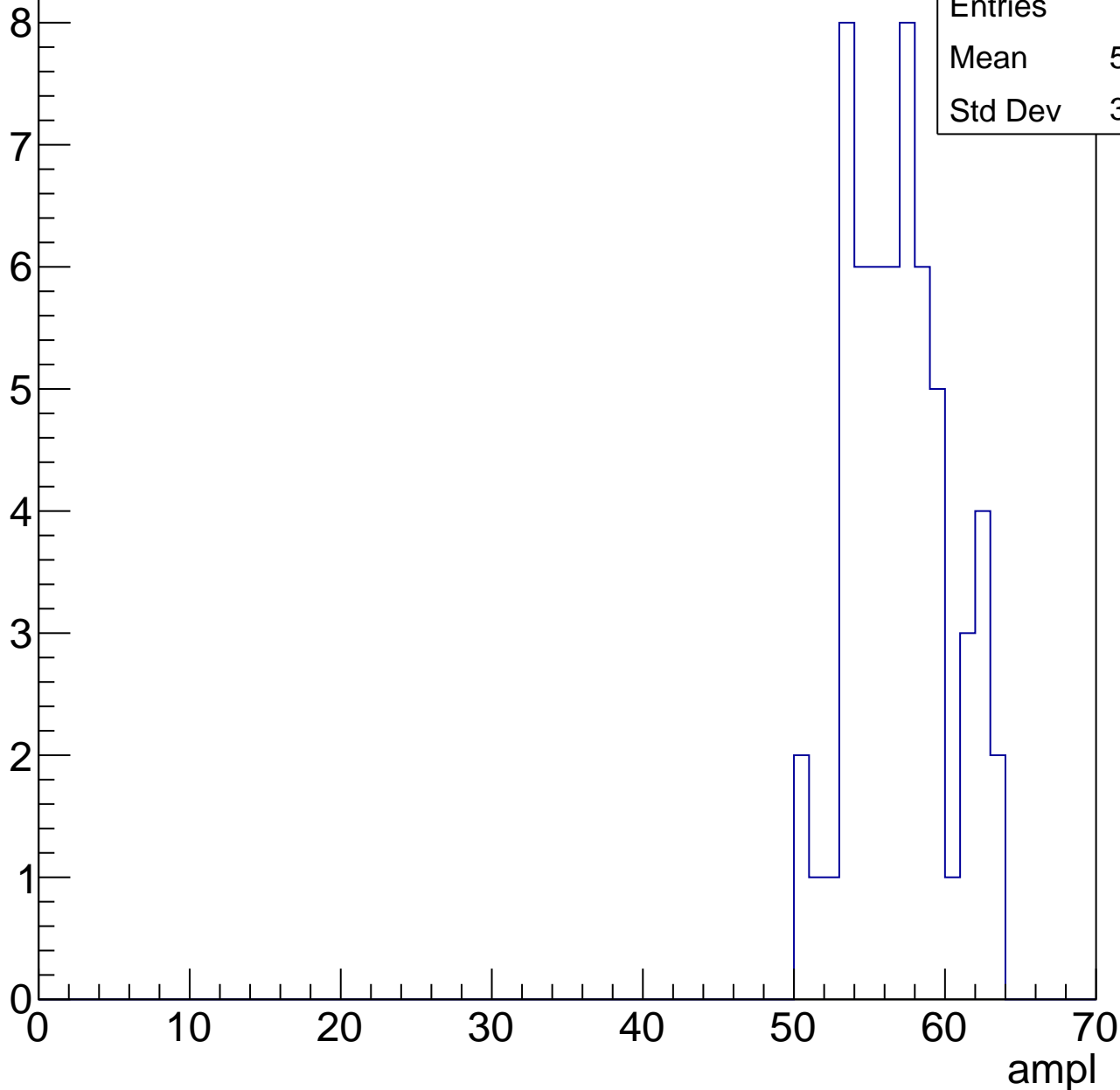


B1L102S, U26-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.49
Std Dev	3.217

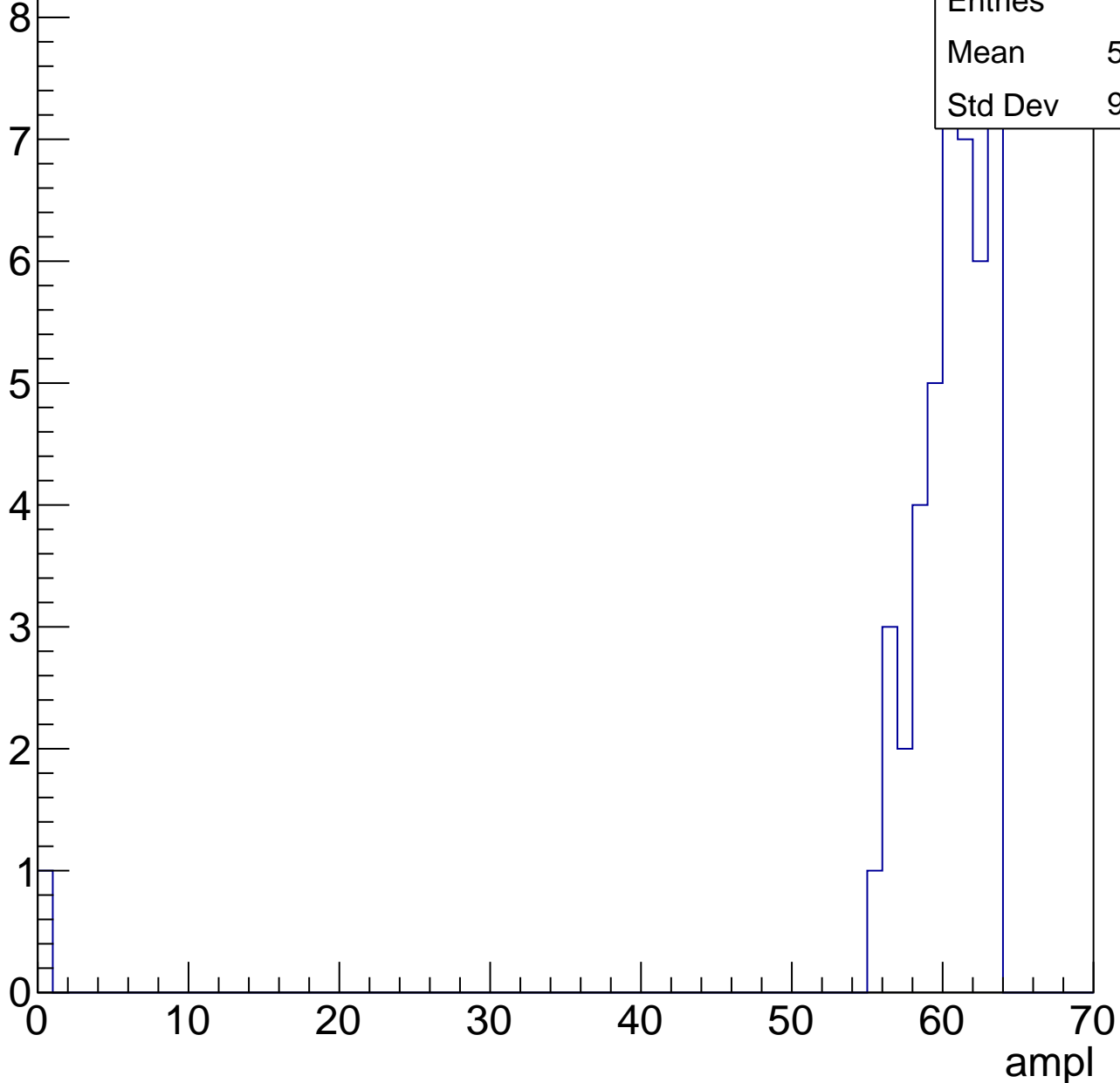


B1L102S, U26-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.82
Std Dev	9.132



B1L102S, U26-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U26-ch108, adc0

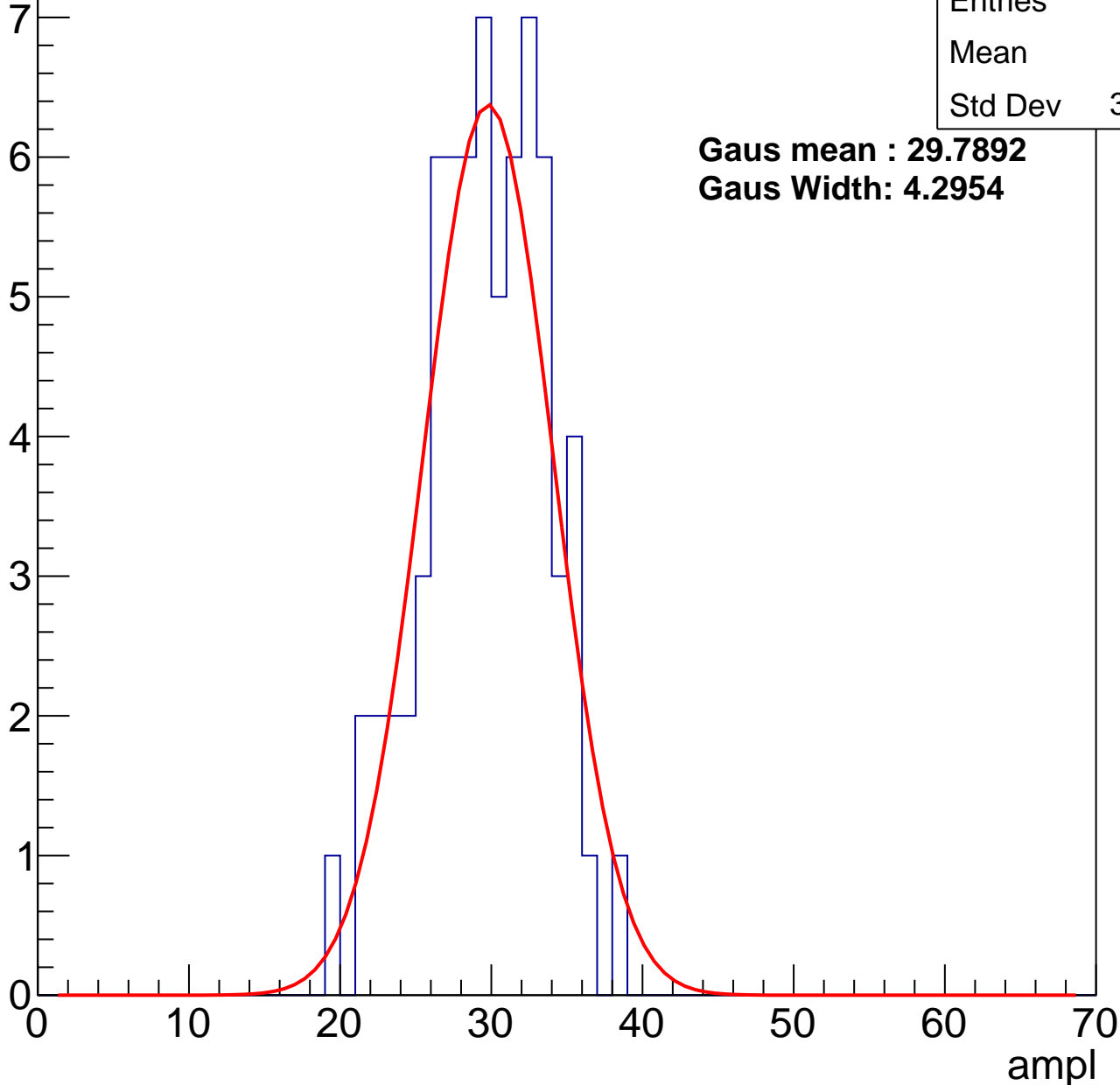
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.1
Std Dev	3.993

Gaus mean : 29.7892

Gaus Width: 4.2954



B1L102S, U26-ch108, adc1

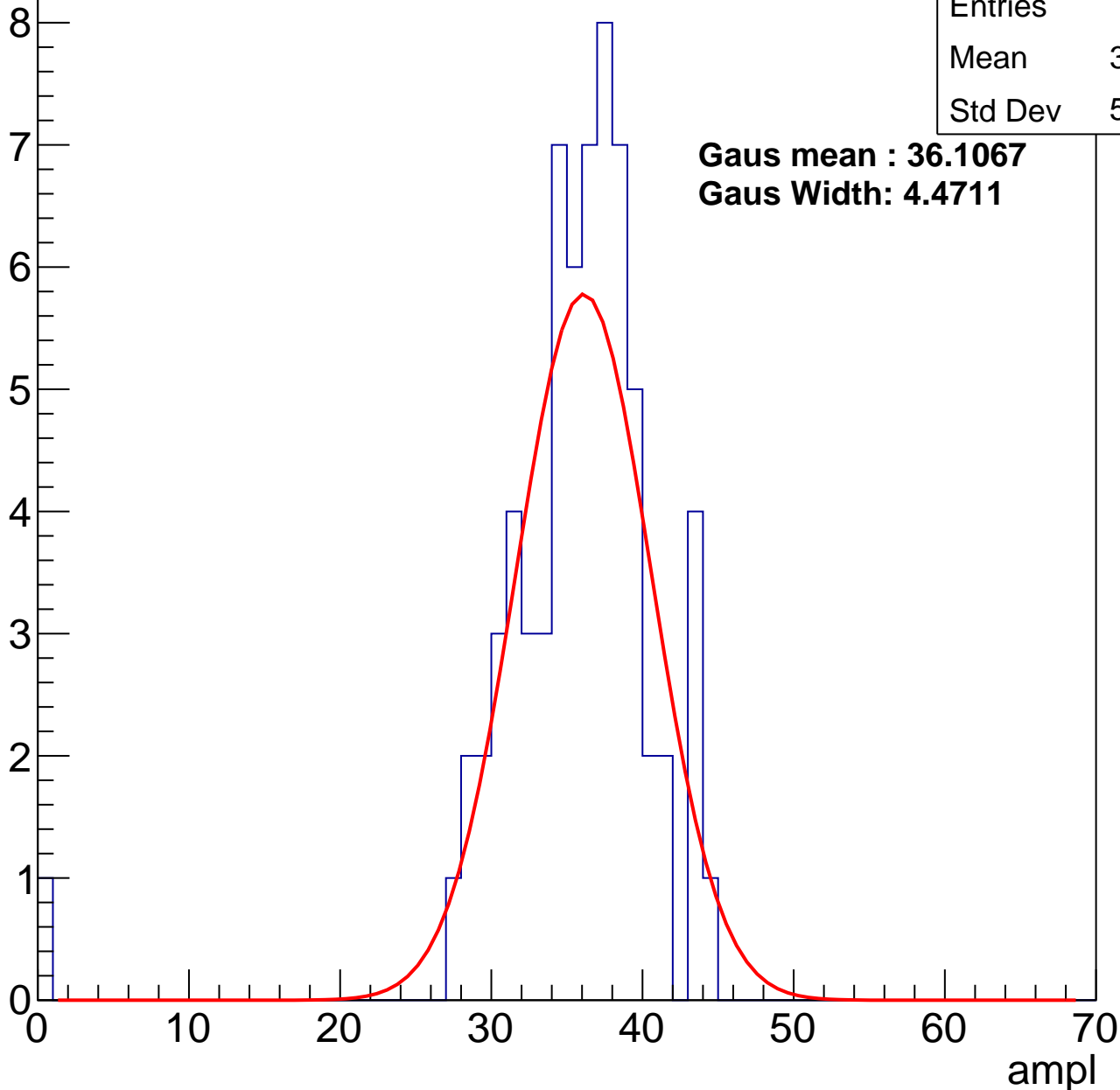
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.07
Std Dev	5.796

Gaus mean : 36.1067

Gaus Width: 4.4711



B1L102S, U26-ch108, adc2

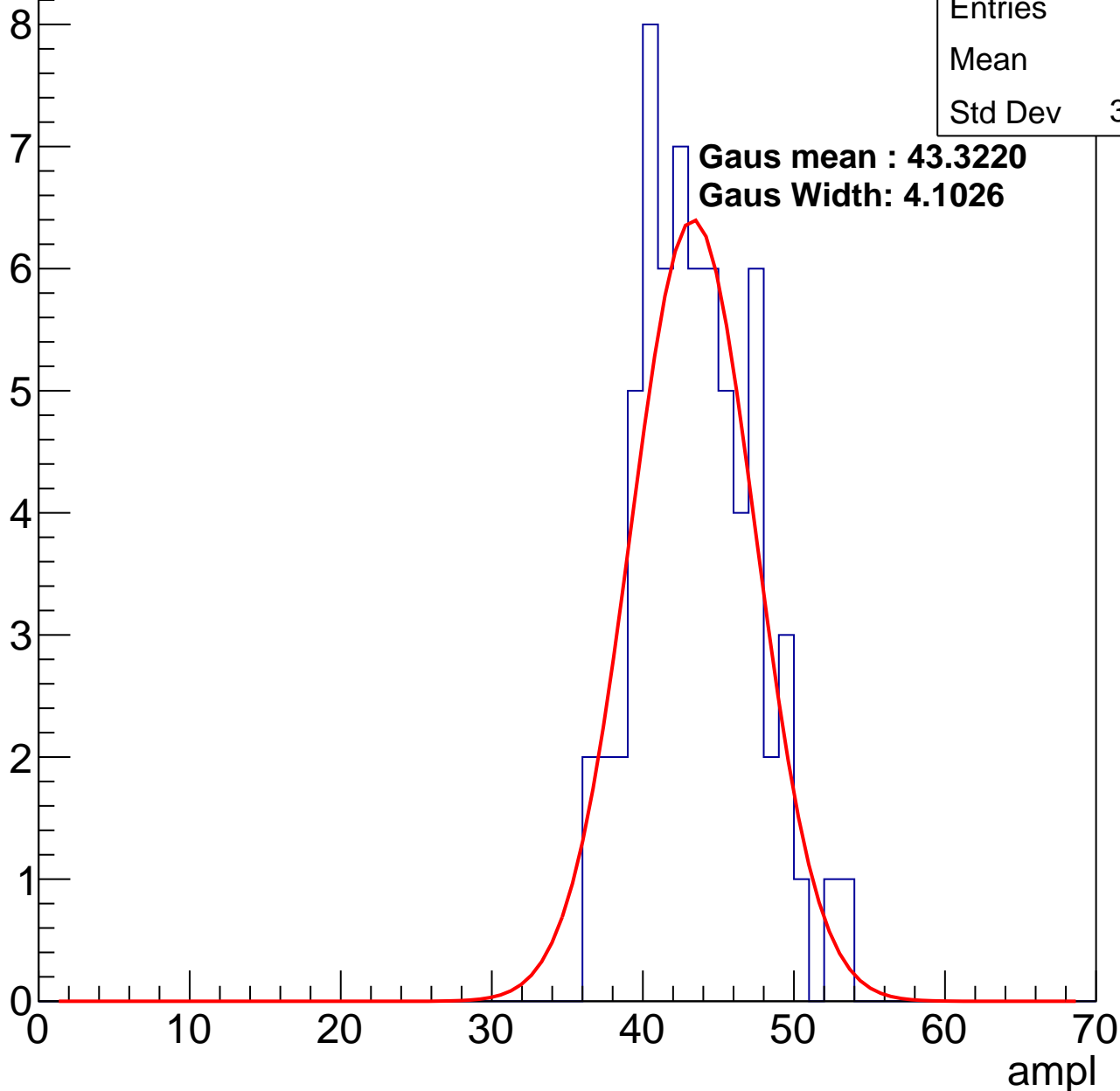
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.1
Std Dev	3.762

Gaus mean : 43.3220

Gaus Width: 4.1026

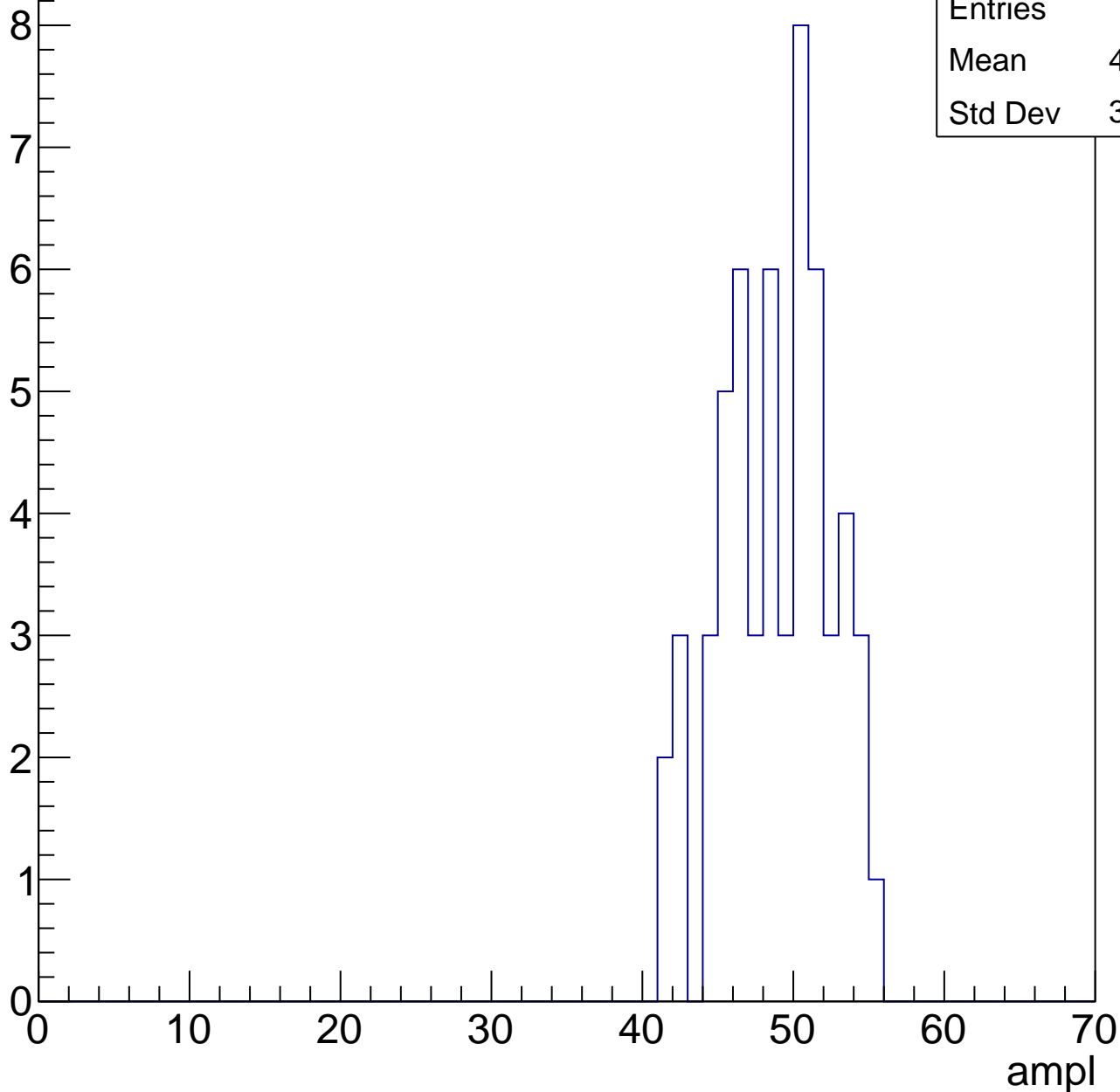


B1L102S, U26-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.36
Std Dev	3.568

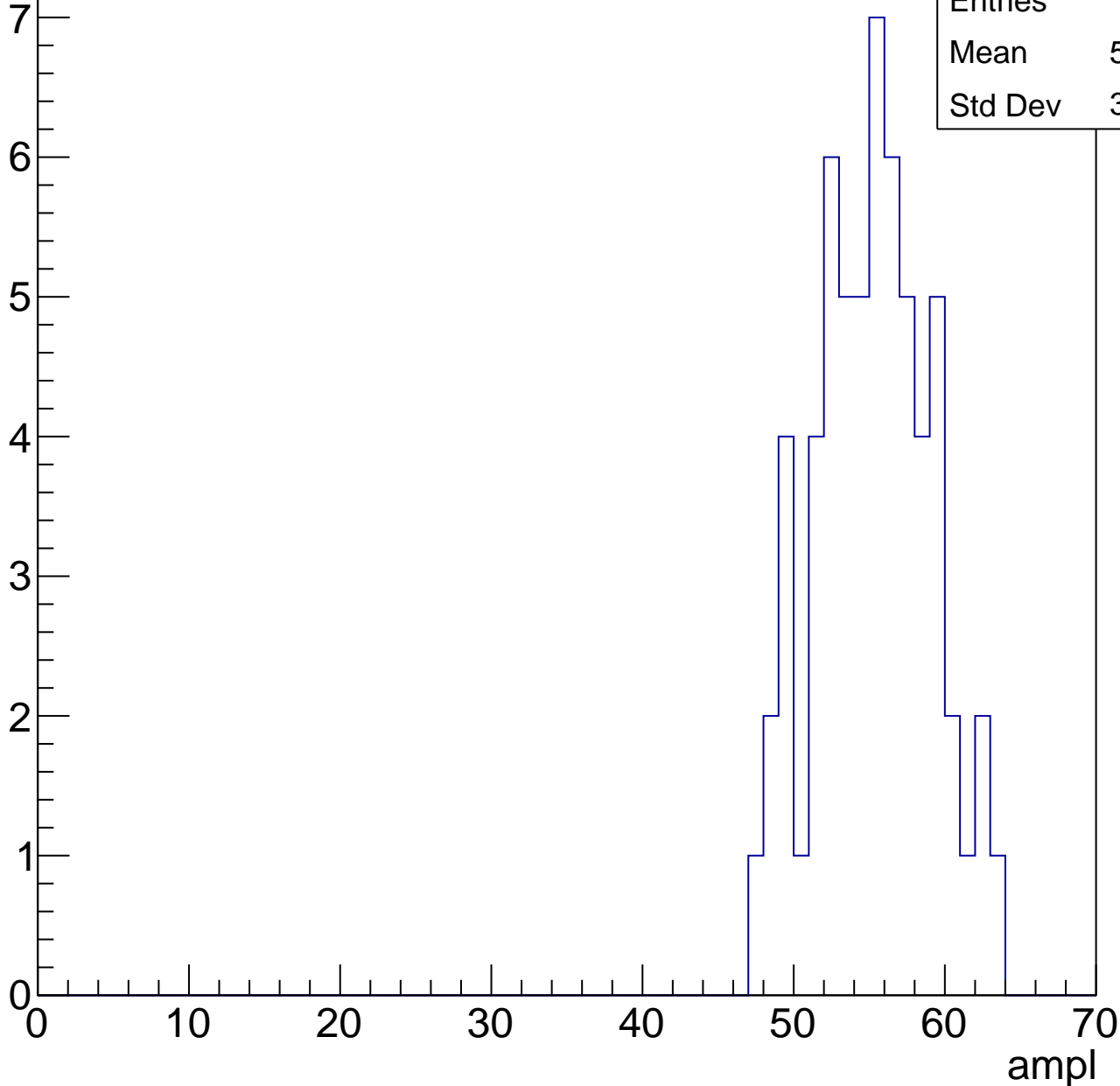


B1L102S, U26-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.77
Std Dev	3.756

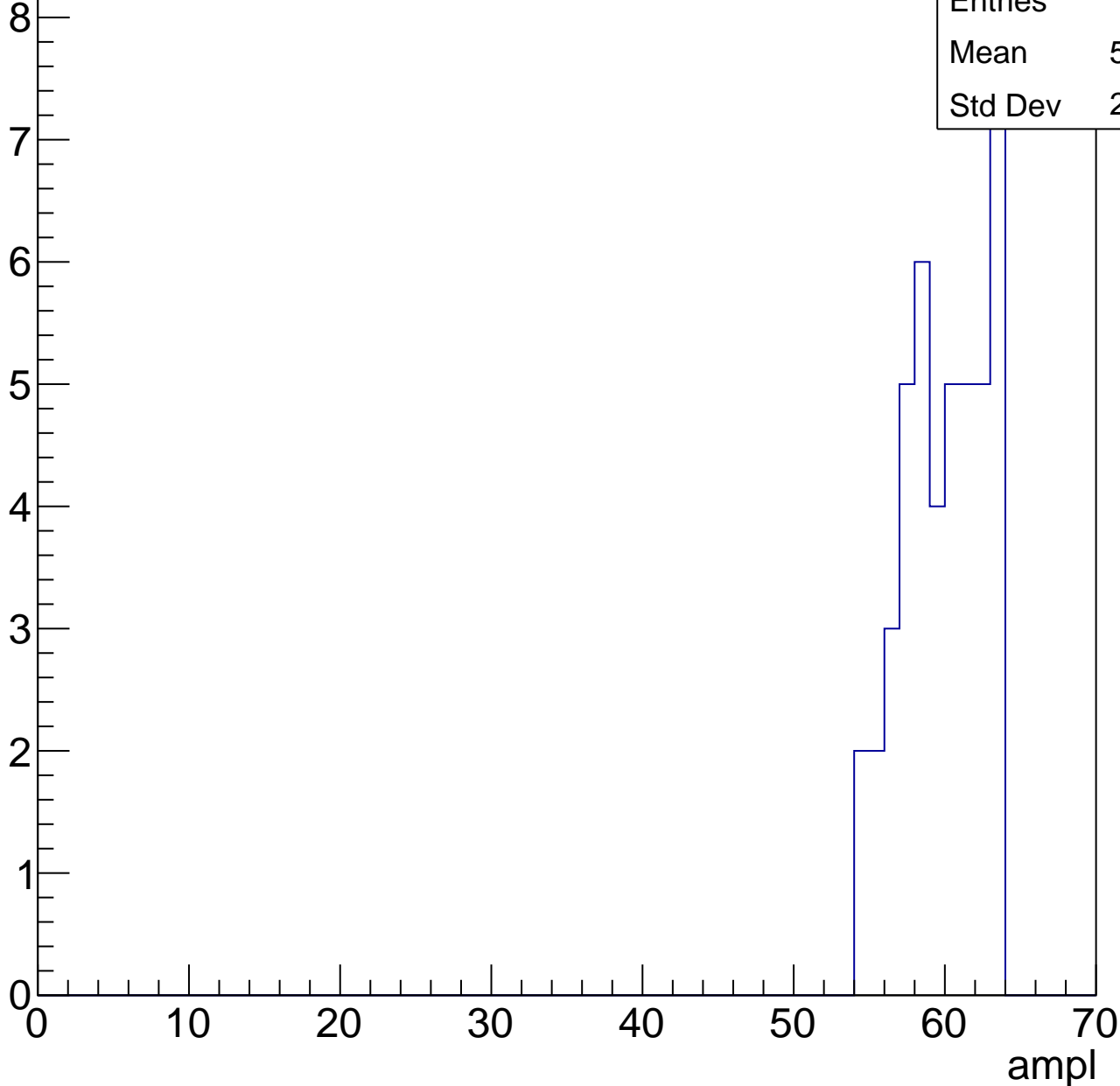


B1L102S, U26-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

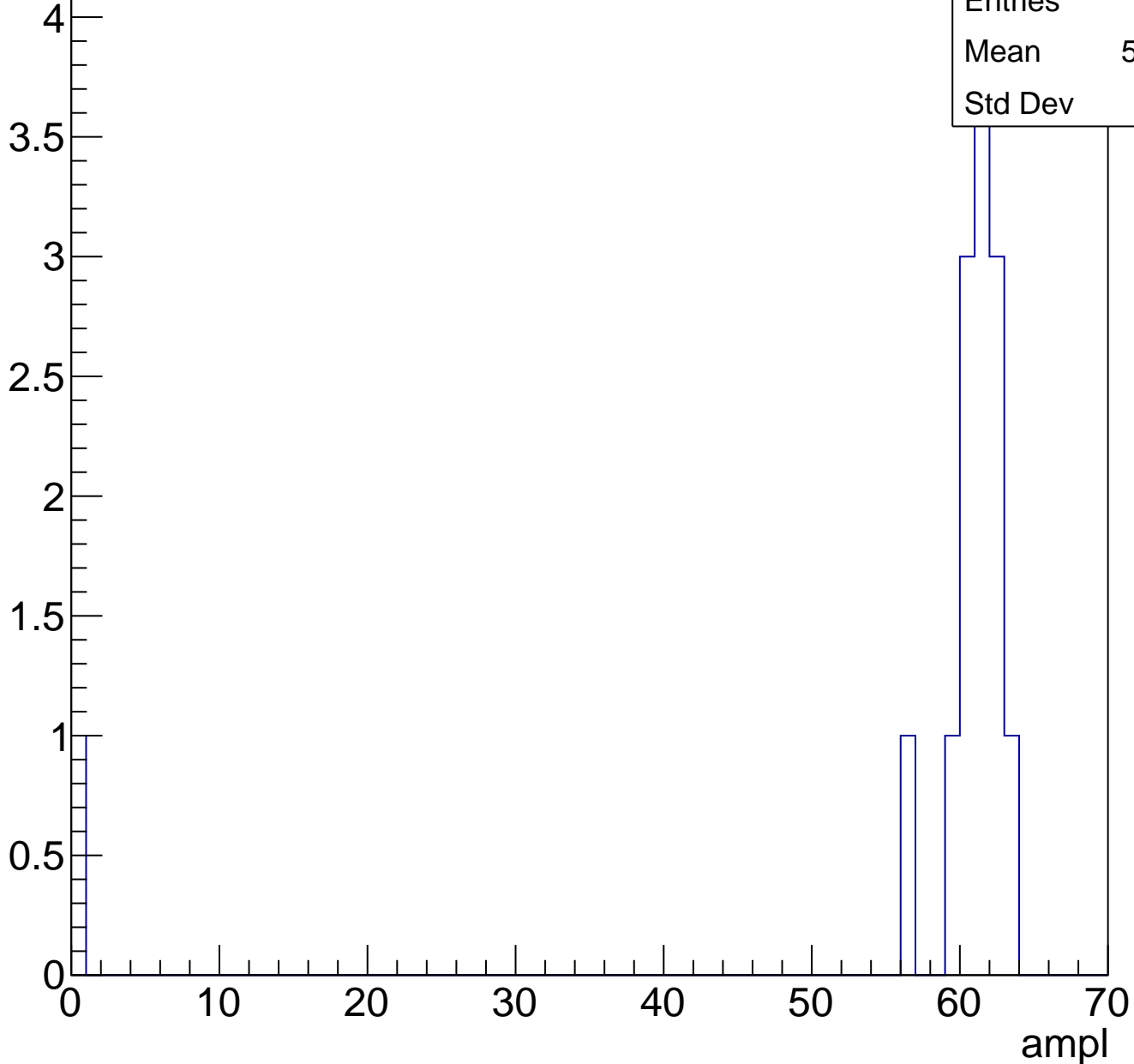
Entries	45
Mean	59.42
Std Dev	2.687



B1L102S, U26-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	14
Mean	56.29
Std Dev	15.7

B1L102S, U26-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch109, adc0

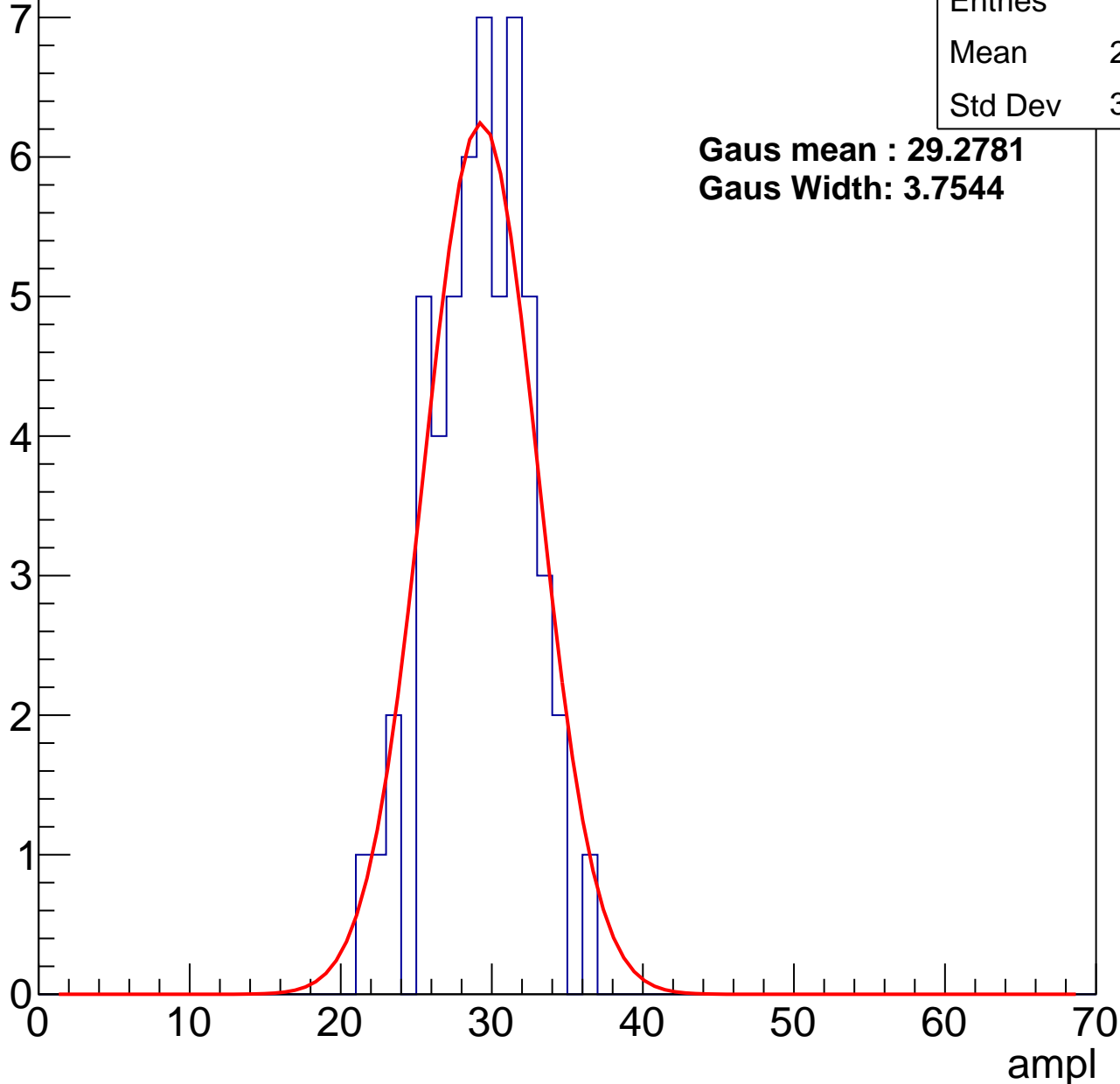
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	28.78
Std Dev	3.189

Gaus mean : 29.2781

Gaus Width: 3.7544



B1L102S, U26-ch109, adc1

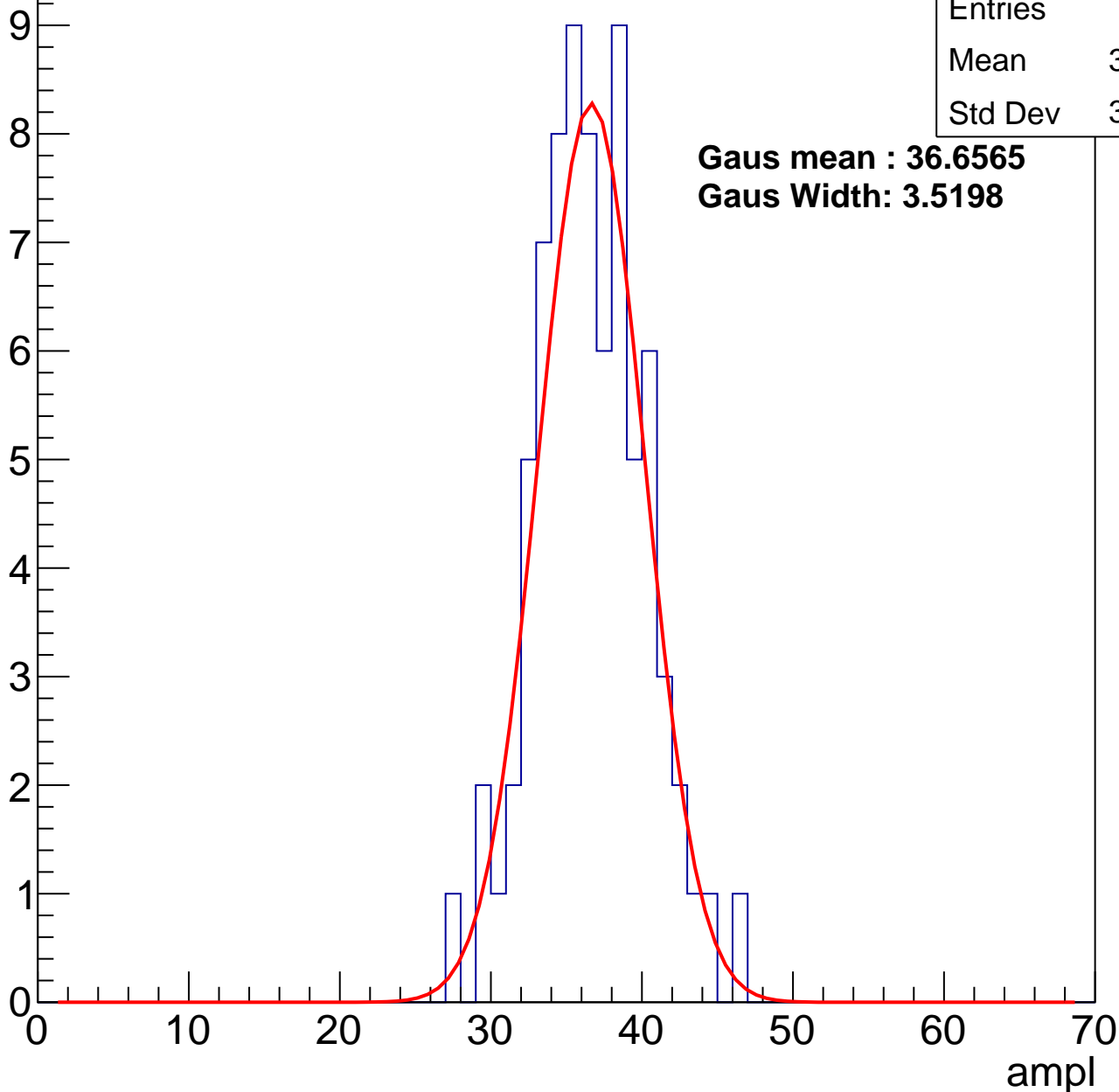
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.13
Std Dev	3.587

Gaus mean : 36.6565

Gaus Width: 3.5198



B1L102S, U26-ch109, adc2

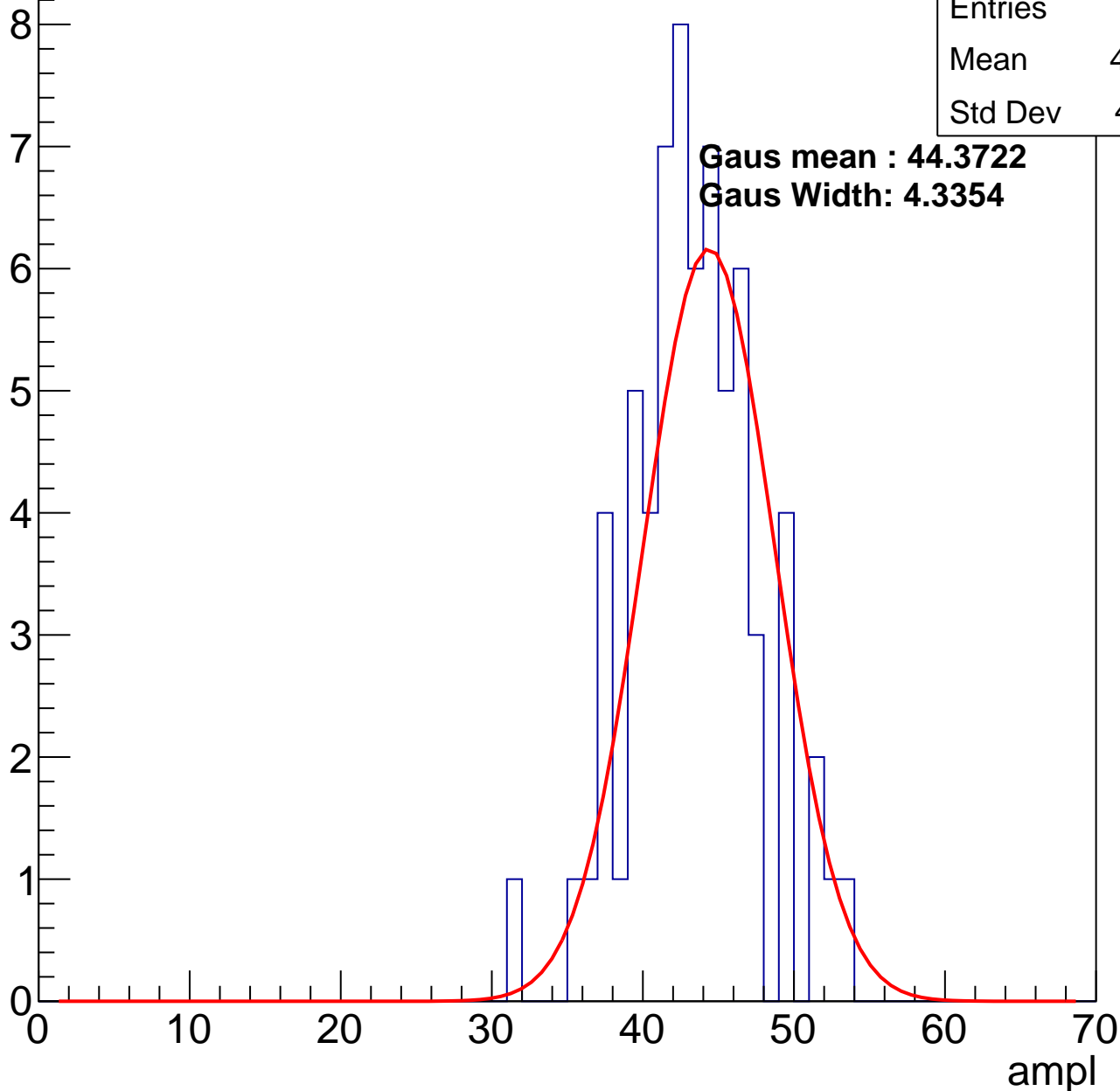
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.94
Std Dev	4.161

Gaus mean : 44.3722

Gaus Width: 4.3354

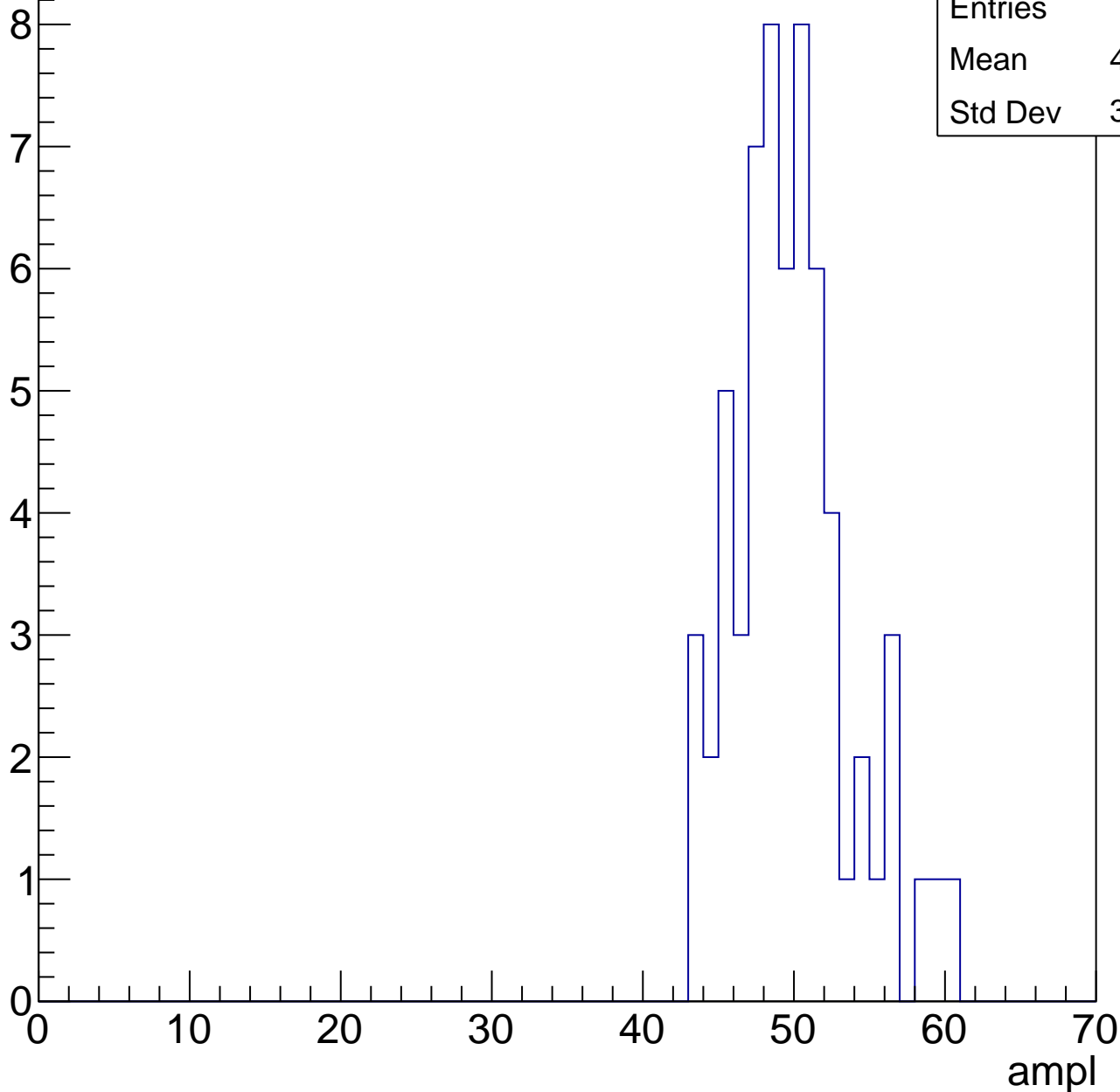


B1L102S, U26-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.39
Std Dev	3.833

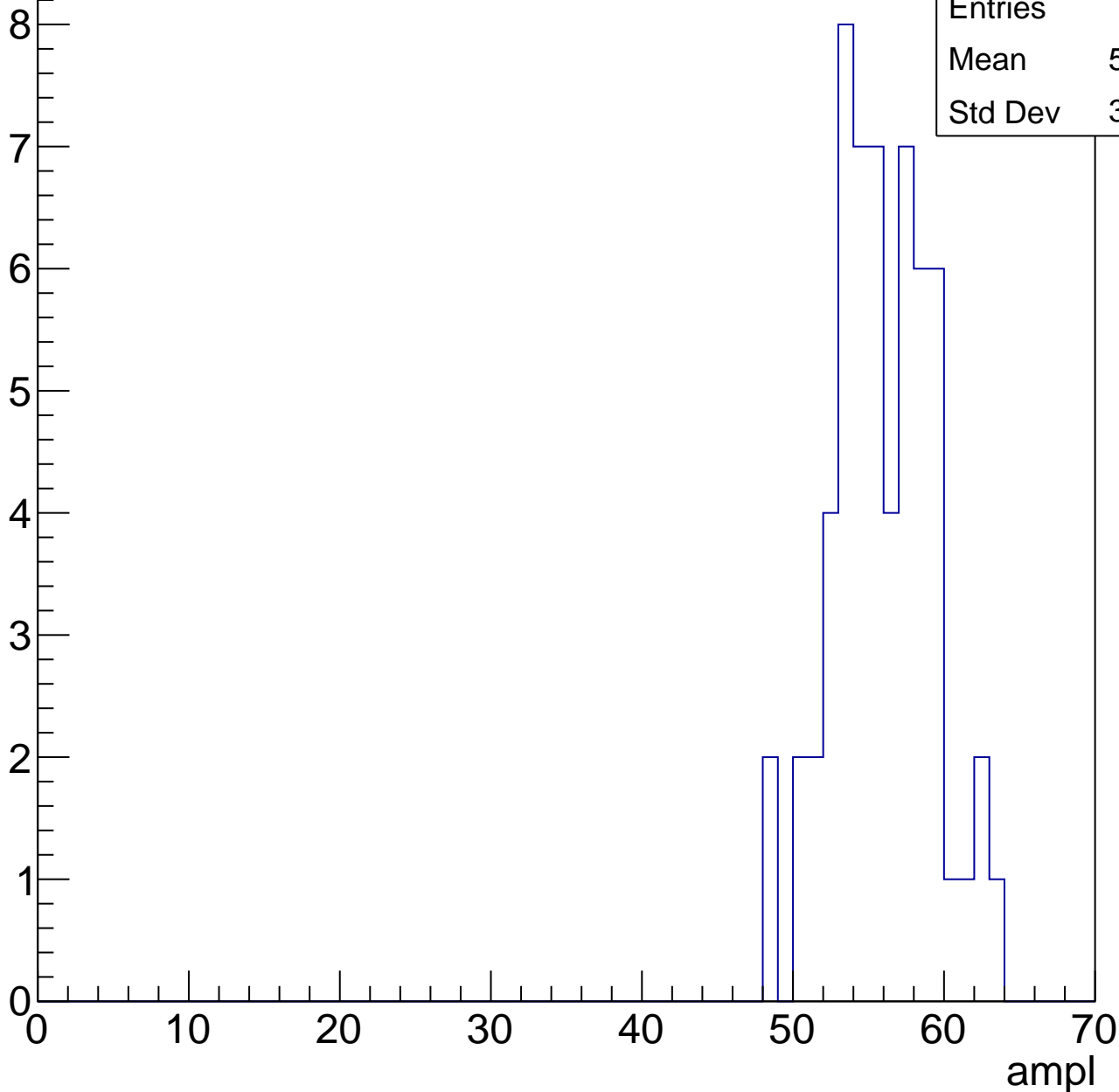


B1L102S, U26-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.43
Std Dev	3.288

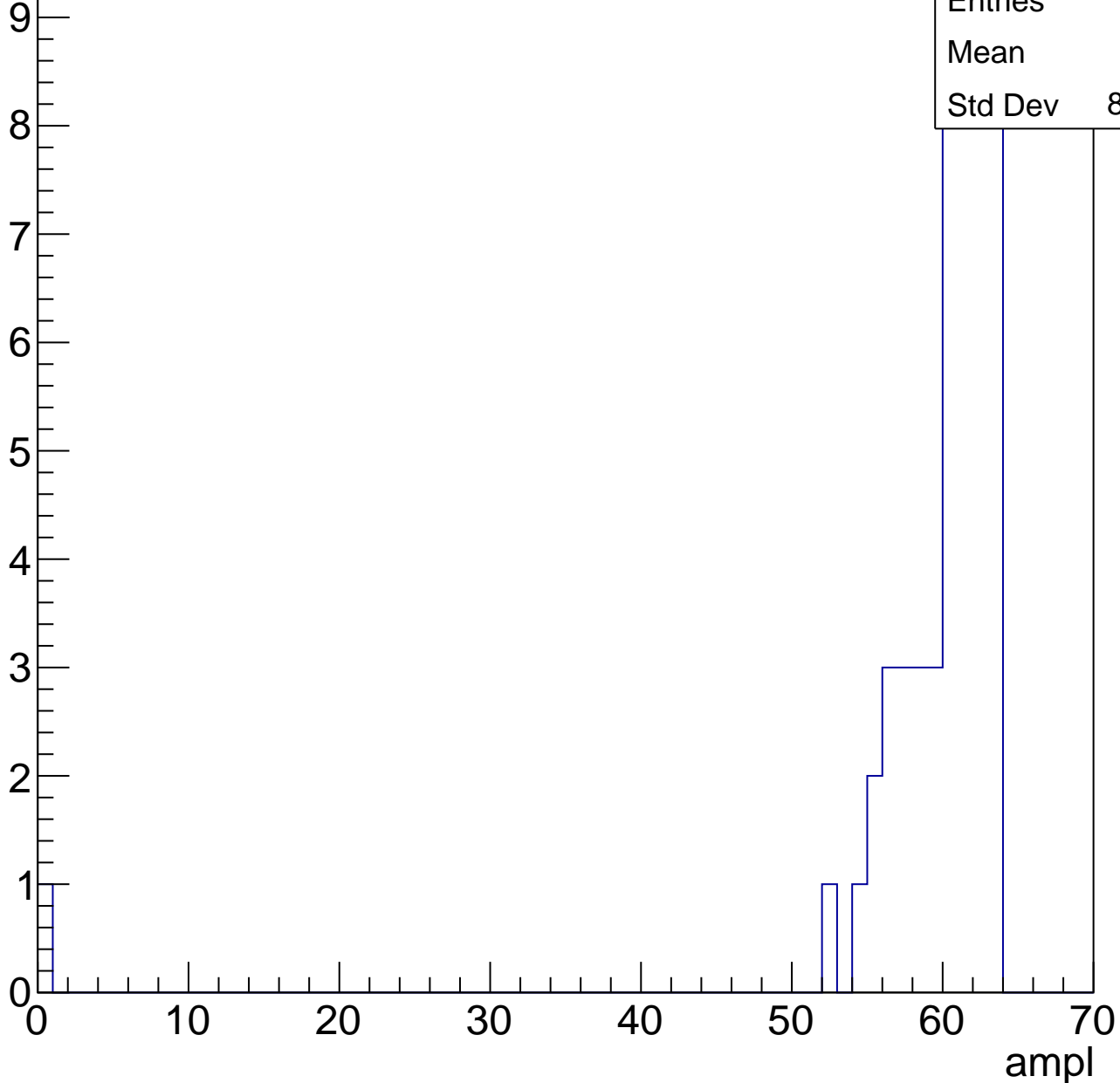


B1L102S, U26-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.7
Std Dev	8.796



B1L102S, U26-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L102S, U26-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch110, adc0

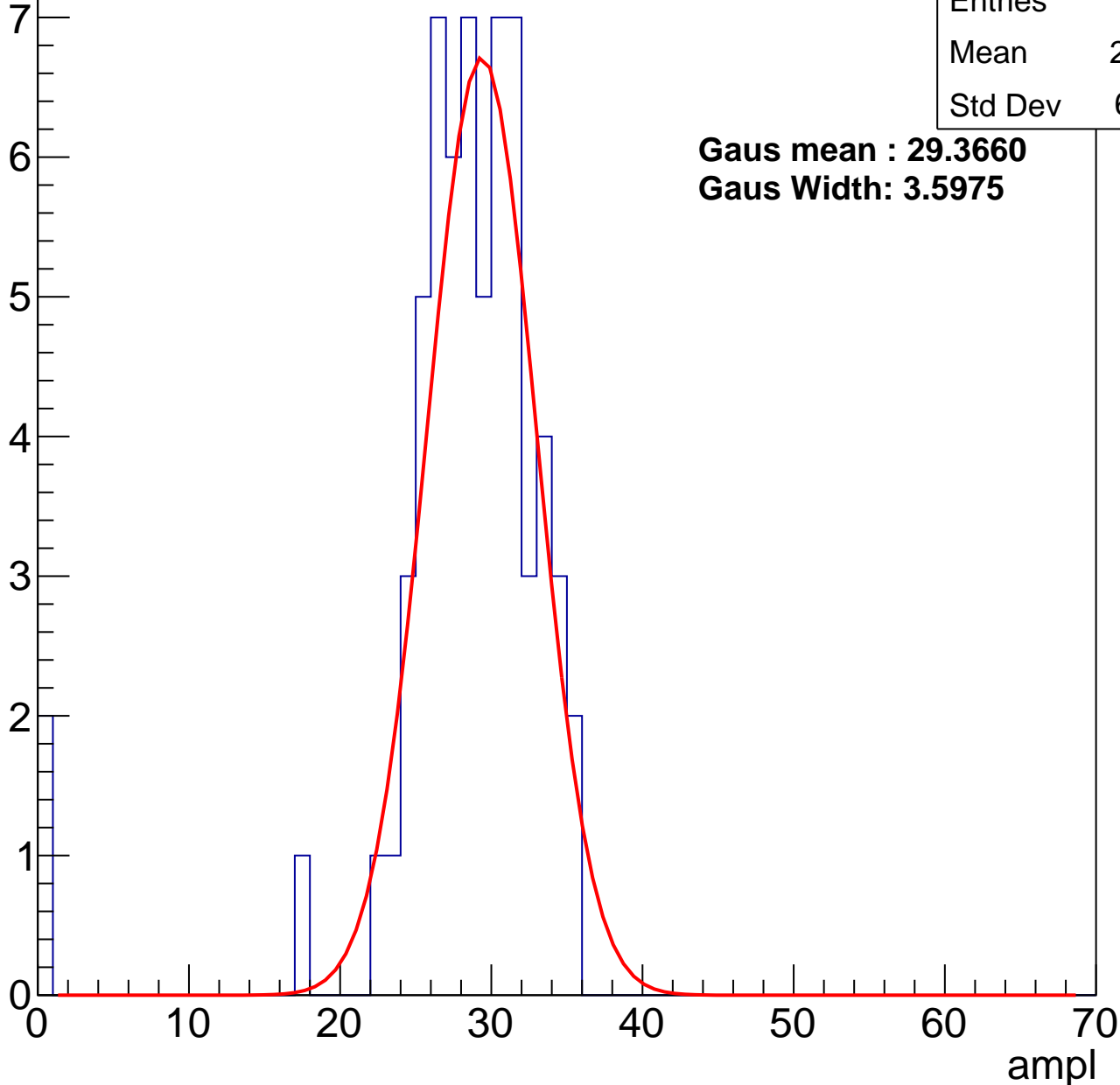
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.67
Std Dev	6.021

Gaus mean : 29.3660

Gaus Width: 3.5975



B1L102S, U26-ch110, adc1

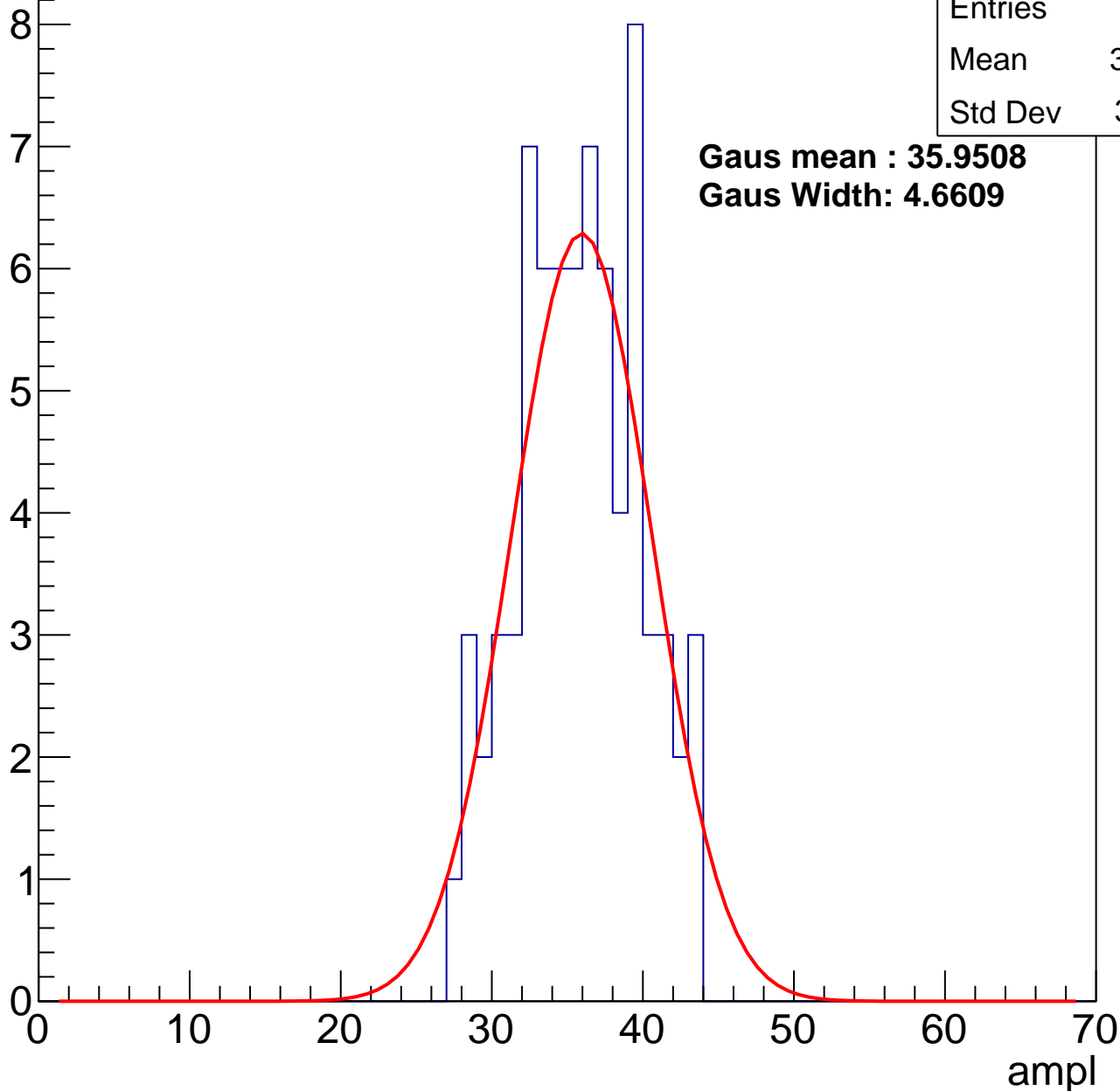
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.37
Std Dev	3.981

Gaus mean : 35.9508

Gaus Width: 4.6609



B1L102S, U26-ch110, adc2

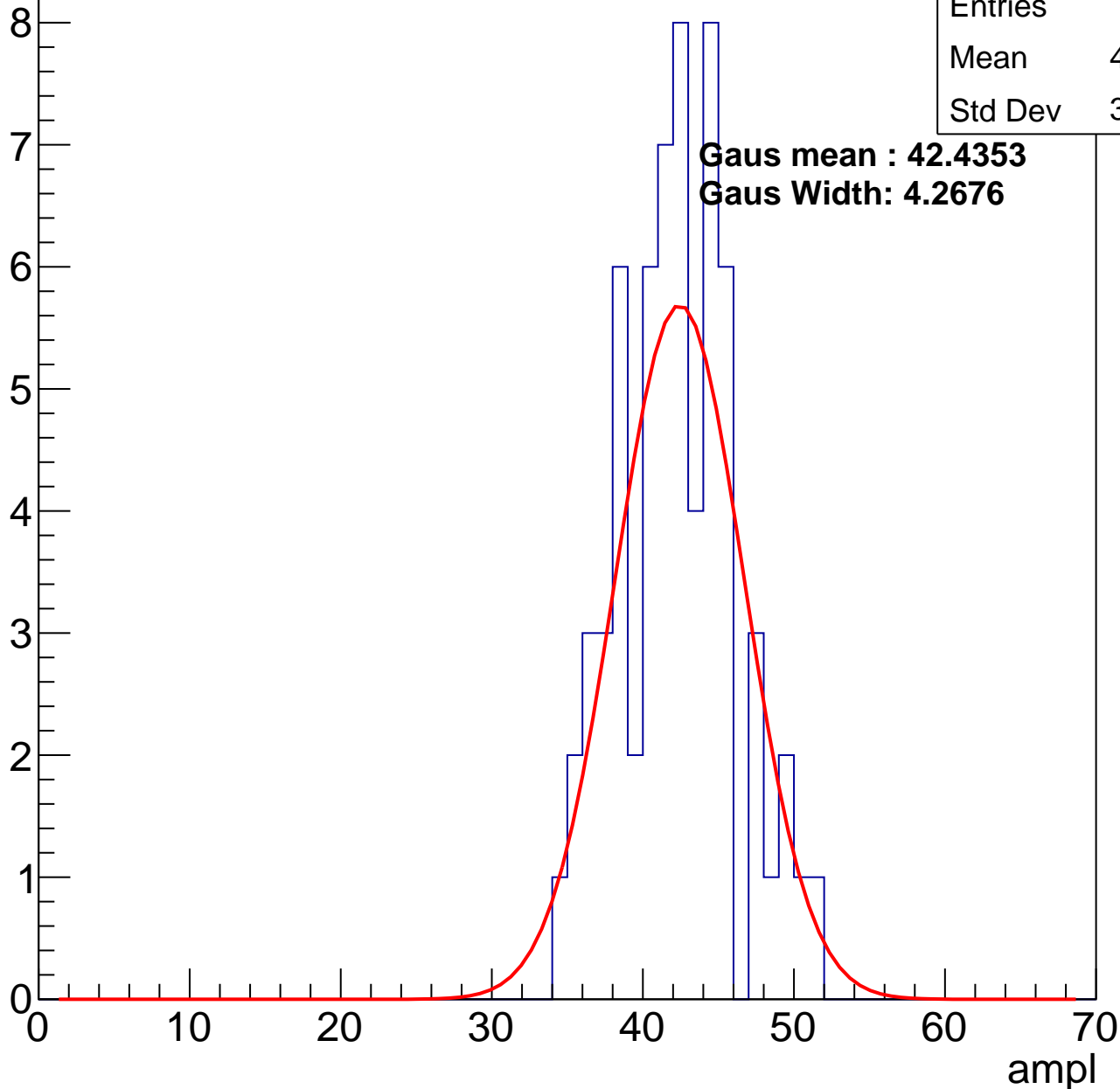
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.78
Std Dev	3.814

Gaus mean : 42.4353

Gaus Width: 4.2676

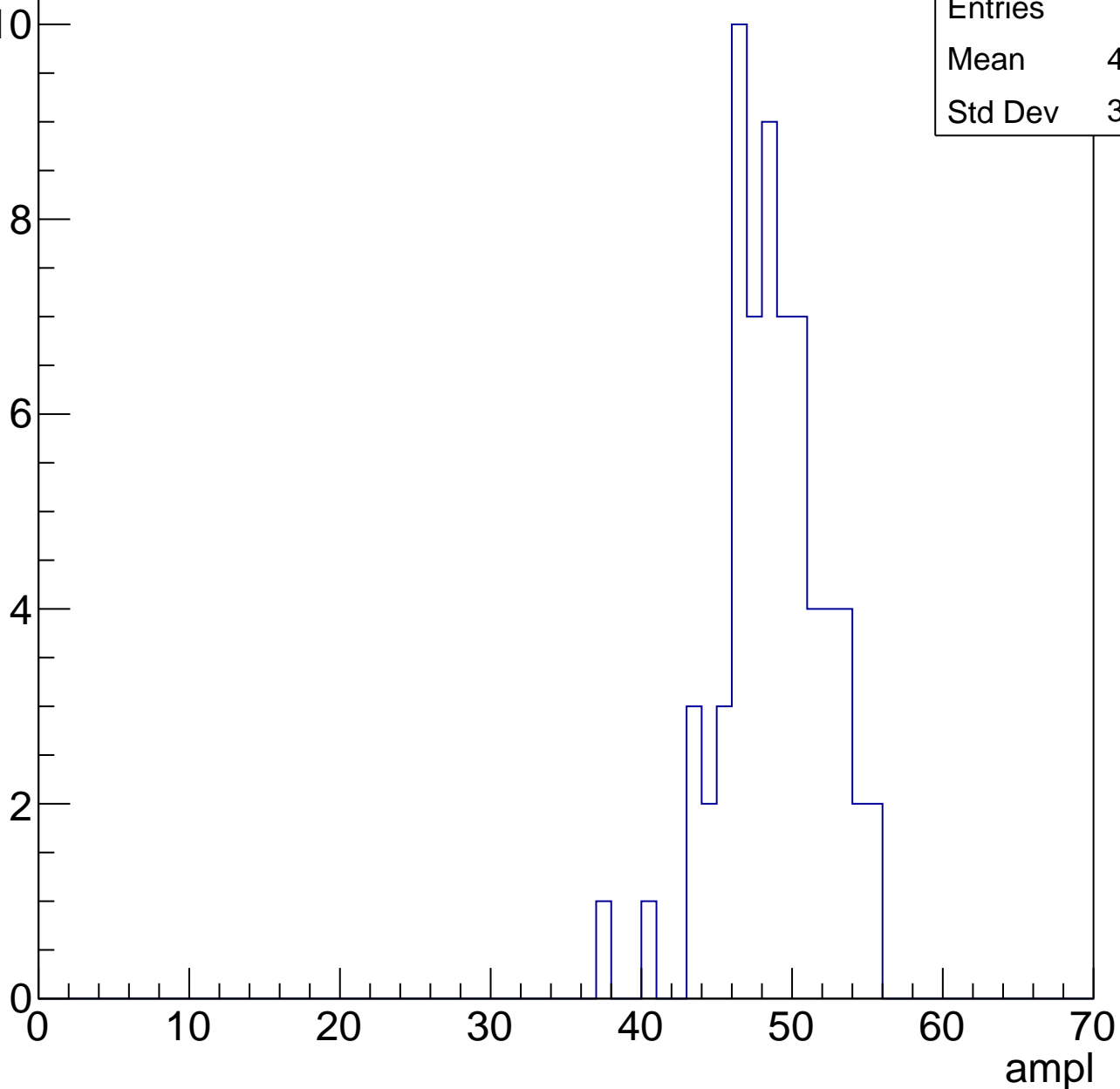


B1L102S, U26-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.26
Std Dev	3.408

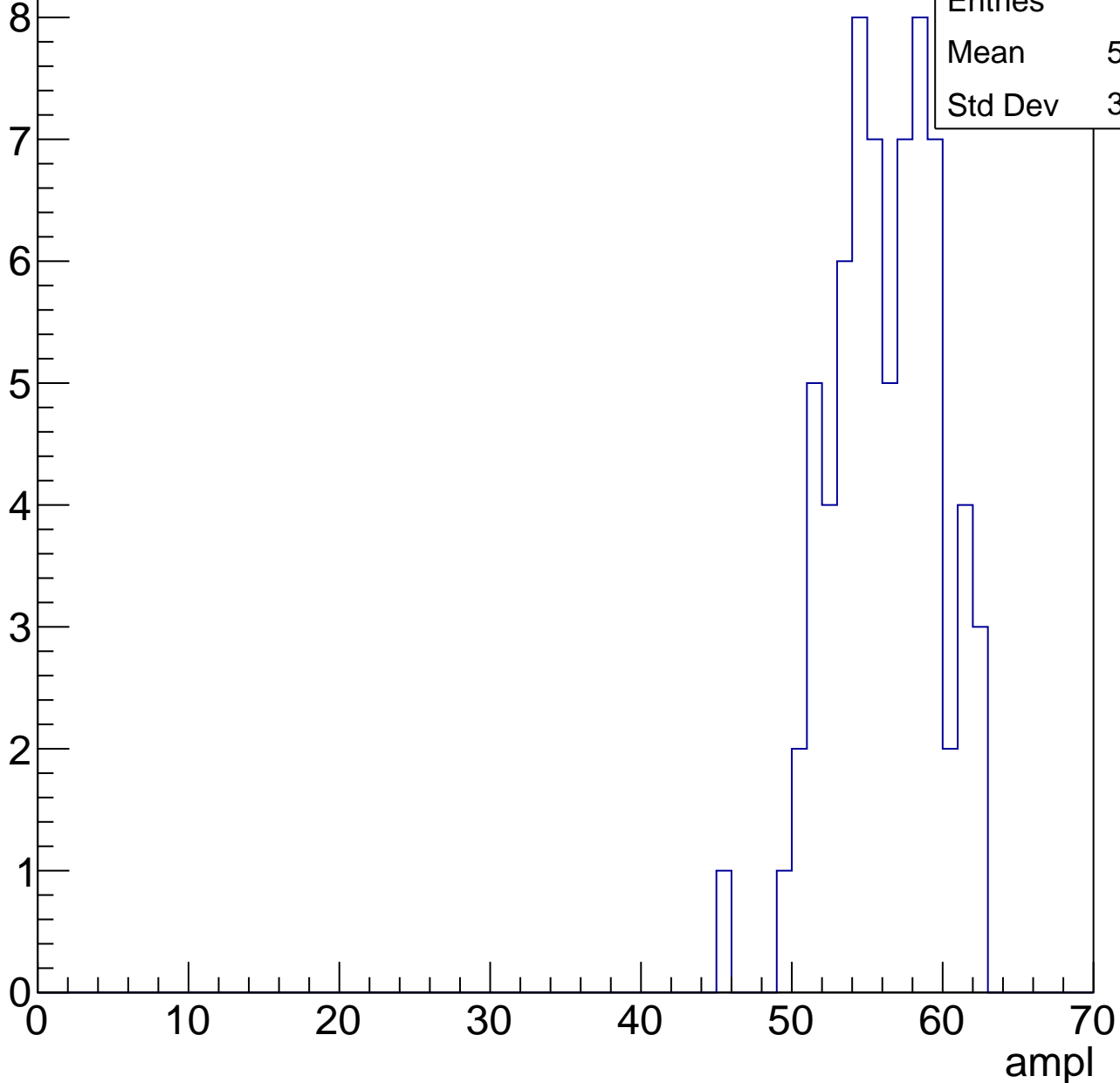


B1L102S, U26-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.69
Std Dev	3.503

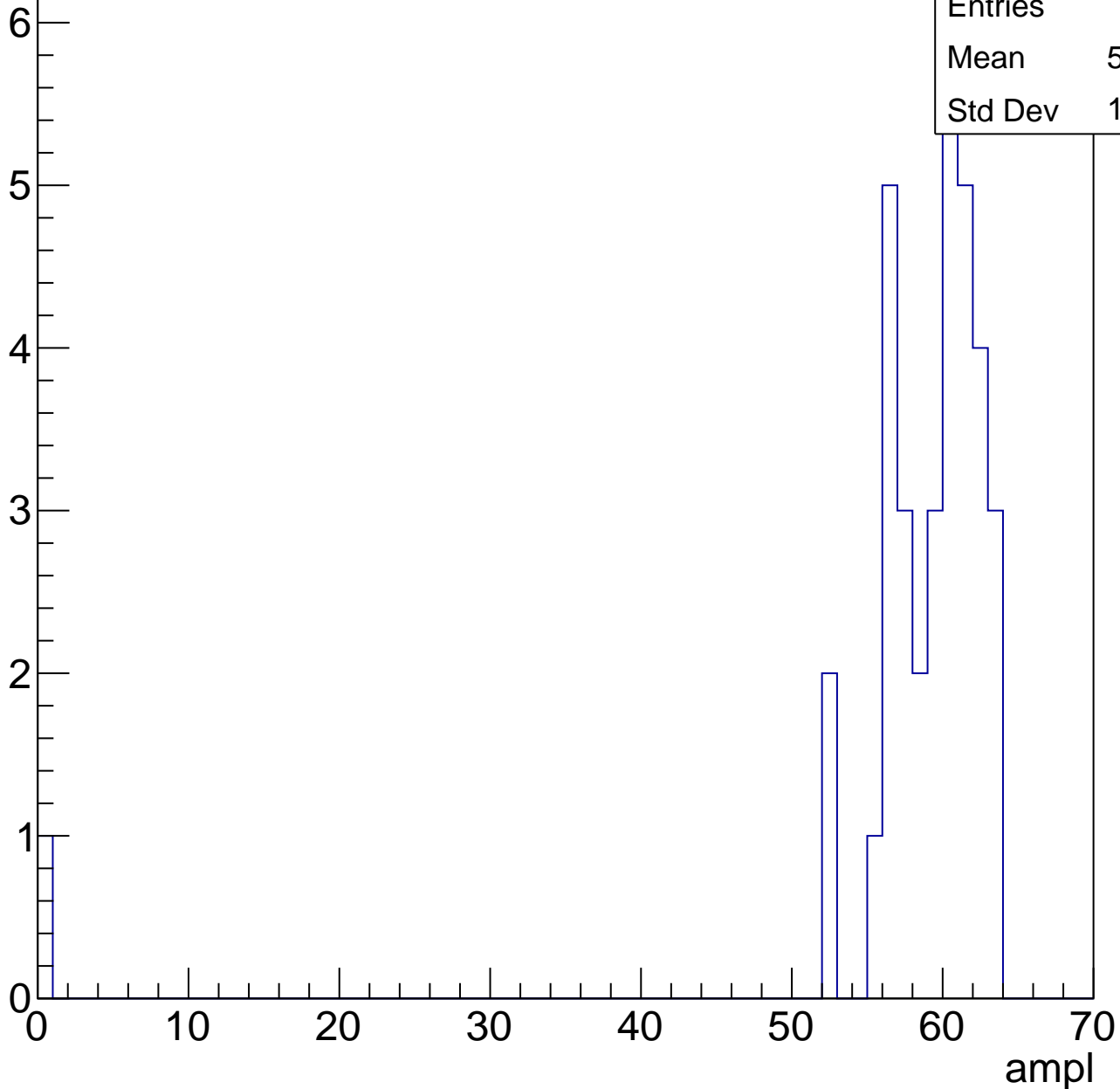


B1L102S, U26-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	57.29
Std Dev	10.23

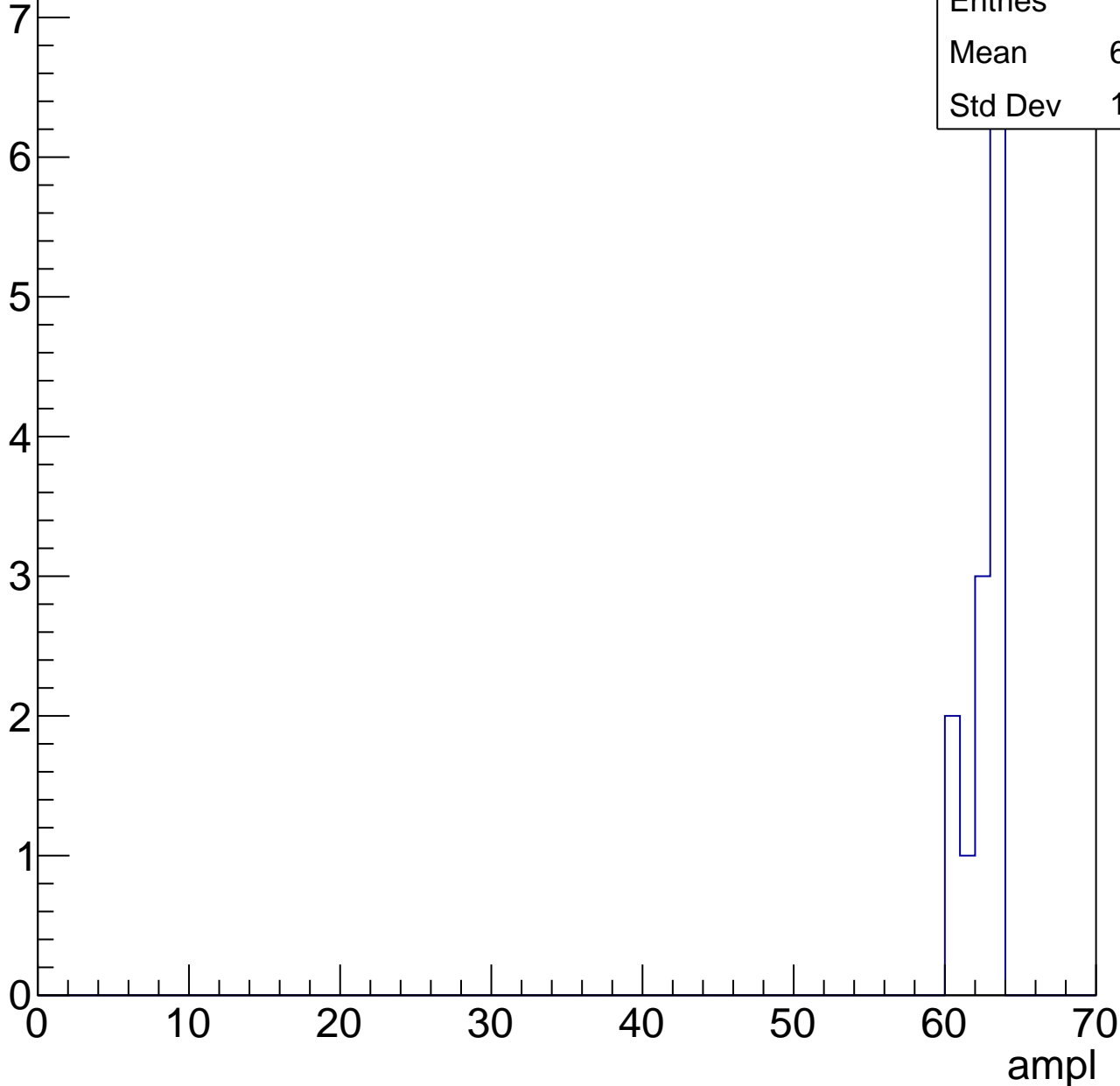


B1L102S, U26-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	62.15
Std Dev	1.099



B1L102S, U26-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch111, adc0

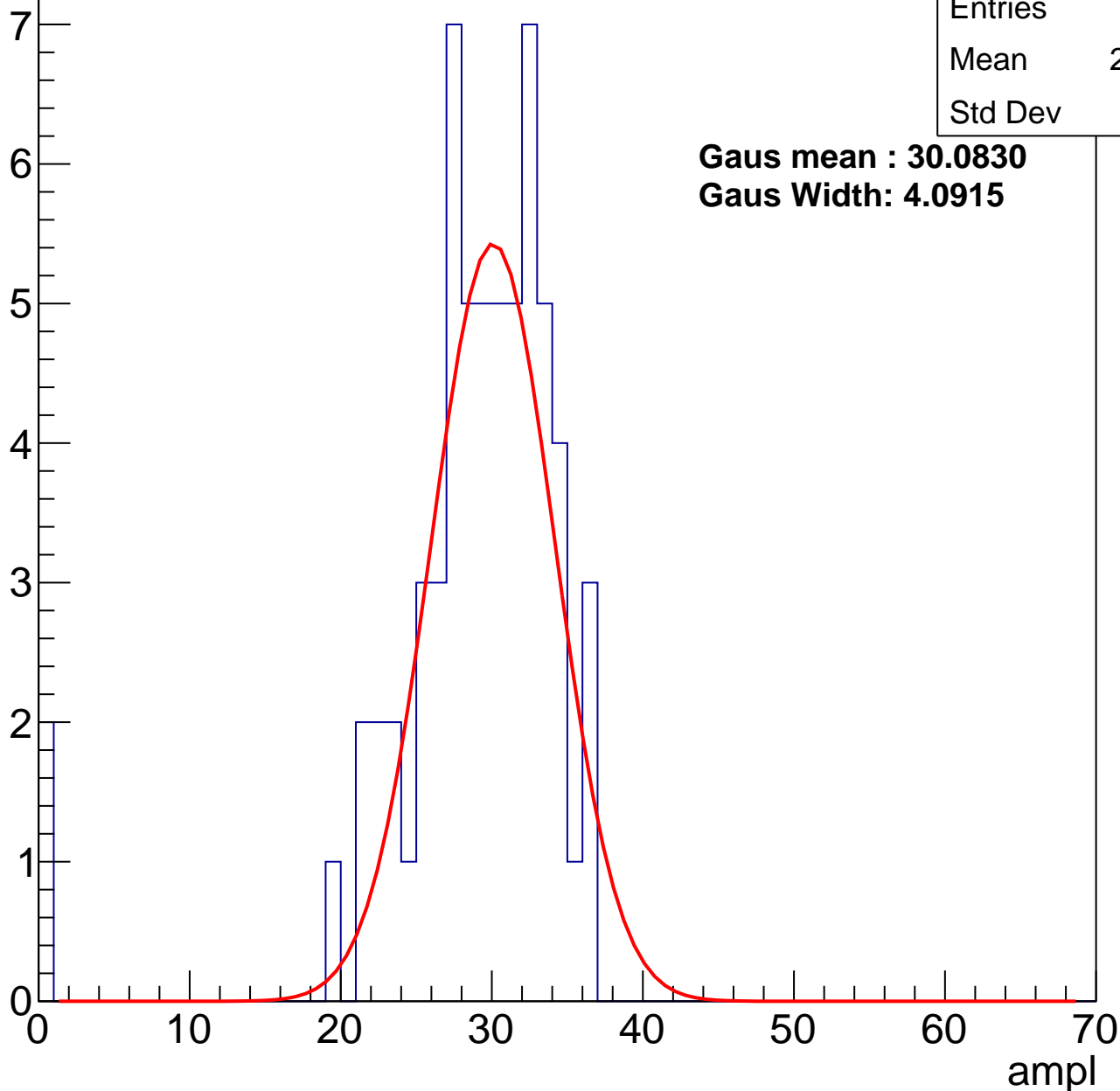
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.17
Std Dev	6.45

Gaus mean : 30.0830

Gaus Width: 4.0915



B1L102S, U26-ch111, adc1

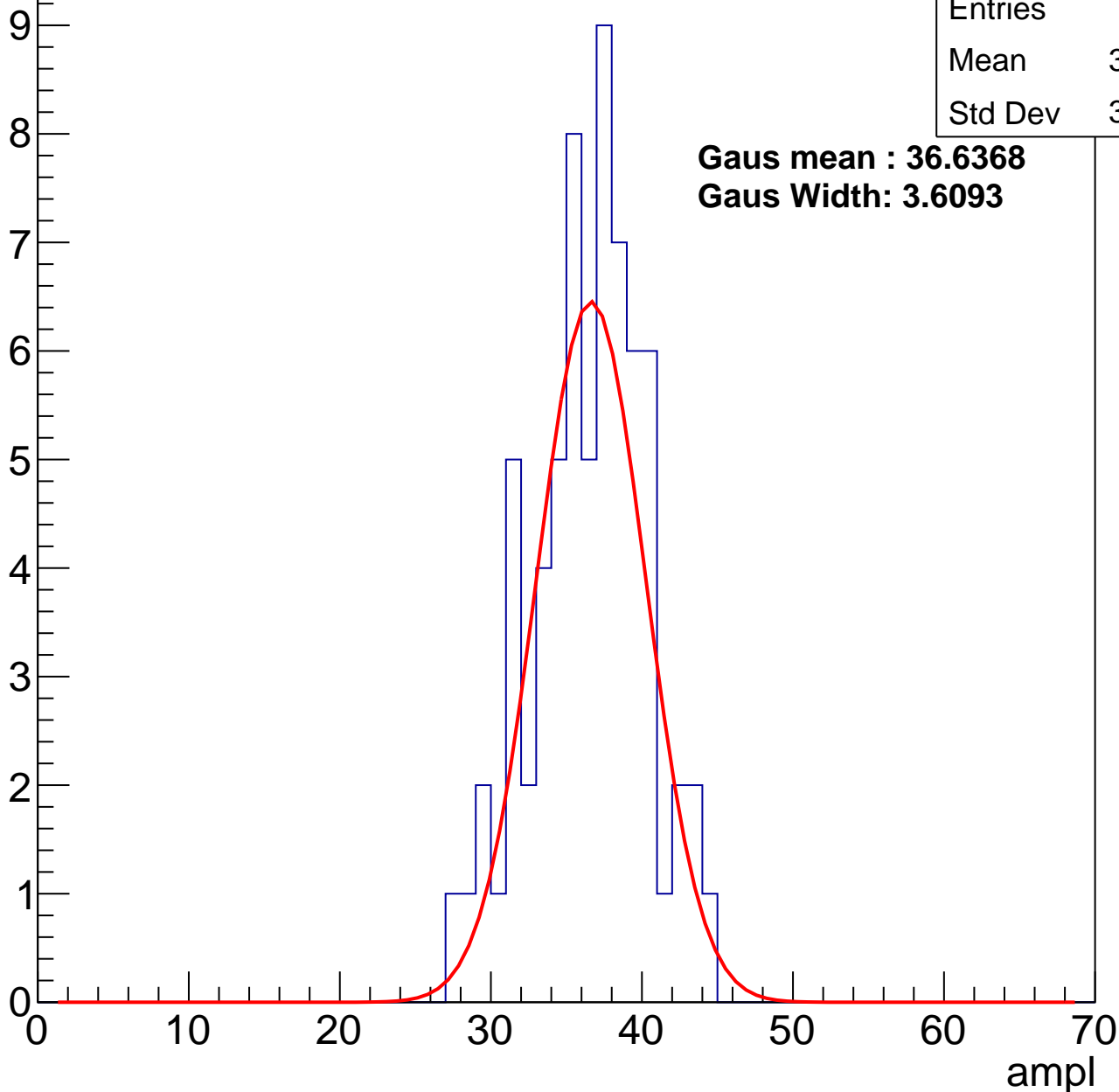
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.06
Std Dev	3.729

Gaus mean : 36.6368

Gaus Width: 3.6093



B1L102S, U26-ch111, adc2

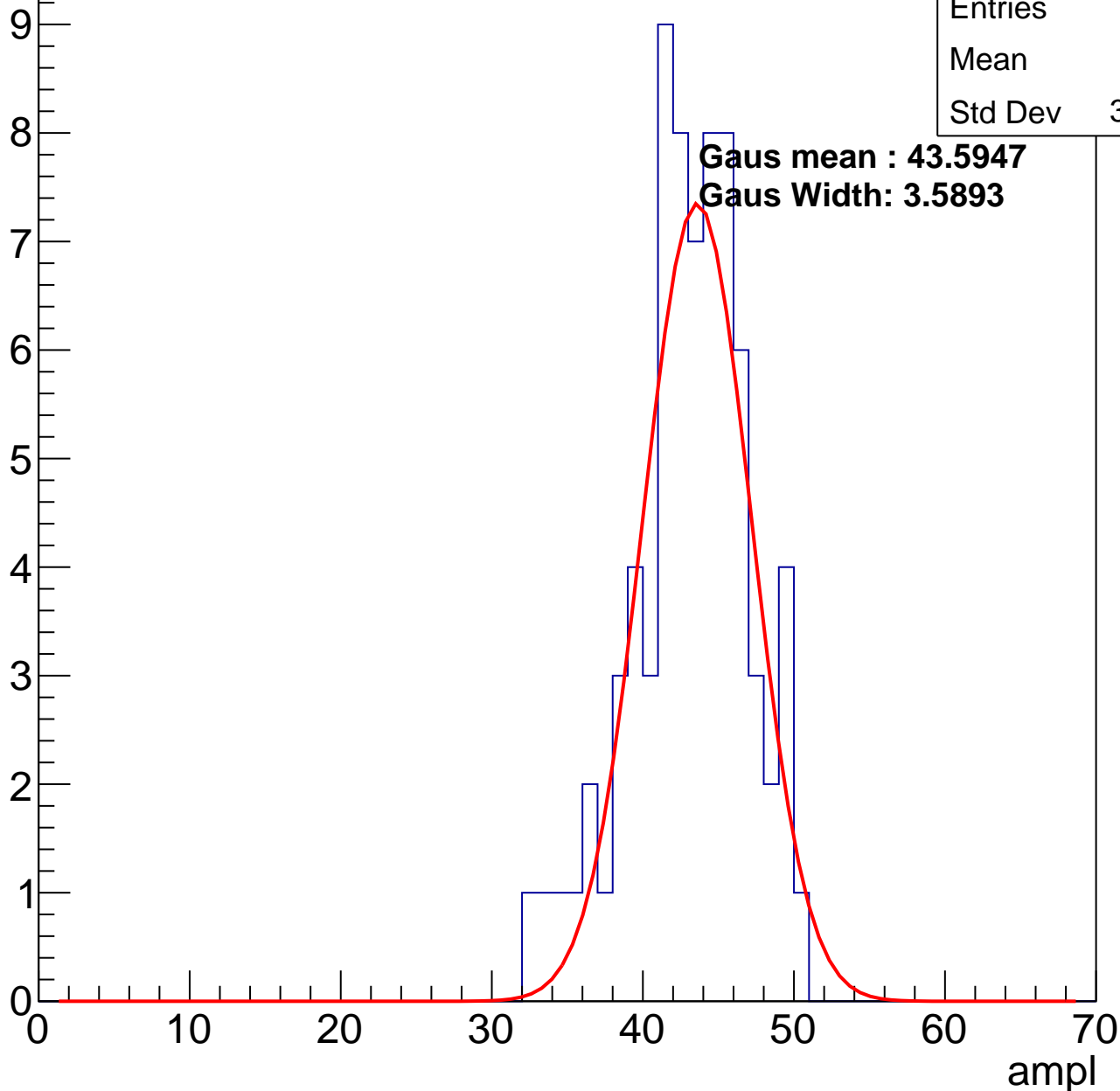
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.6
Std Dev	3.853

Gaus mean : 43.5947

Gaus Width: 3.5893

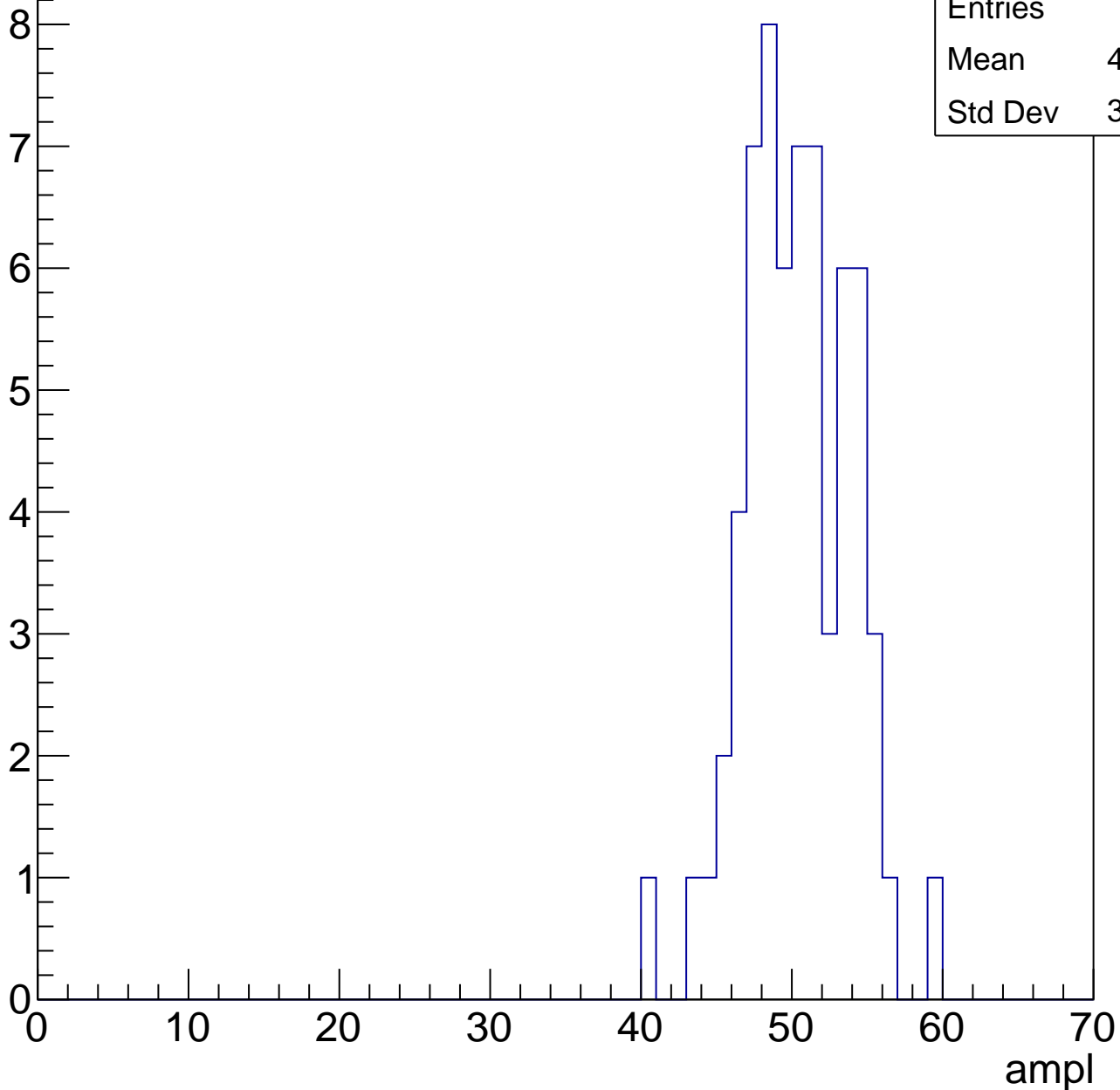


B1L102S, U26-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.89
Std Dev	3.447

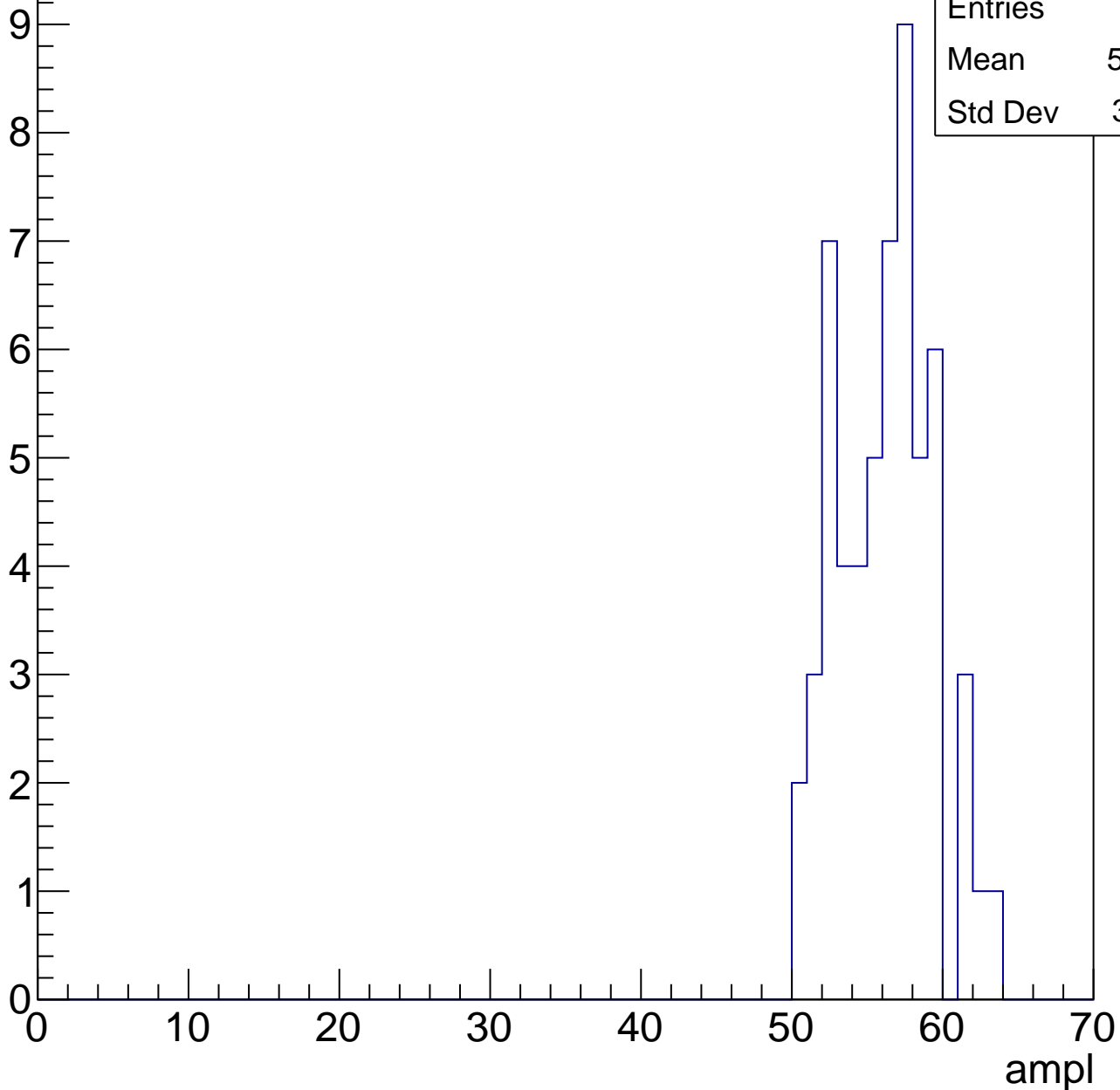


B1L102S, U26-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.74
Std Dev	3.121

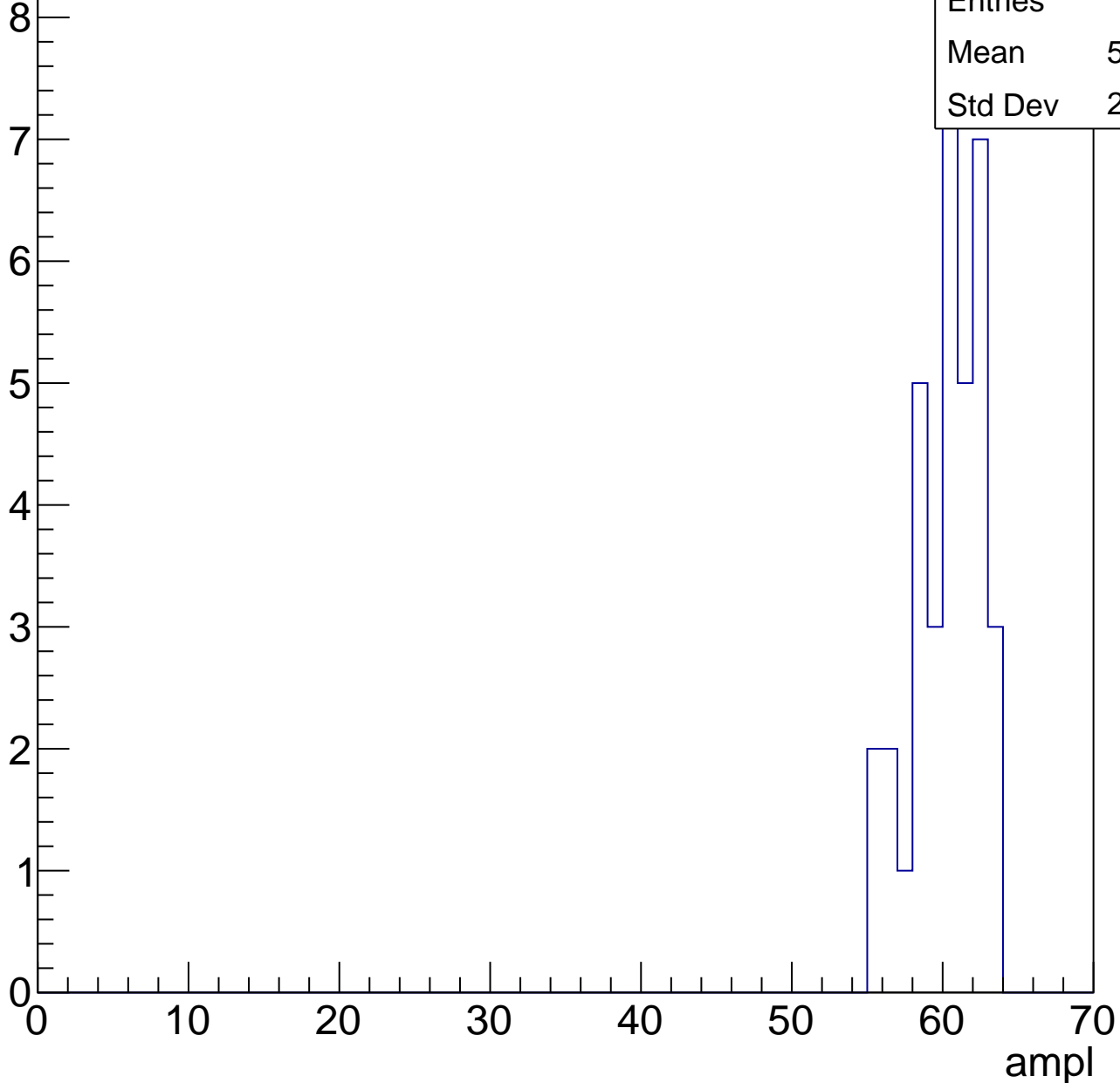


B1L102S, U26-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.83
Std Dev	2.192

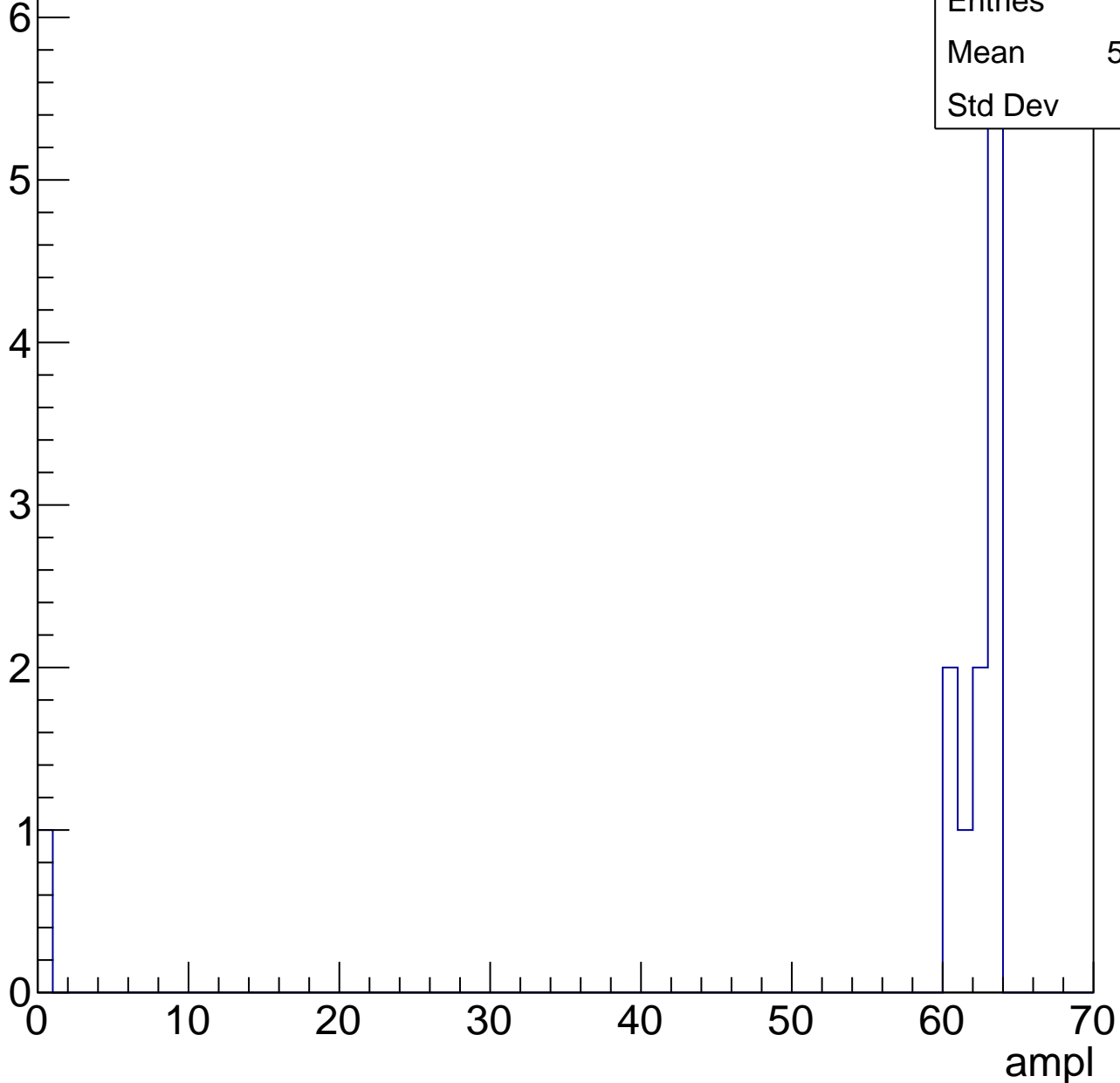


B1L102S, U26-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	56.92
Std Dev	17.2



B1L102S, U26-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L102S, U26-ch112, adc0

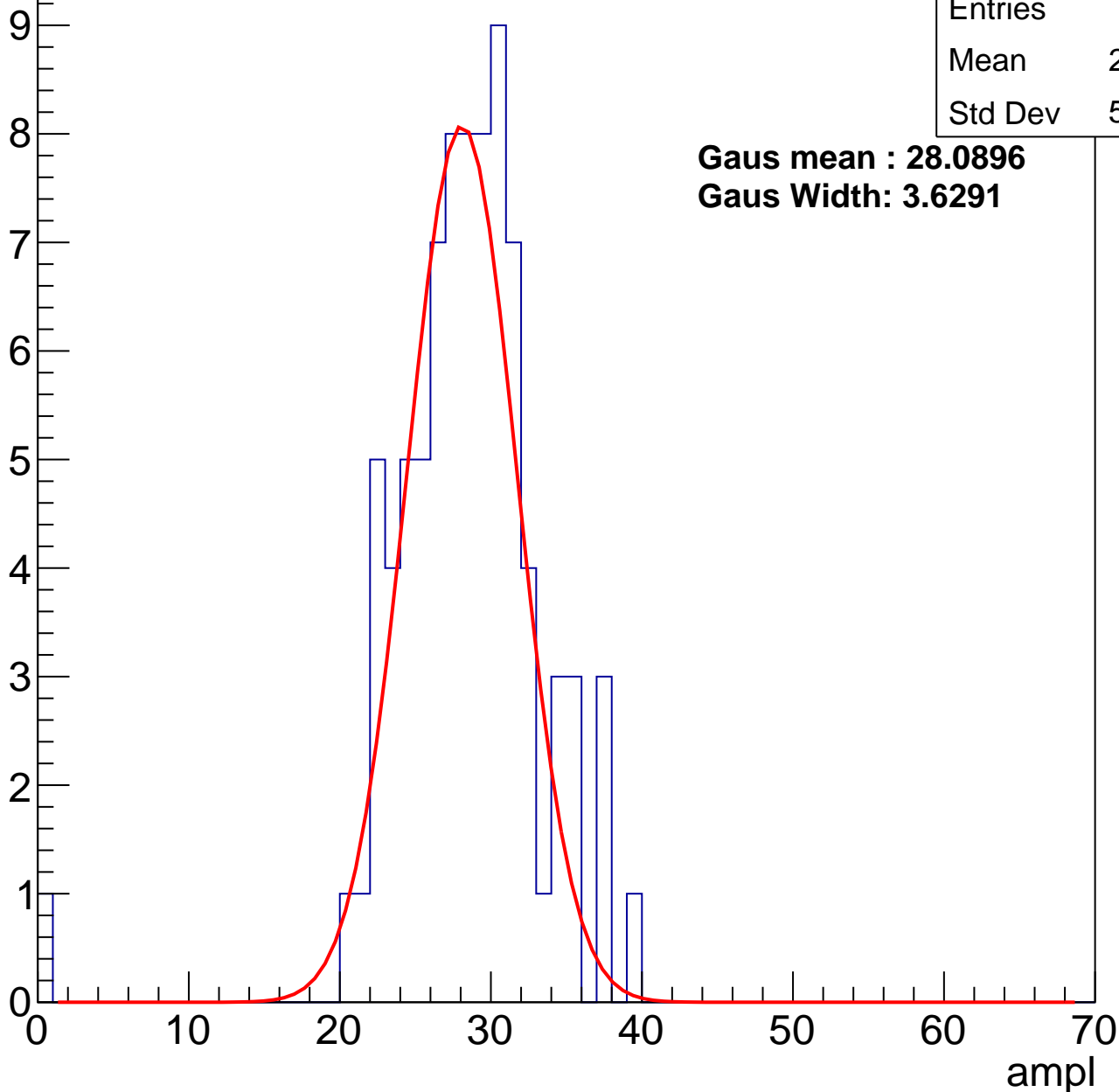
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.94
Std Dev	5.062

Gaus mean : 28.0896

Gaus Width: 3.6291



B1L102S, U26-ch112, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	36.57
Std Dev	3.714

Gaus mean : 36.5069

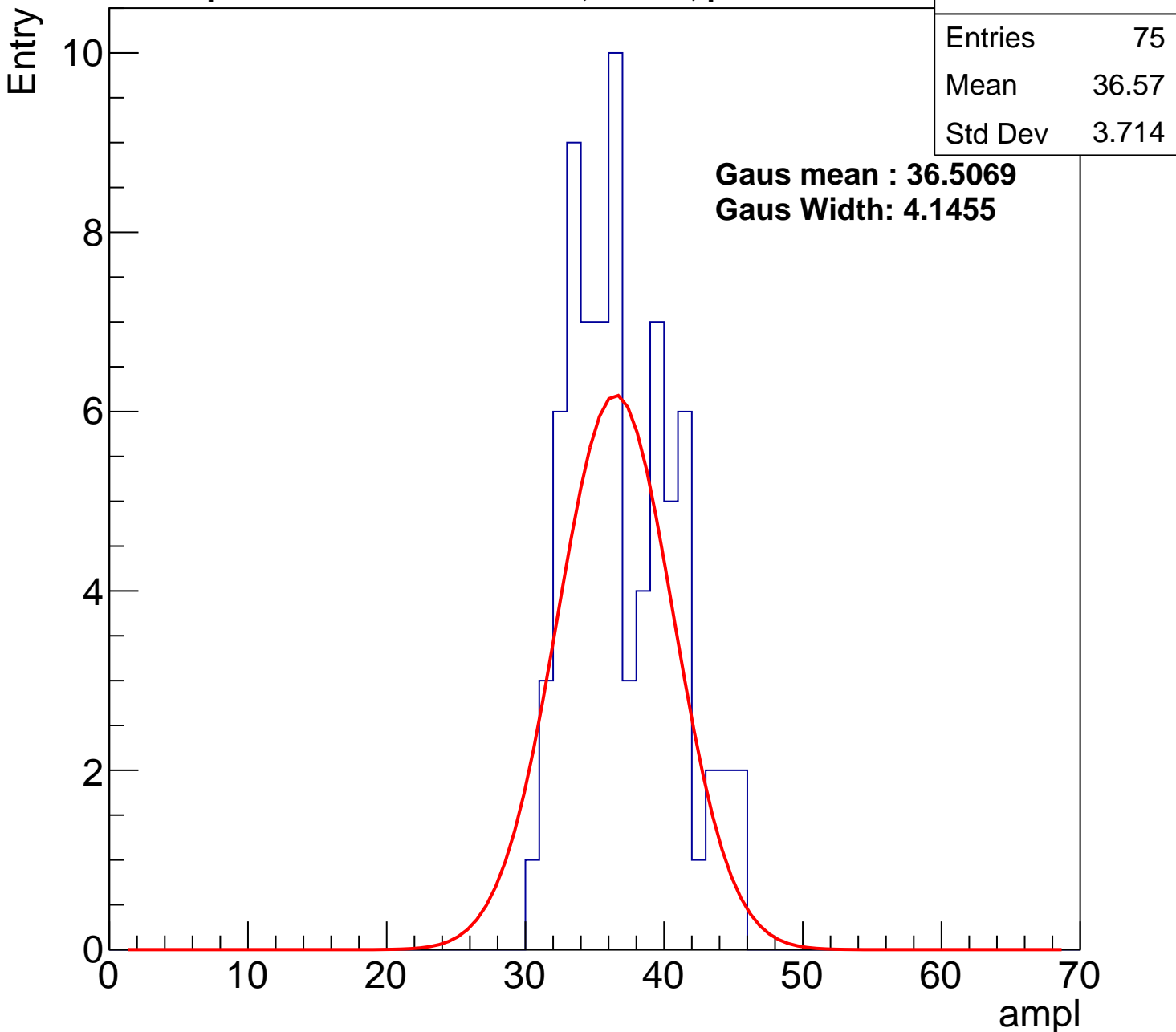
Gaus Width: 4.1455

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch112, adc2

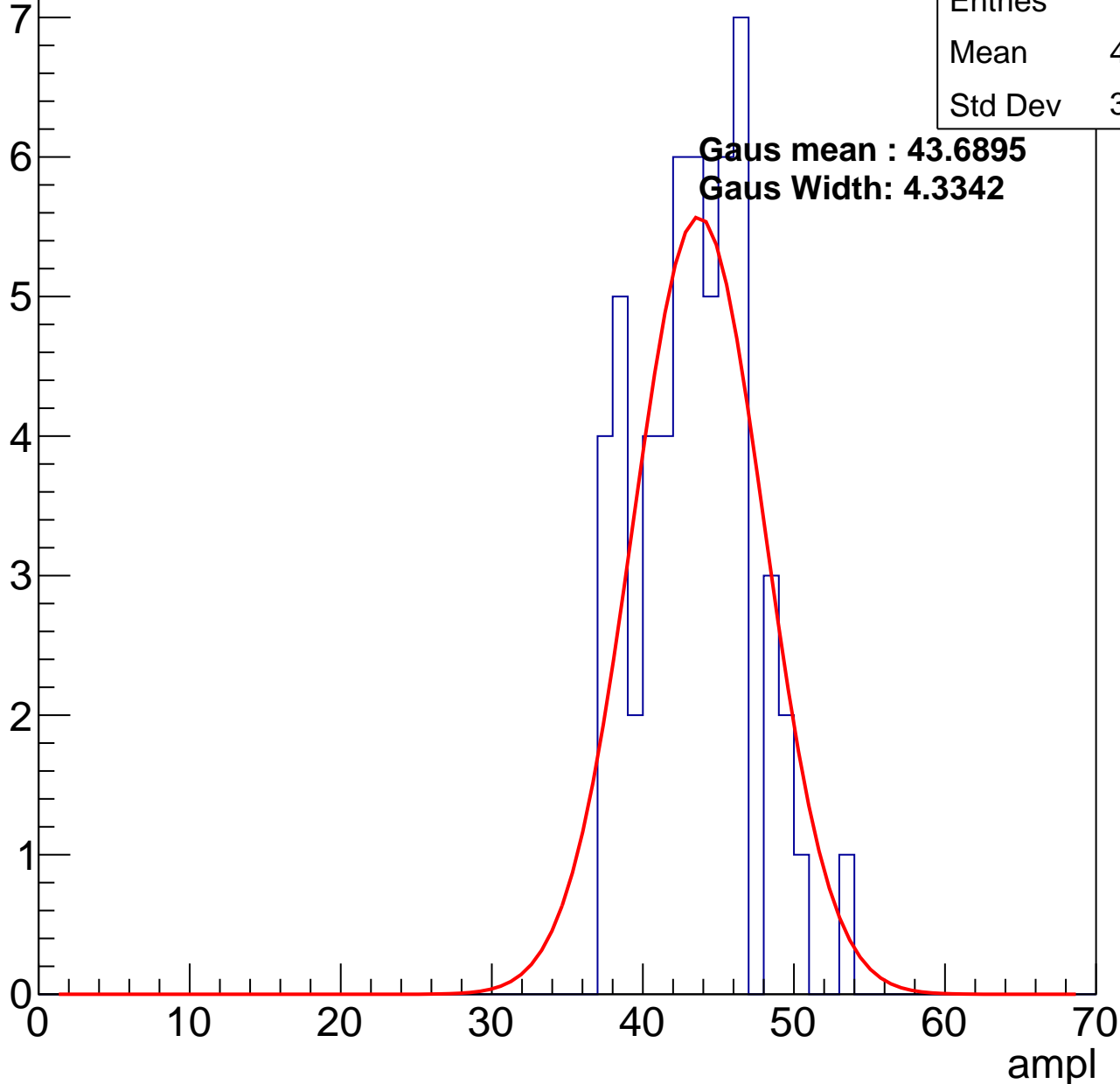
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.98
Std Dev	3.652

Gaus mean : 43.6895

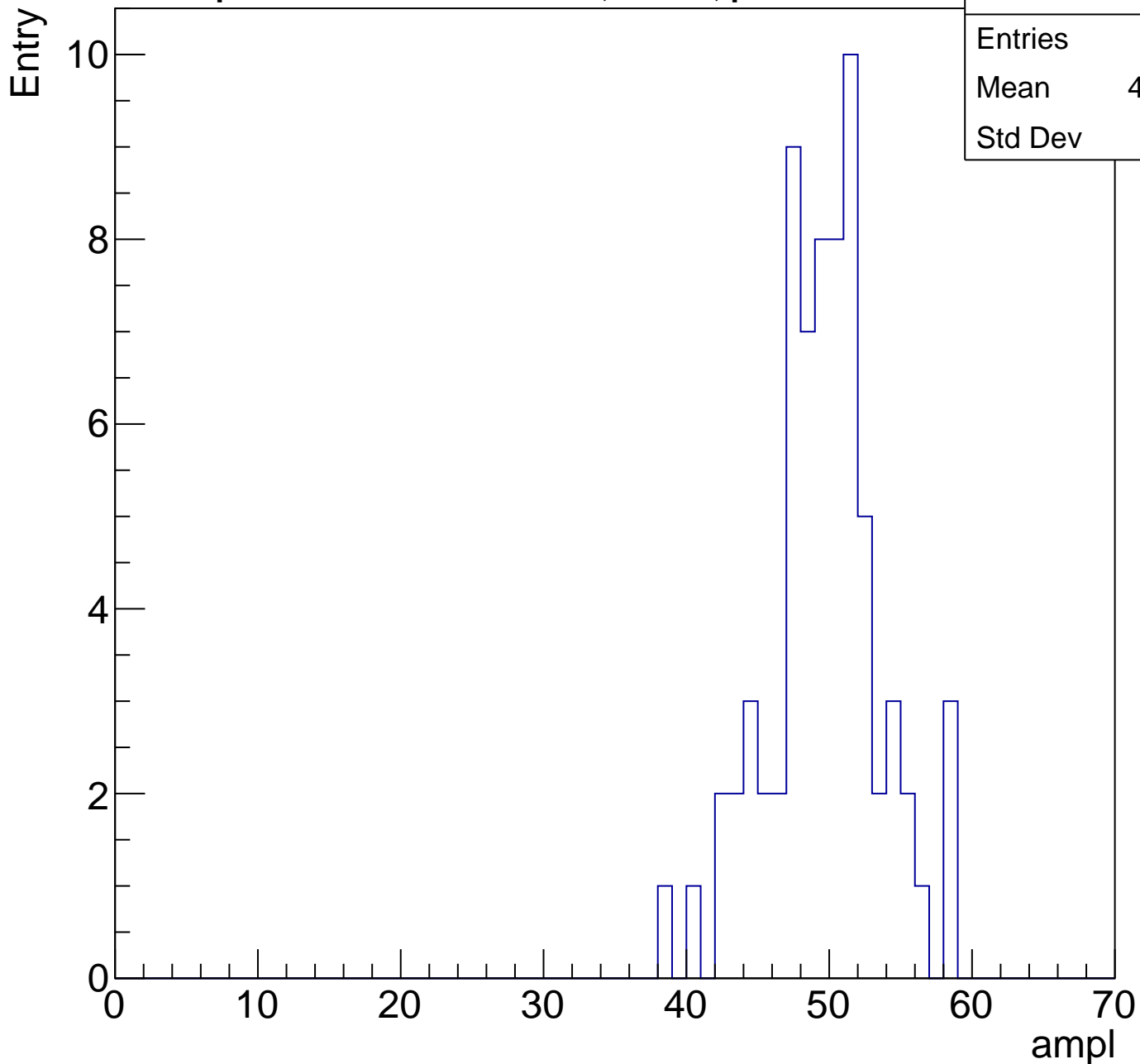
Gaus Width: 4.3342



B1L102S, U26-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	49.17
Std Dev	3.94

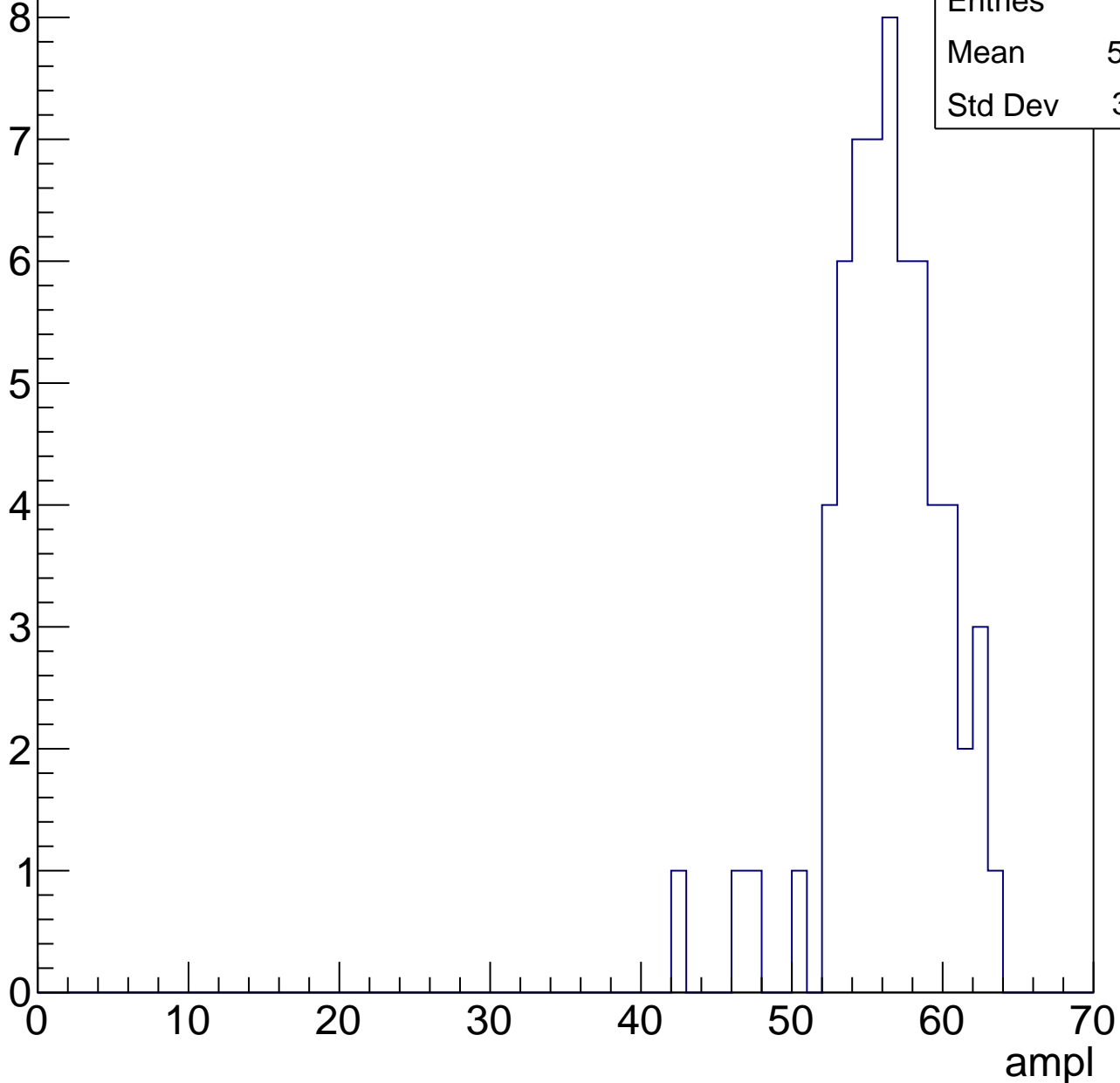


B1L102S, U26-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.79
Std Dev	3.811

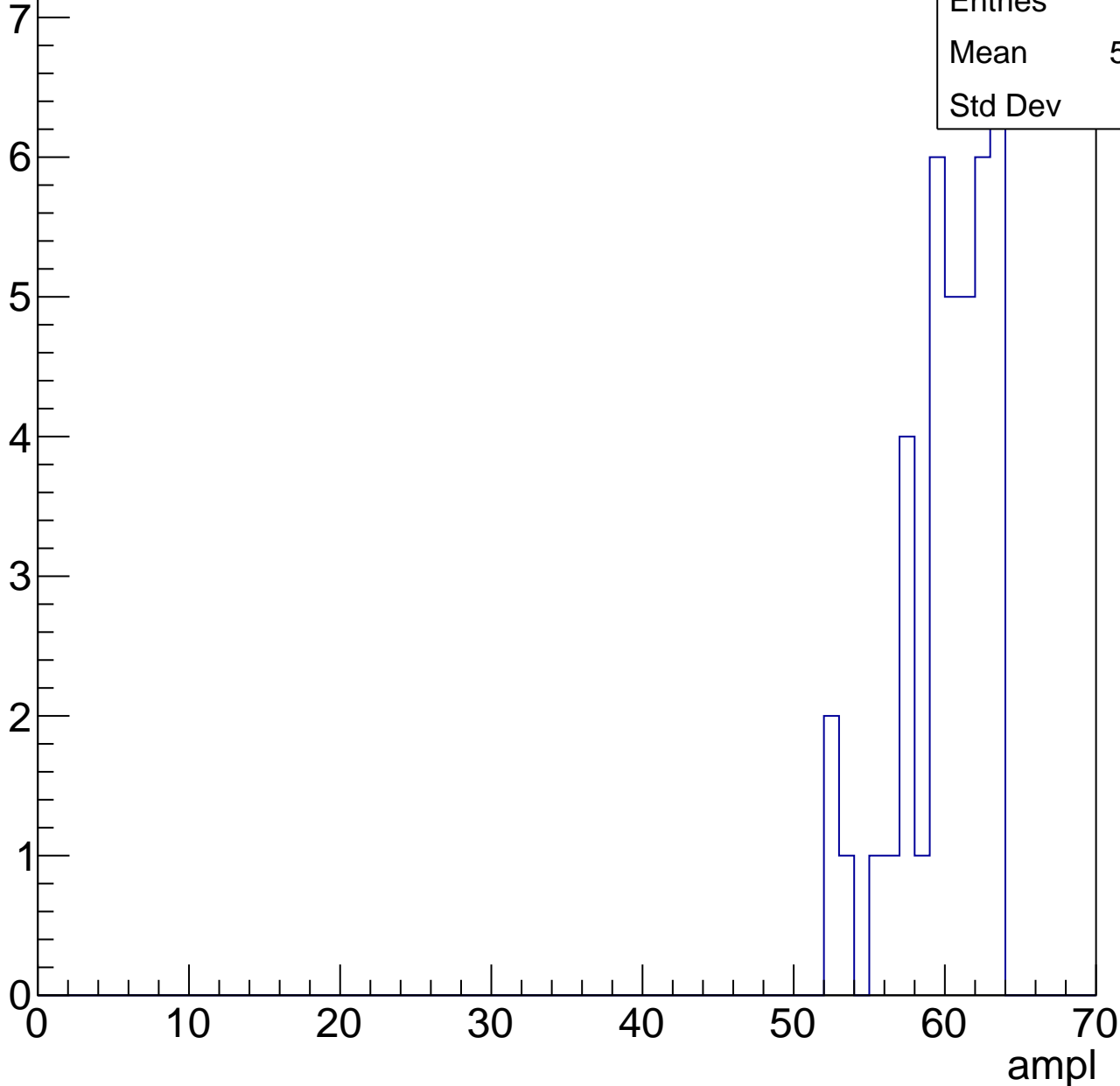


B1L102S, U26-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

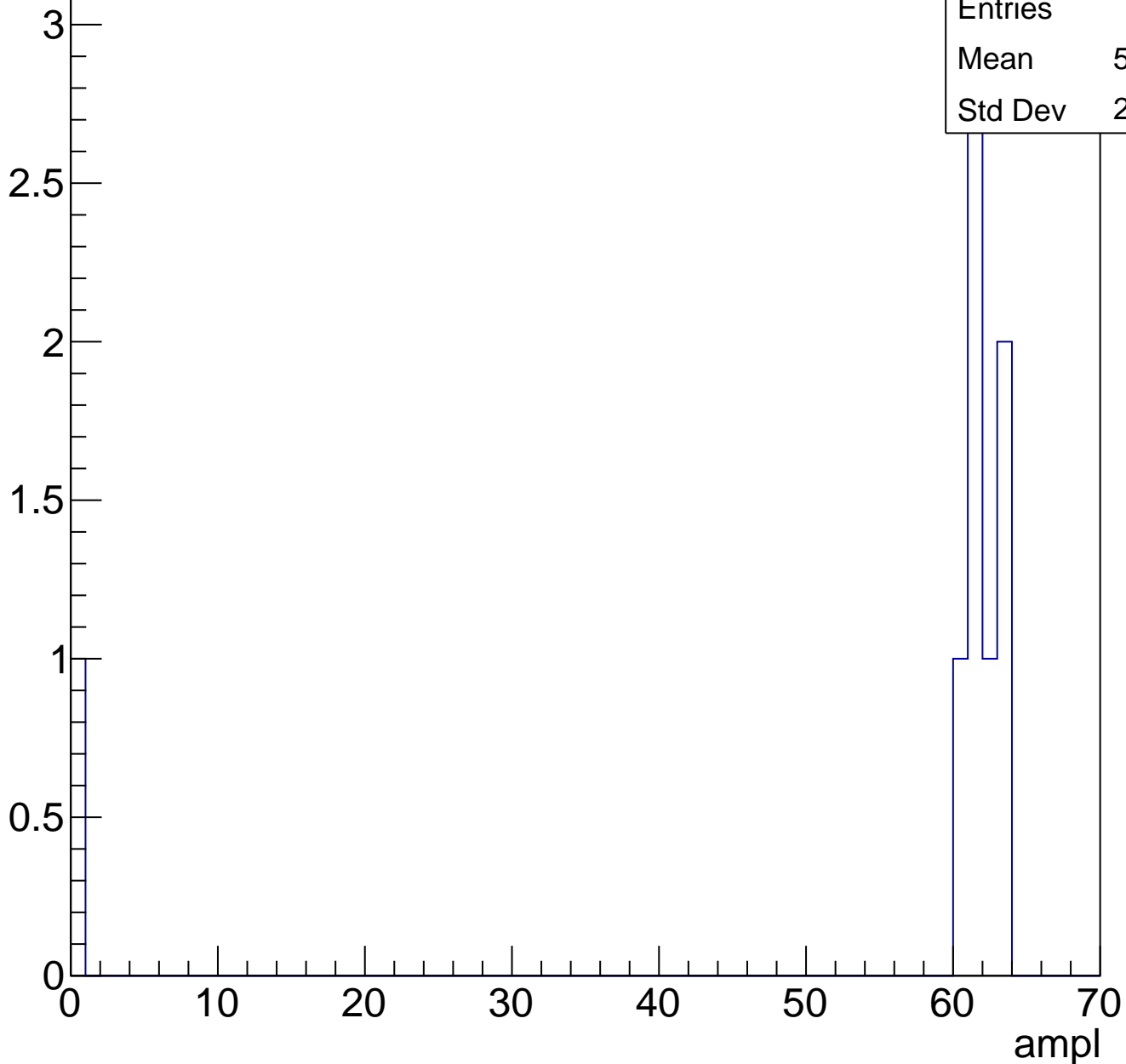
Entries	39
Mean	59.64
Std Dev	3



B1L102S, U26-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch113, adc0

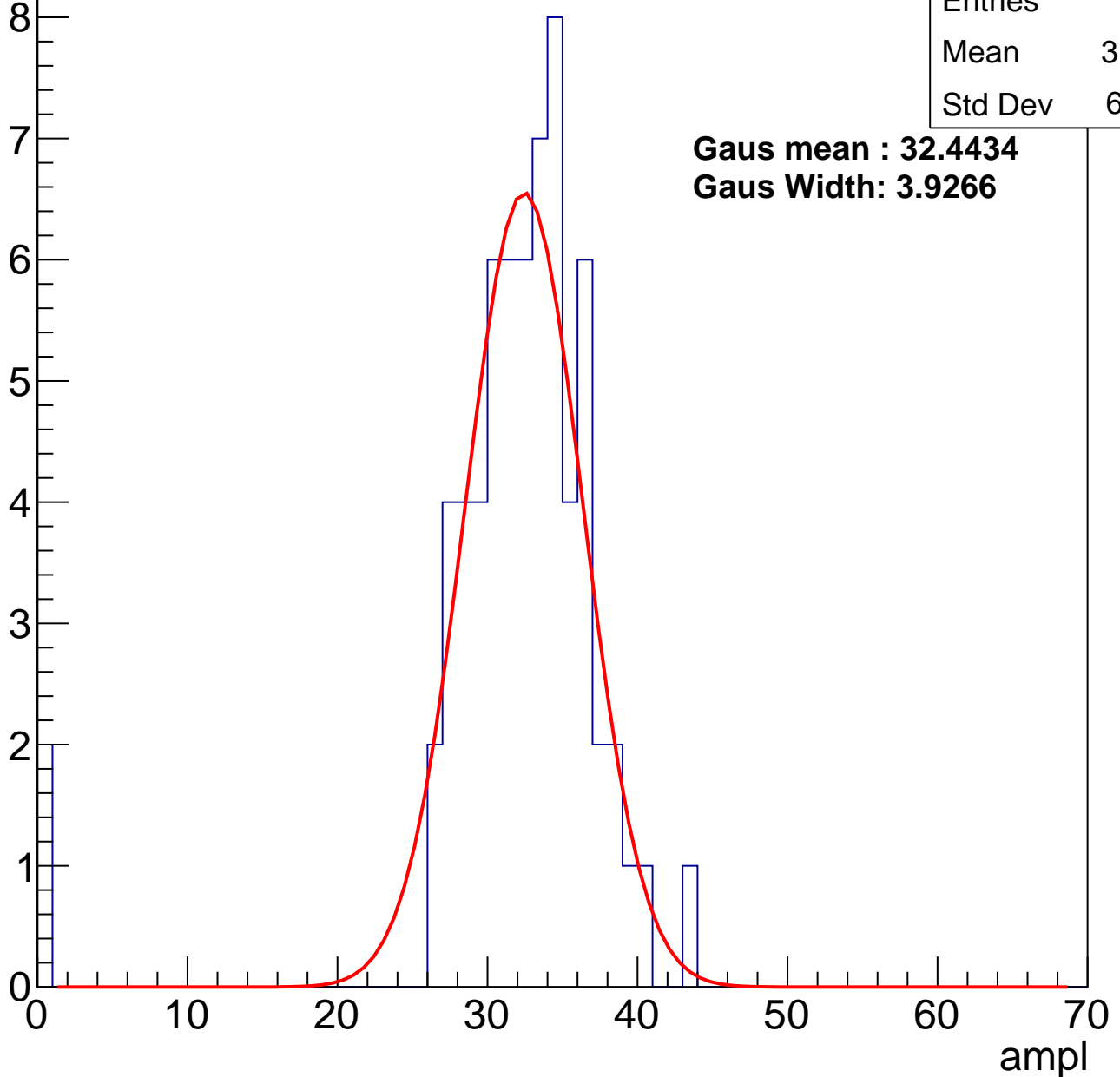
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	31.47
Std Dev	6.581

Gaus mean : 32.4434

Gaus Width: 3.9266



B1L102S, U26-ch113, adc1

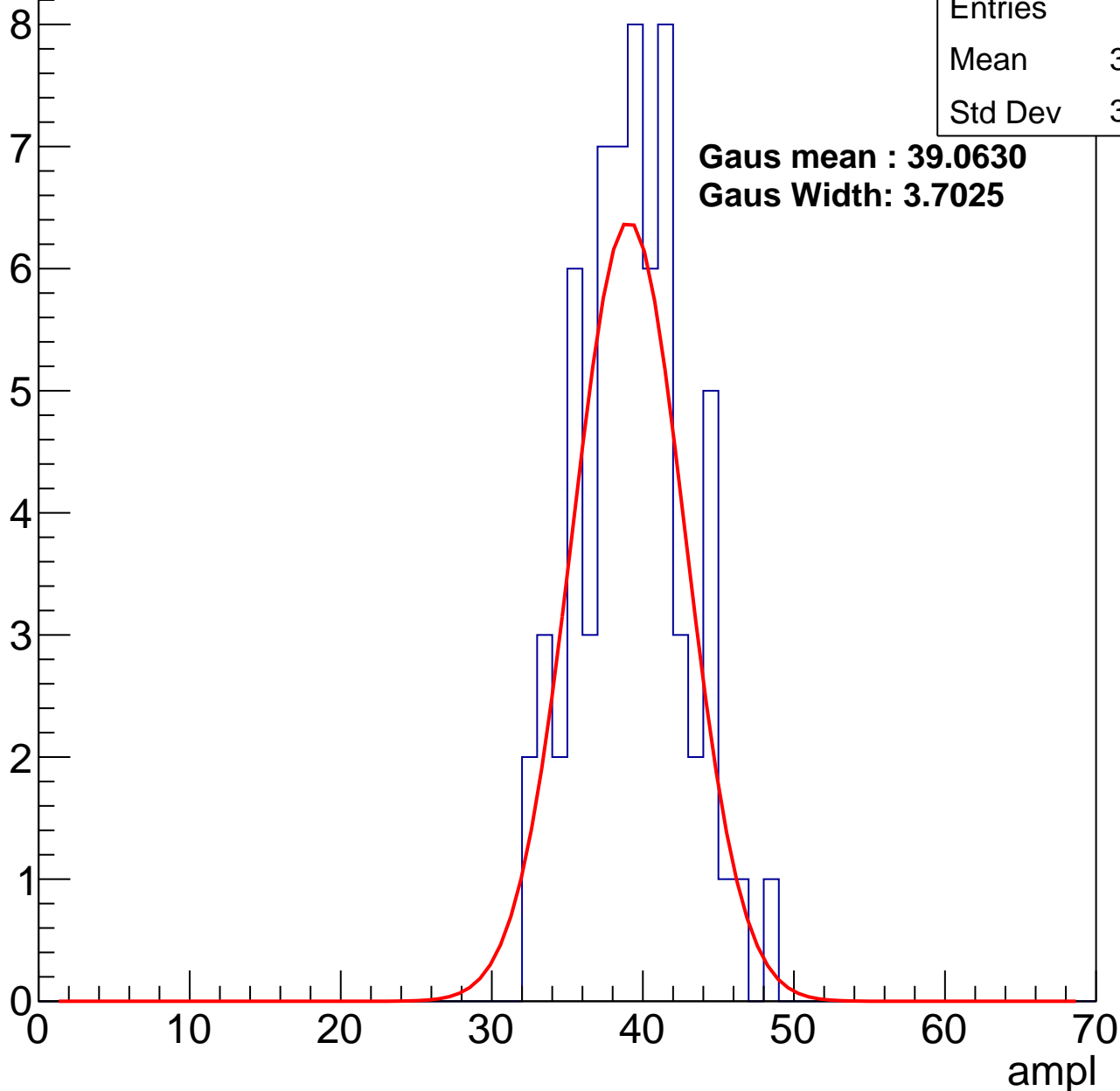
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	38.85
Std Dev	3.509

Gaus mean : 39.0630

Gaus Width: 3.7025



B1L102S, U26-ch113, adc2

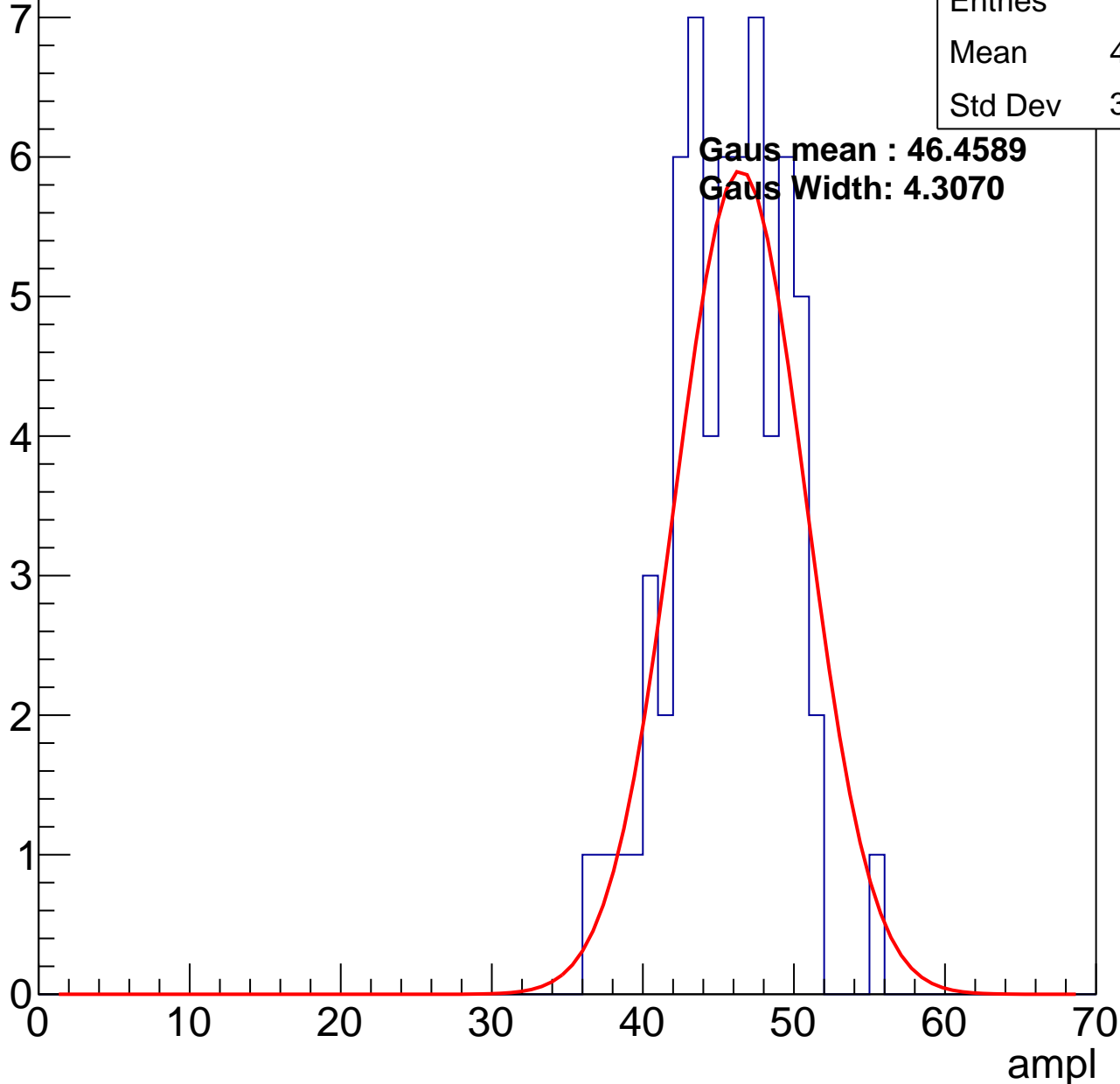
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	45.22
Std Dev	3.744

Gaus mean : 46.4589

Gaus Width: 4.3070

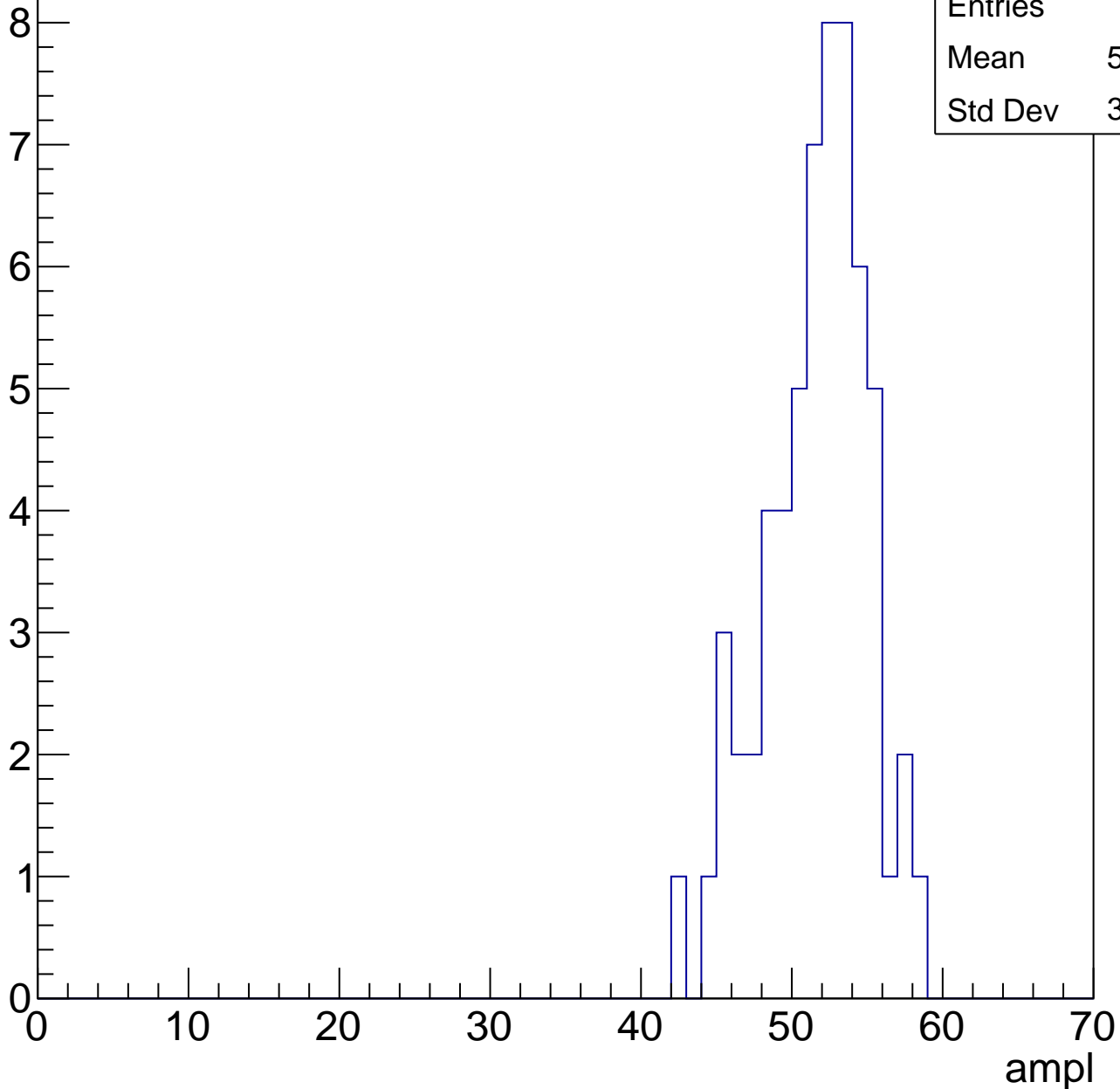


B1L102S, U26-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	51.15
Std Dev	3.405

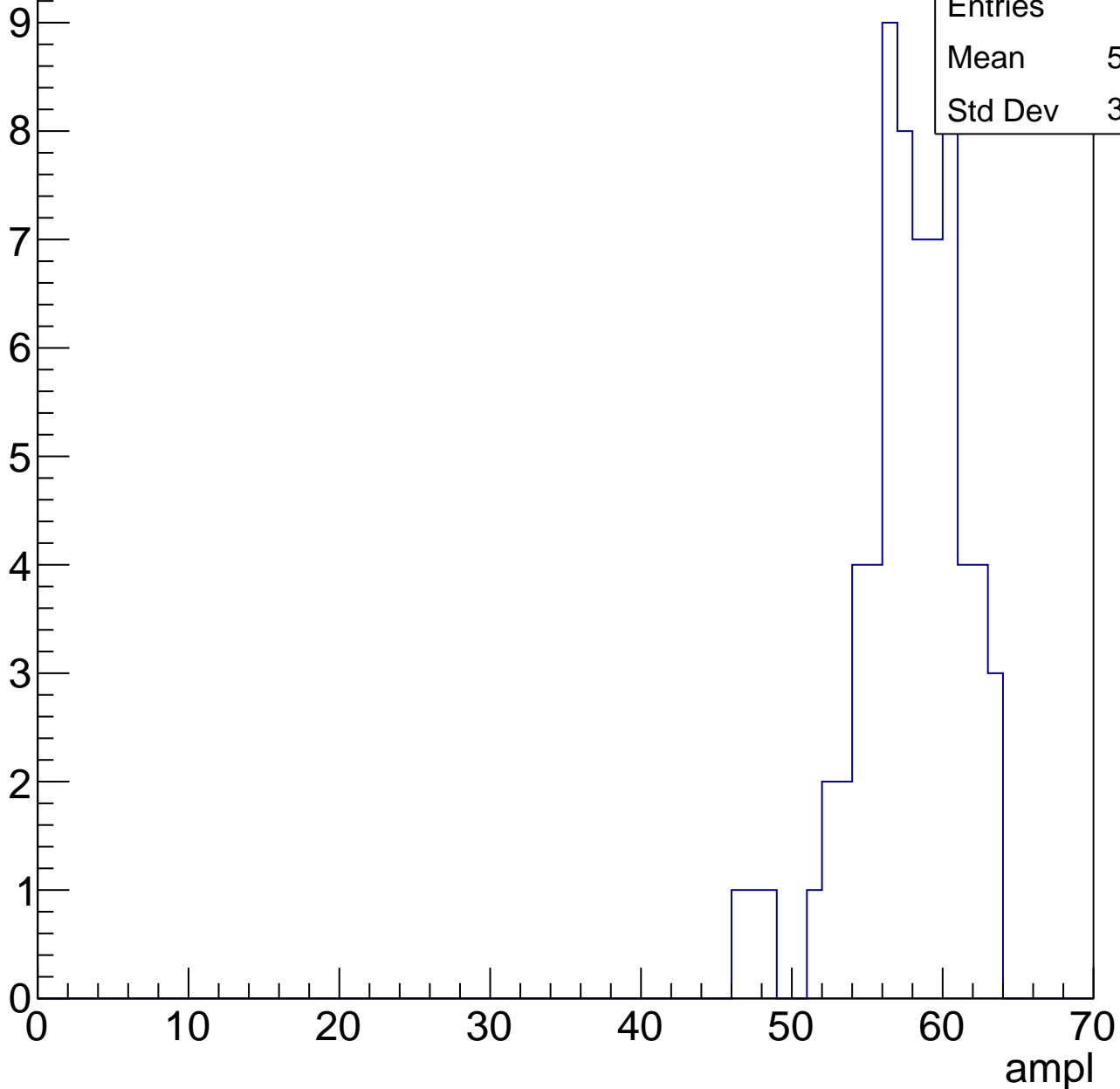


B1L102S, U26-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

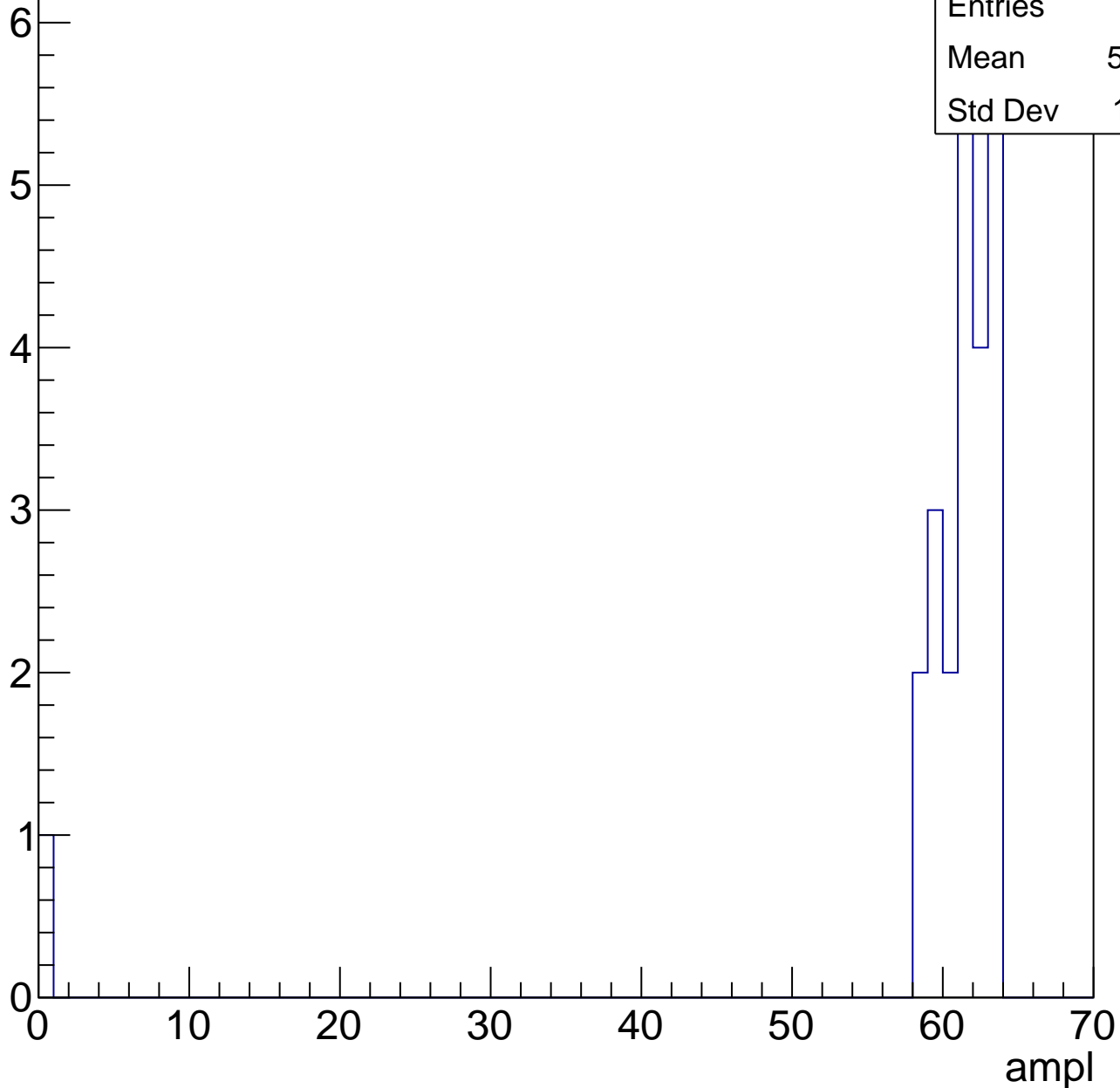
Entries	66
Mean	57.24
Std Dev	3.602



B1L102S, U26-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

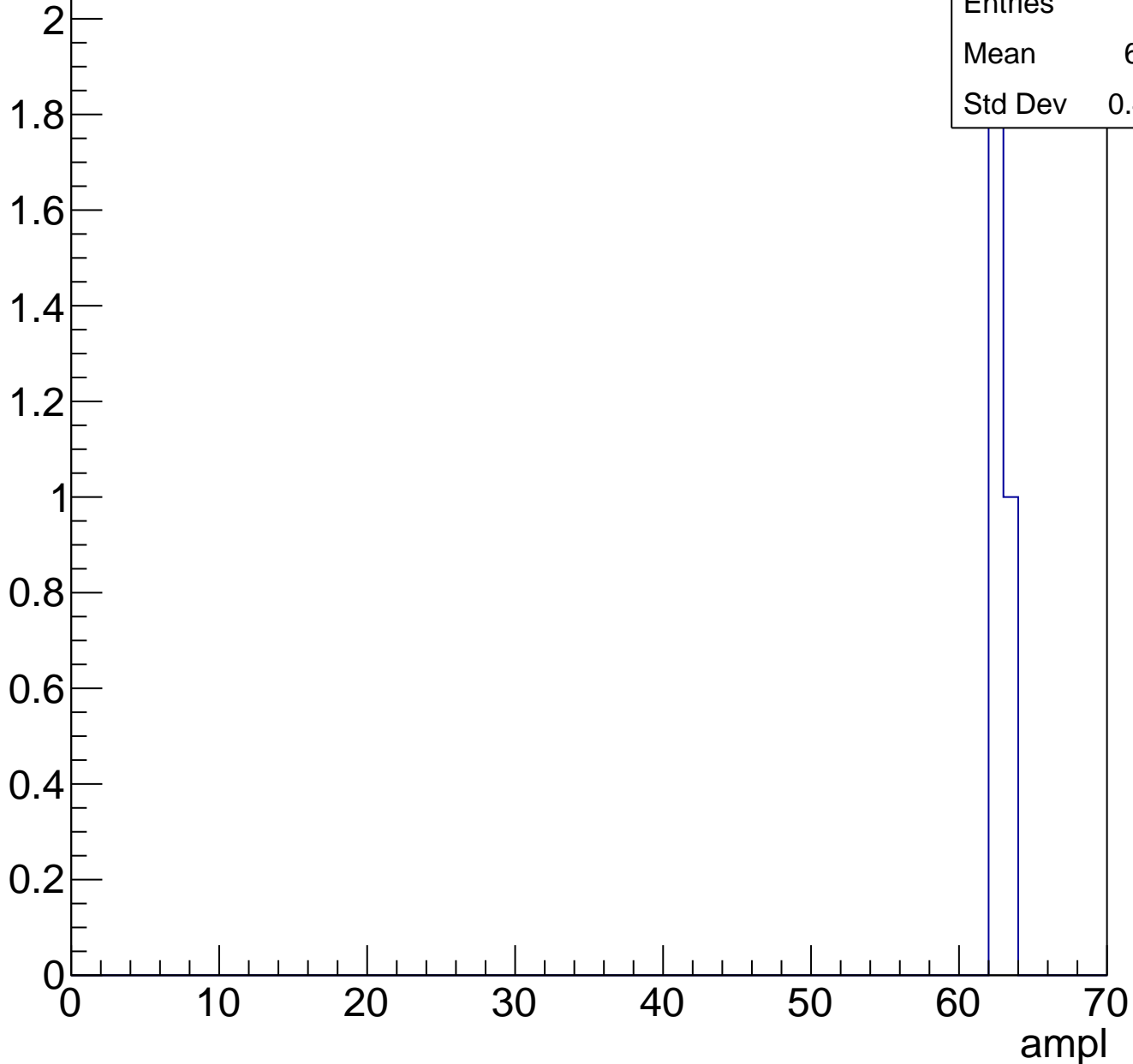
Entry



B1L102S, U26-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch114, adc0

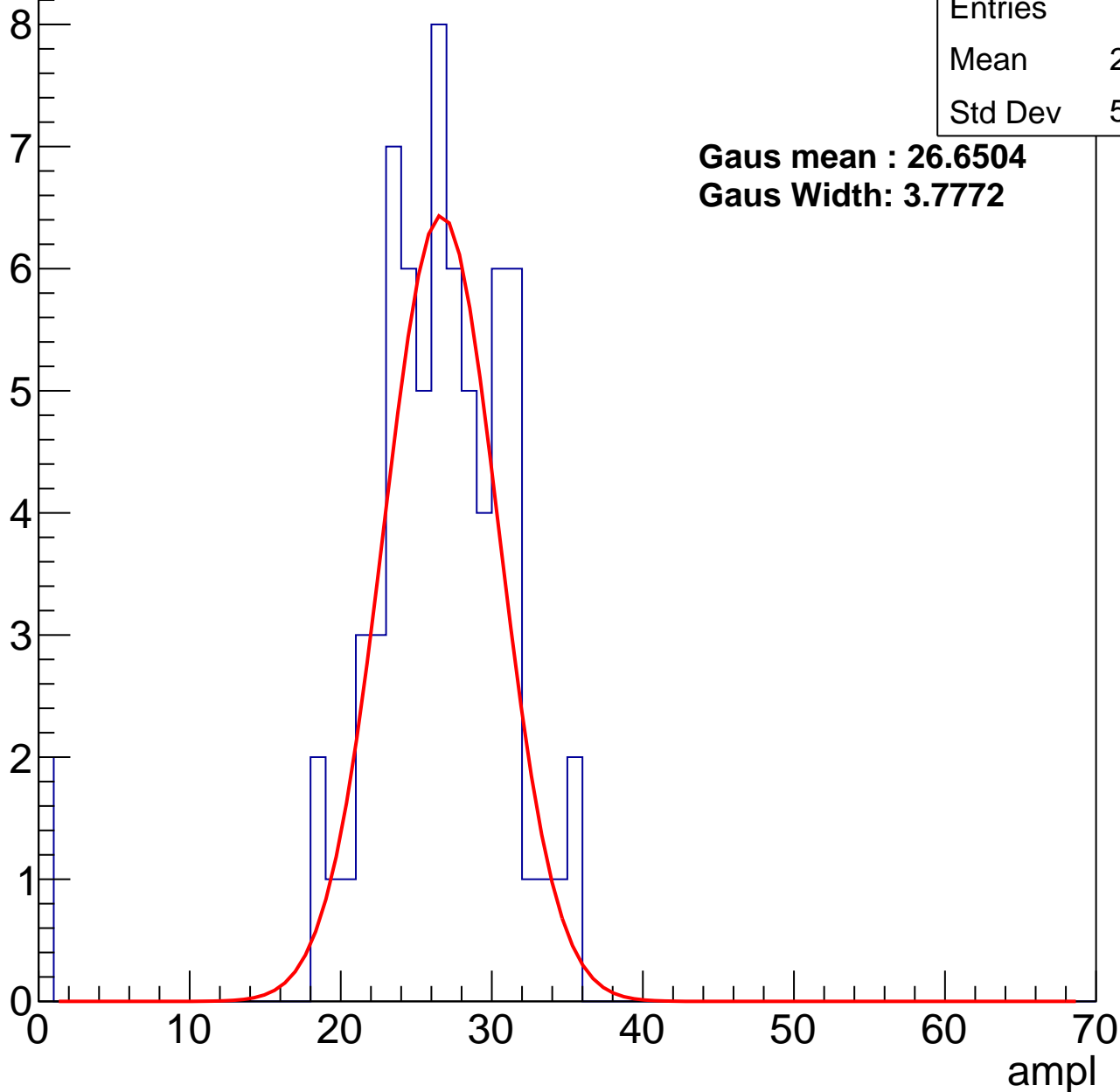
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	25.64
Std Dev	5.843

Gaus mean : 26.6504

Gaus Width: 3.7772



B1L102S, U26-ch114, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	34.22
Std Dev	3.956

Gaus mean : 34.7034

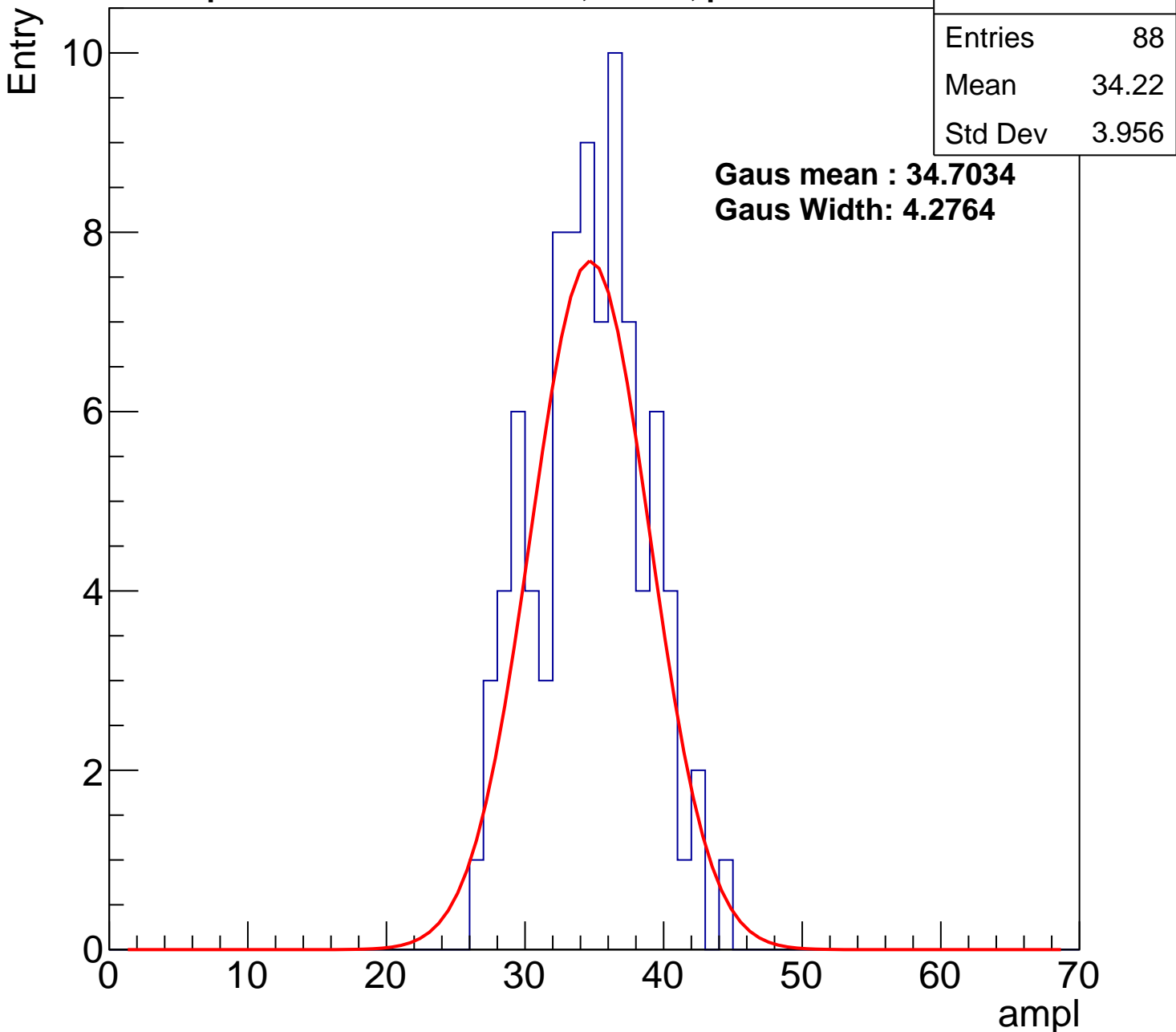
Gaus Width: 4.2764

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch114, adc2

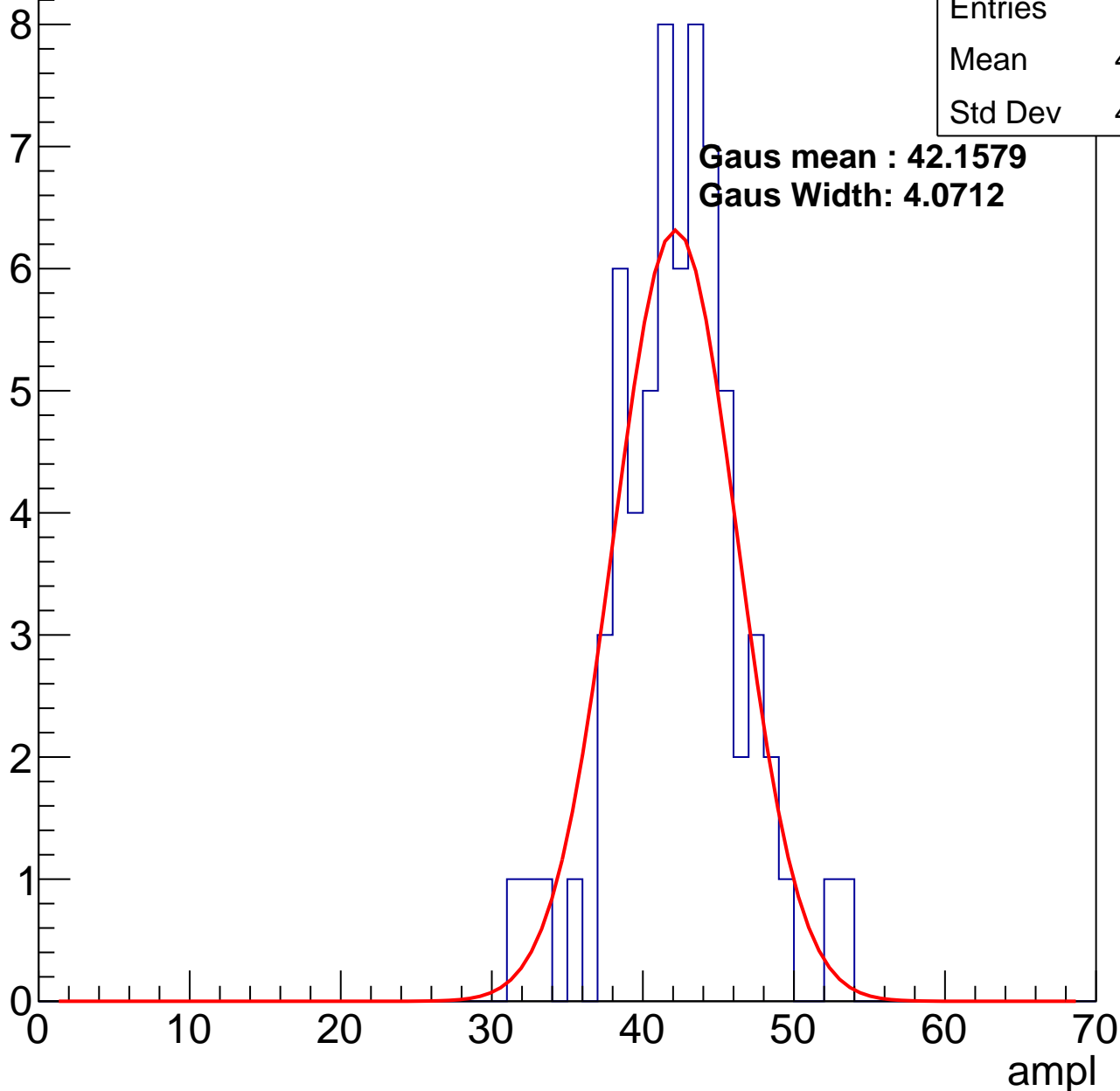
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.91
Std Dev	4.111

Gaus mean : 42.1579

Gaus Width: 4.0712

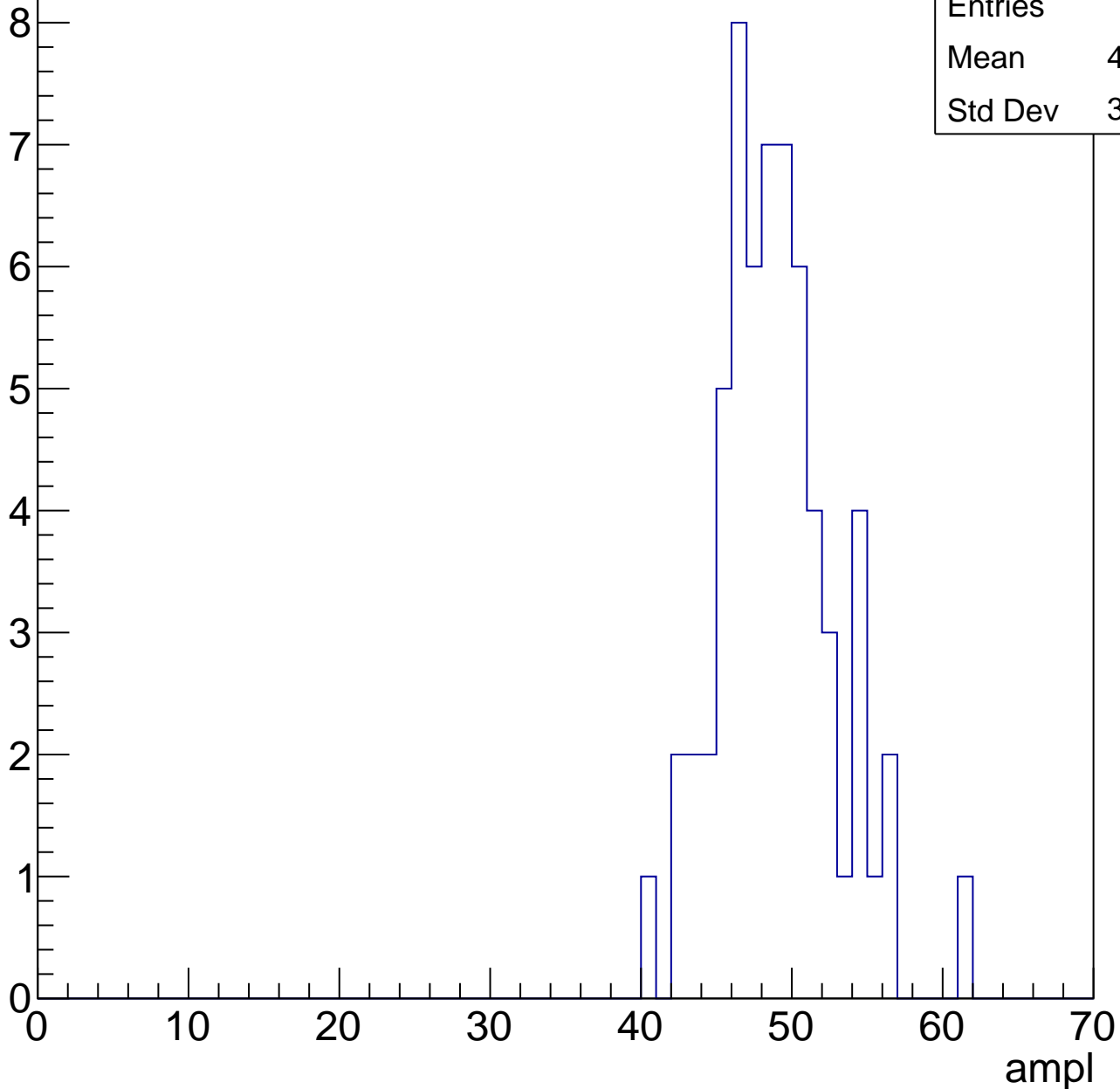


B1L102S, U26-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.53
Std Dev	3.838

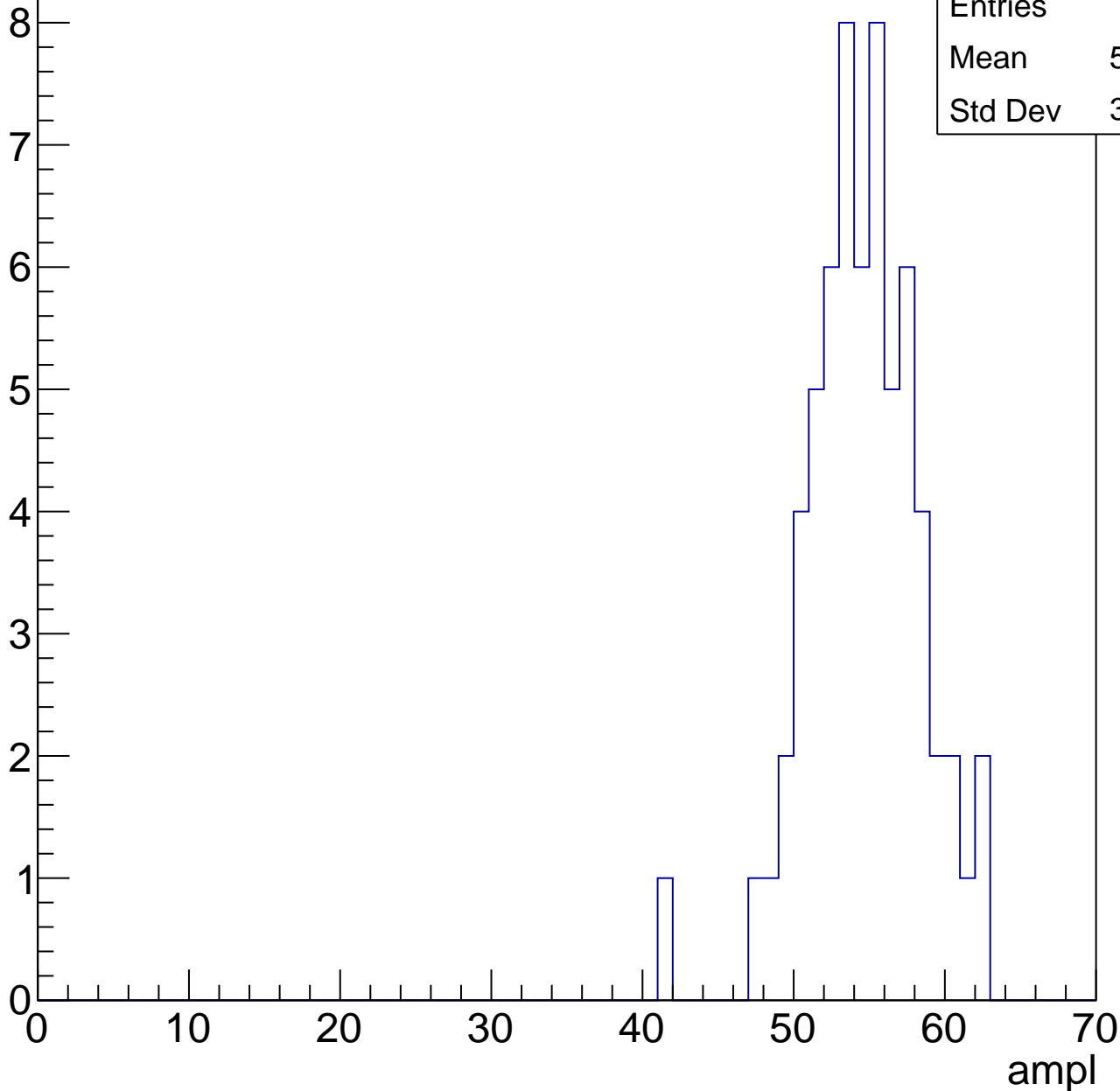


B1L102S, U26-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.16
Std Dev	3.726

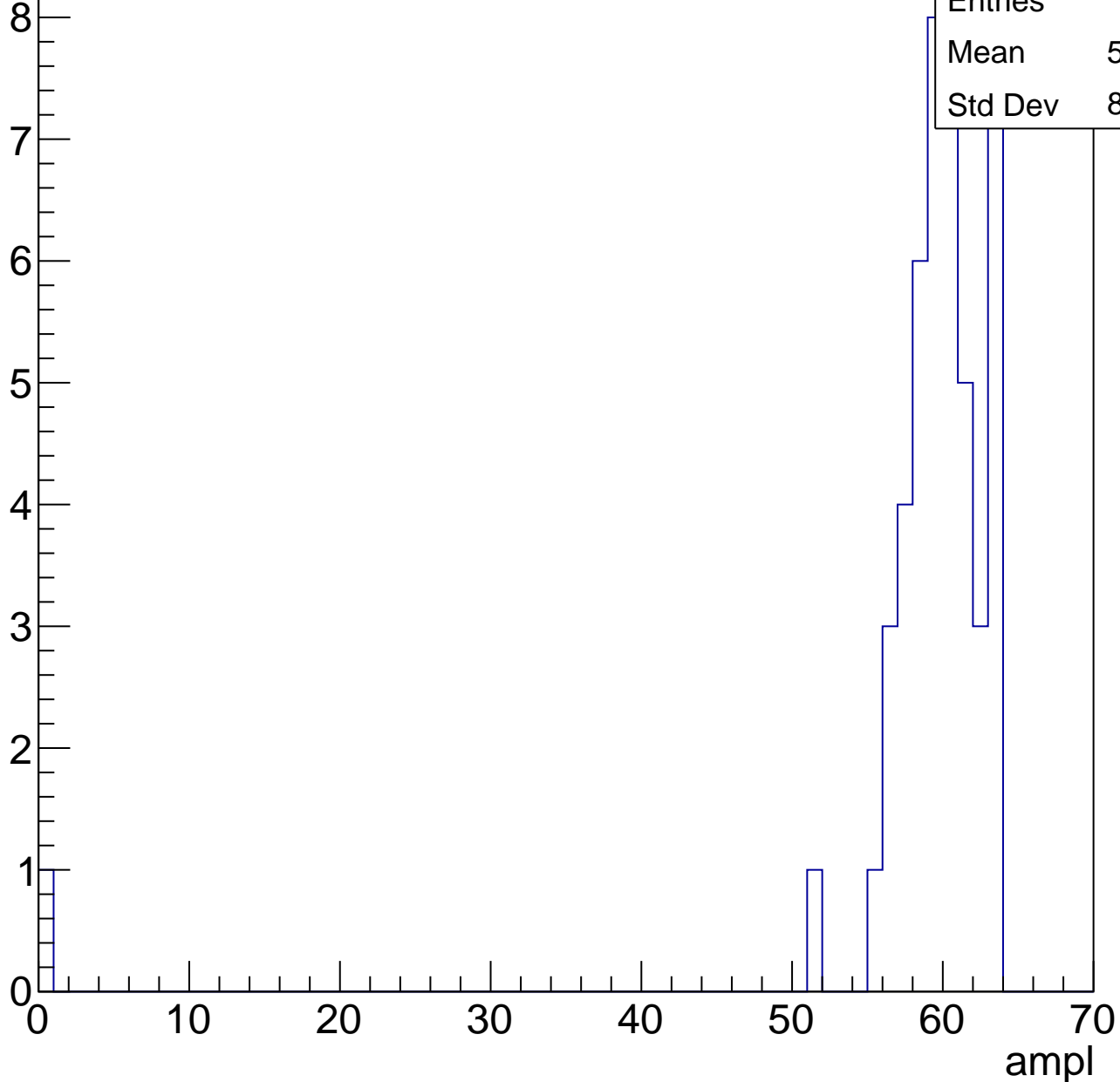


B1L102S, U26-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.27
Std Dev	8.859

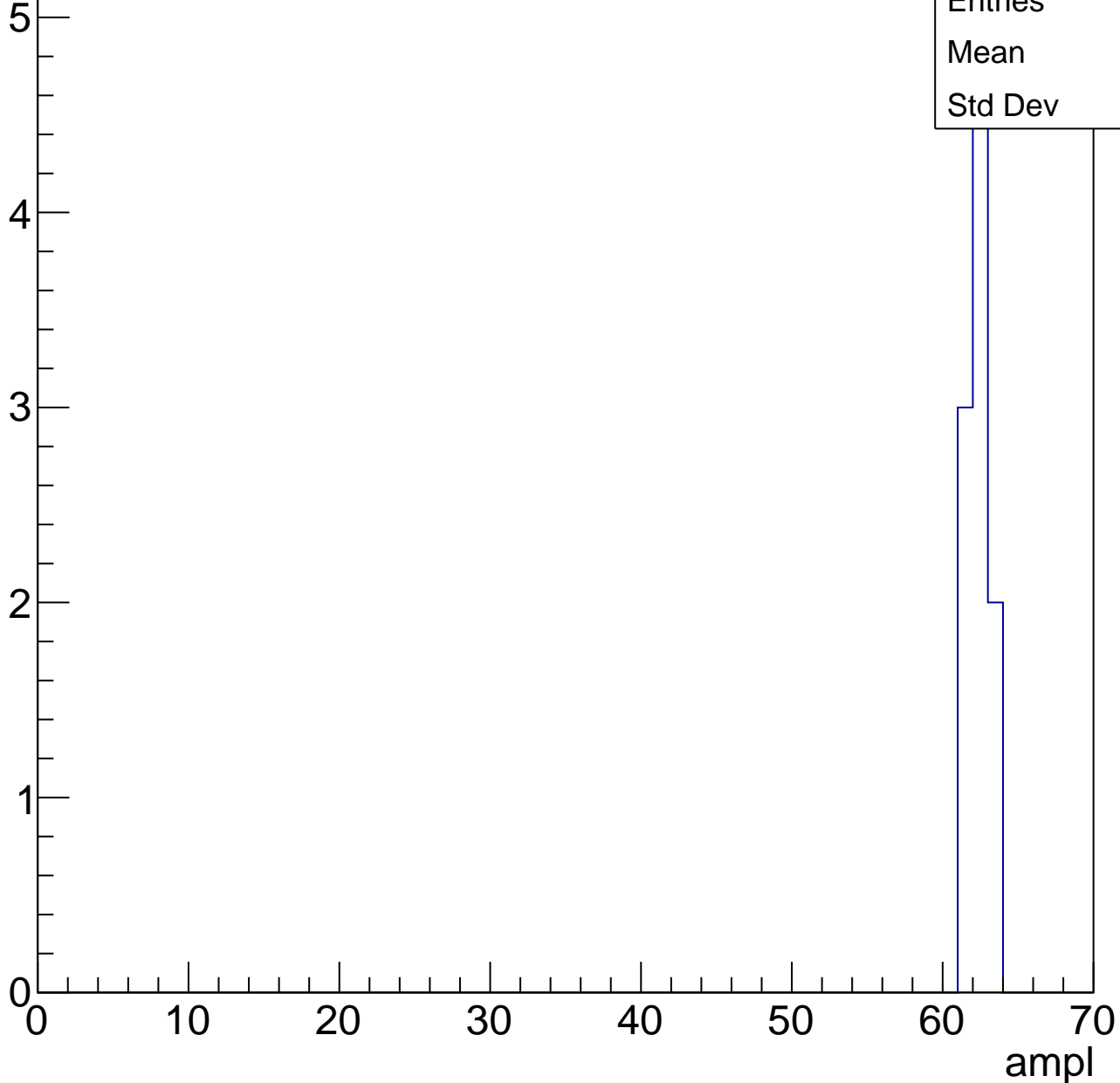


B1L102S, U26-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.9
Std Dev	0.7



B1L102S, U26-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch115, adc0

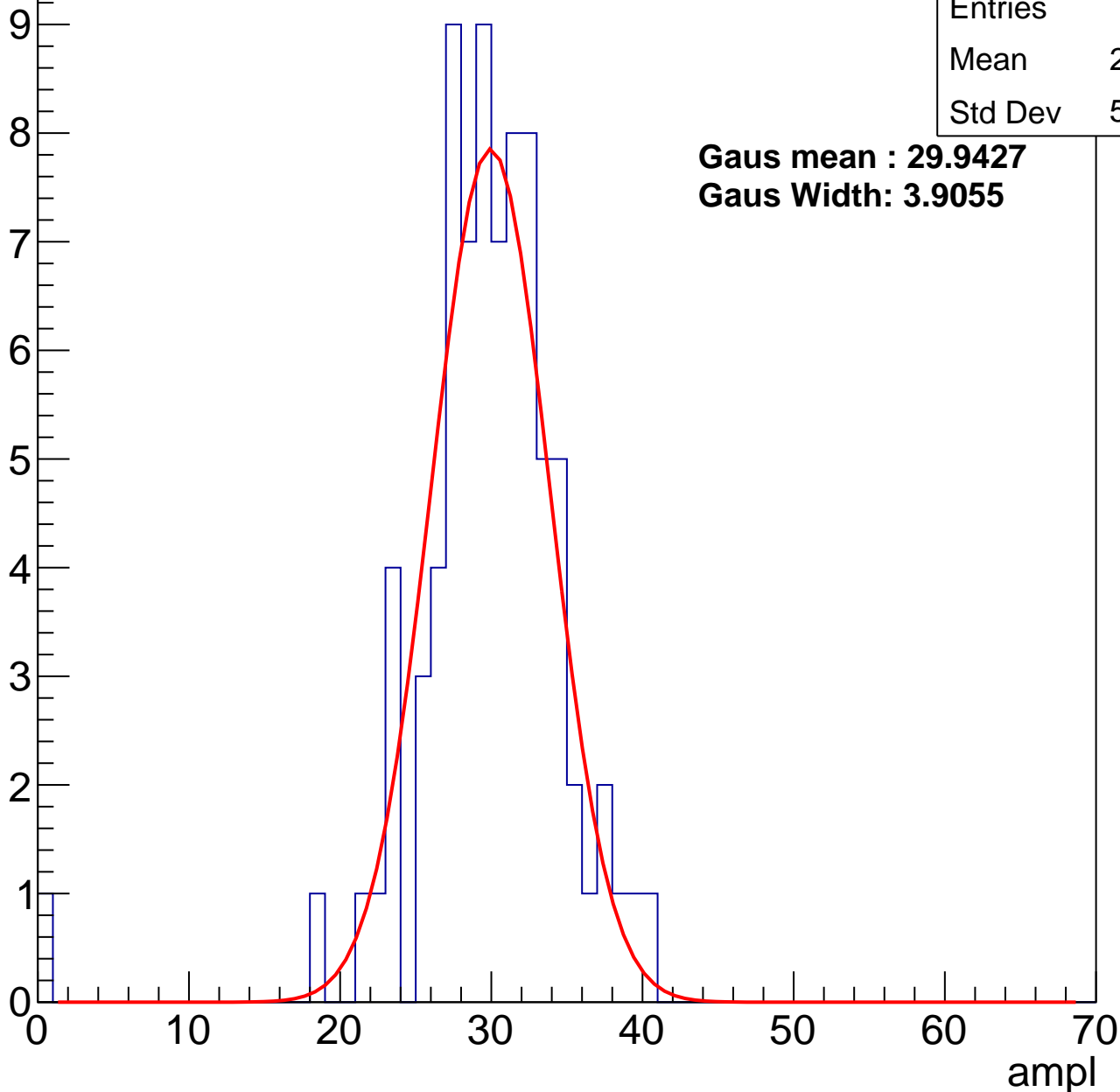
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.36
Std Dev	5.215

Gaus mean : 29.9427

Gaus Width: 3.9055



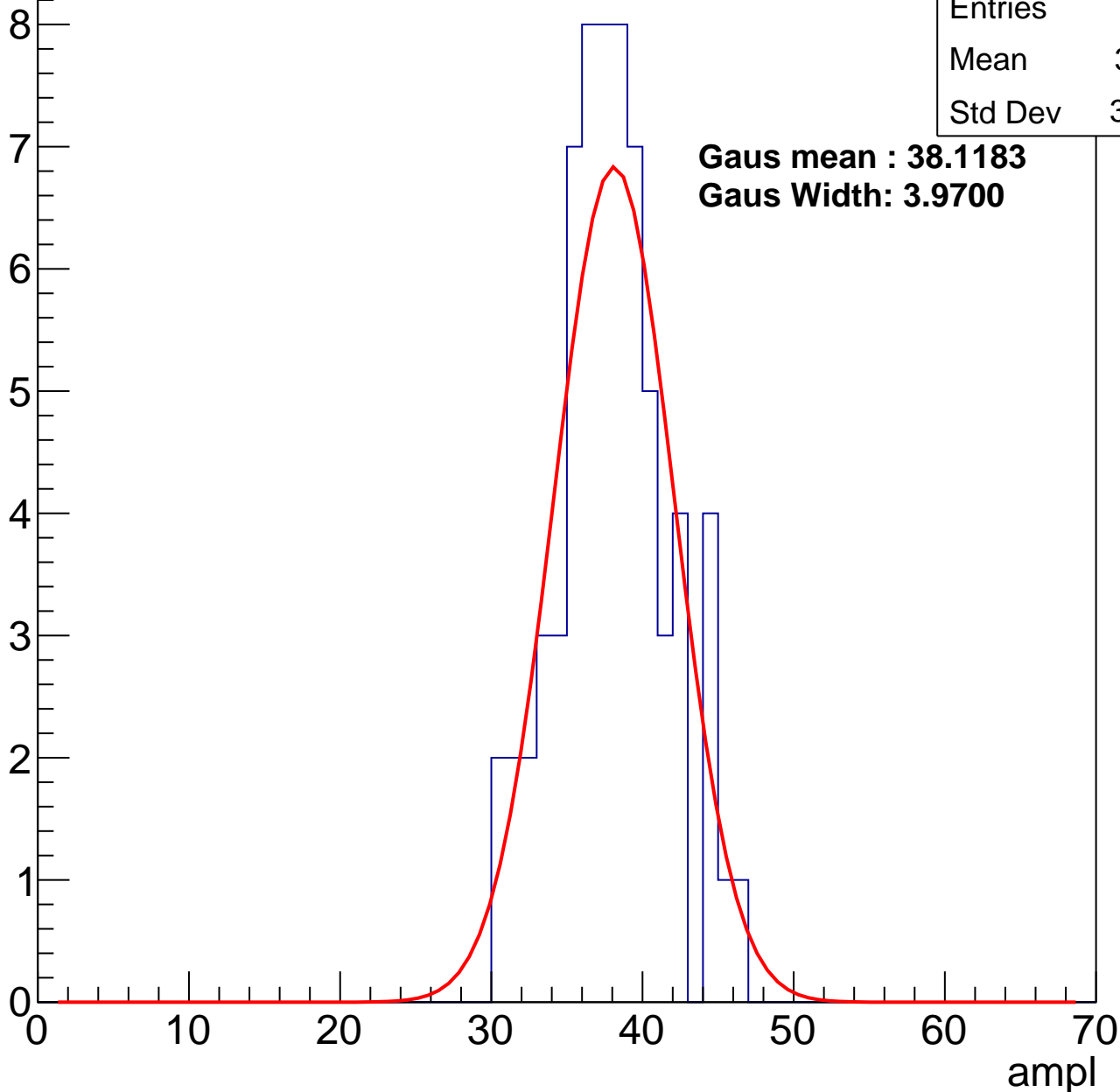
B1L102S, U26-ch115, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.51
Std Dev	3.603

Gaus mean : 38.1183
Gaus Width: 3.9700



B1L102S, U26-ch115, adc2

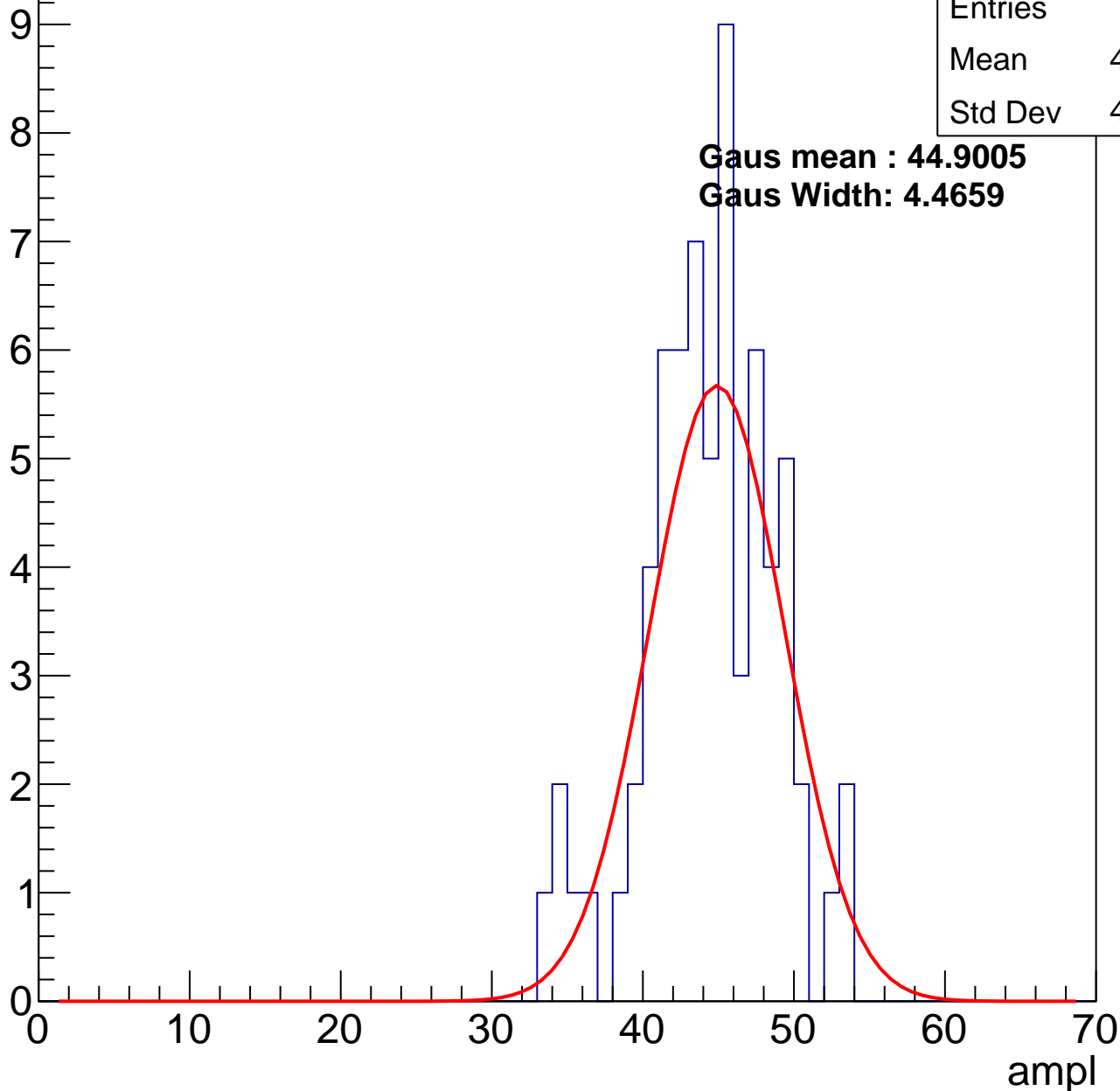
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.93
Std Dev	4.309

Gaus mean : 44.9005

Gaus Width: 4.4659

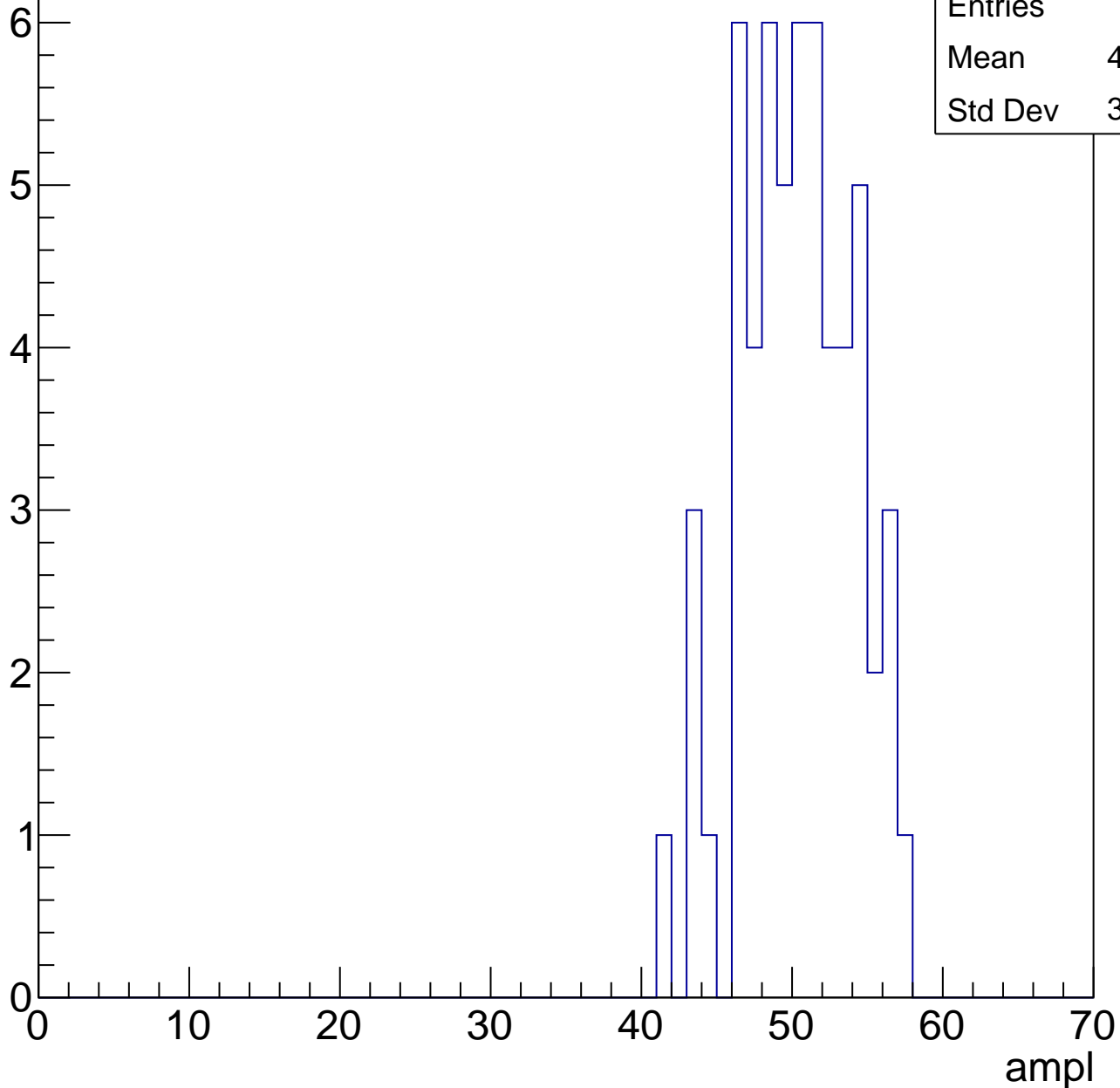


B1L102S, U26-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.86
Std Dev	3.673

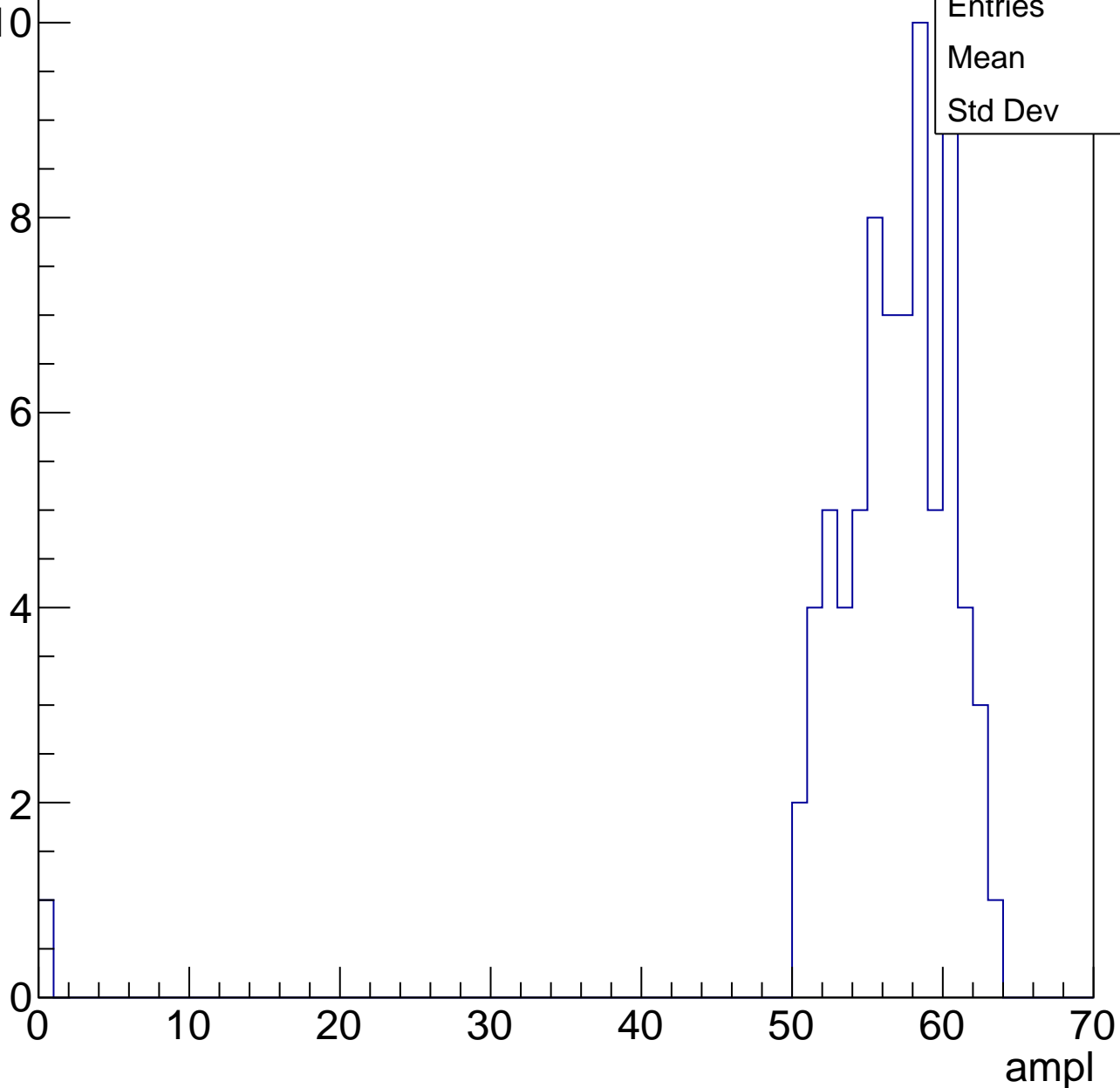


B1L102S, U26-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	55.8
Std Dev	7.25

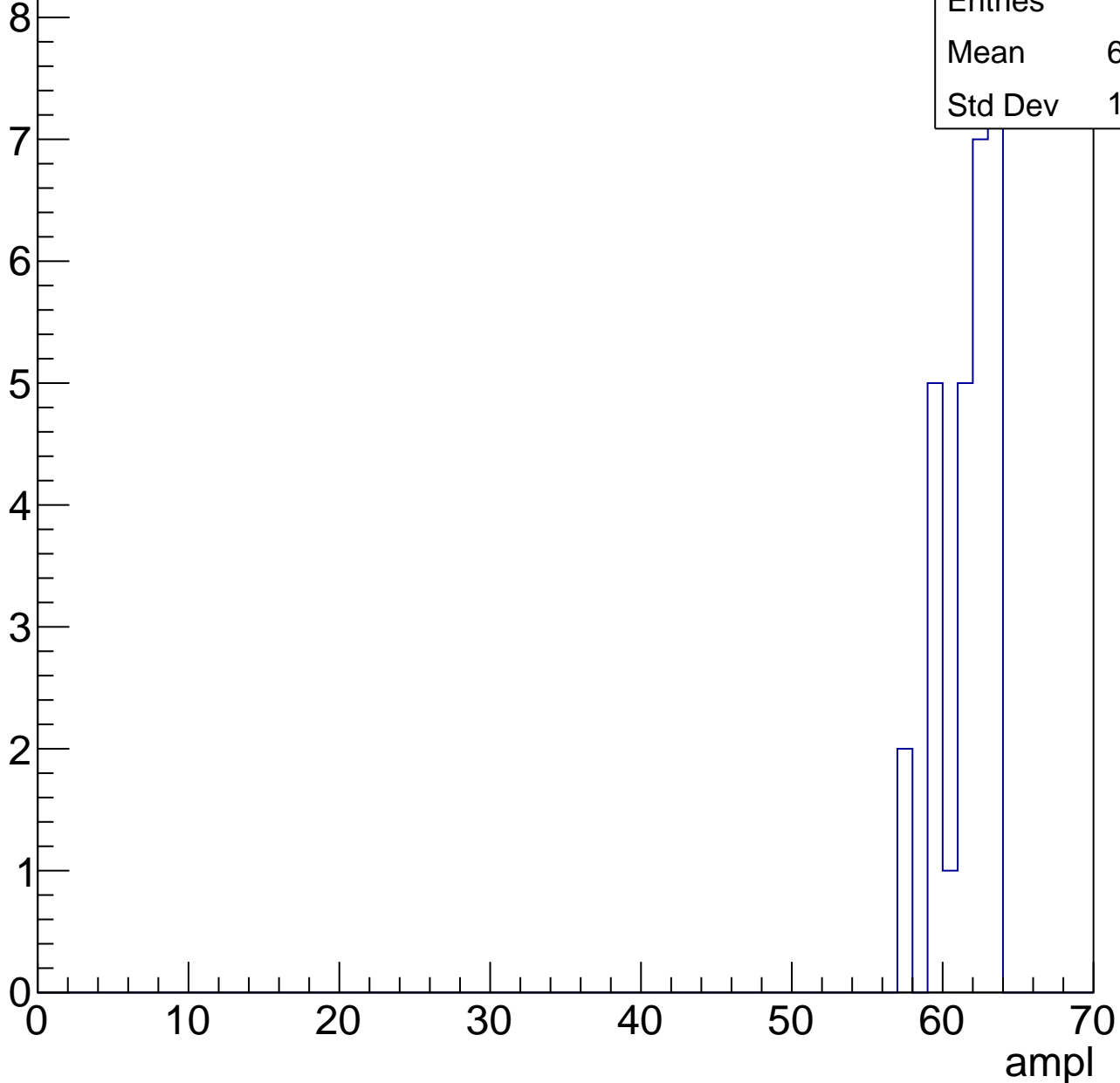


B1L102S, U26-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	61.14
Std Dev	1.807



B1L102S, U26-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch116, adc0

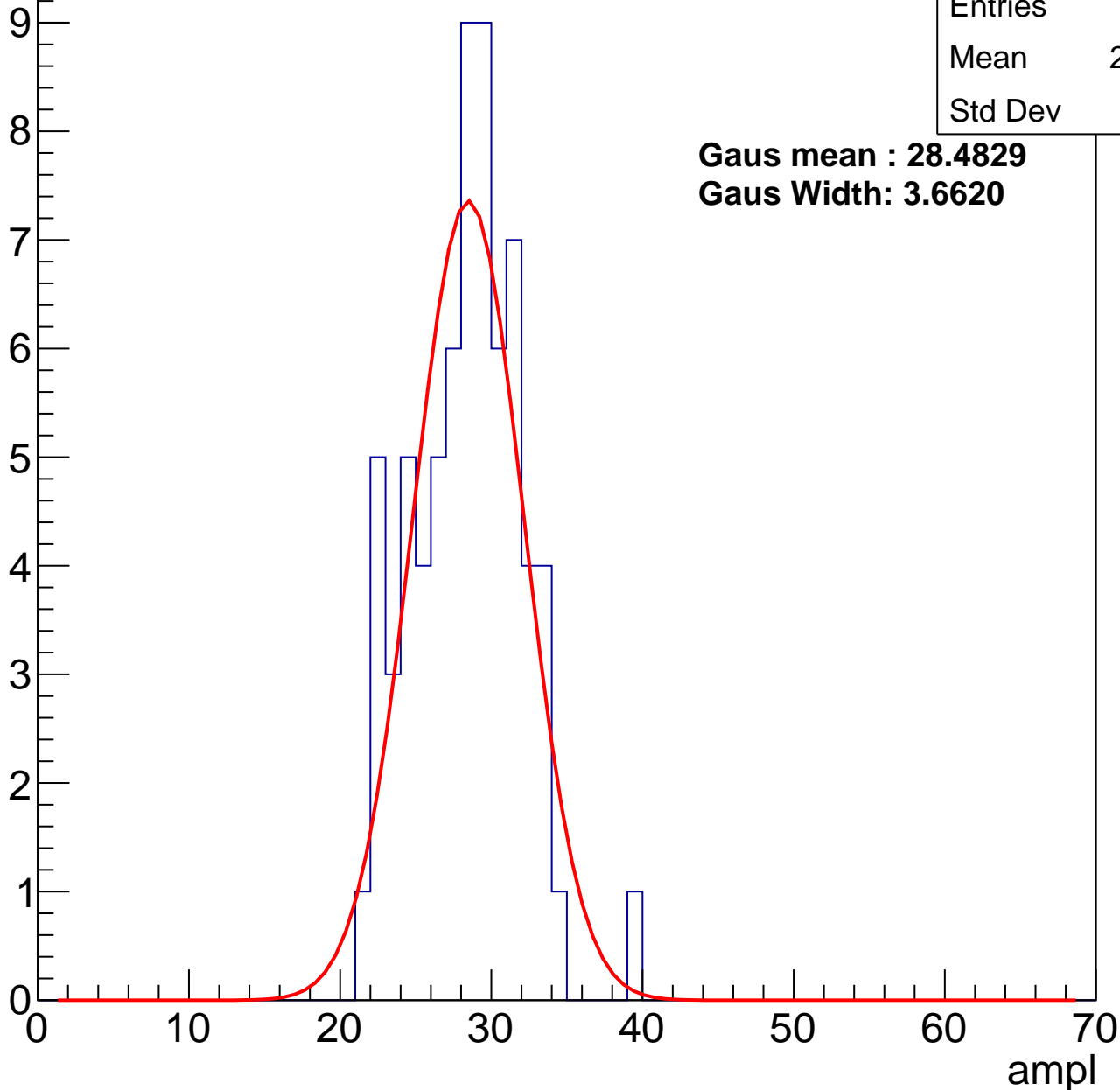
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.93
Std Dev	3.51

Gaus mean : 28.4829

Gaus Width: 3.6620



B1L102S, U26-ch116, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	35.28
Std Dev	3.633

Gaus mean : 34.9486

Gaus Width: 4.5074

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L102S, U26-ch116, adc2

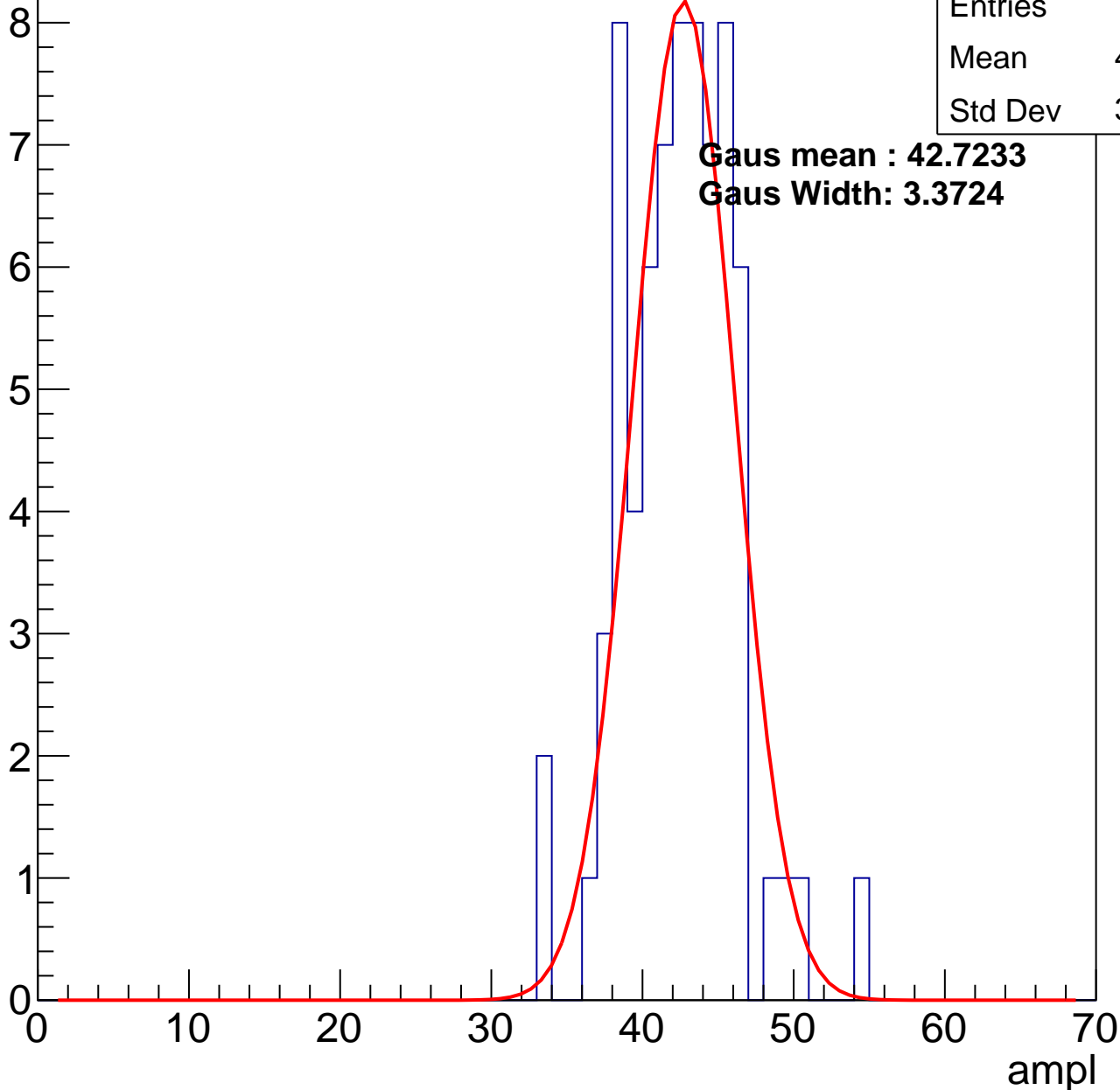
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.01
Std Dev	3.661

Gaus mean : 42.7233

Gaus Width: 3.3724

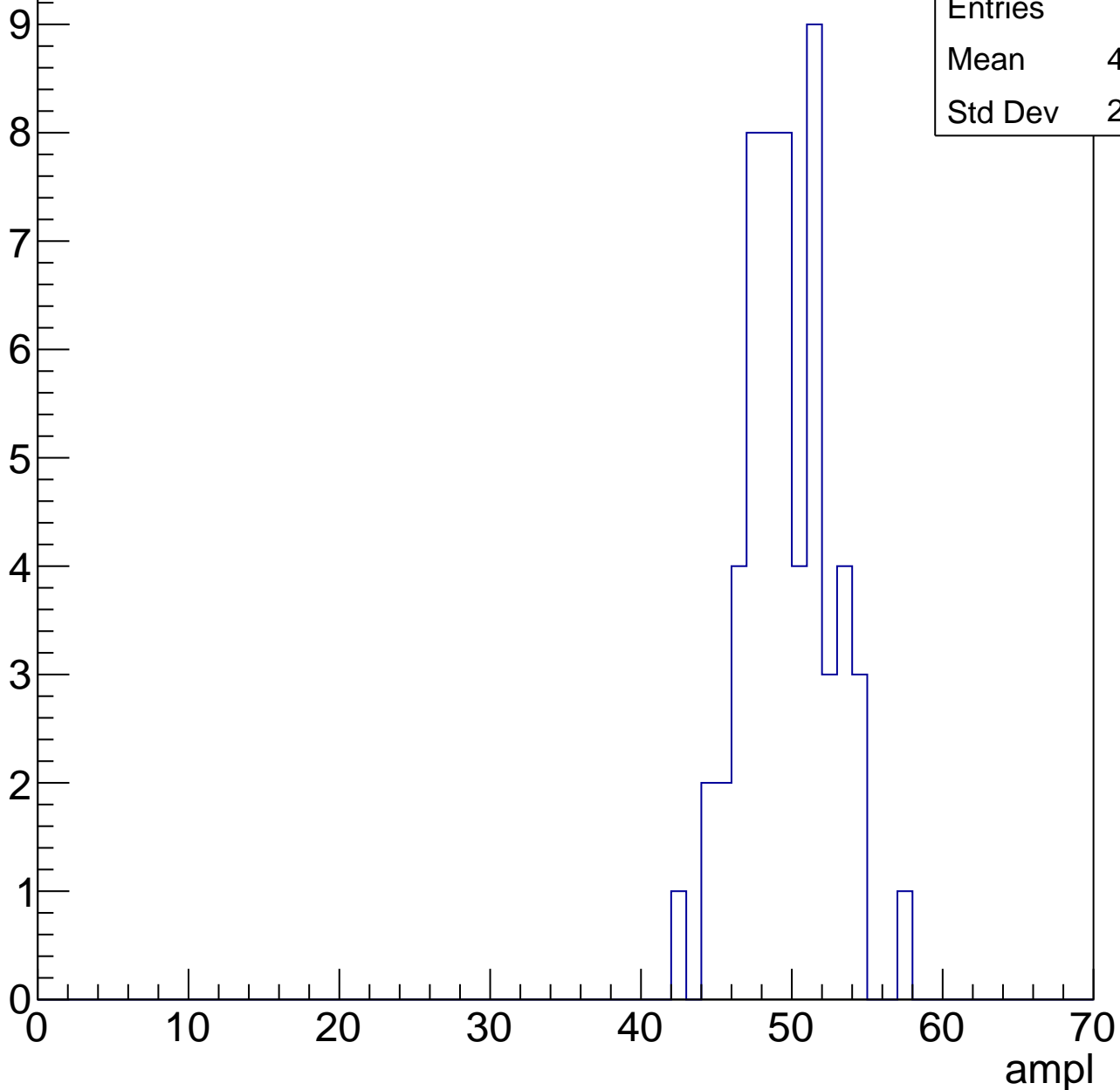


B1L102S, U26-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.16
Std Dev	2.889



B1L102S, U26-ch116, adc4

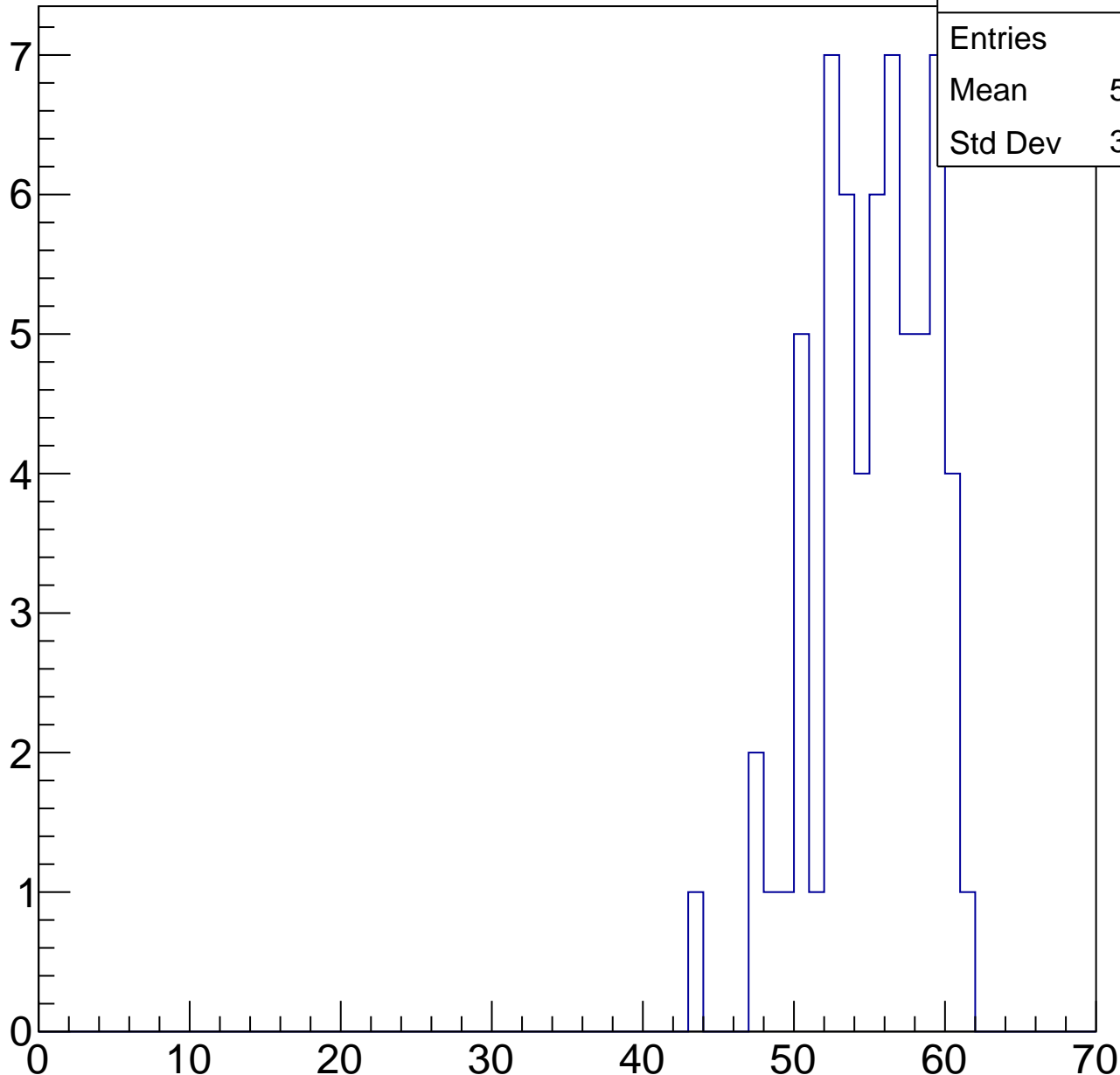
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	63
Mean	54.67
Std Dev	3.784

ampl

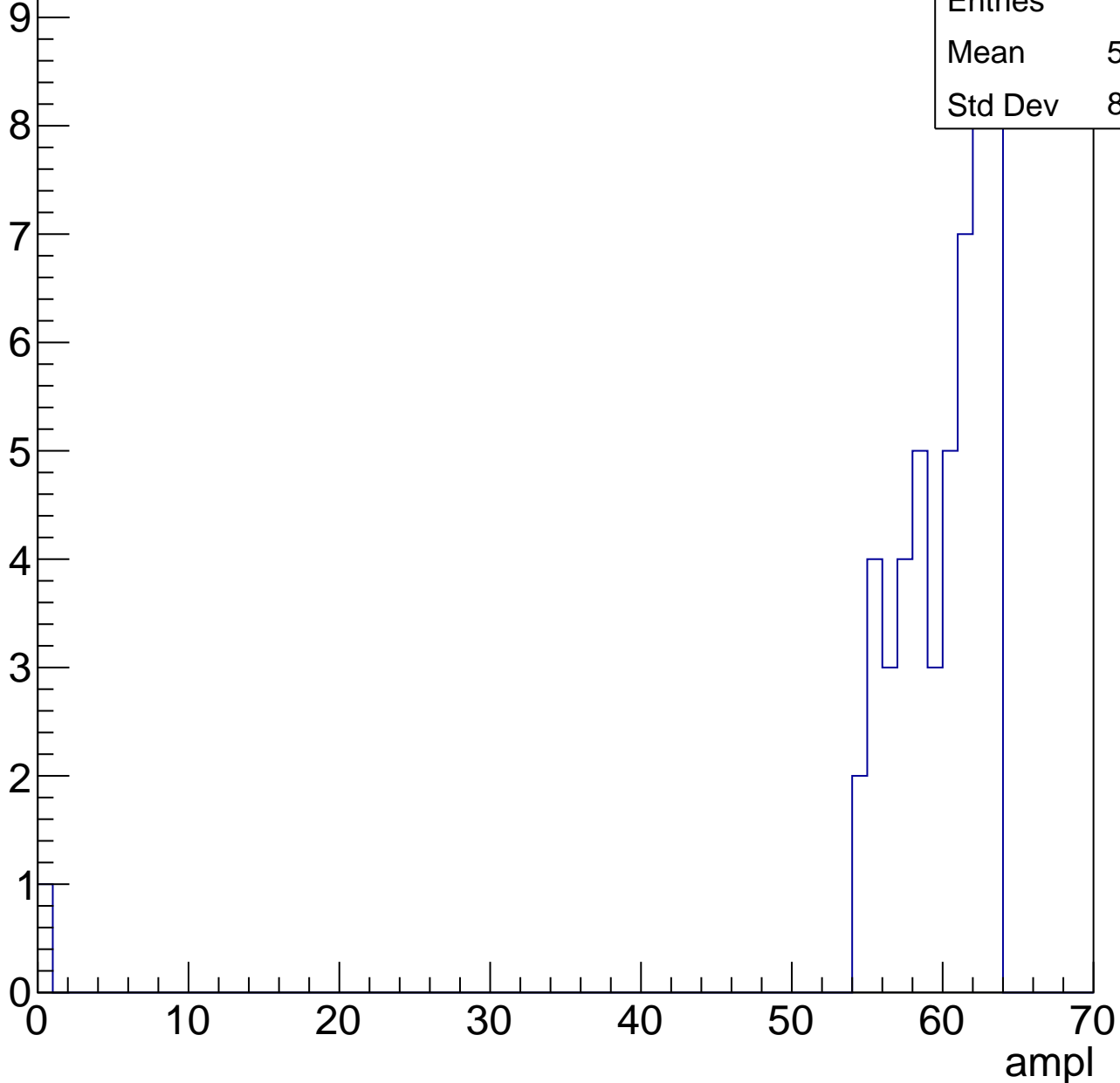


B1L102S, U26-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.43
Std Dev	8.709



B1L102S, U26-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

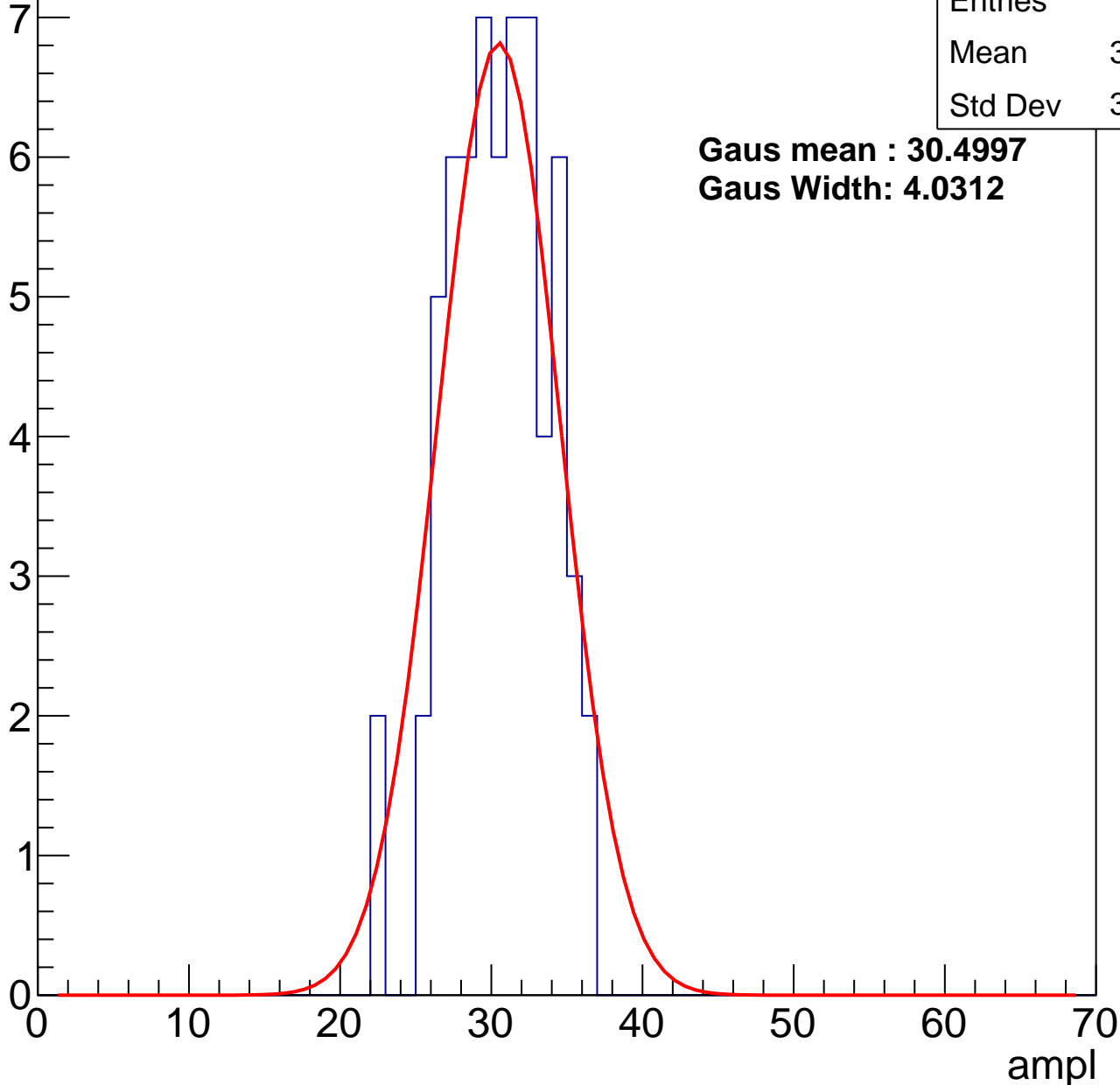
B1L102S, U26-ch117, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.02
Std Dev	3.229

Gaus mean : 30.4997
Gaus Width: 4.0312



B1L102S, U26-ch117, adc1

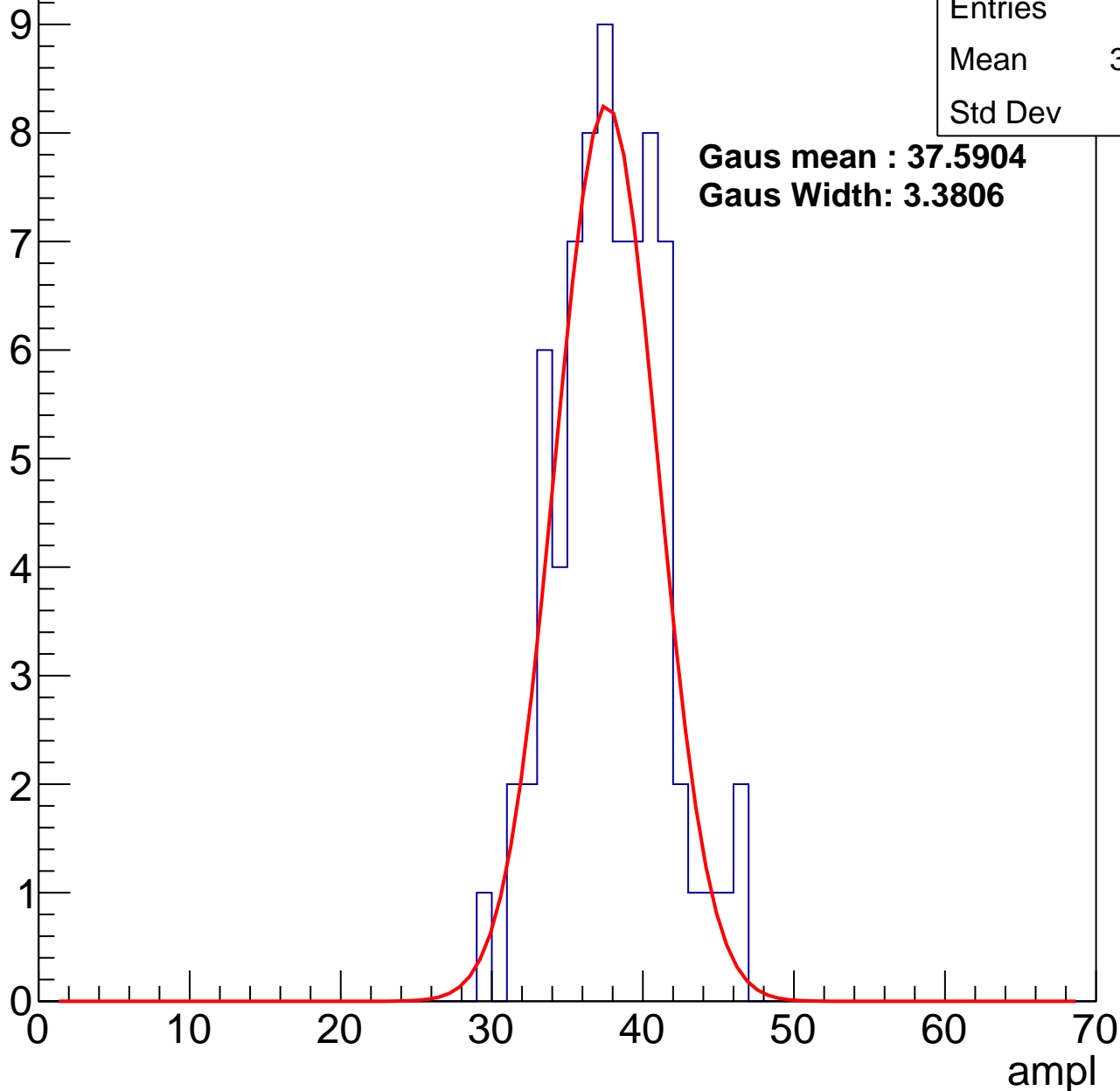
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	37.45
Std Dev	3.5

Gaus mean : 37.5904

Gaus Width: 3.3806



B1L102S, U26-ch117, adc2

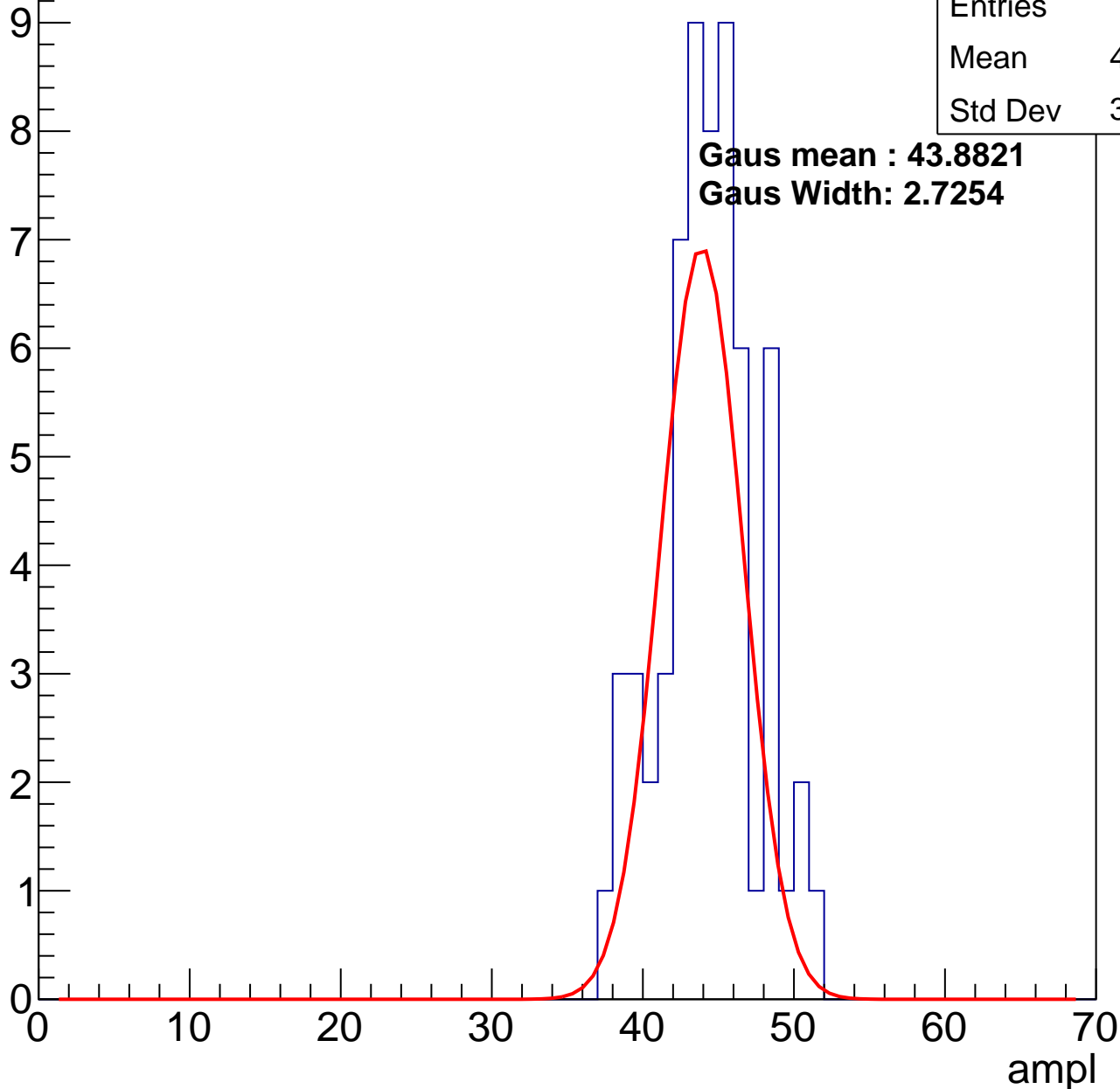
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.87
Std Dev	3.139

Gaus mean : 43.8821

Gaus Width: 2.7254

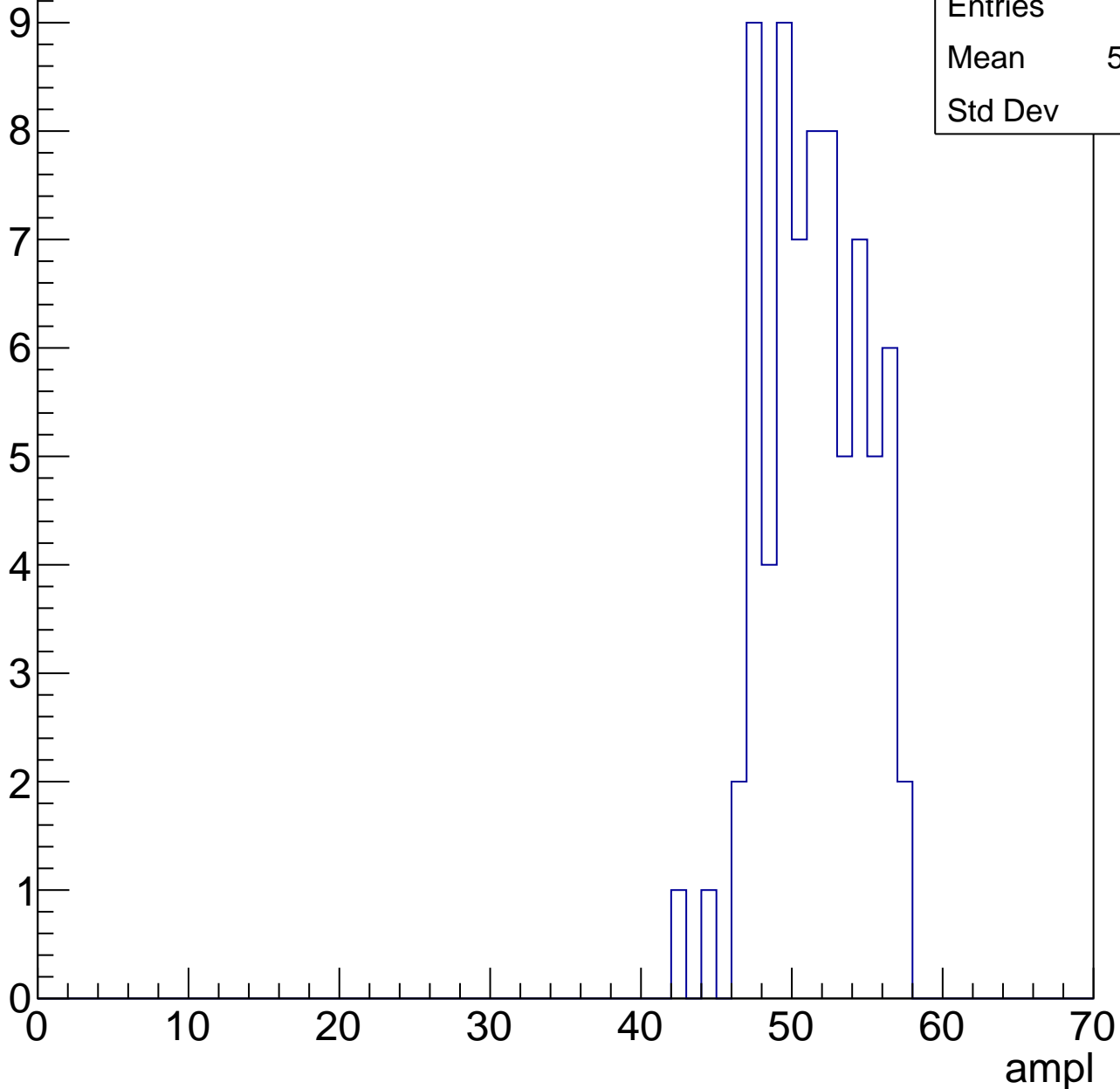


B1L102S, U26-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	51.03
Std Dev	3.28

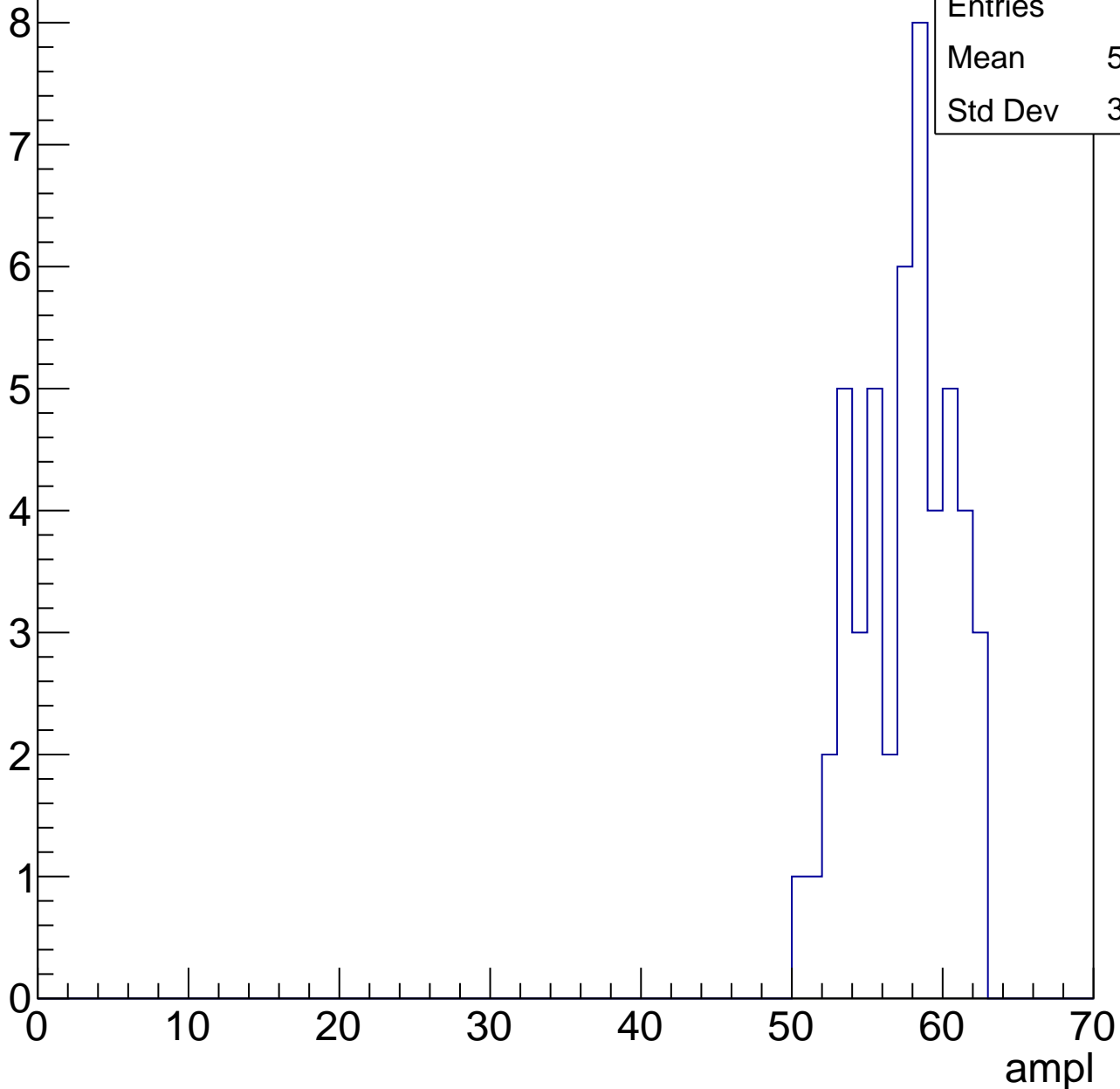


B1L102S, U26-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.96
Std Dev	3.103

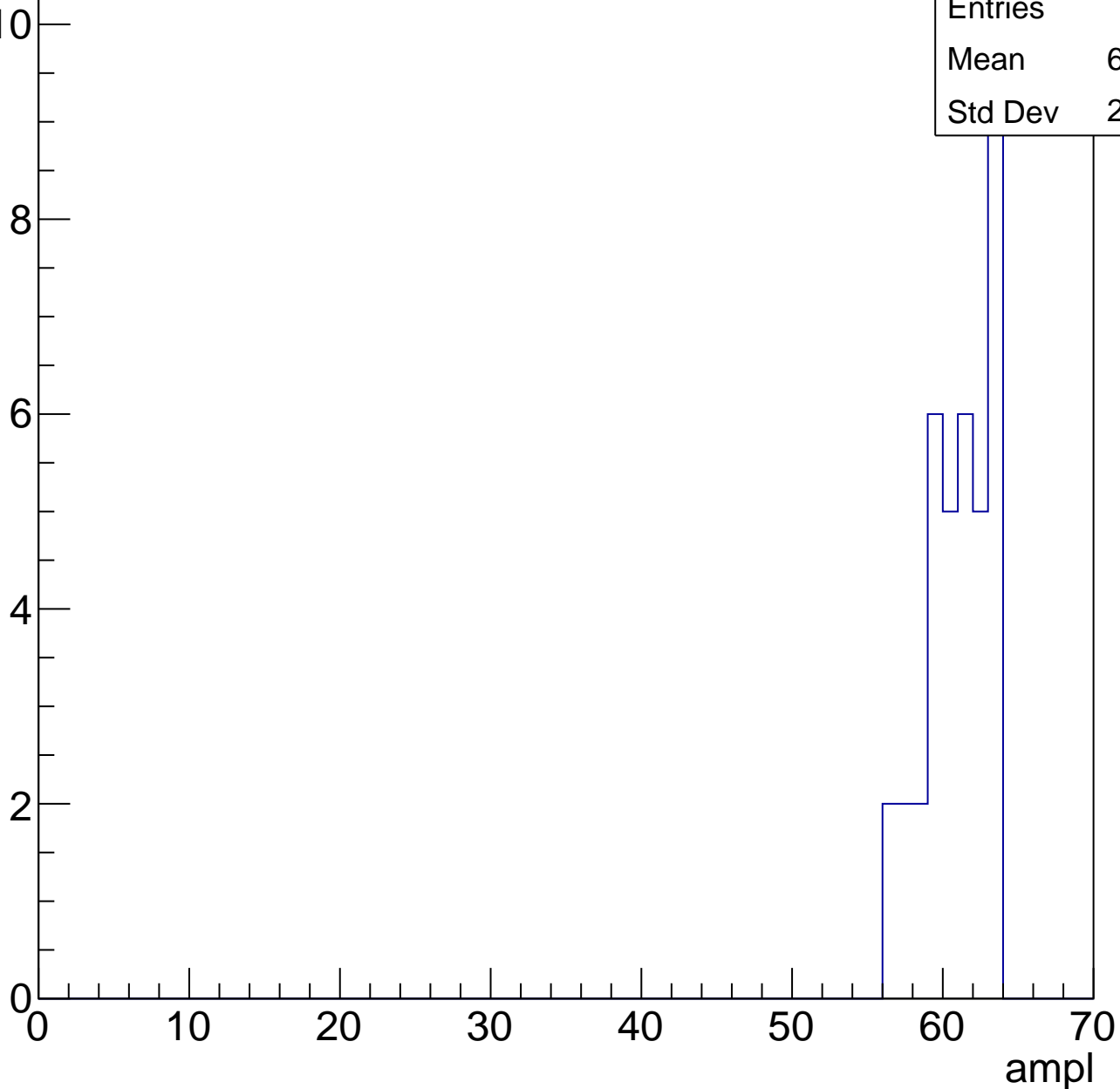


B1L102S, U26-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	60.58
Std Dev	2.098



B1L102S, U26-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	41.33
Std Dev	29.23

ampl

B1L102S, U26-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch118, adc0

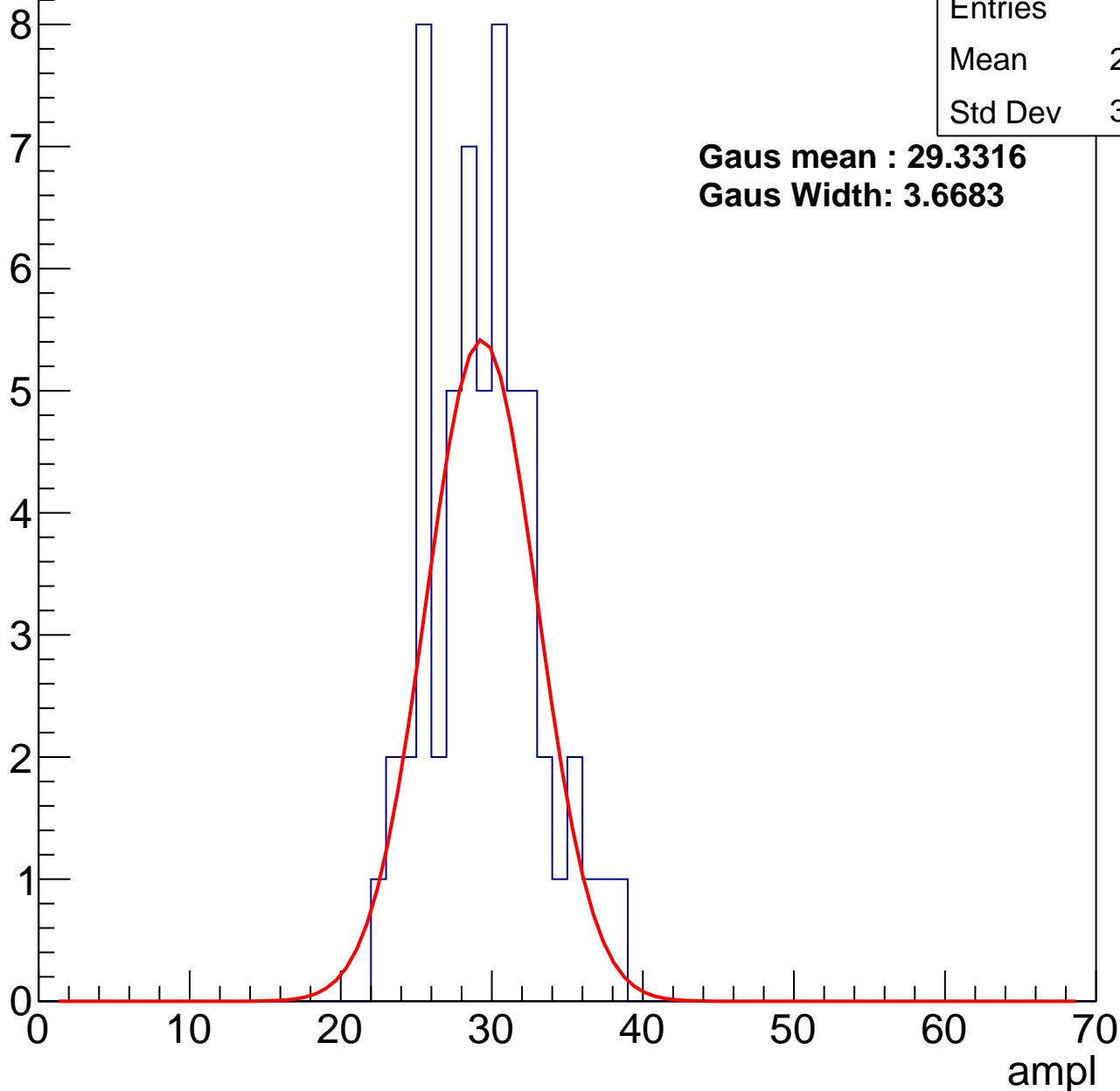
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.97
Std Dev	3.572

Gaus mean : 29.3316

Gaus Width: 3.6683



B1L102S, U26-ch118, adc1

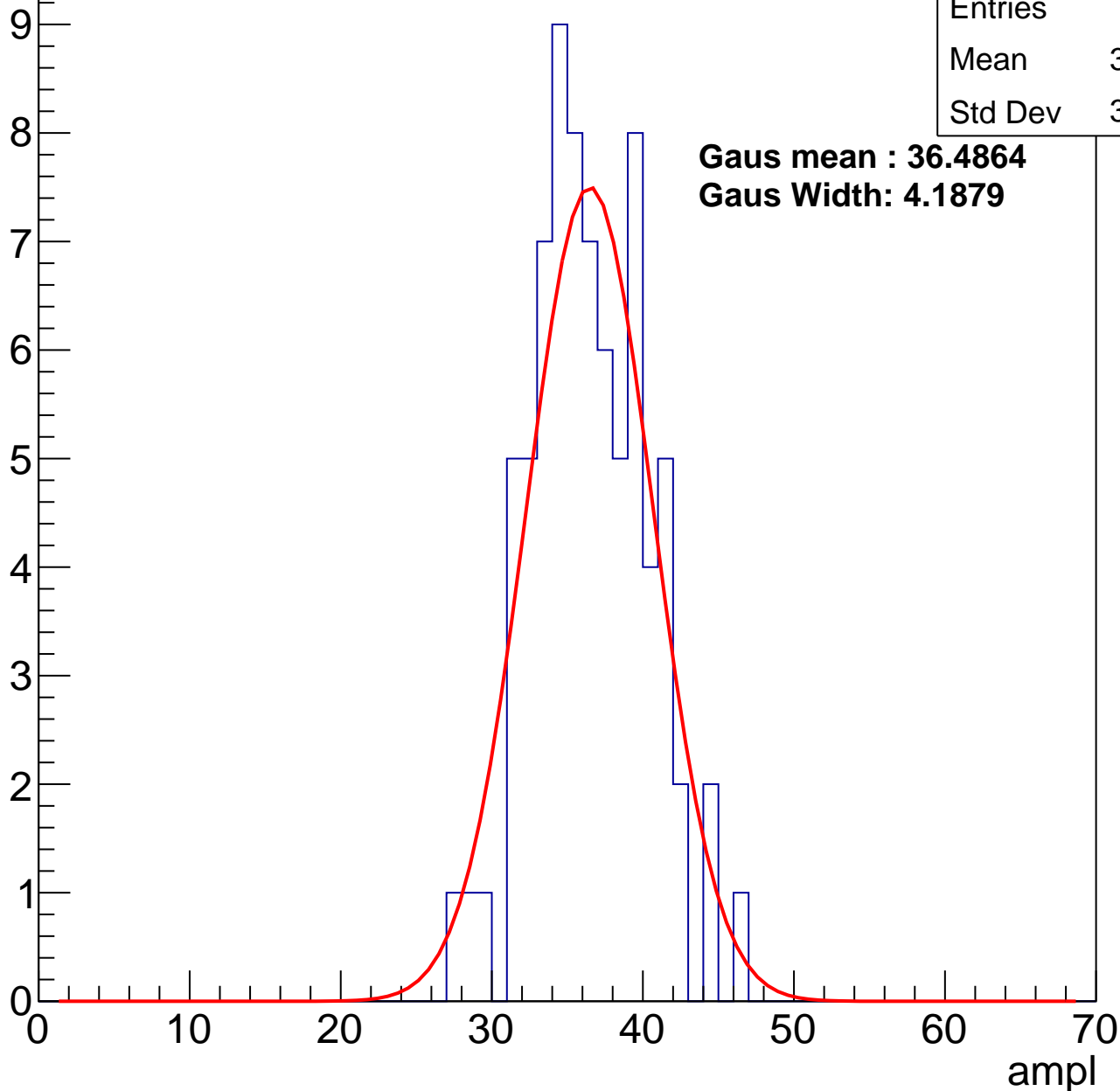
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.04
Std Dev	3.767

Gaus mean : 36.4864

Gaus Width: 4.1879



B1L102S, U26-ch118, adc2

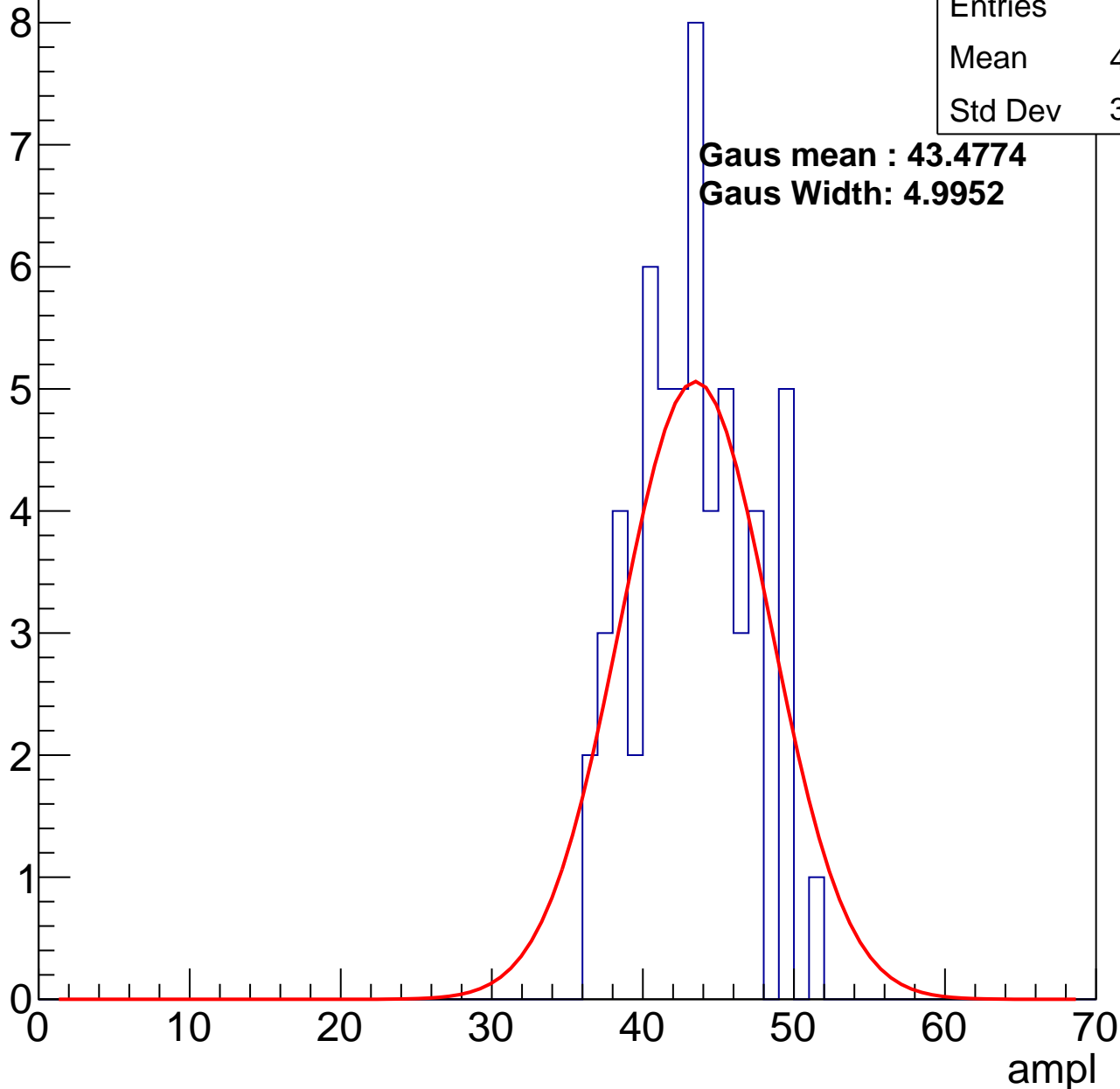
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.72
Std Dev	3.679

Gaus mean : 43.4774

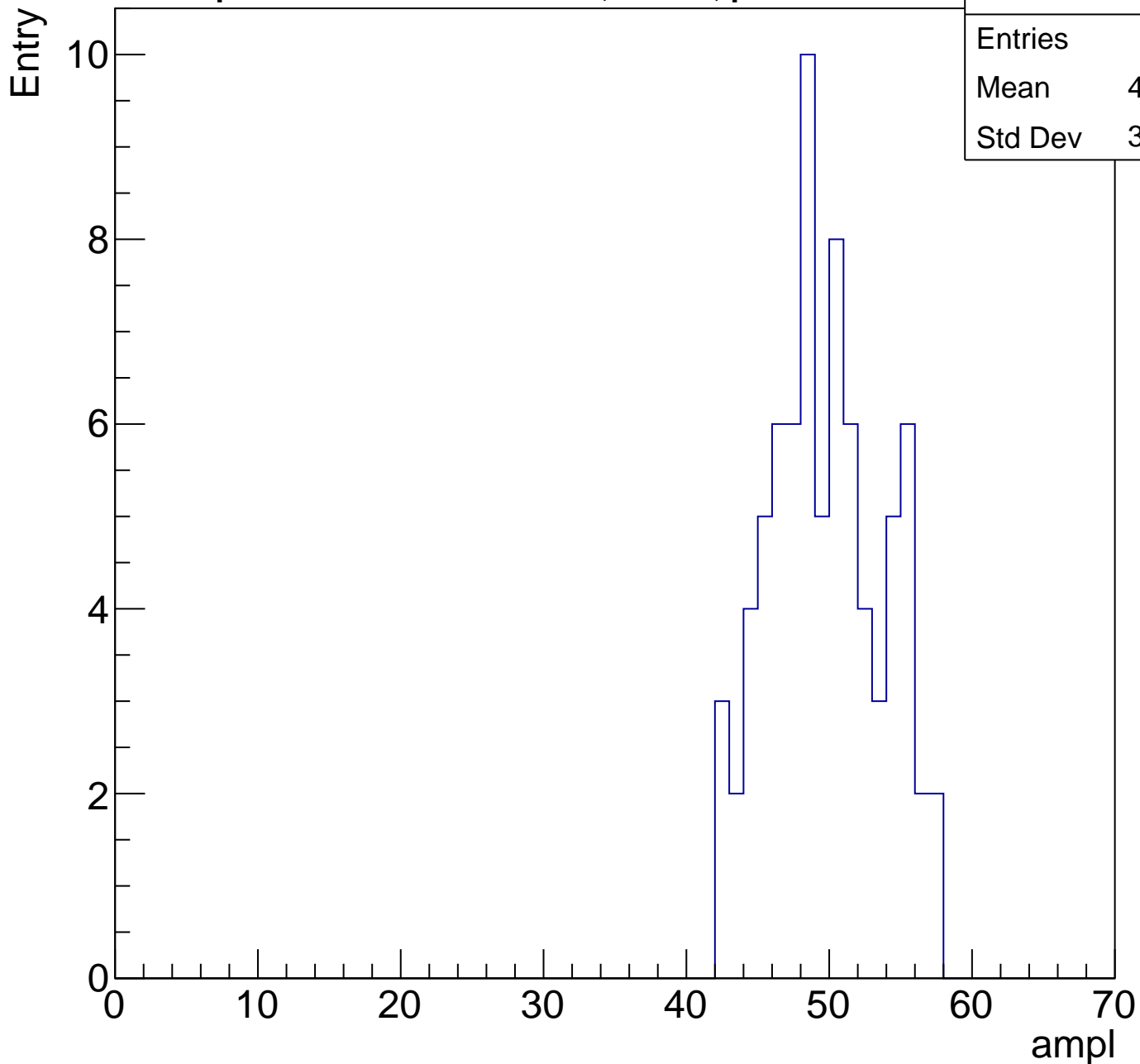
Gaus Width: 4.9952



B1L102S, U26-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	49.29
Std Dev	3.888

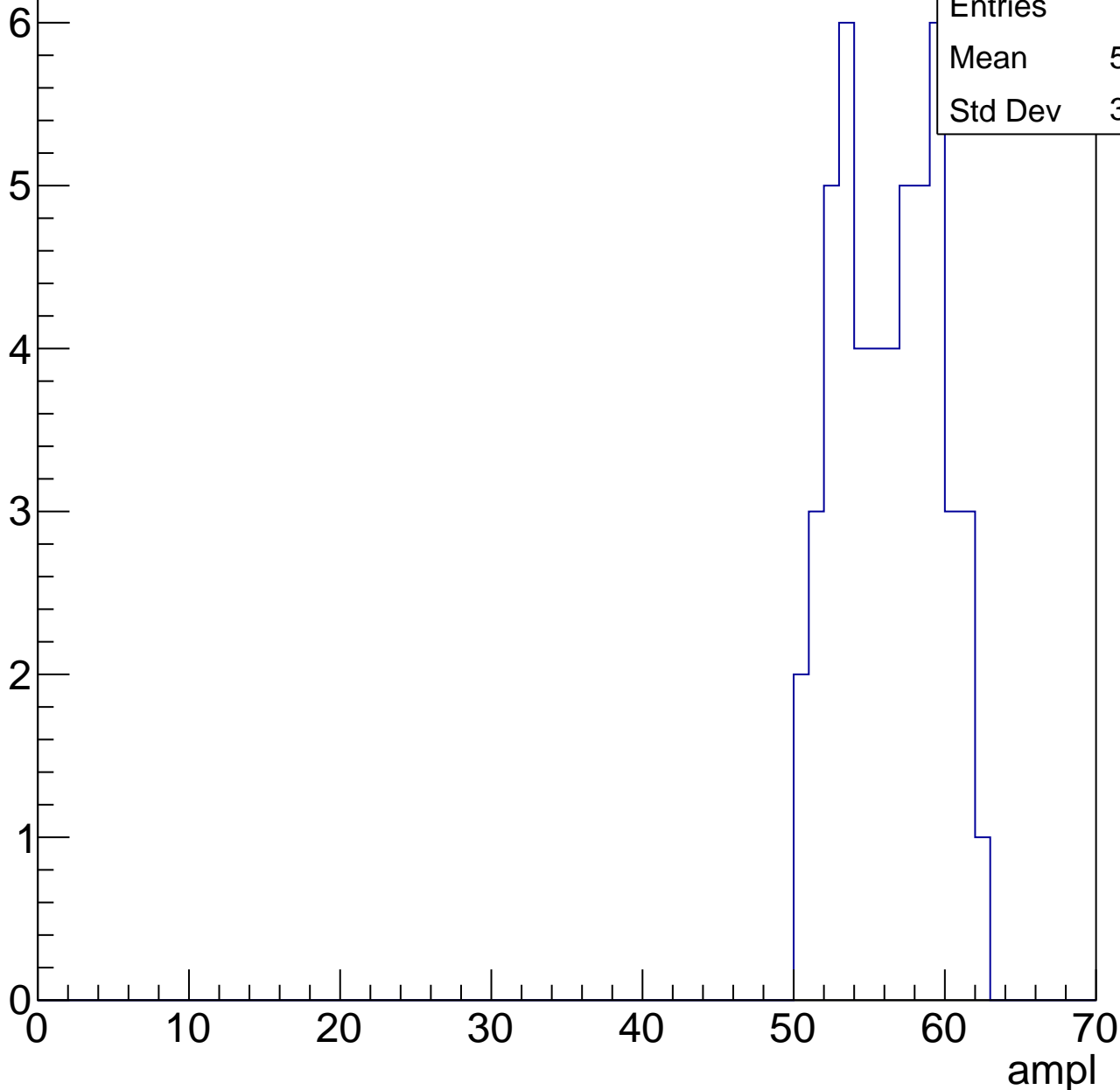


B1L102S, U26-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.78
Std Dev	3.244

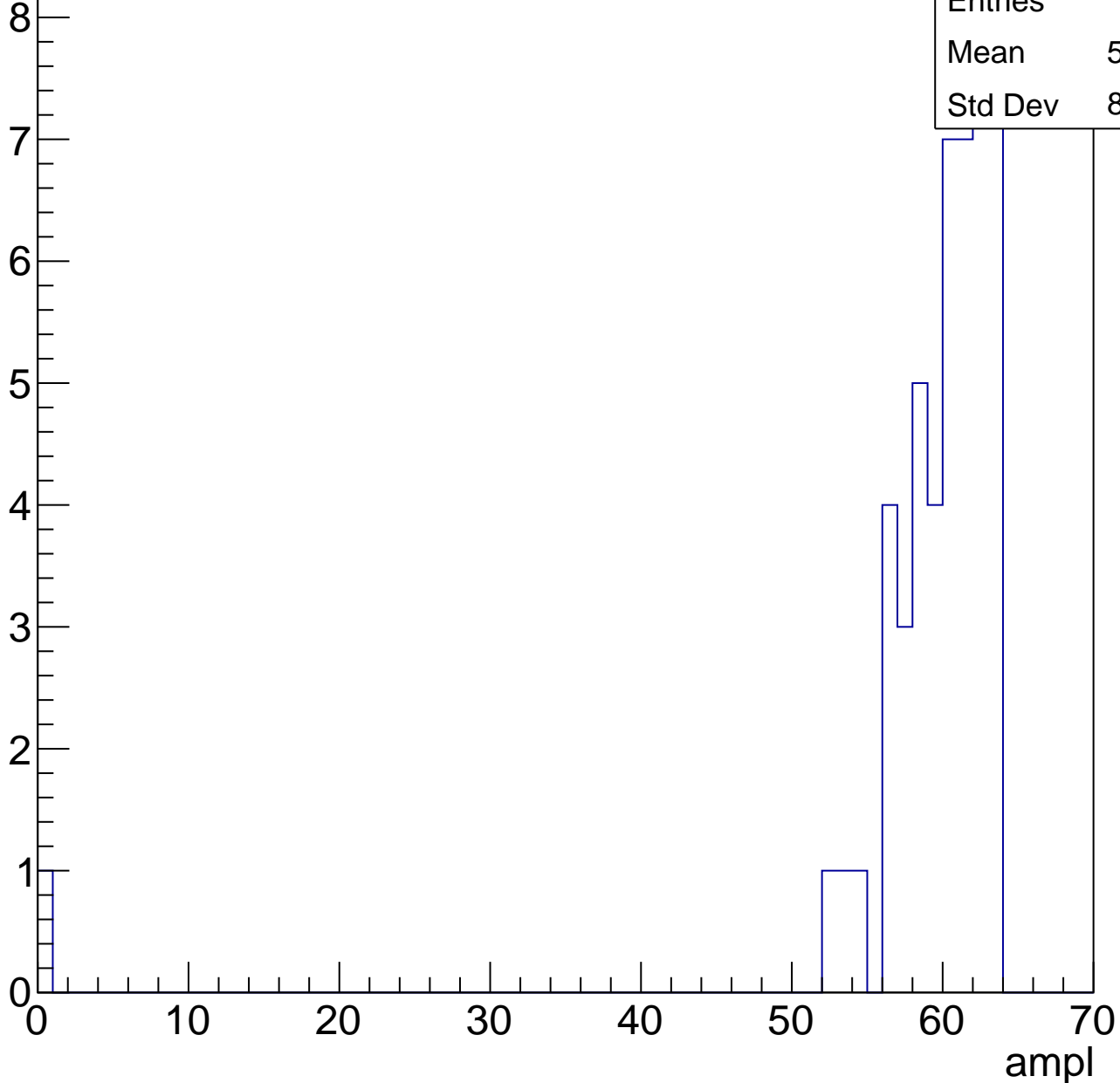


B1L102S, U26-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.54
Std Dev	8.796



B1L102S, U26-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U26-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U26-ch119, adc0

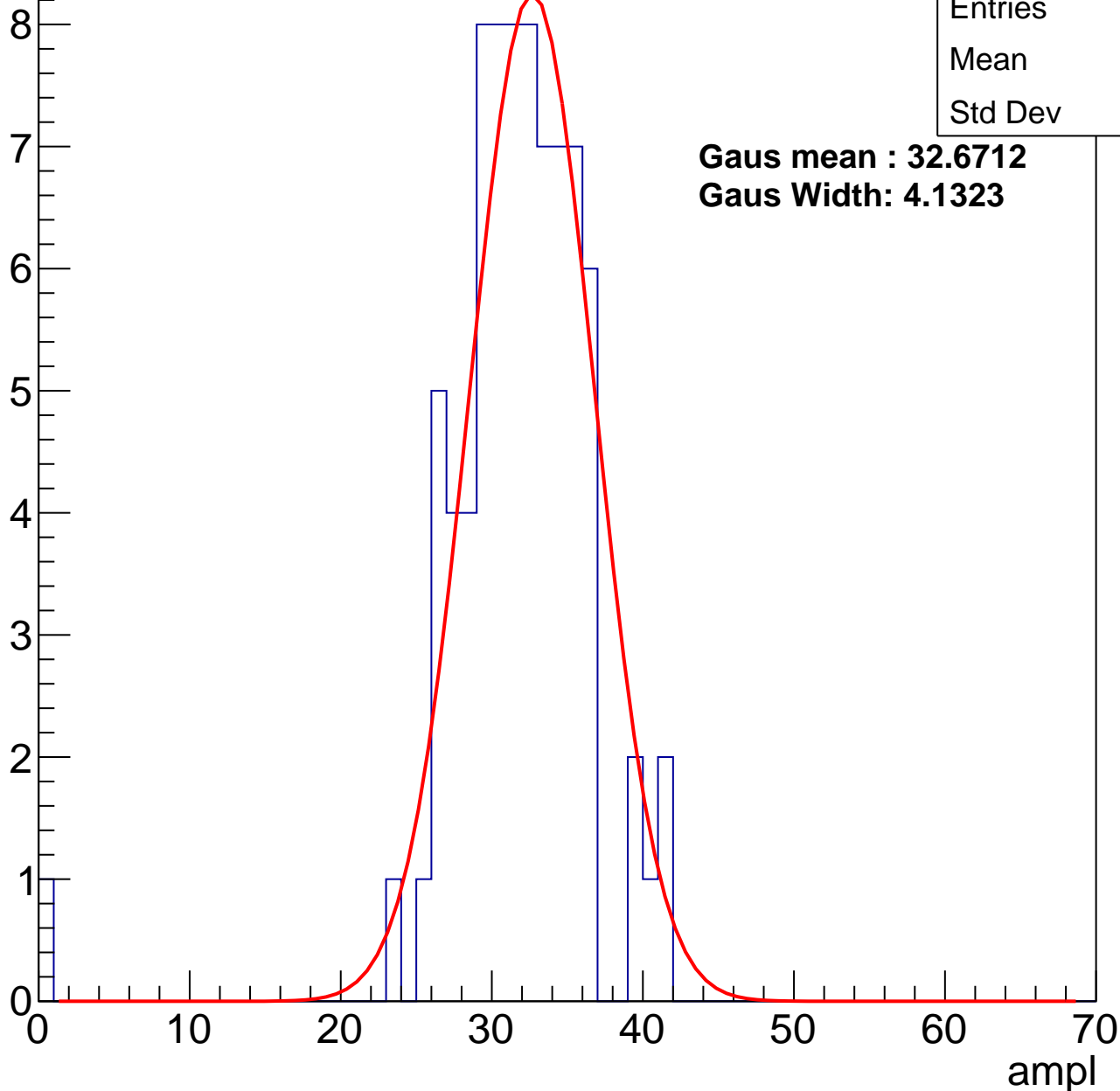
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	31.3
Std Dev	5.11

Gaus mean : 32.6712

Gaus Width: 4.1323



B1L102S, U26-ch119, adc1

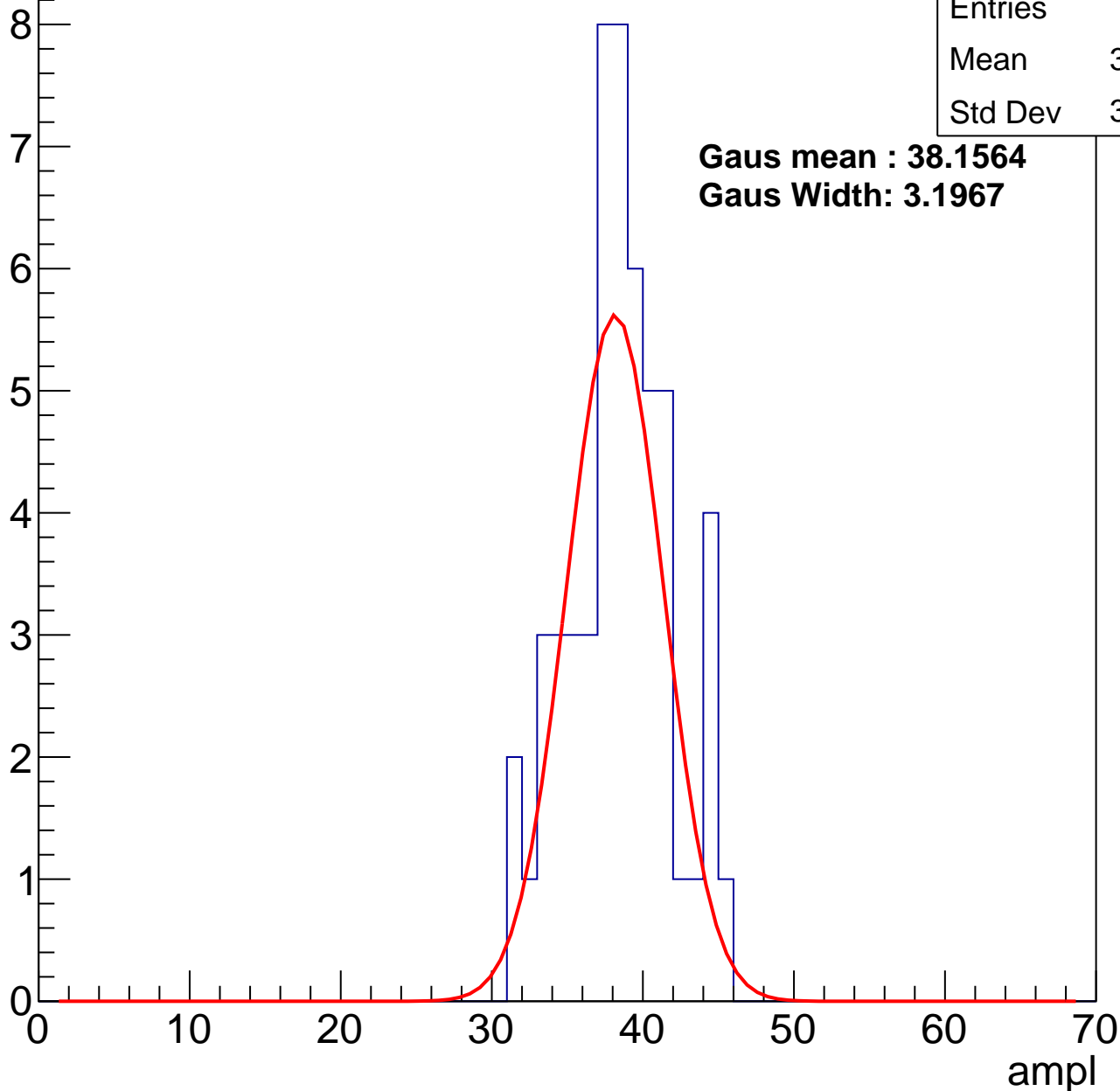
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	38.02
Std Dev	3.358

Gaus mean : 38.1564

Gaus Width: 3.1967



B1L102S, U26-ch119, adc2

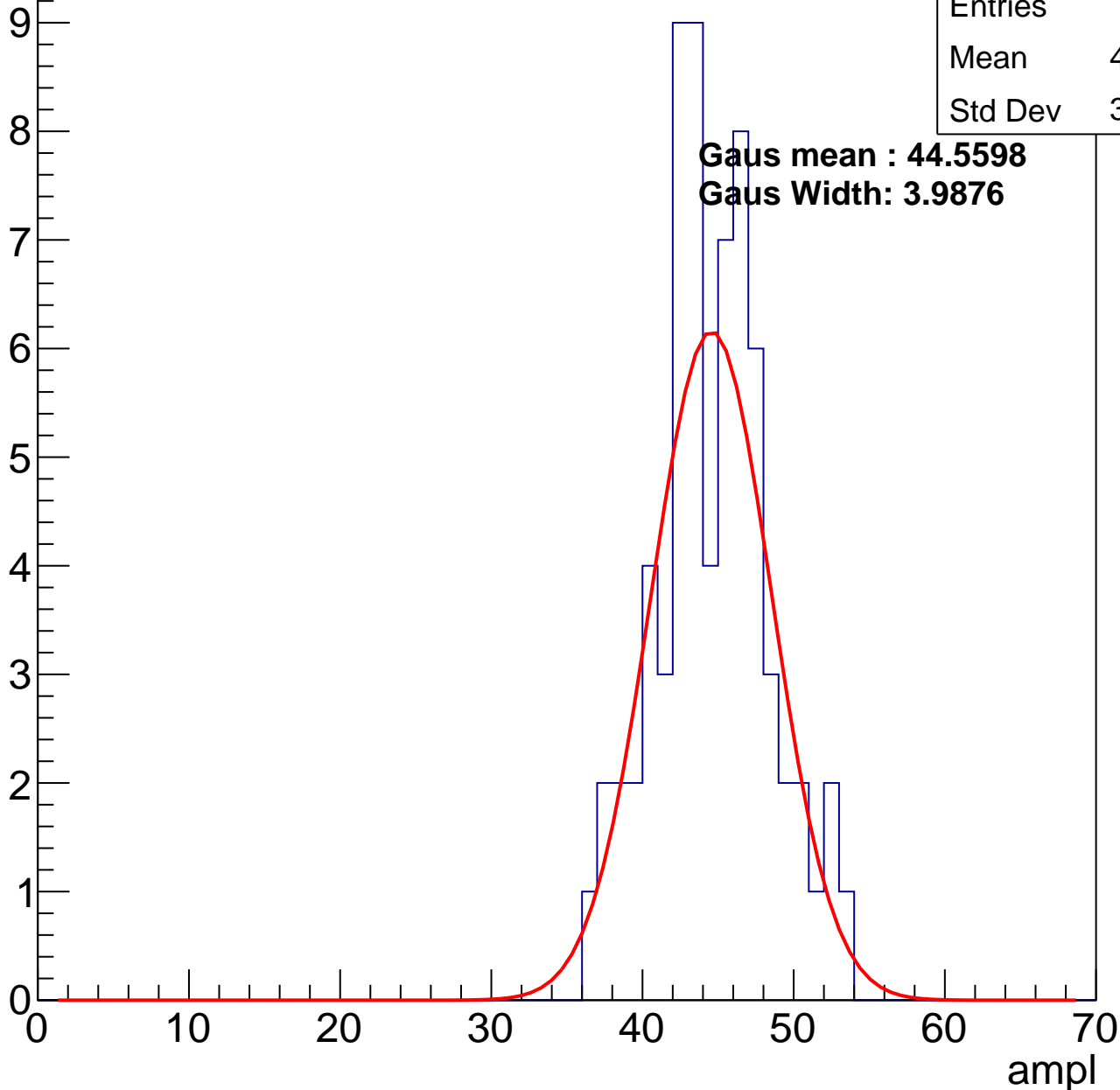
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.16
Std Dev	3.716

Gaus mean : 44.5598

Gaus Width: 3.9876

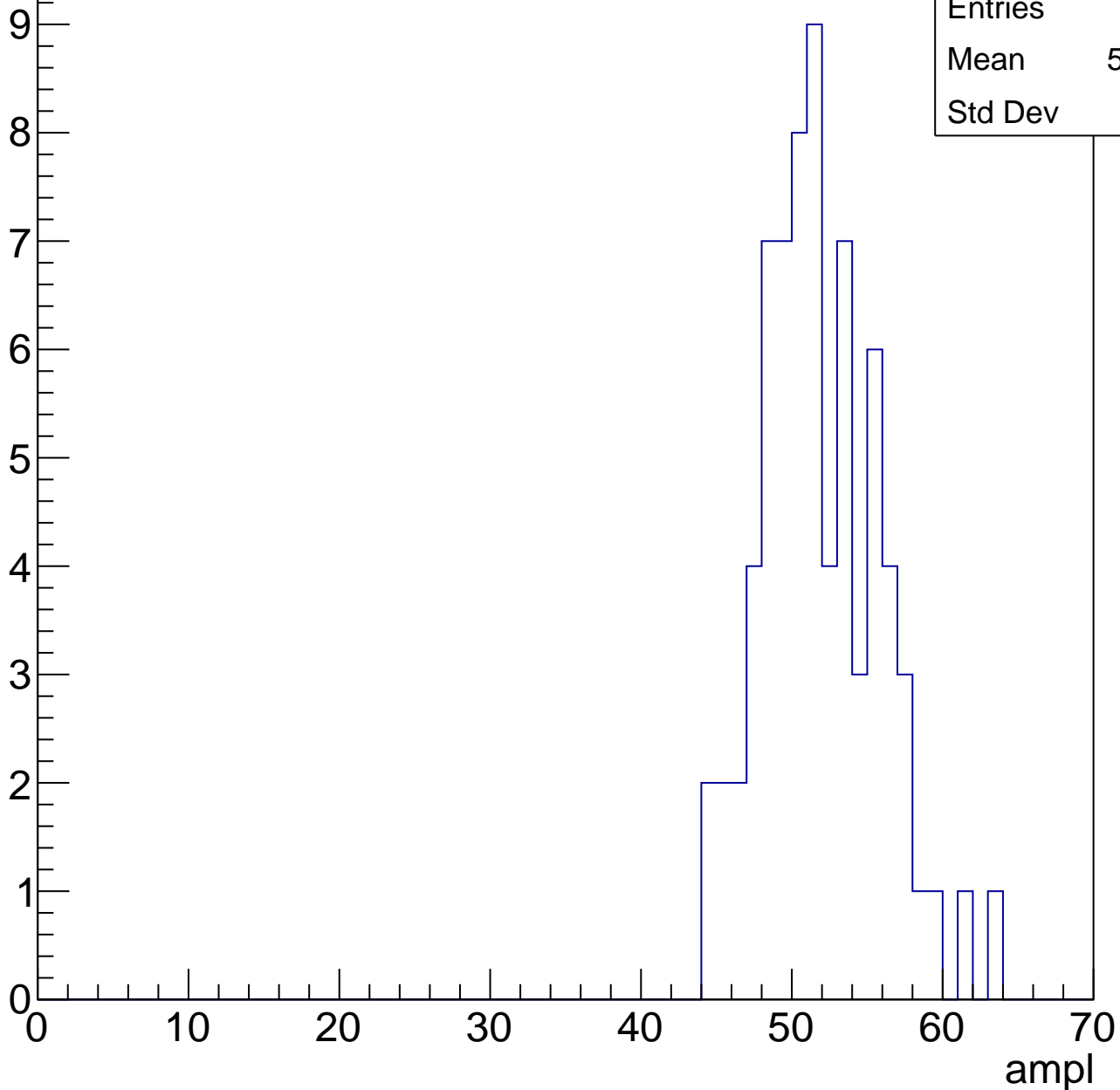


B1L102S, U26-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	51.43
Std Dev	3.89

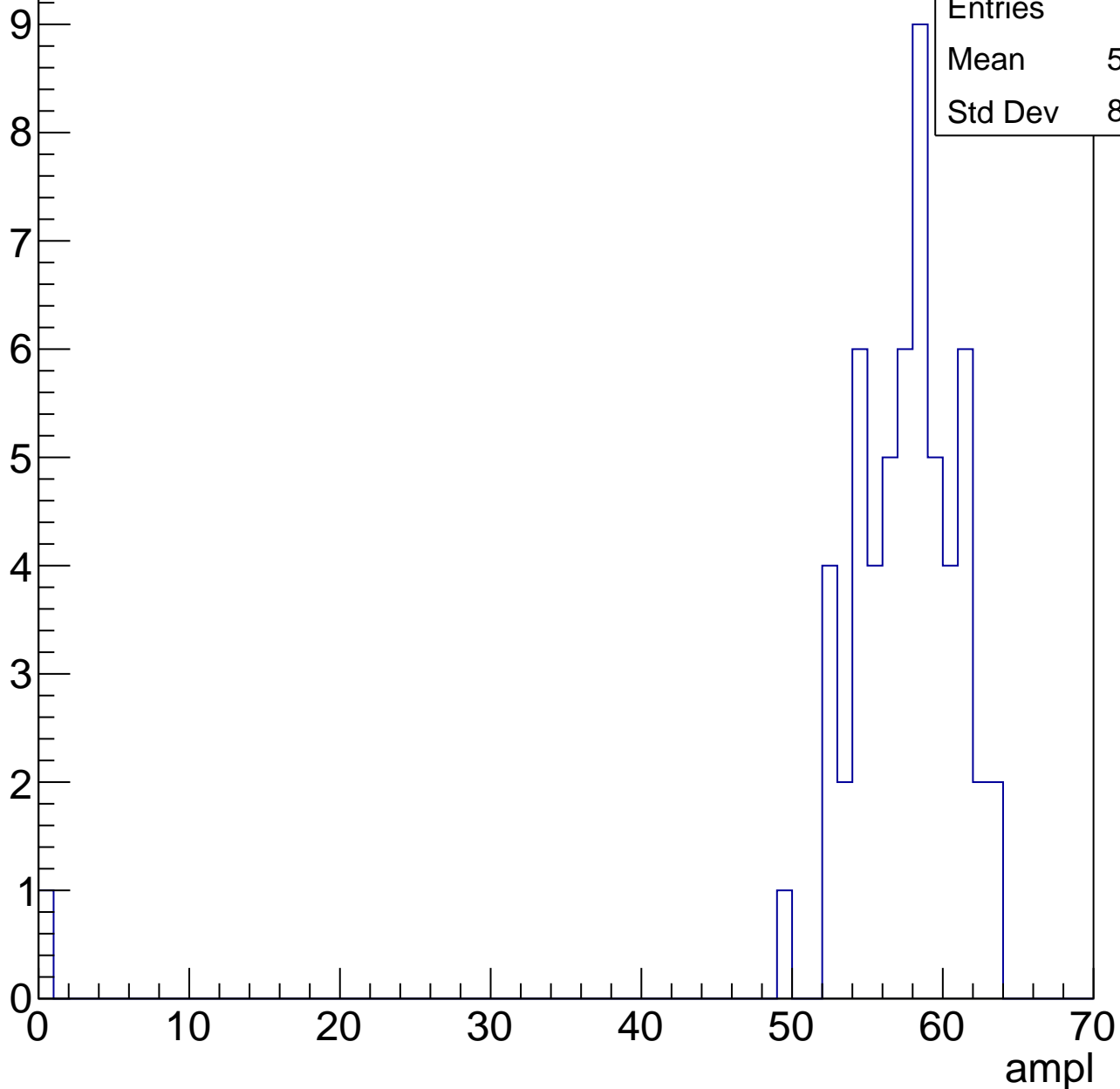


B1L102S, U26-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.18
Std Dev	8.126

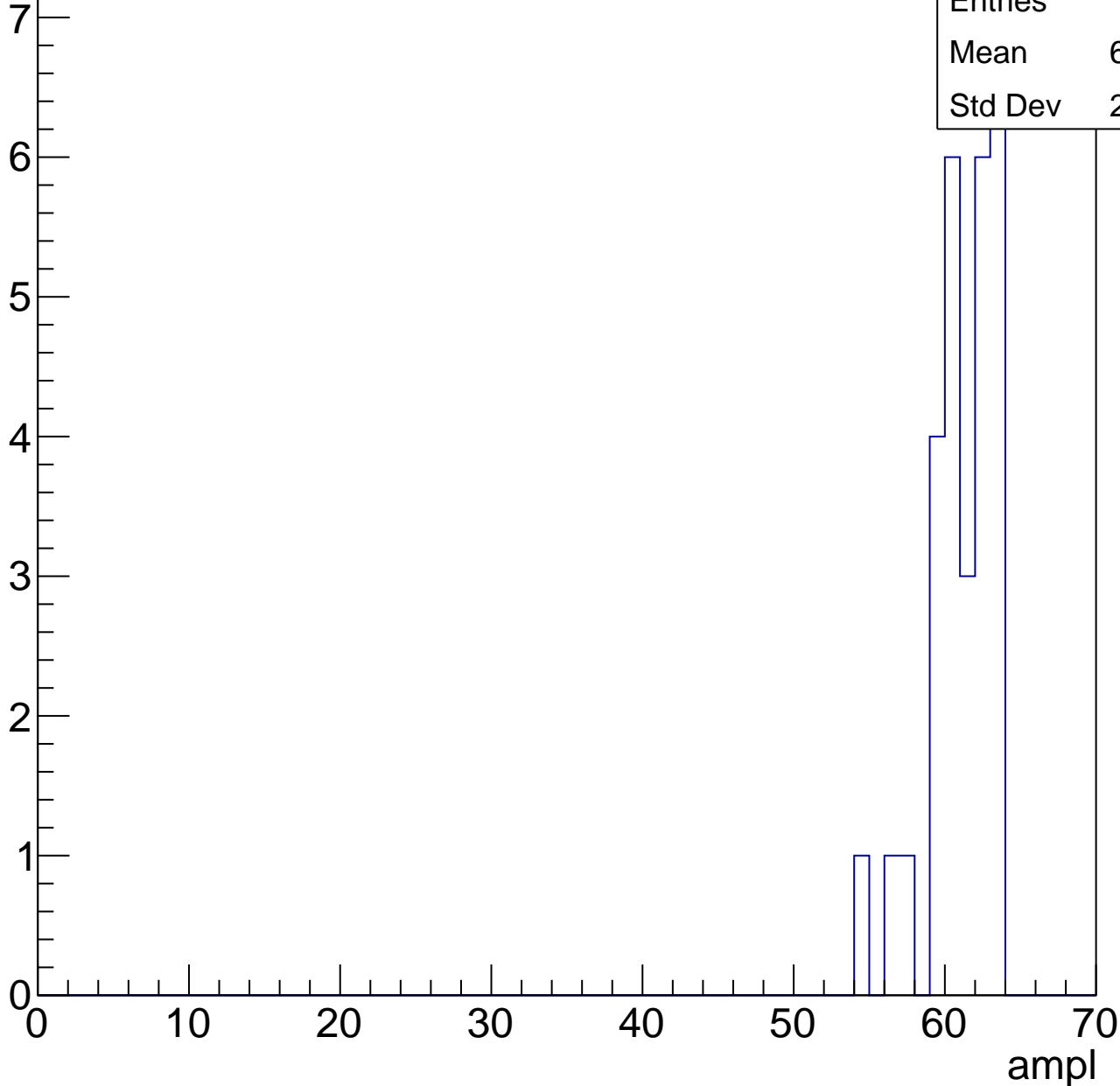


B1L102S, U26-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	60.66
Std Dev	2.217



B1L102S, U26-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

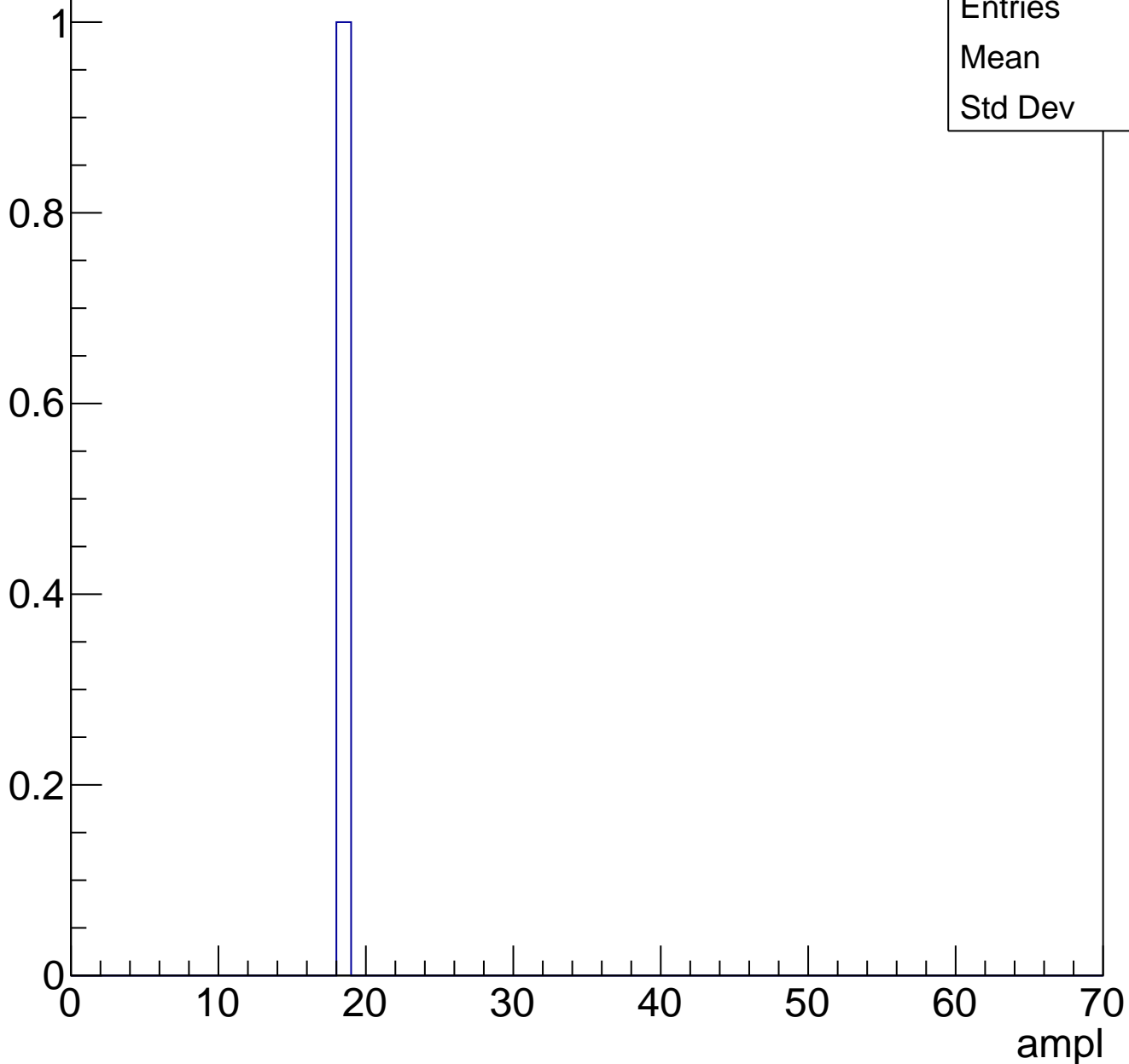
Entries	2
Mean	62
Std Dev	0

ampl

B1L102S, U26-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch120, adc0

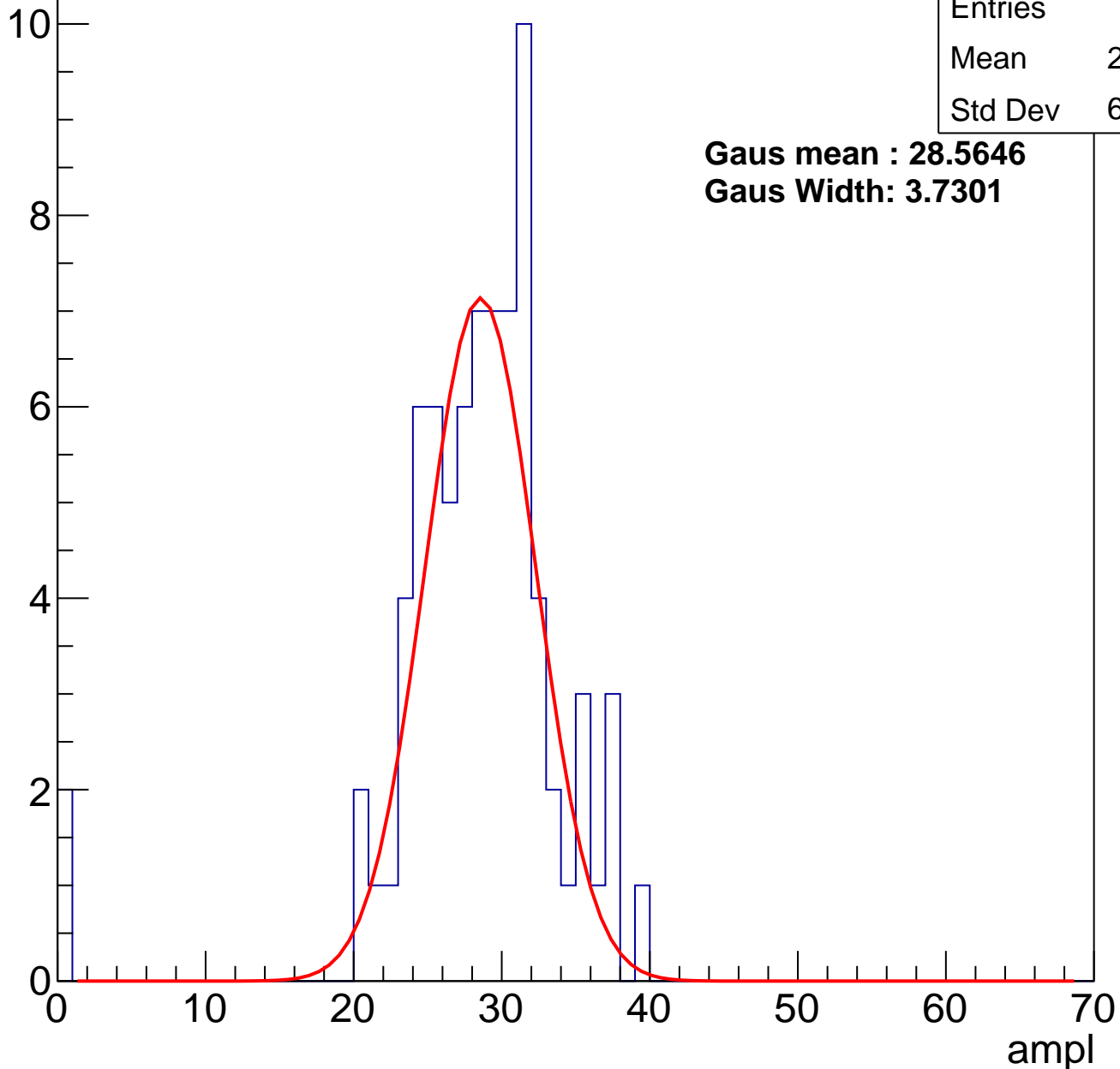
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	27.84
Std Dev	6.064

Gaus mean : 28.5646

Gaus Width: 3.7301

Entry



B1L102S, U26-ch120, adc1

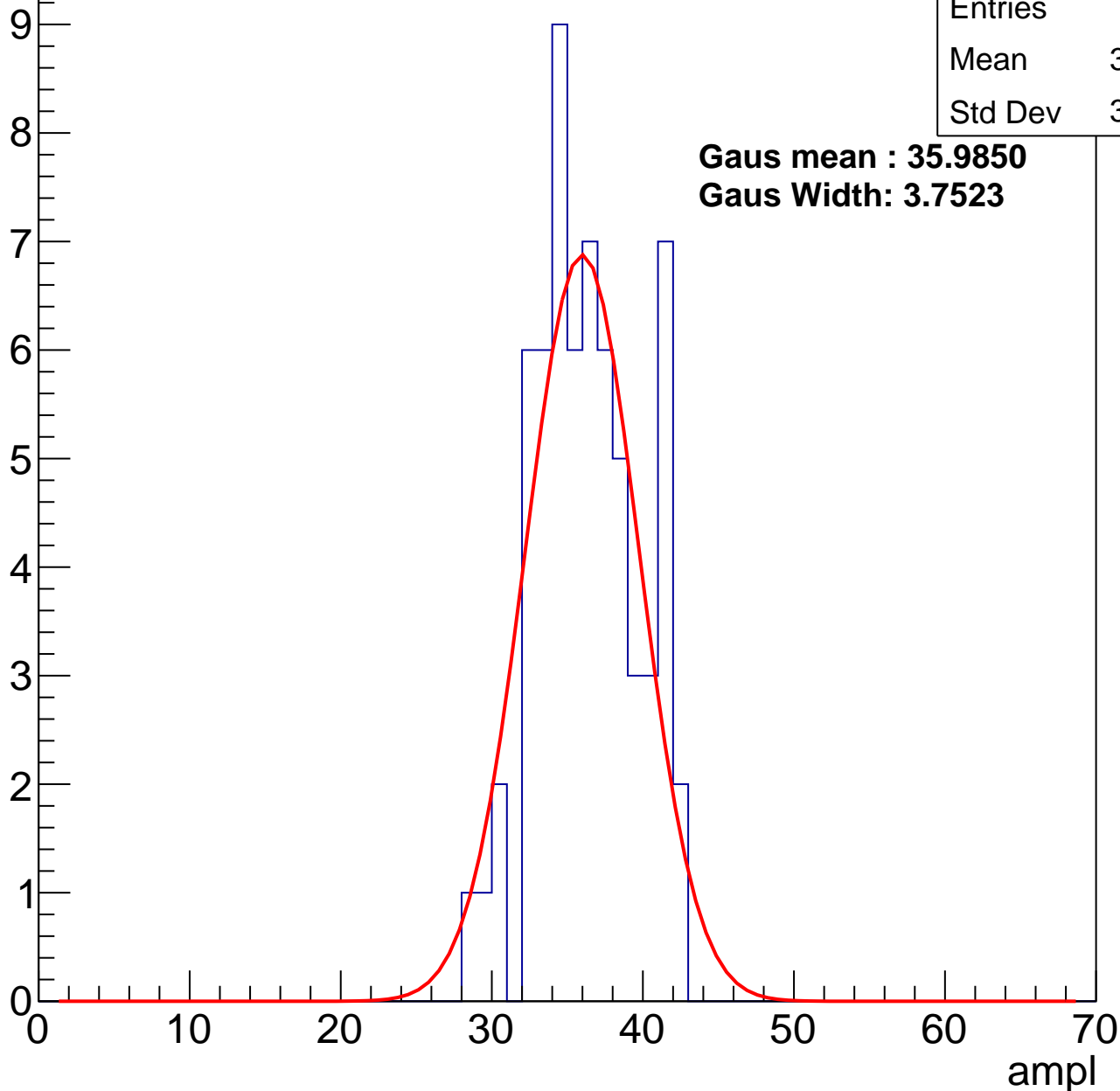
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.86
Std Dev	3.363

Gaus mean : 35.9850

Gaus Width: 3.7523



B1L102S, U26-ch120, adc2

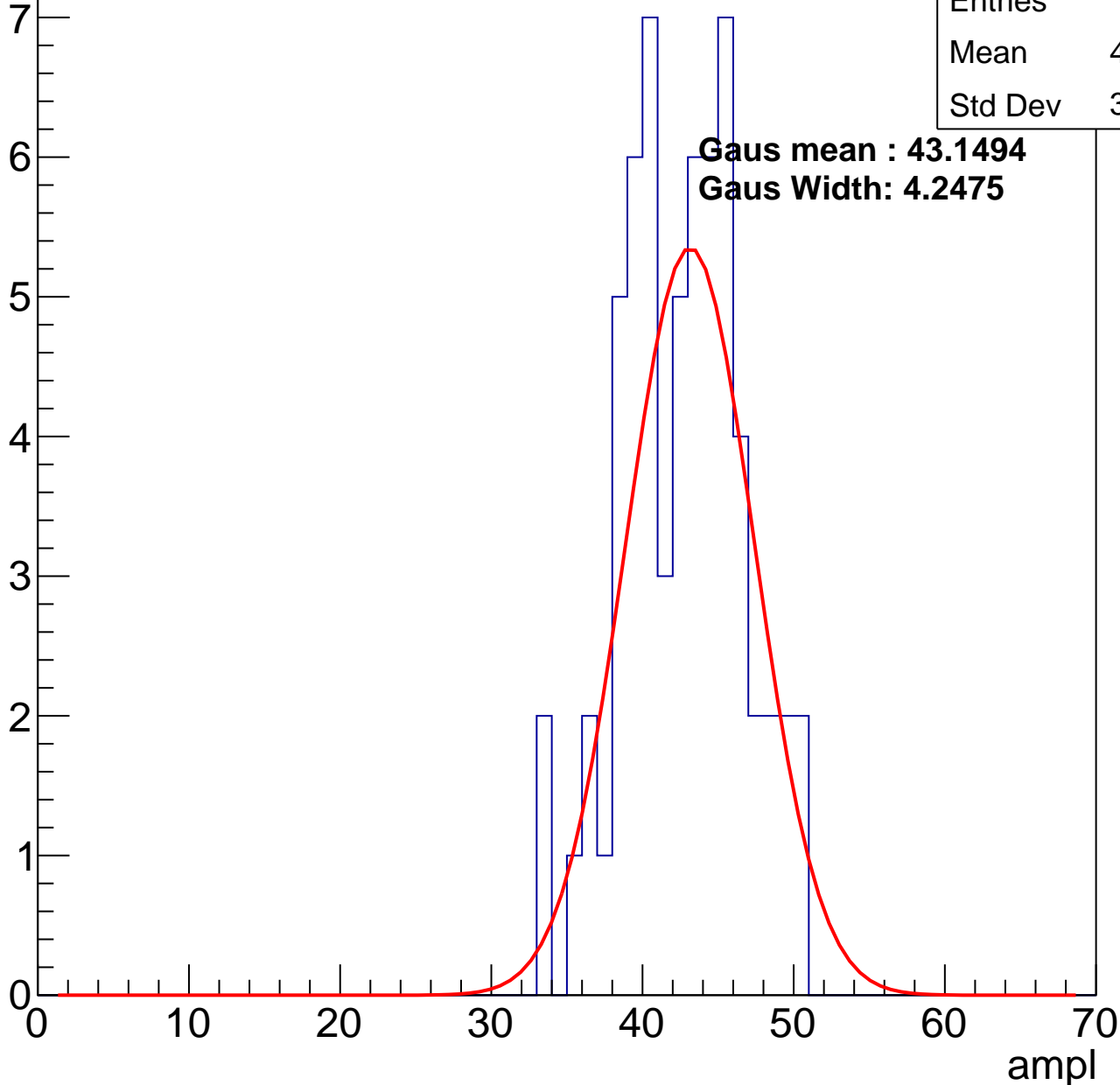
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.16
Std Dev	3.937

Gaus mean : 43.1494

Gaus Width: 4.2475

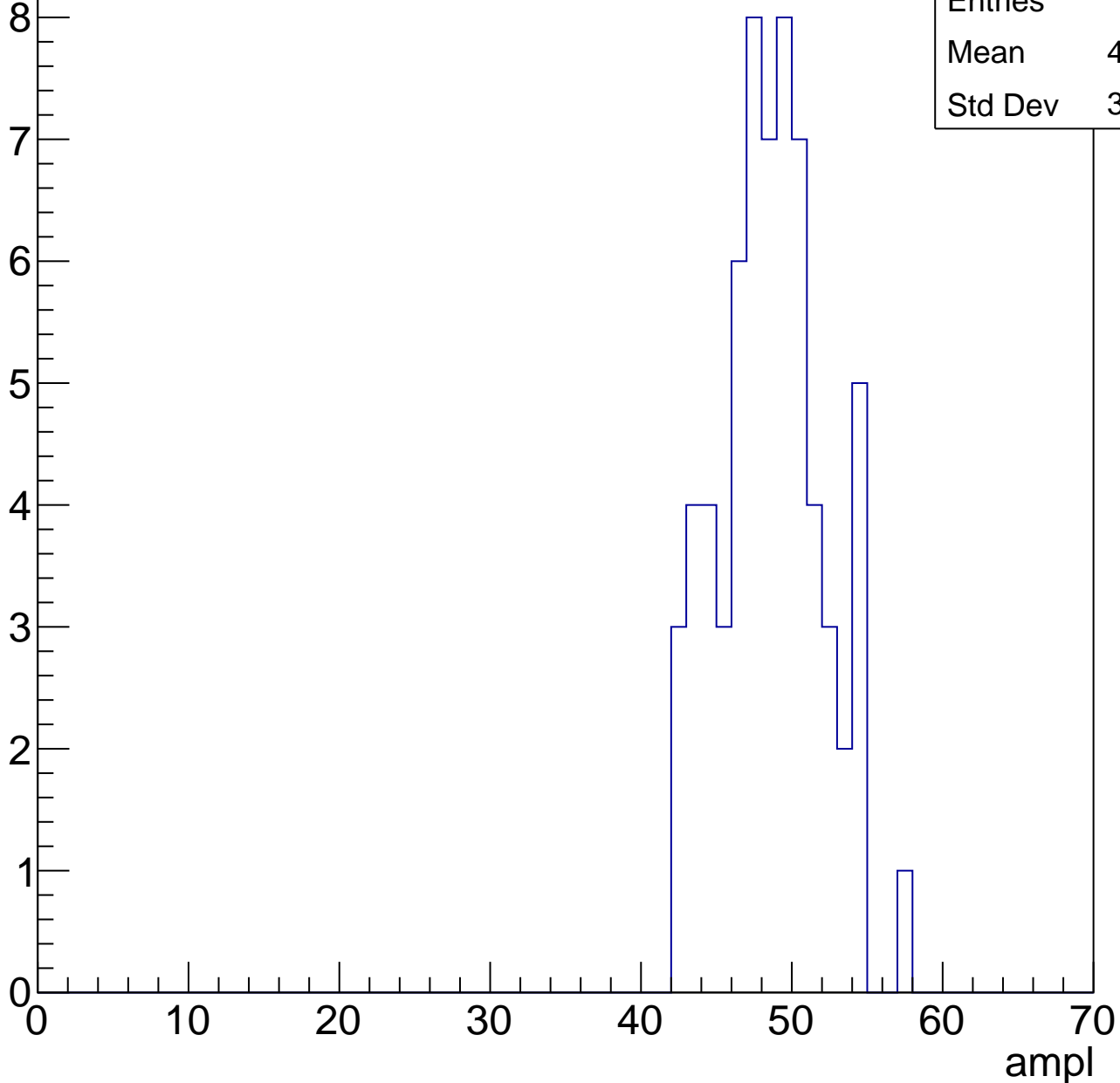


B1L102S, U26-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.18
Std Dev	3.419

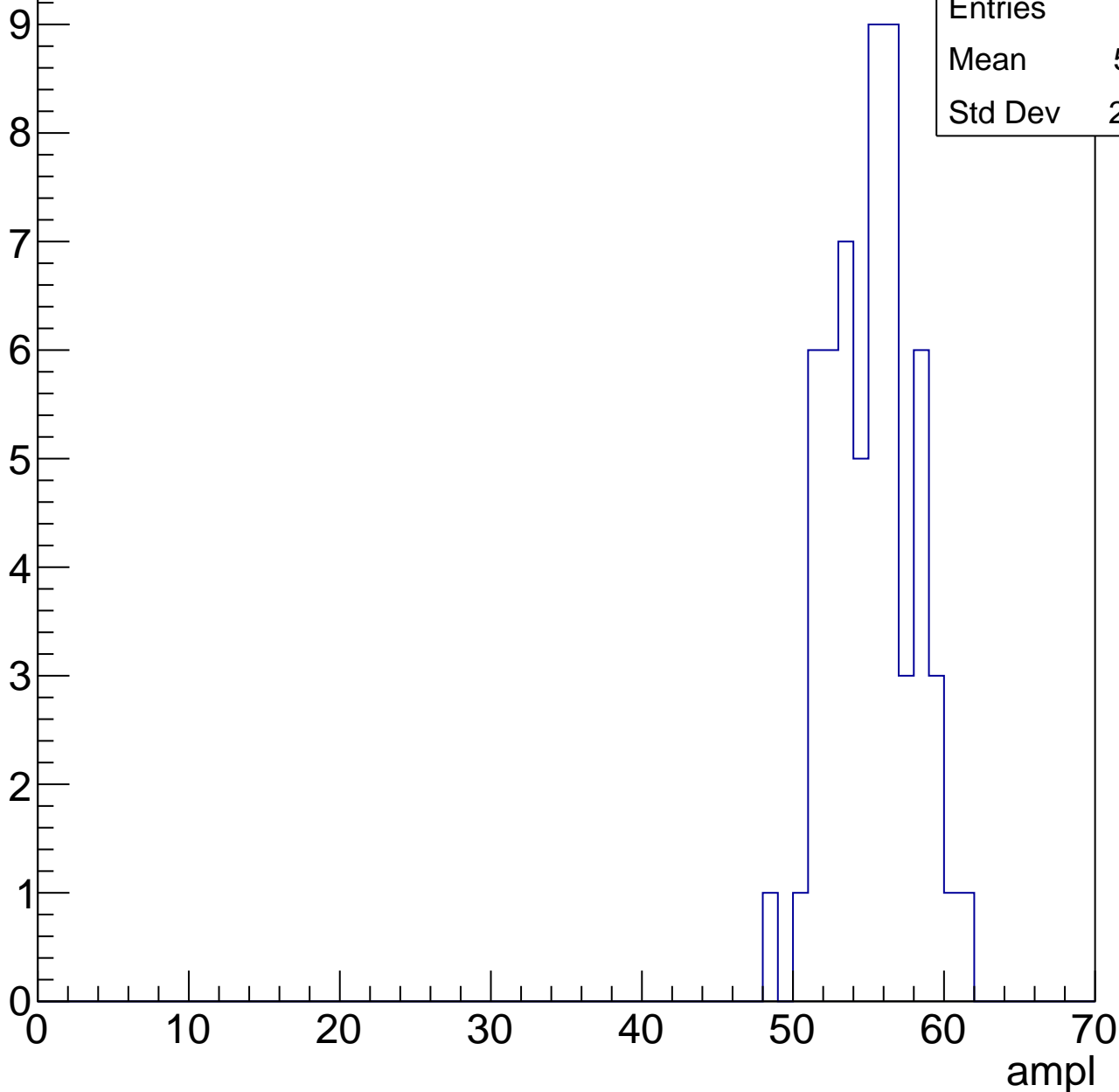


B1L102S, U26-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.71
Std Dev	2.742

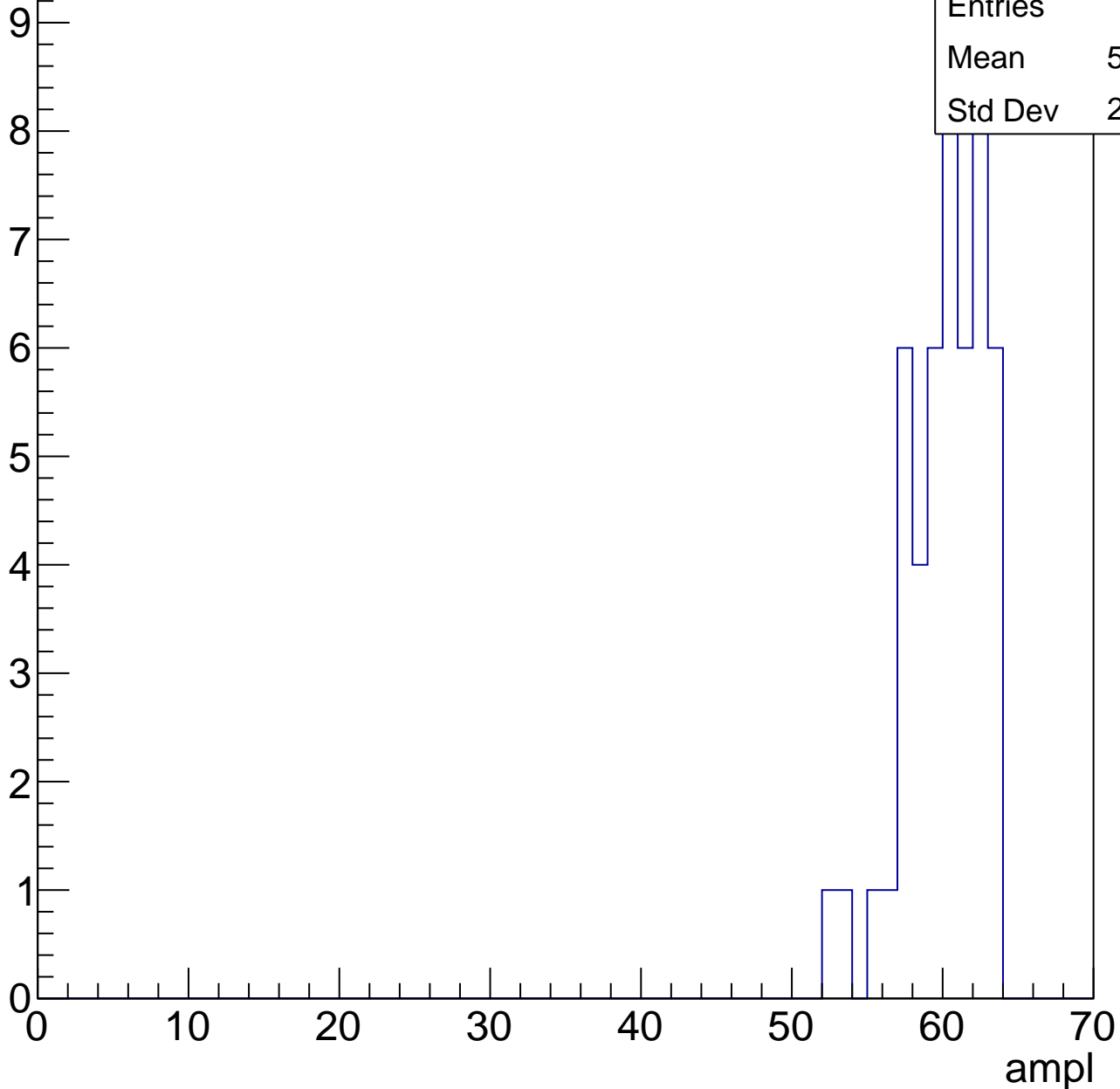


B1L102S, U26-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	59.72
Std Dev	2.538



B1L102S, U26-ch120, adc6

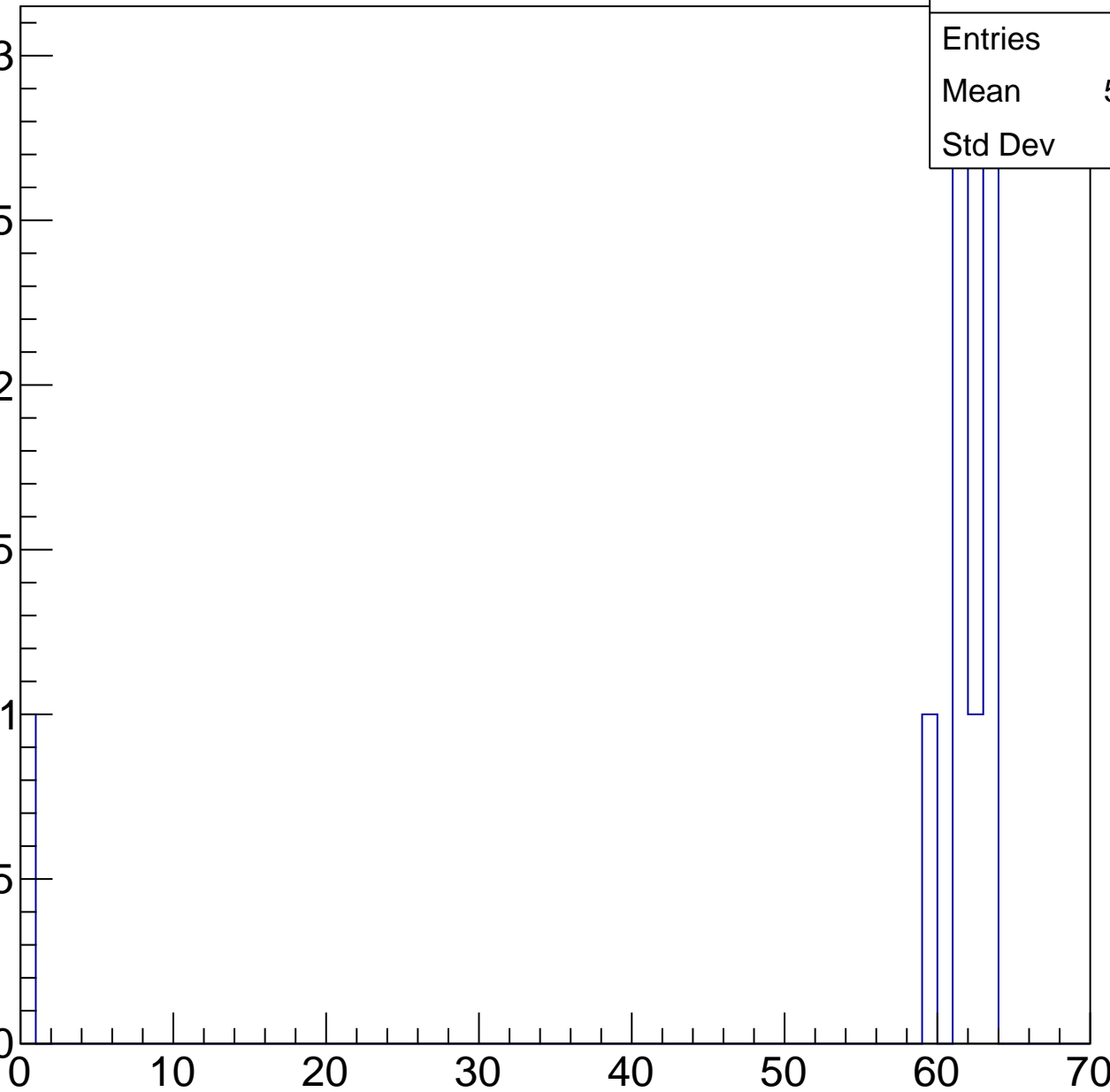
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.78
Std Dev	19.41

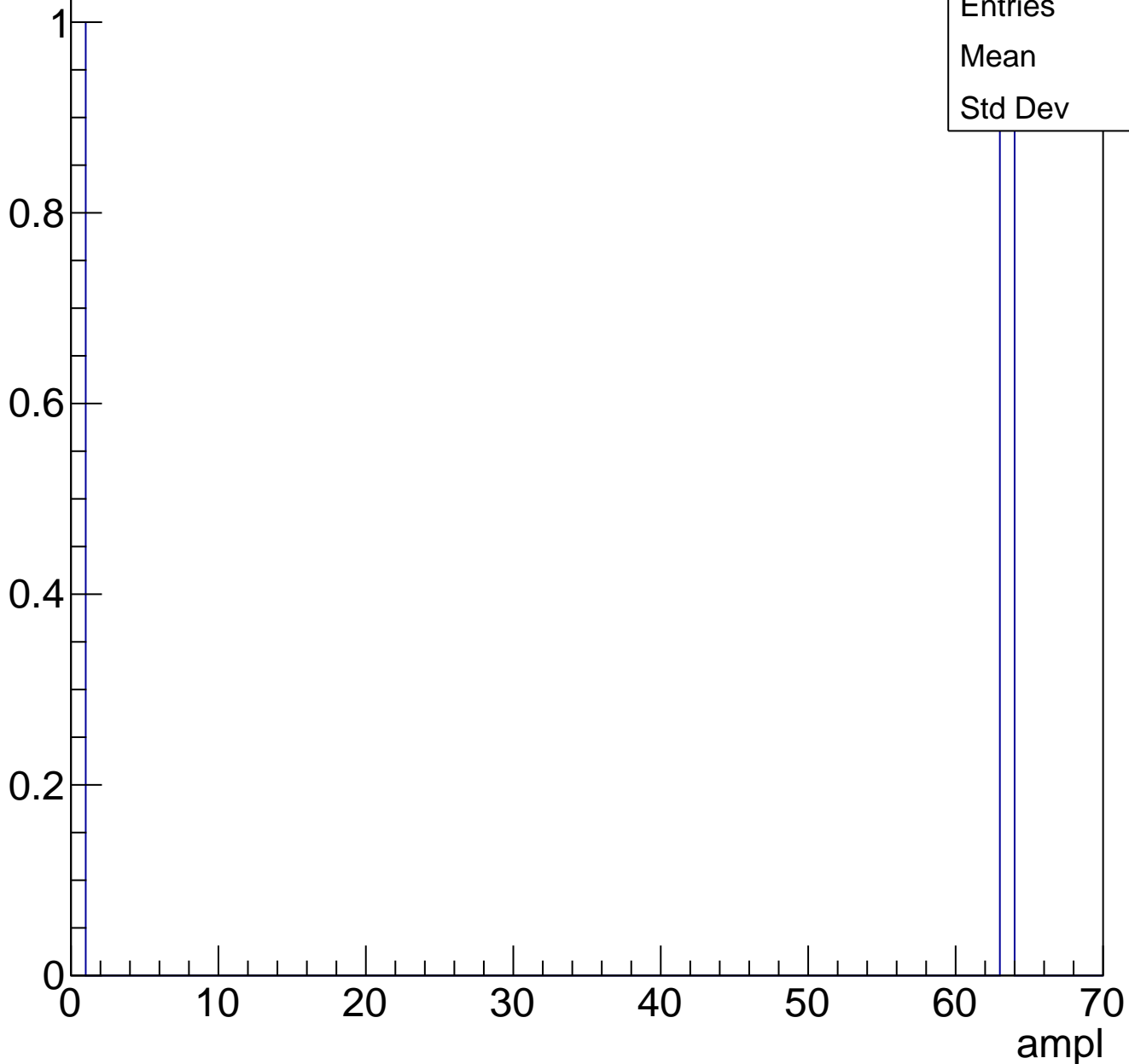
ampl



B1L102S, U26-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch121, adc0

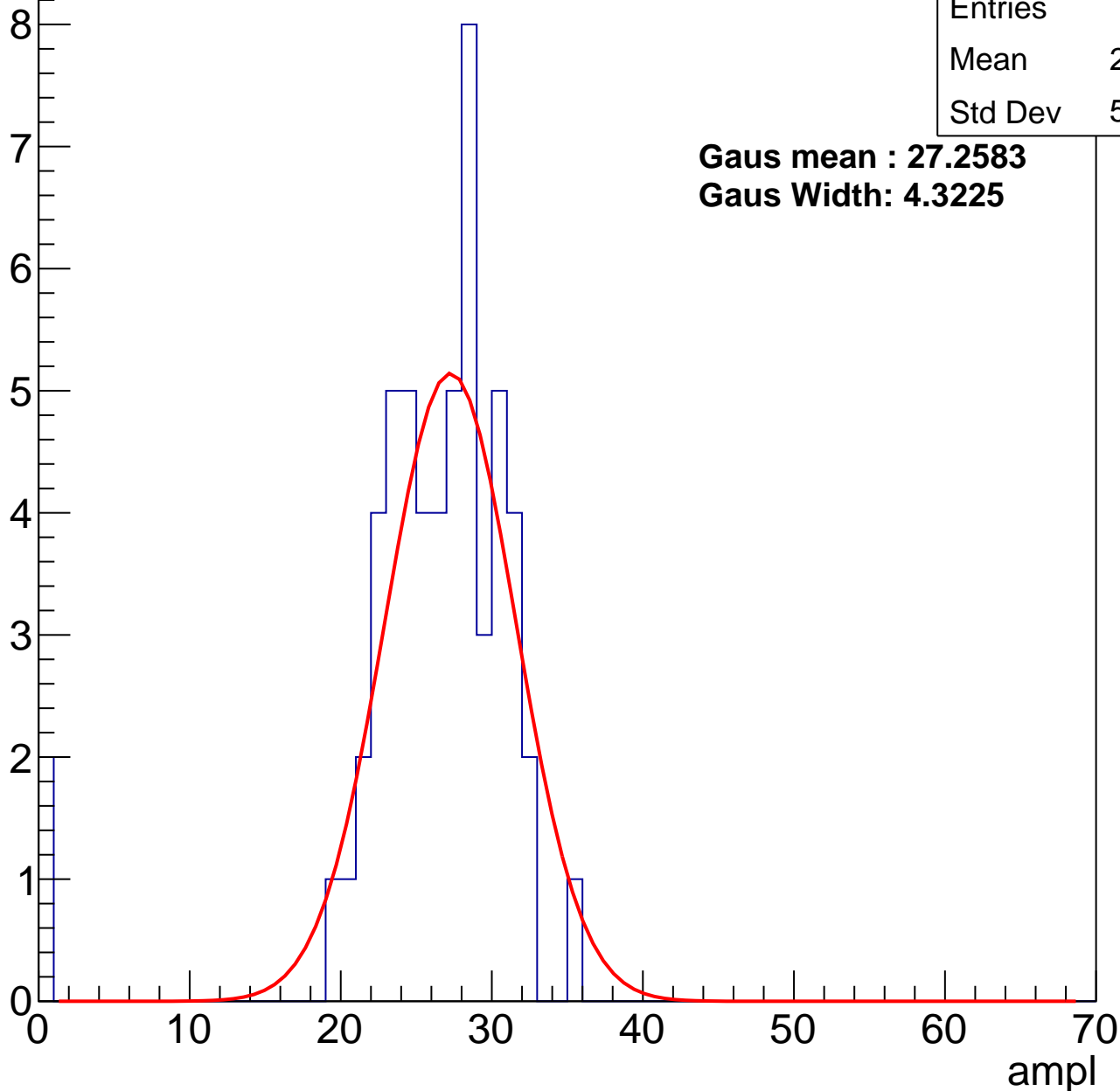
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	25.48
Std Dev	5.979

Gaus mean : 27.2583

Gaus Width: 4.3225



B1L102S, U26-ch121, adc1

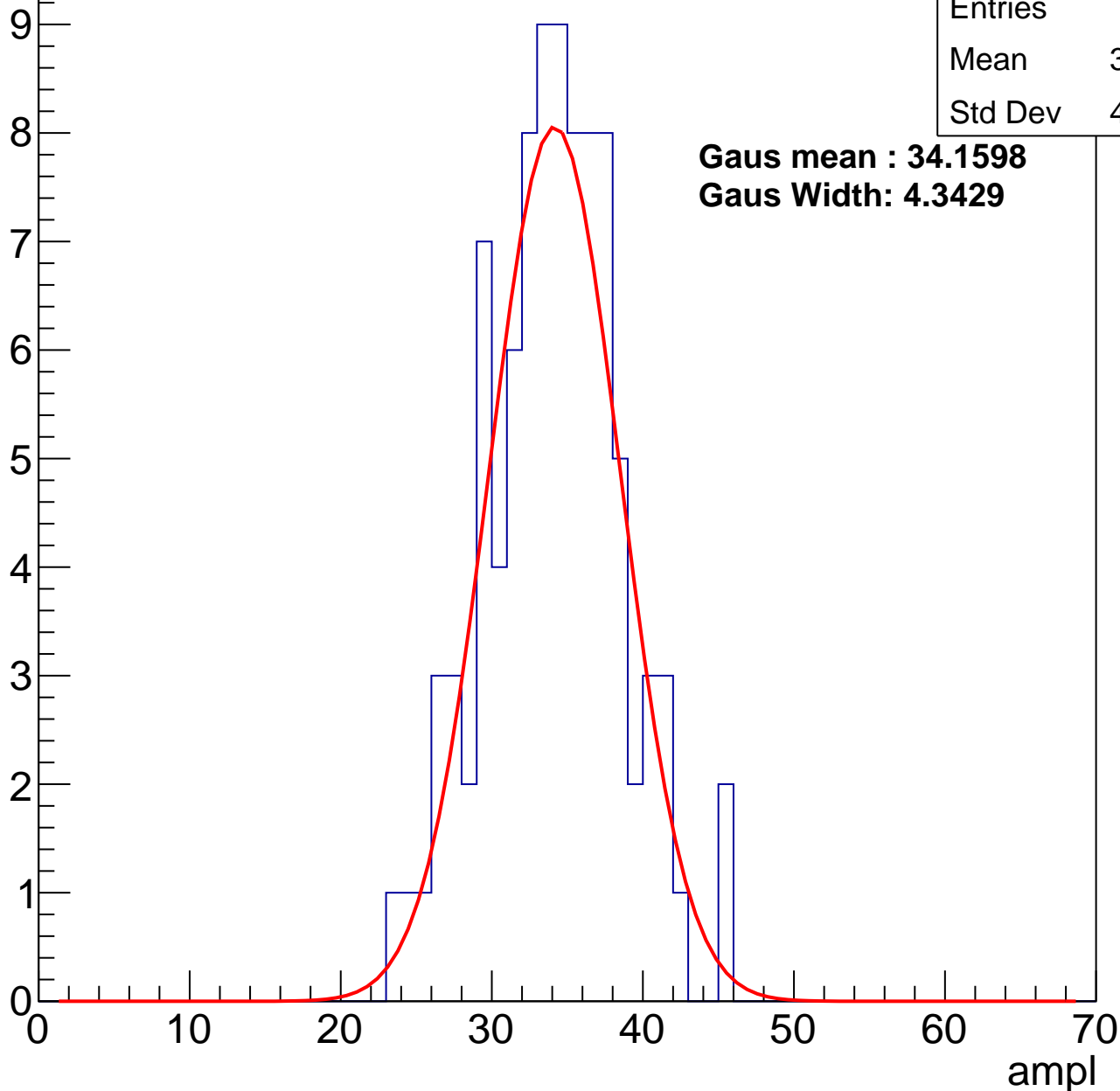
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	33.64
Std Dev	4.422

Gaus mean : 34.1598

Gaus Width: 4.3429



B1L102S, U26-ch121, adc2

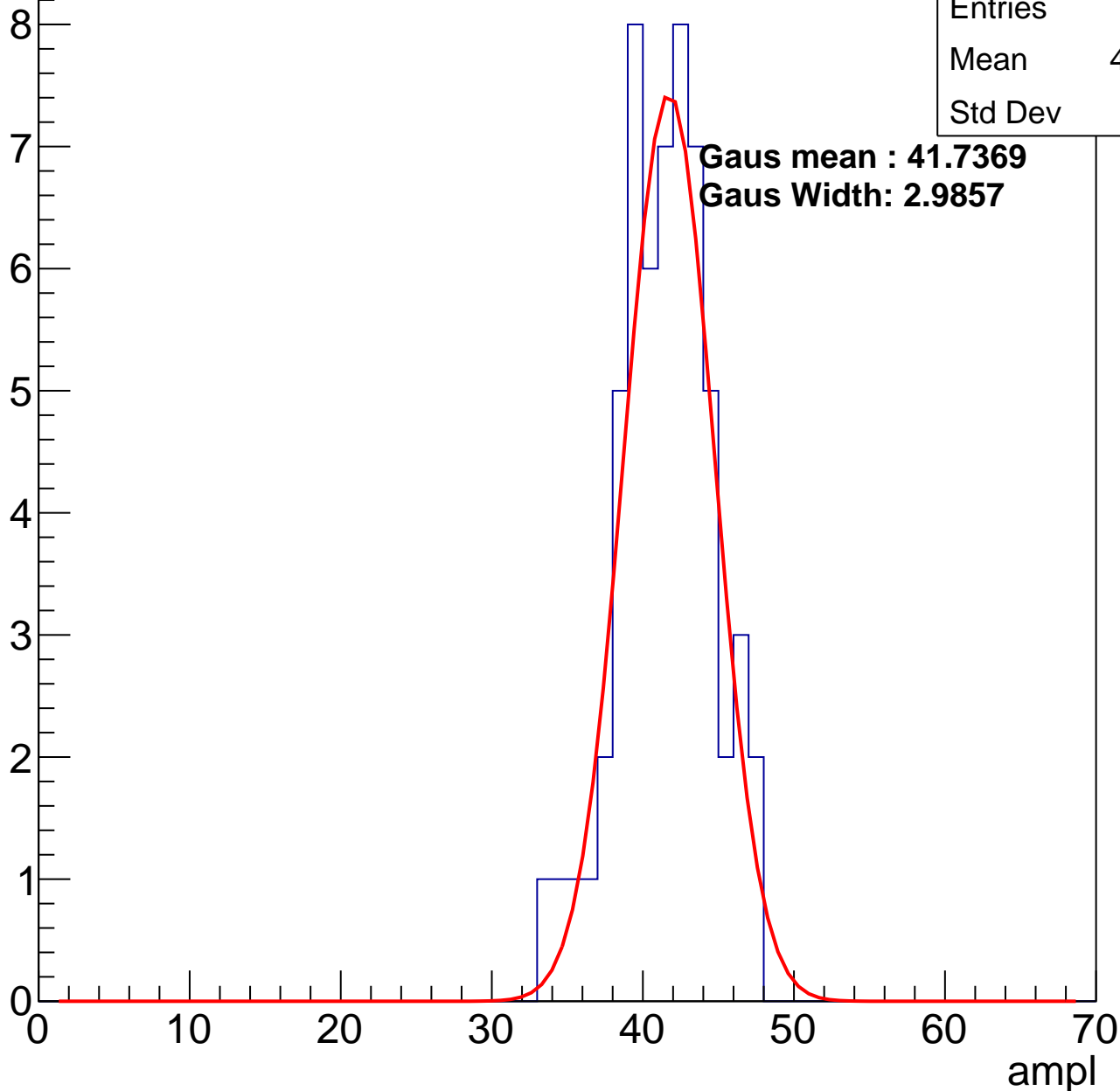
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.02
Std Dev	3.05

Gaus mean : 41.7369

Gaus Width: 2.9857

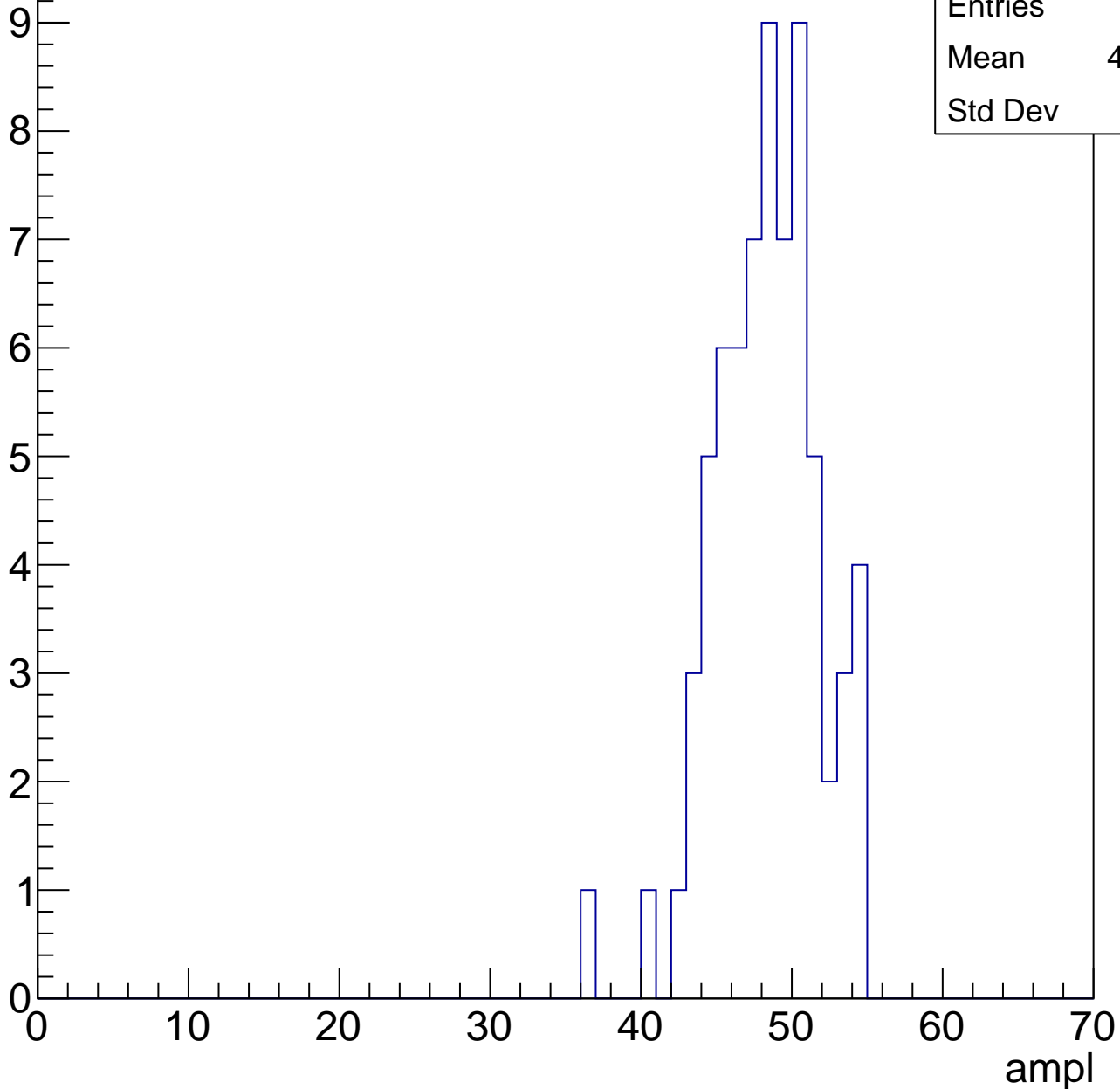


B1L102S, U26-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.84
Std Dev	3.45

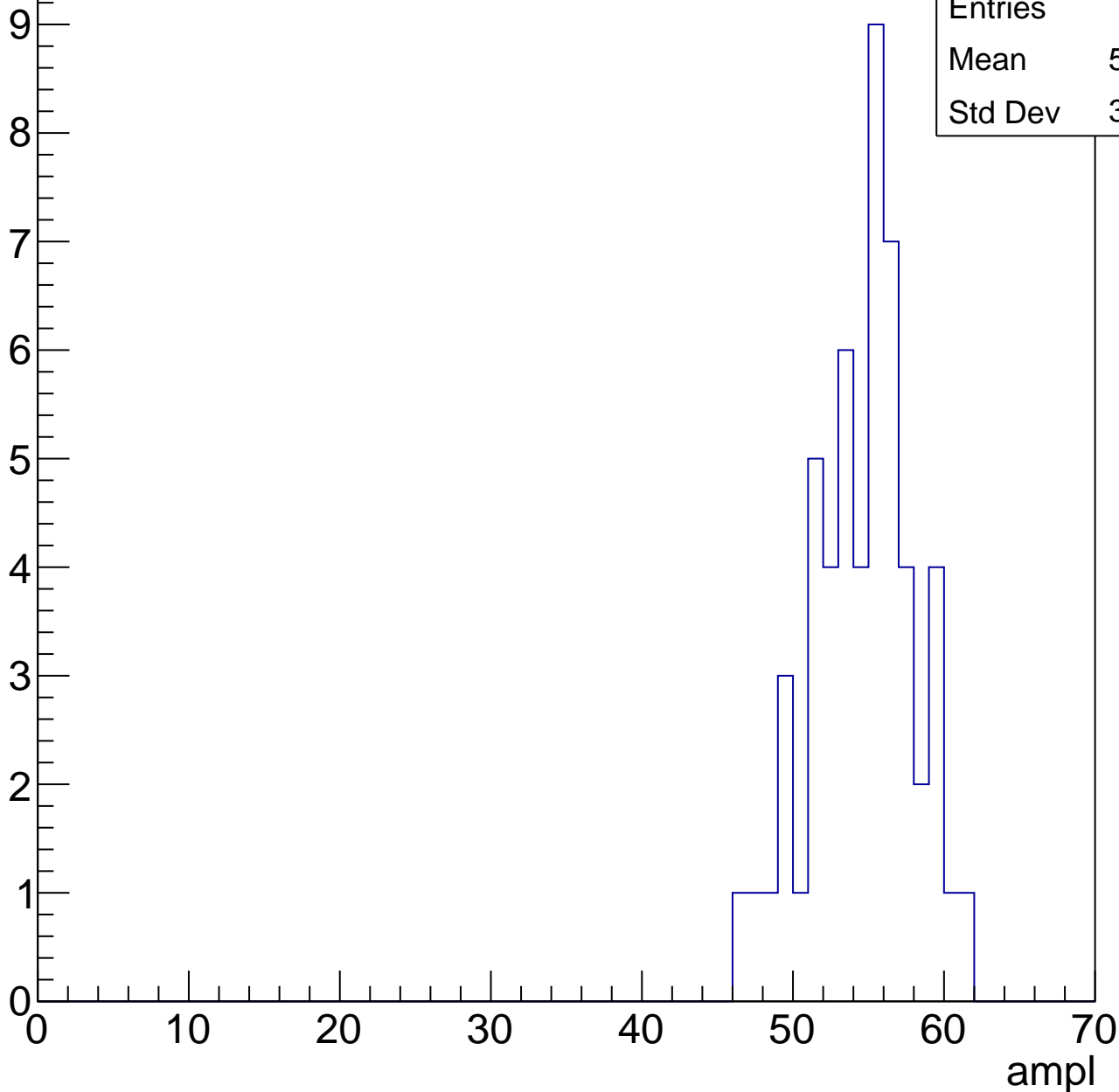


B1L102S, U26-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.13
Std Dev	3.322

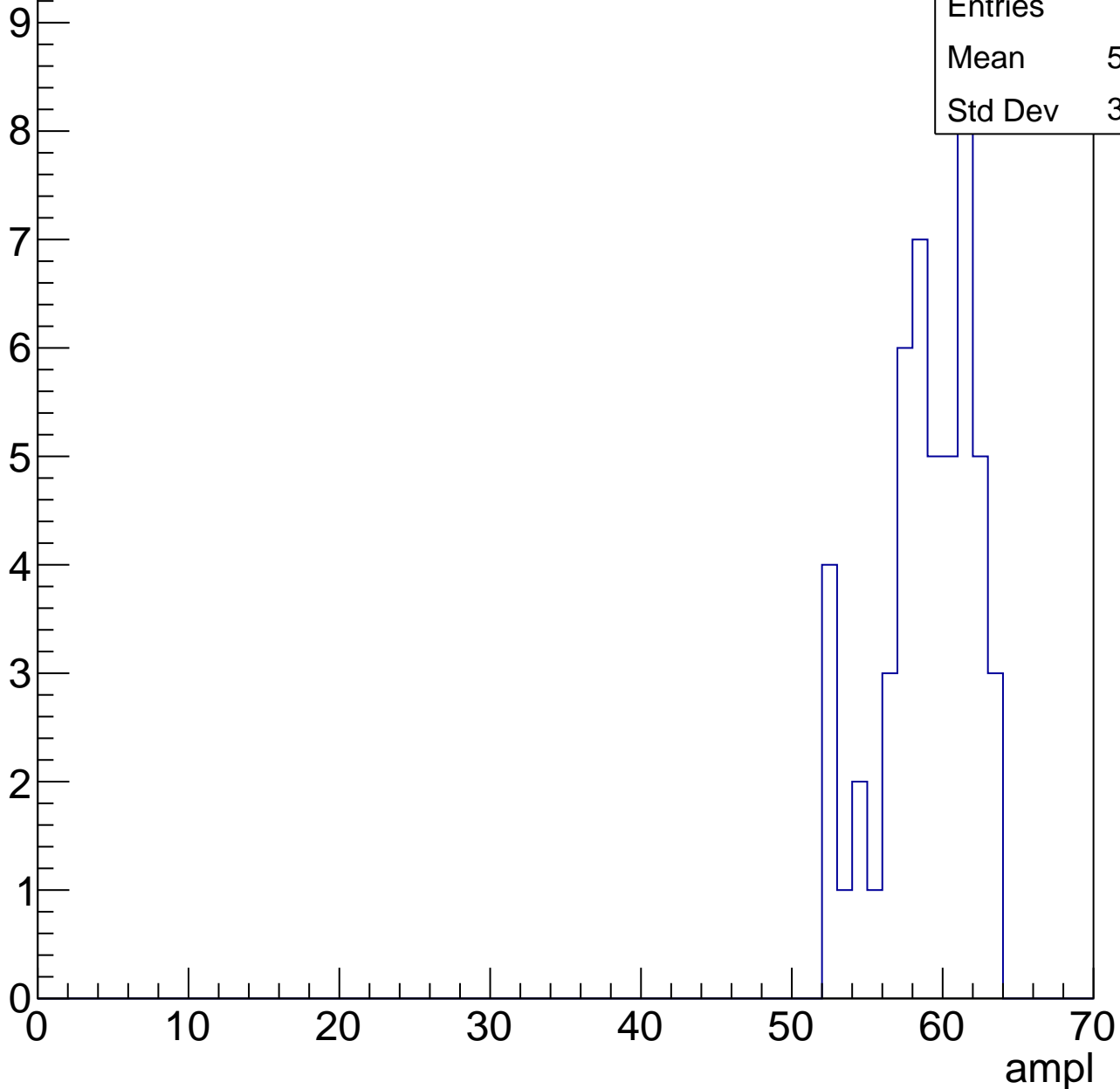


B1L102S, U26-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.49
Std Dev	3.057

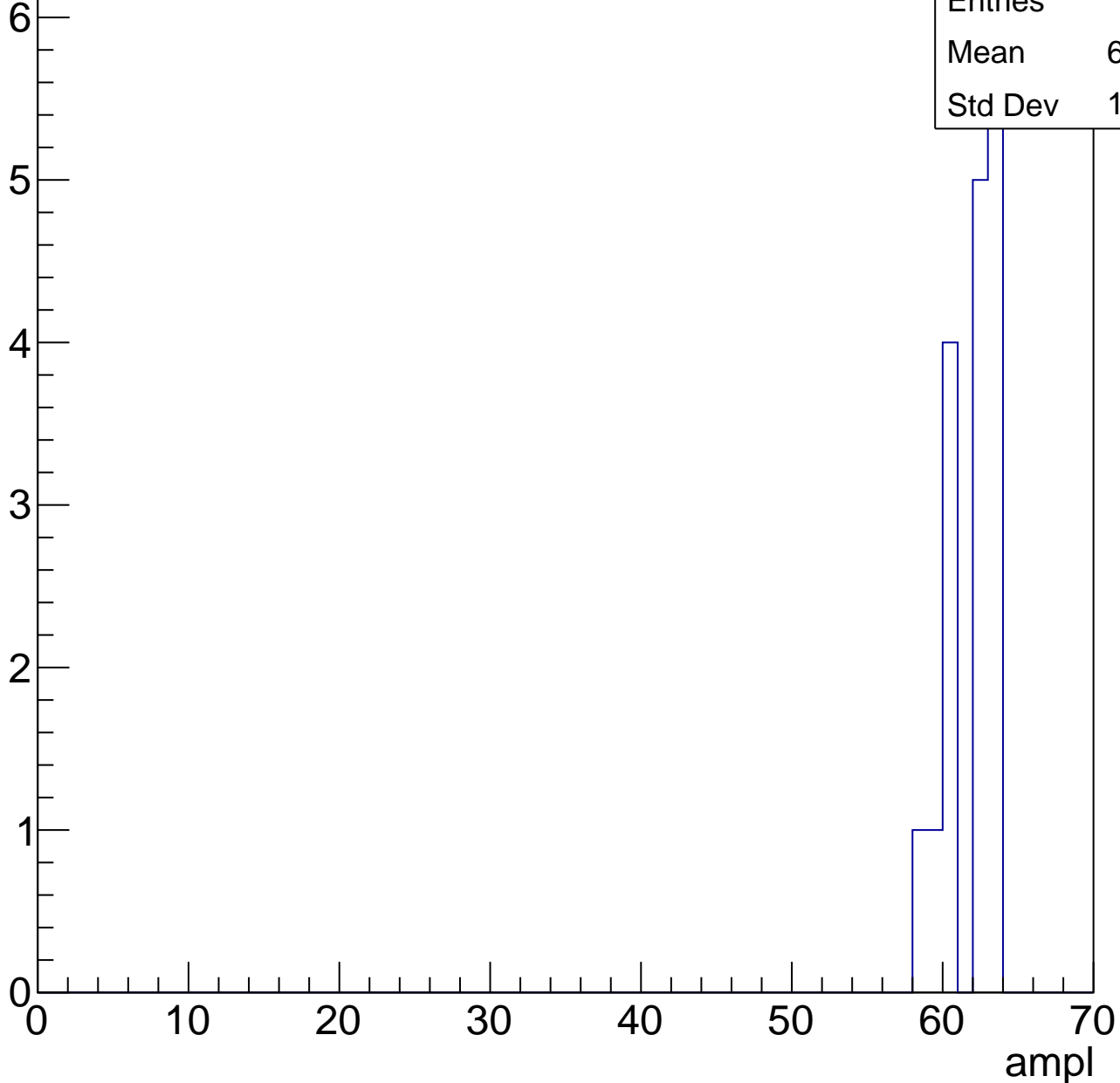


B1L102S, U26-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.47
Std Dev	1.576



B1L102S, U26-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch122, adc0

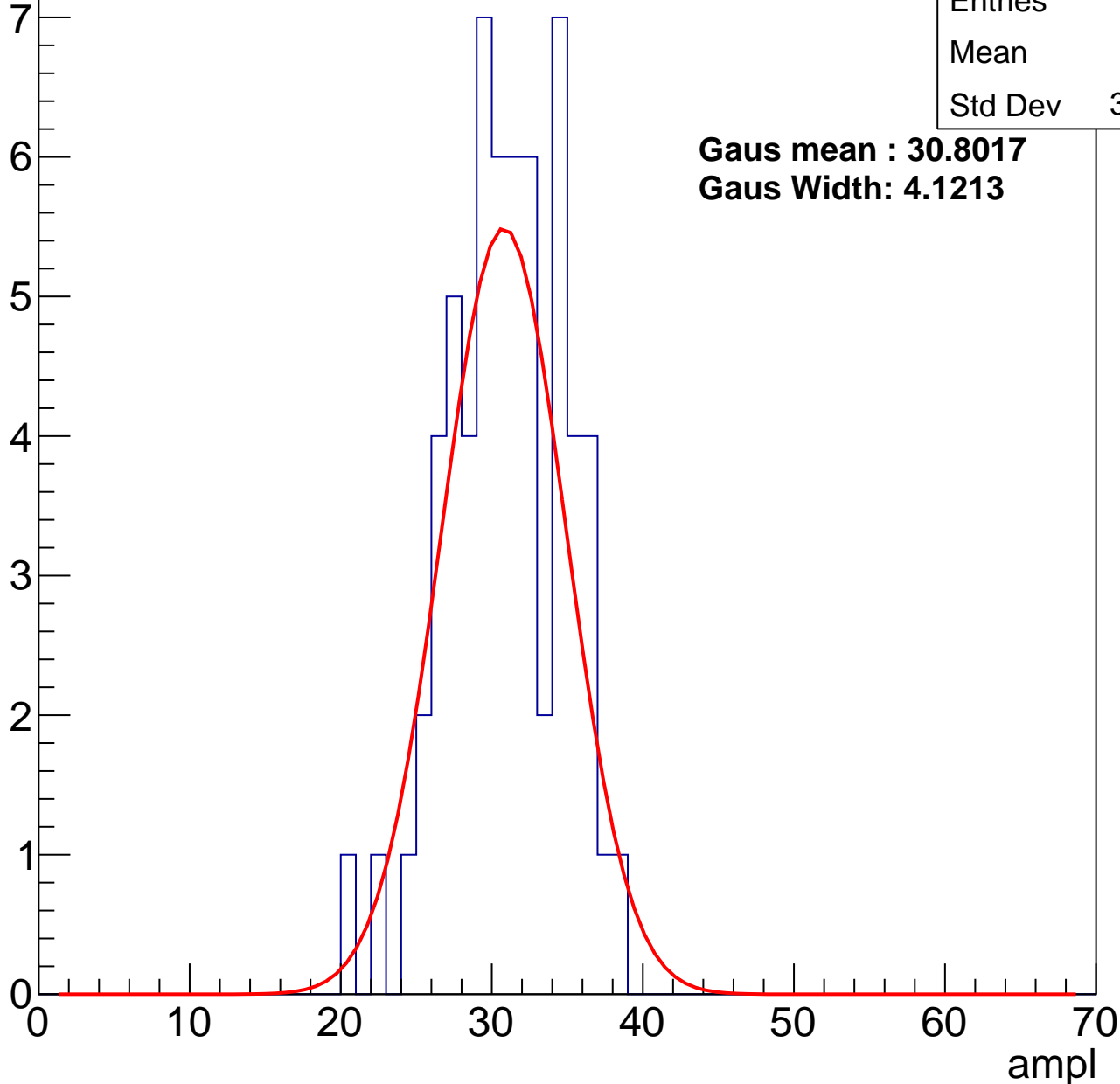
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	30.5
Std Dev	3.775

Gaus mean : 30.8017

Gaus Width: 4.1213



B1L102S, U26-ch122, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	37.25
Std Dev	4.065

Gaus mean : 38.2099

Gaus Width: 4.6755

8

6

4

2

0

0

10

20

30

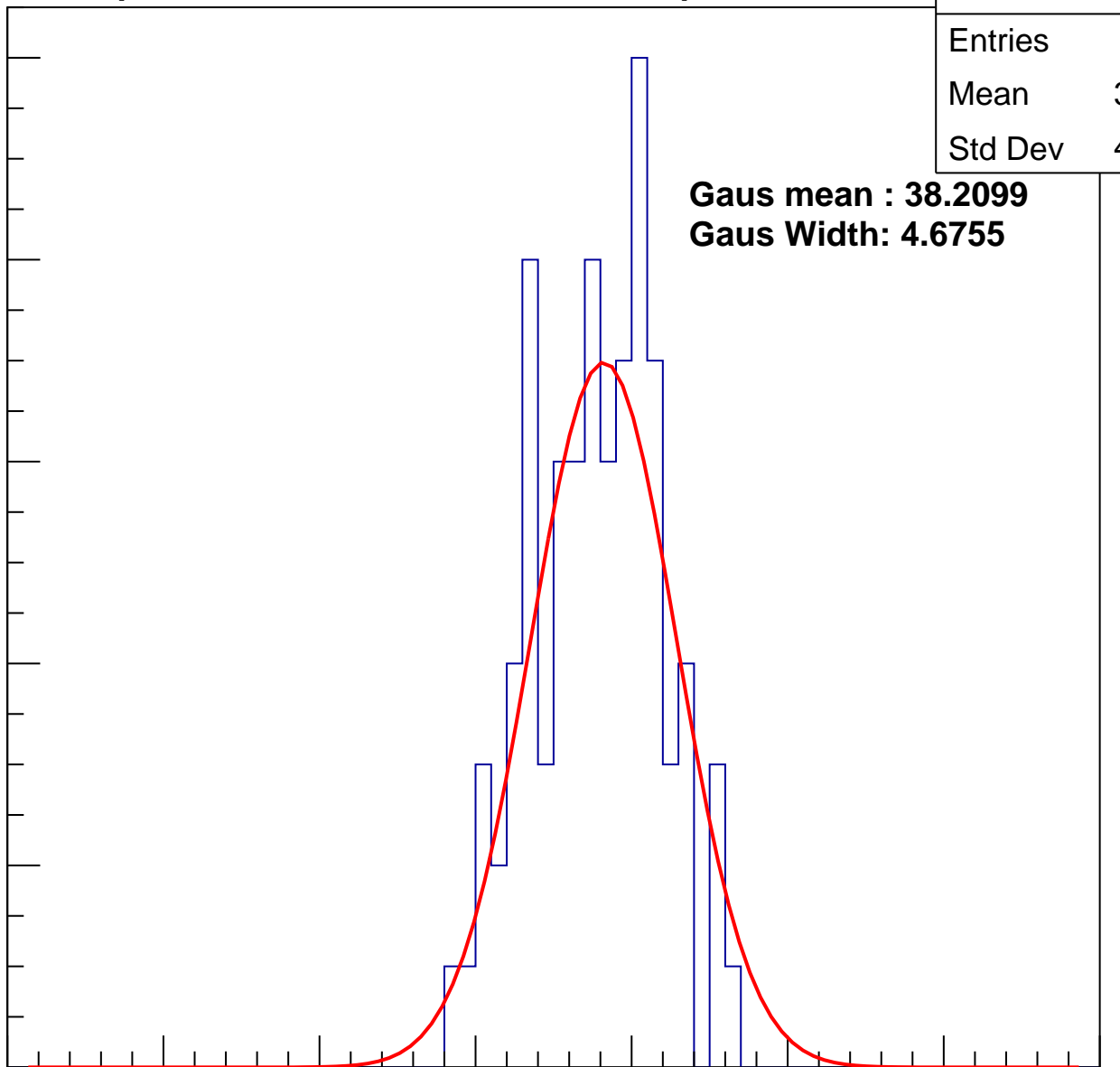
40

50

60

70

ampl



B1L102S, U26-ch122, adc2

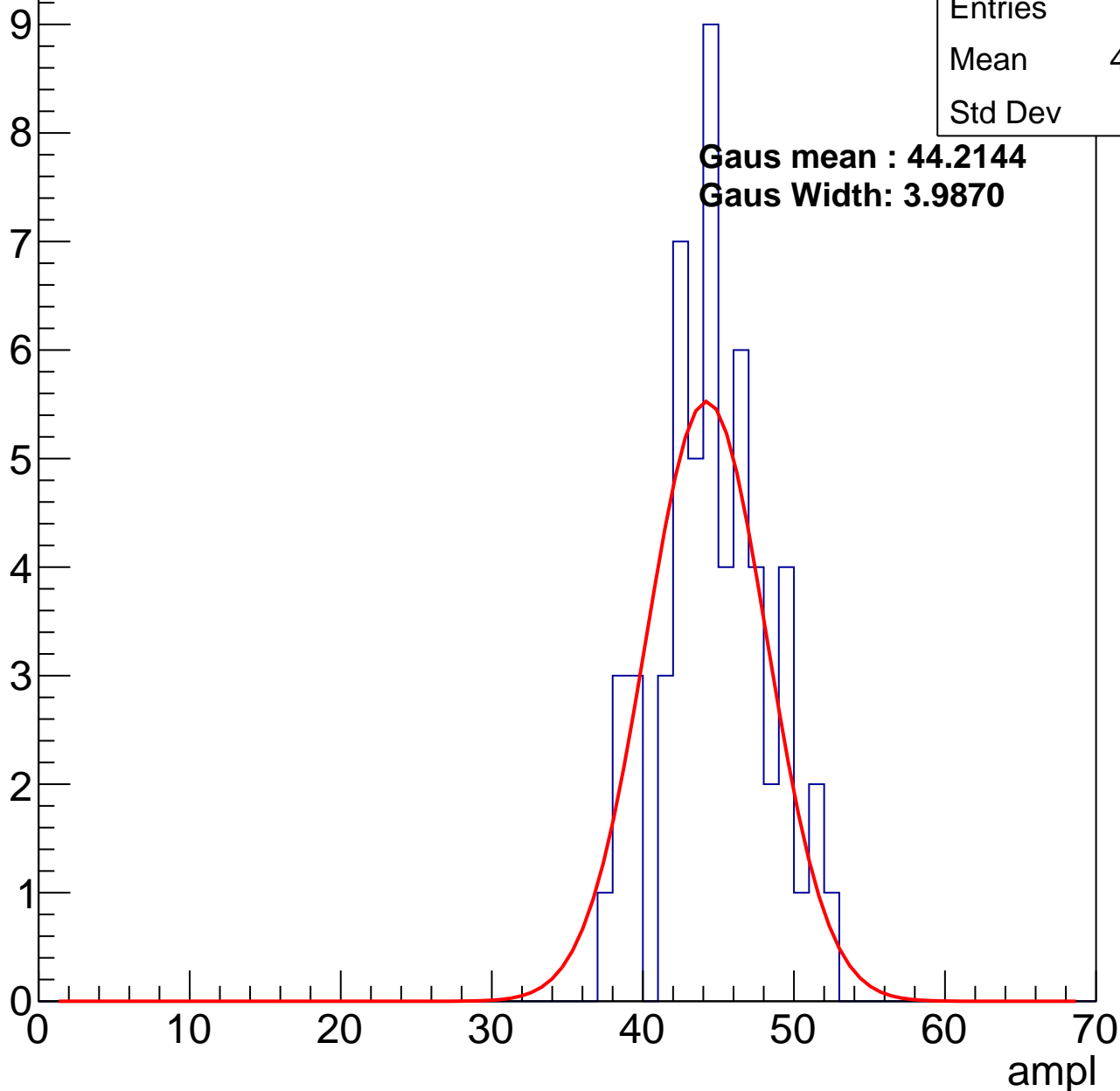
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	44.29
Std Dev	3.52

Gaus mean : 44.2144

Gaus Width: 3.9870

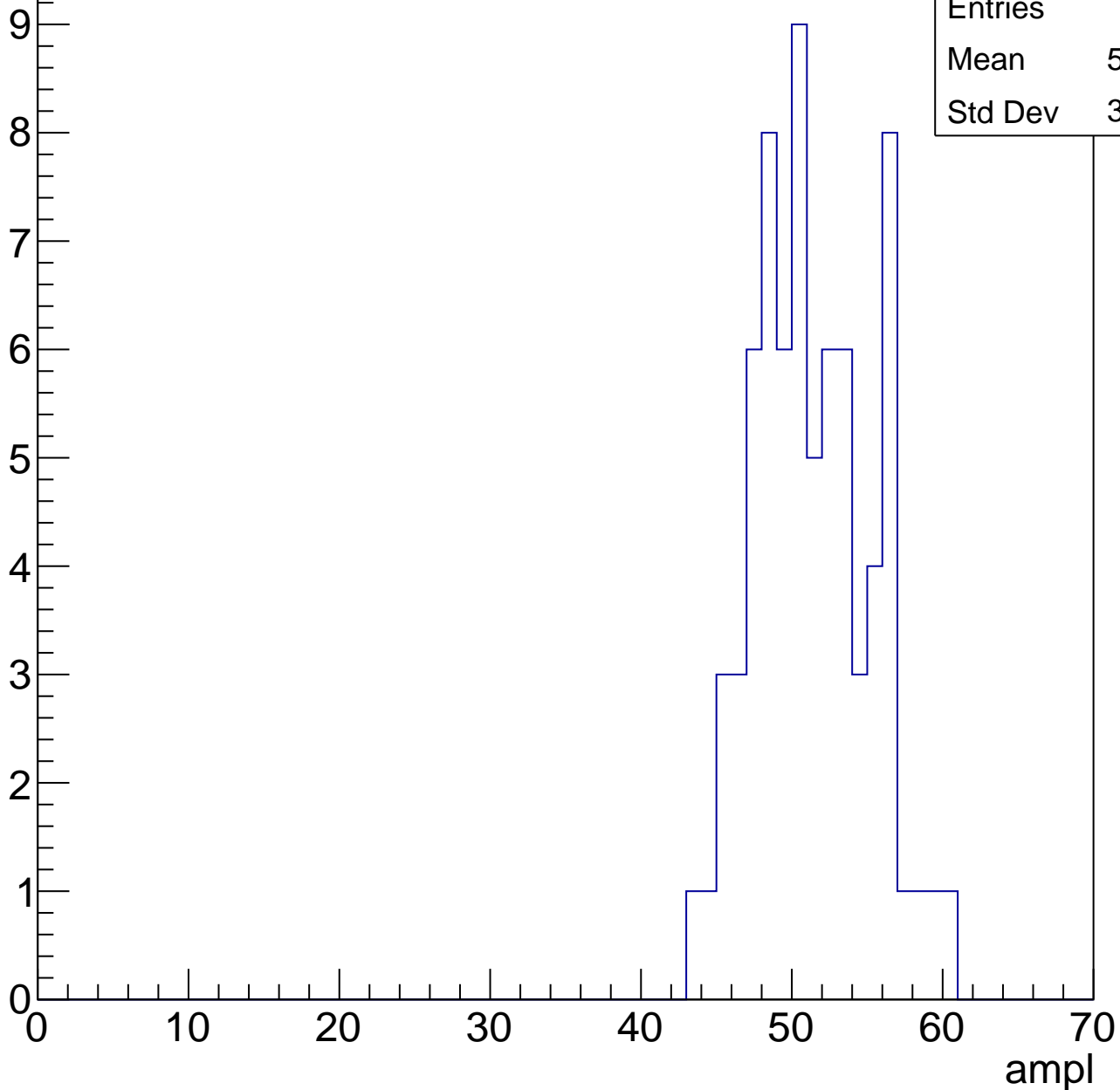


B1L102S, U26-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.95
Std Dev	3.789

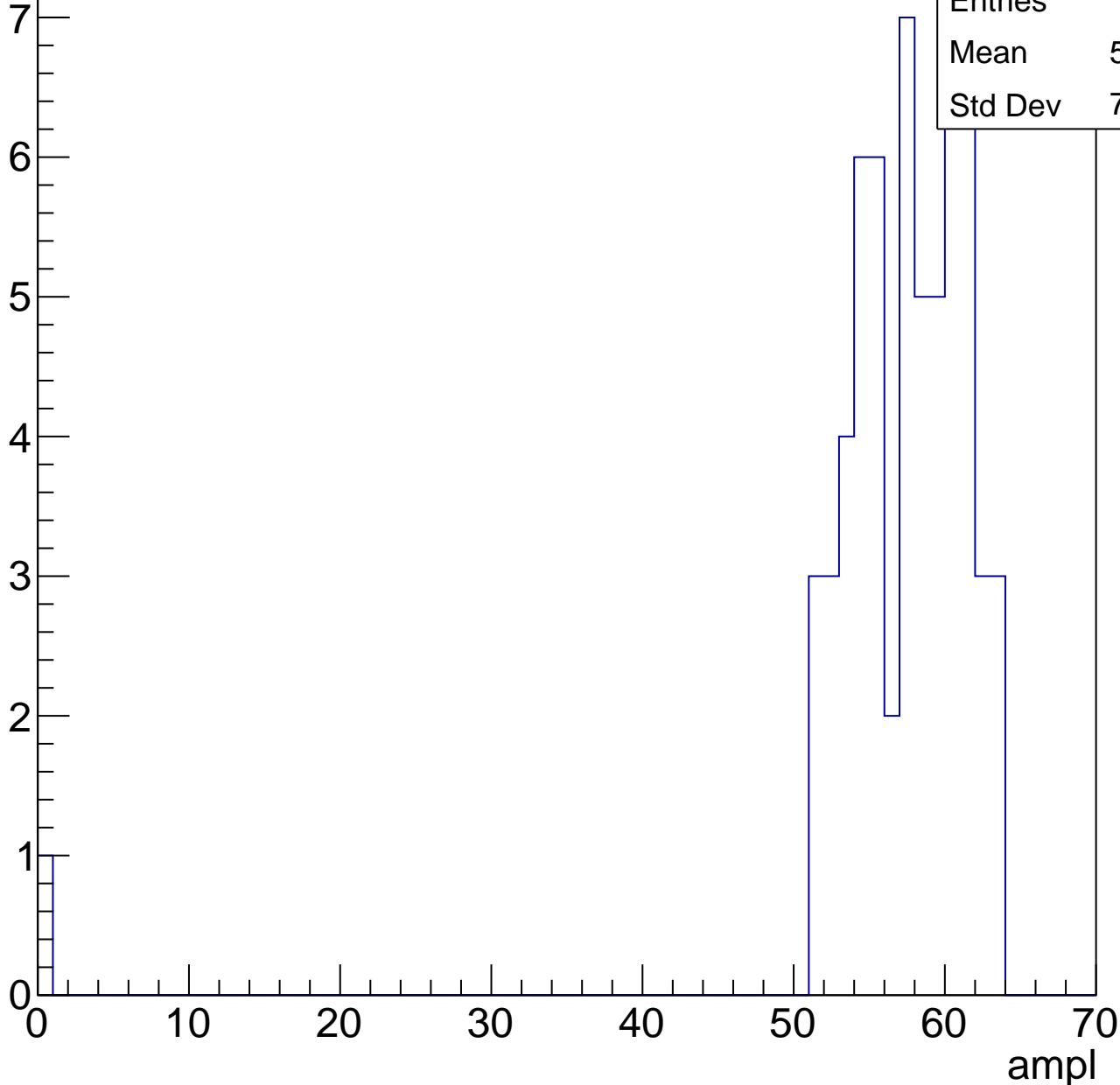


B1L102S, U26-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.34
Std Dev	7.964

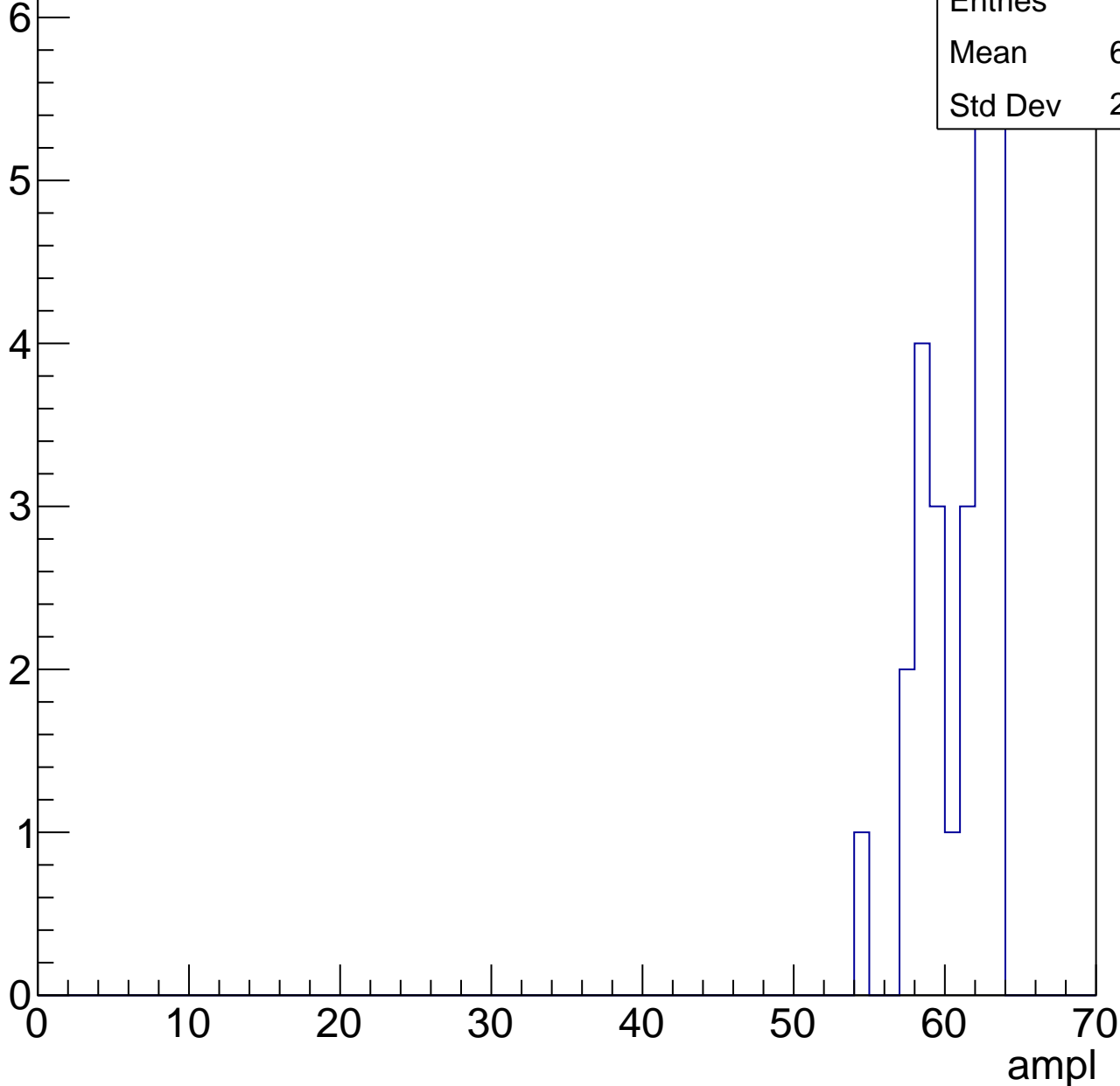


B1L102S, U26-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

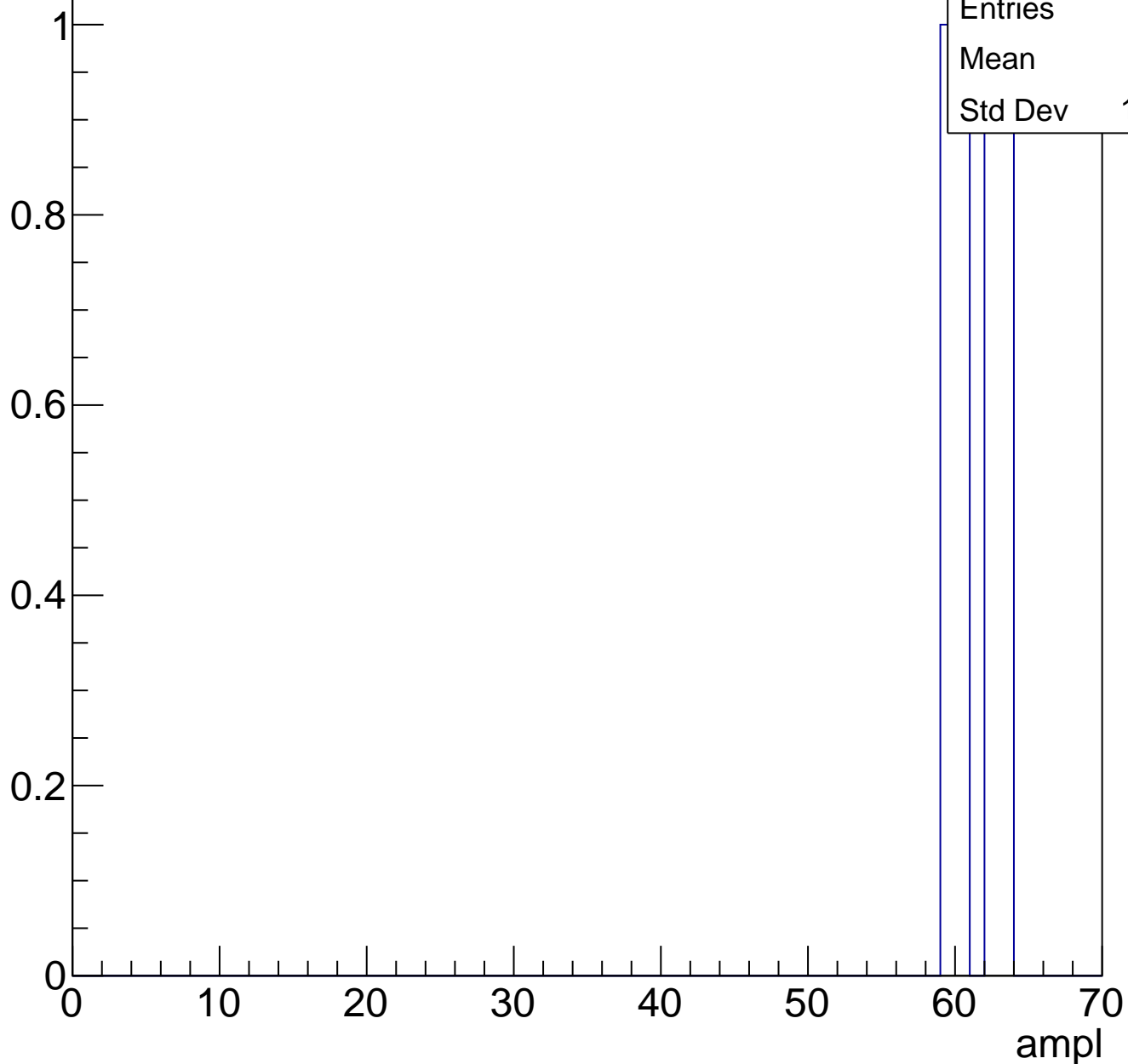
Entries	26
Mean	60.38
Std Dev	2.403



B1L102S, U26-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U26-ch123, adc0

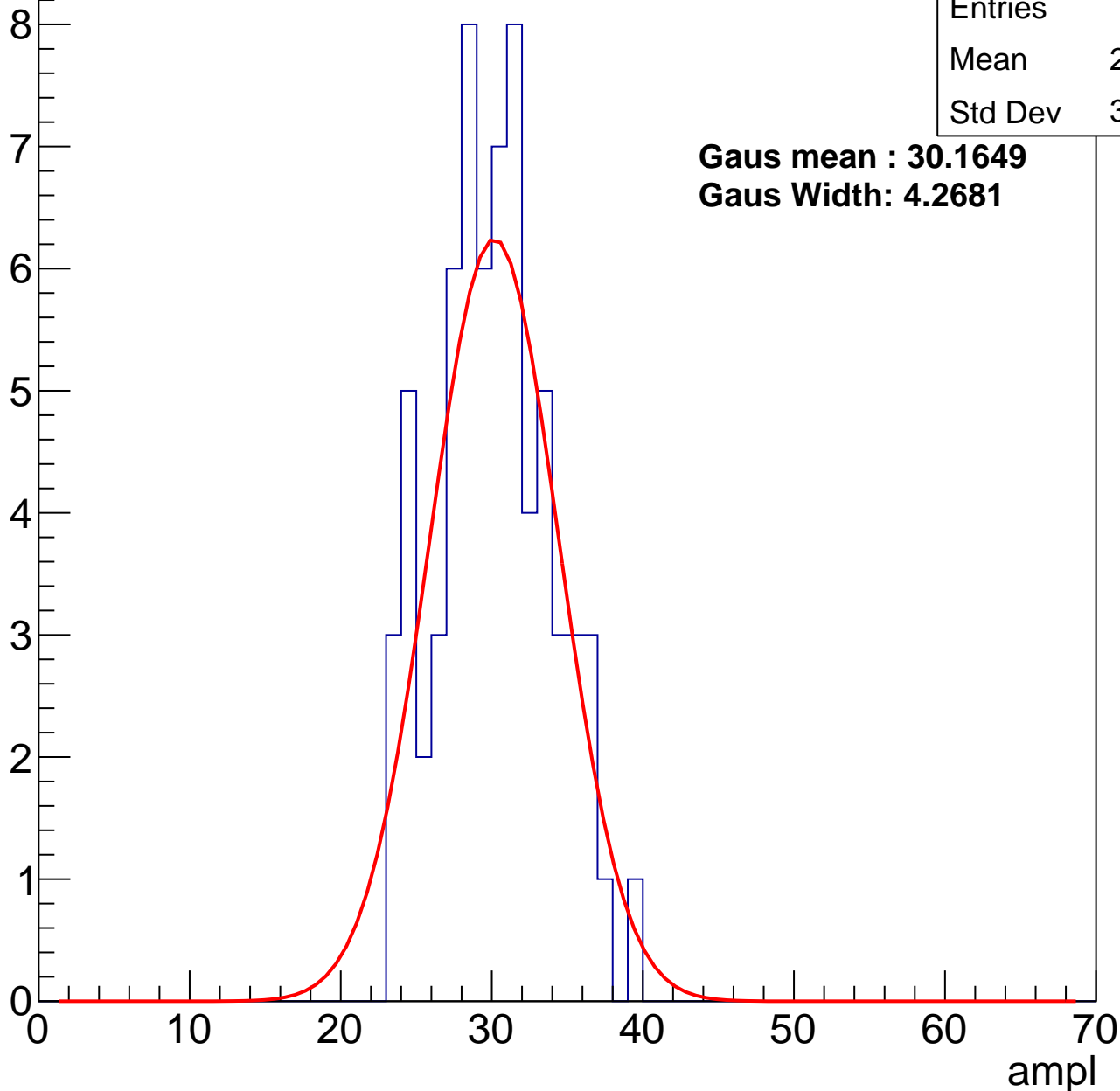
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.69
Std Dev	3.719

Gaus mean : 30.1649

Gaus Width: 4.2681



B1L102S, U26-ch123, adc1

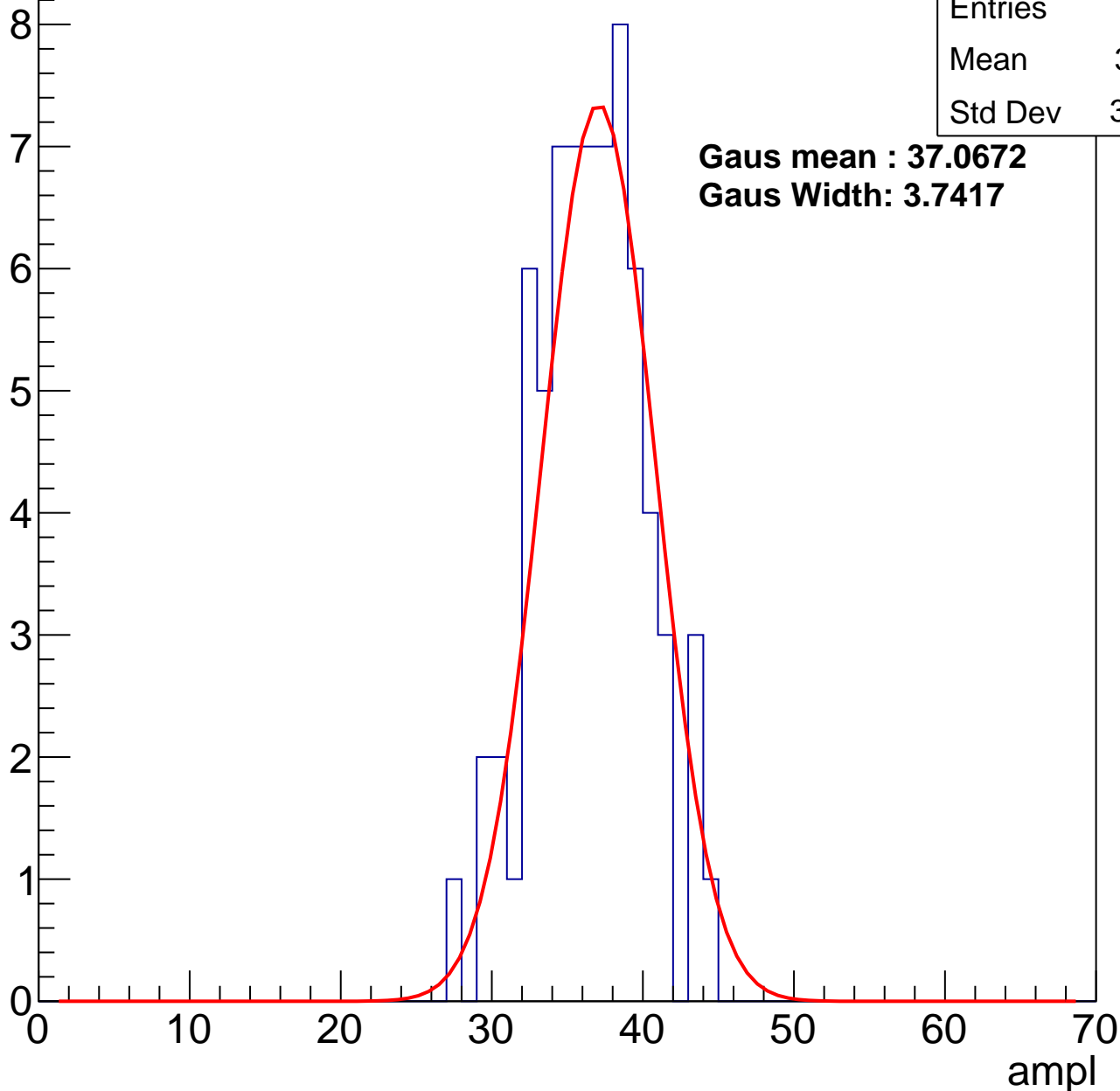
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.01
Std Dev	3.576

Gaus mean : 37.0672

Gaus Width: 3.7417



B1L102S, U26-ch123, adc2

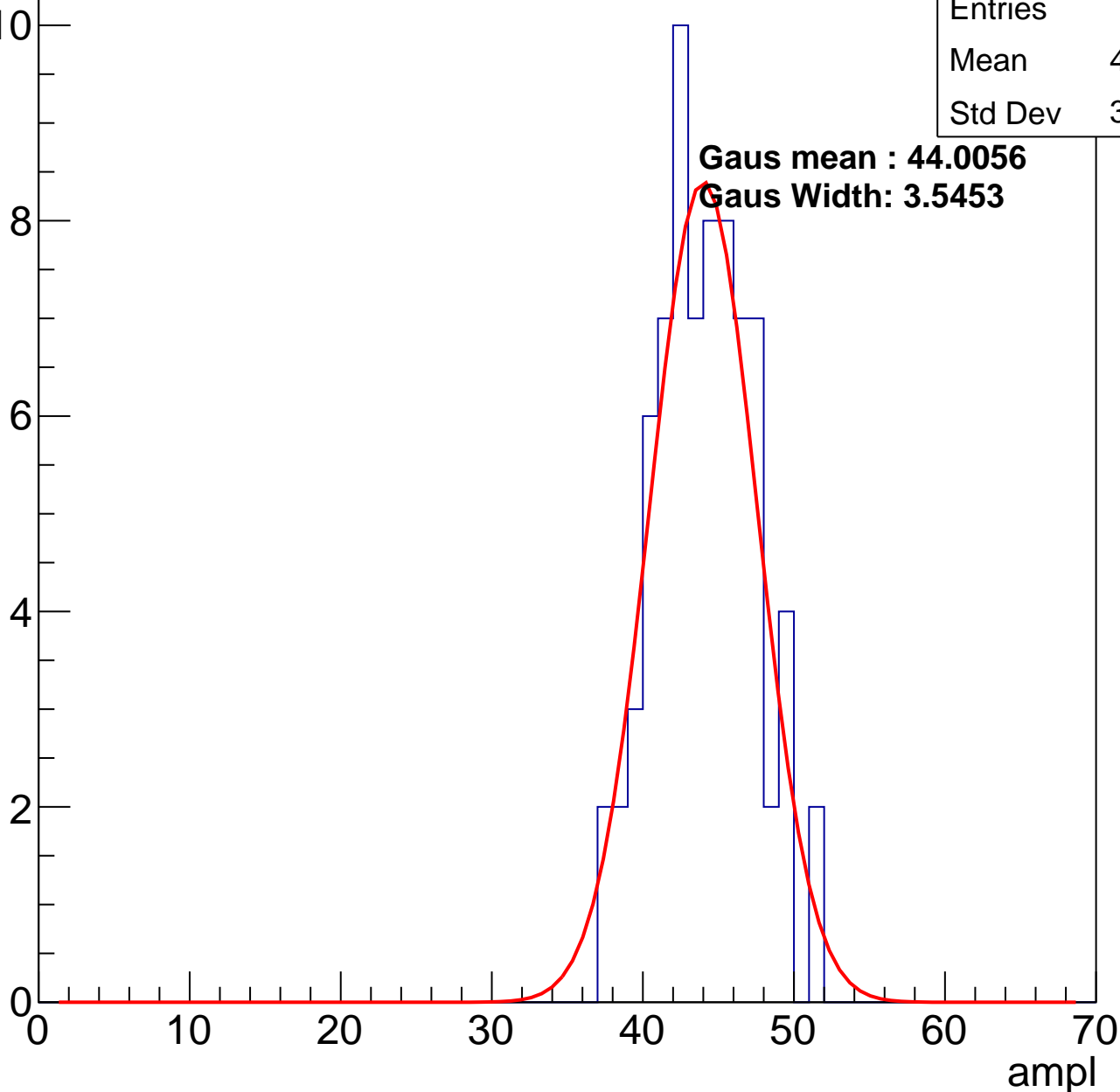
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.63
Std Dev	3.203

Gaus mean : 44.0056

Gaus Width: 3.5453

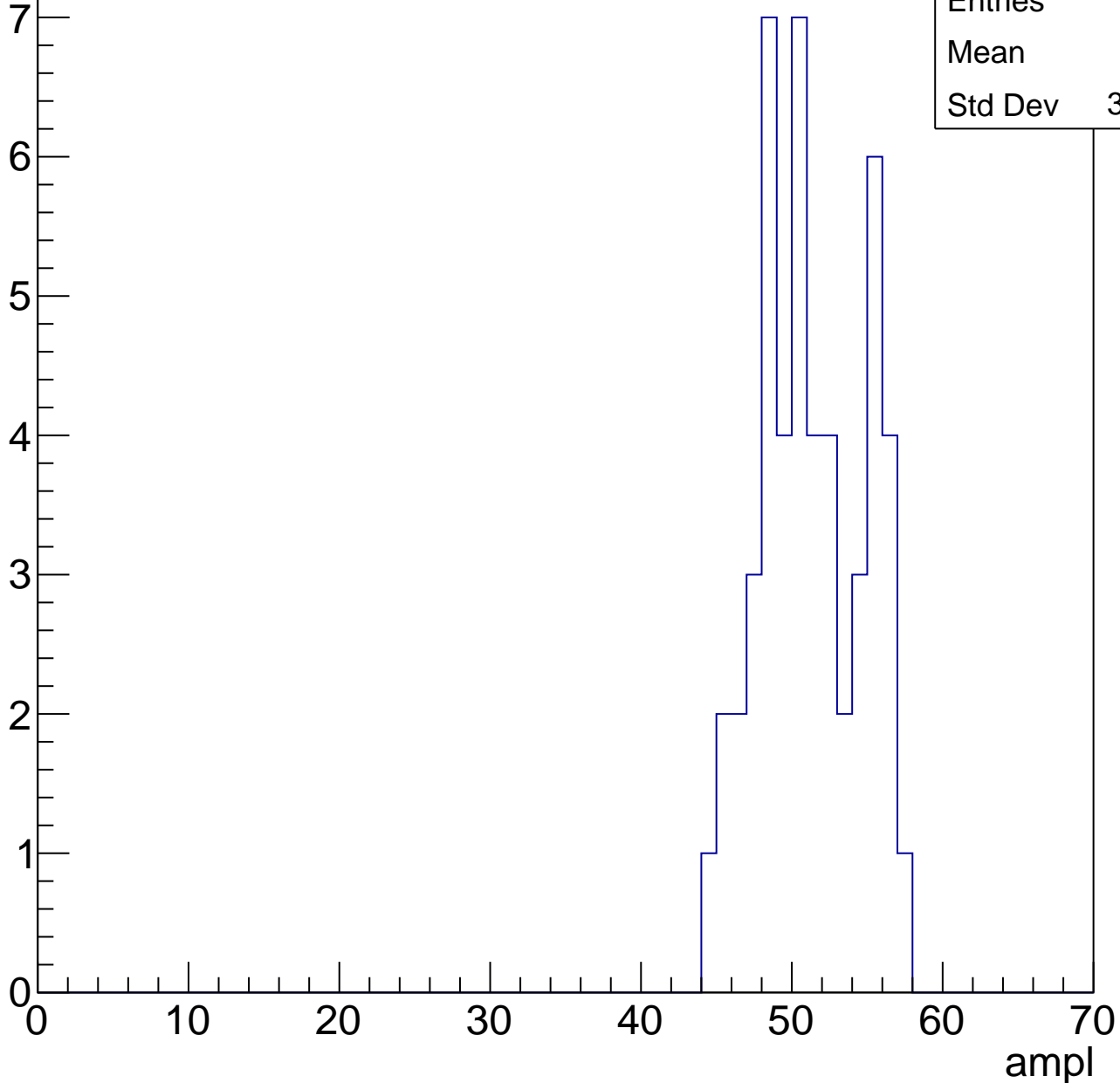


B1L102S, U26-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50.8
Std Dev	3.388

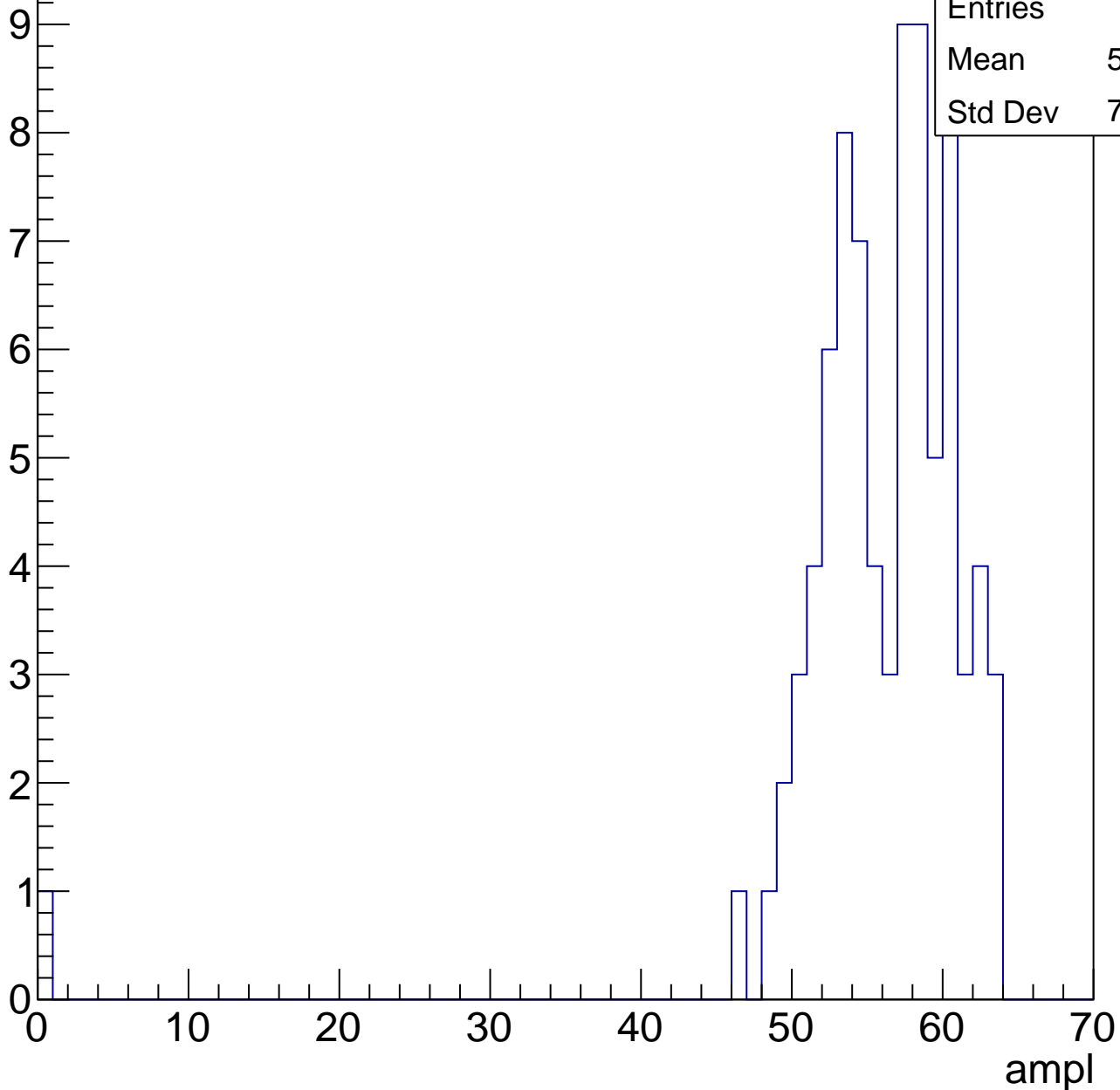


B1L102S, U26-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	55.28
Std Dev	7.327

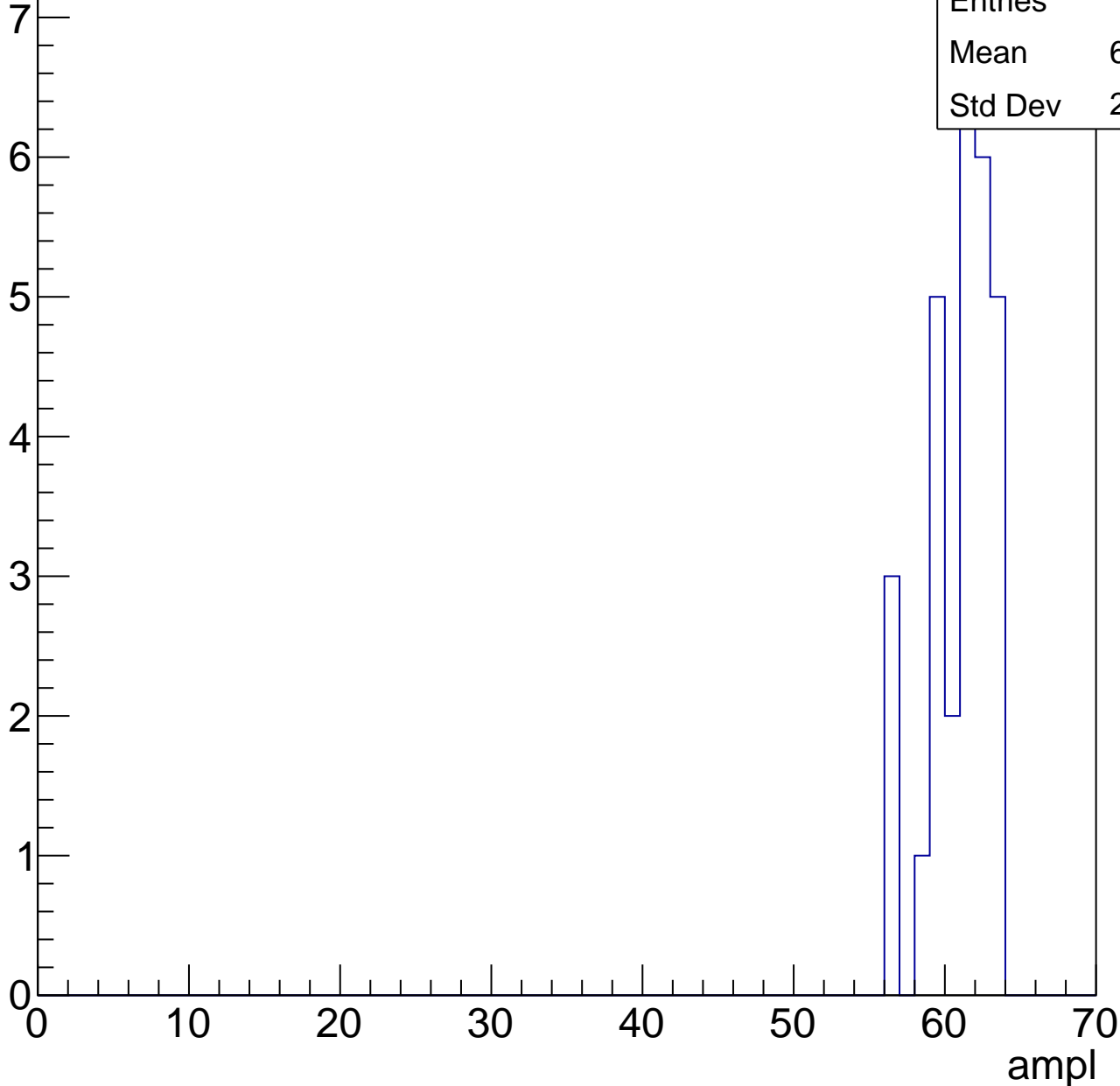


B1L102S, U26-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	60.52
Std Dev	2.078



B1L102S, U26-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch124, adc0

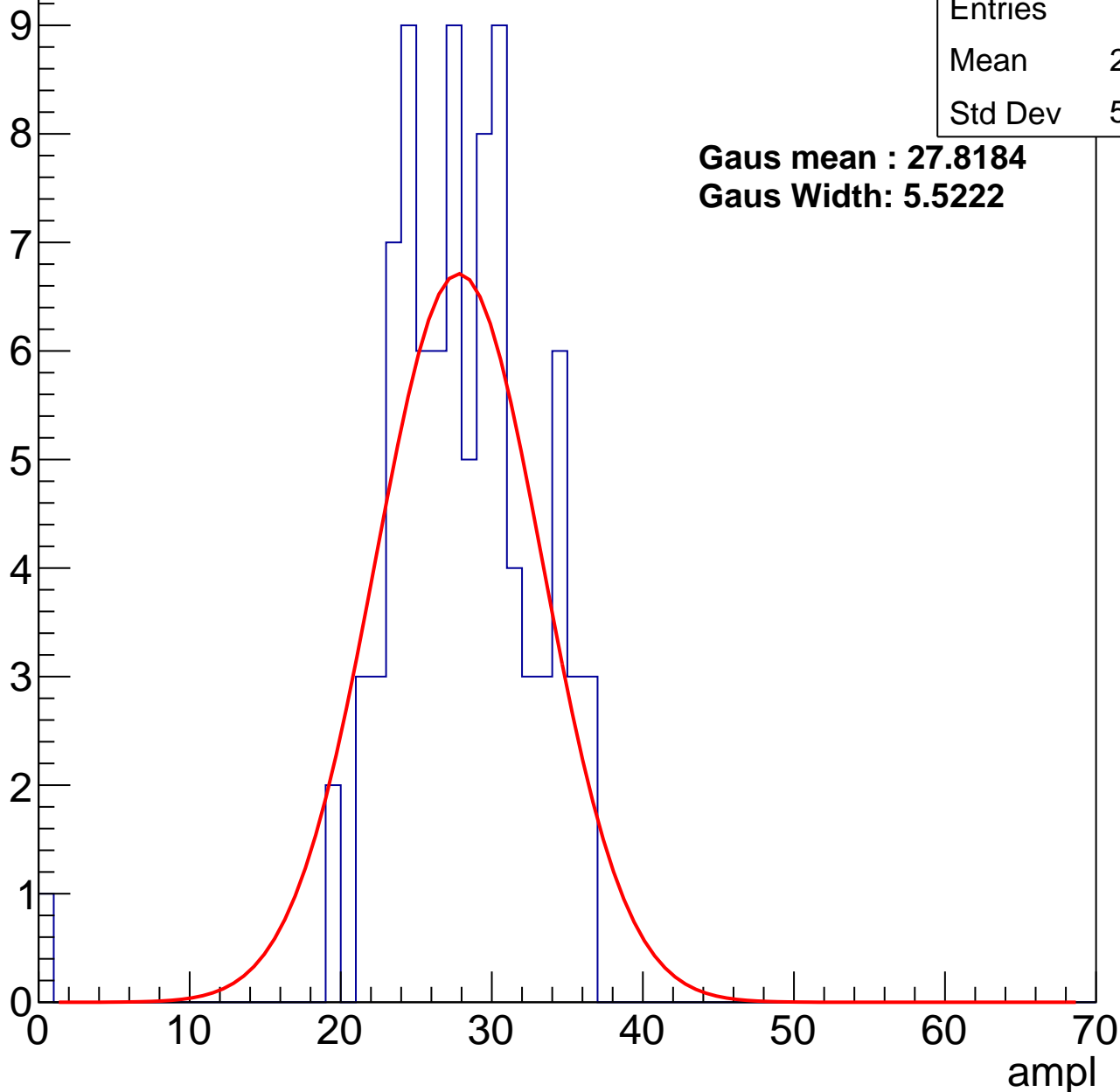
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	27.46
Std Dev	5.097

Gaus mean : 27.8184

Gaus Width: 5.5222



B1L102S, U26-ch124, adc1

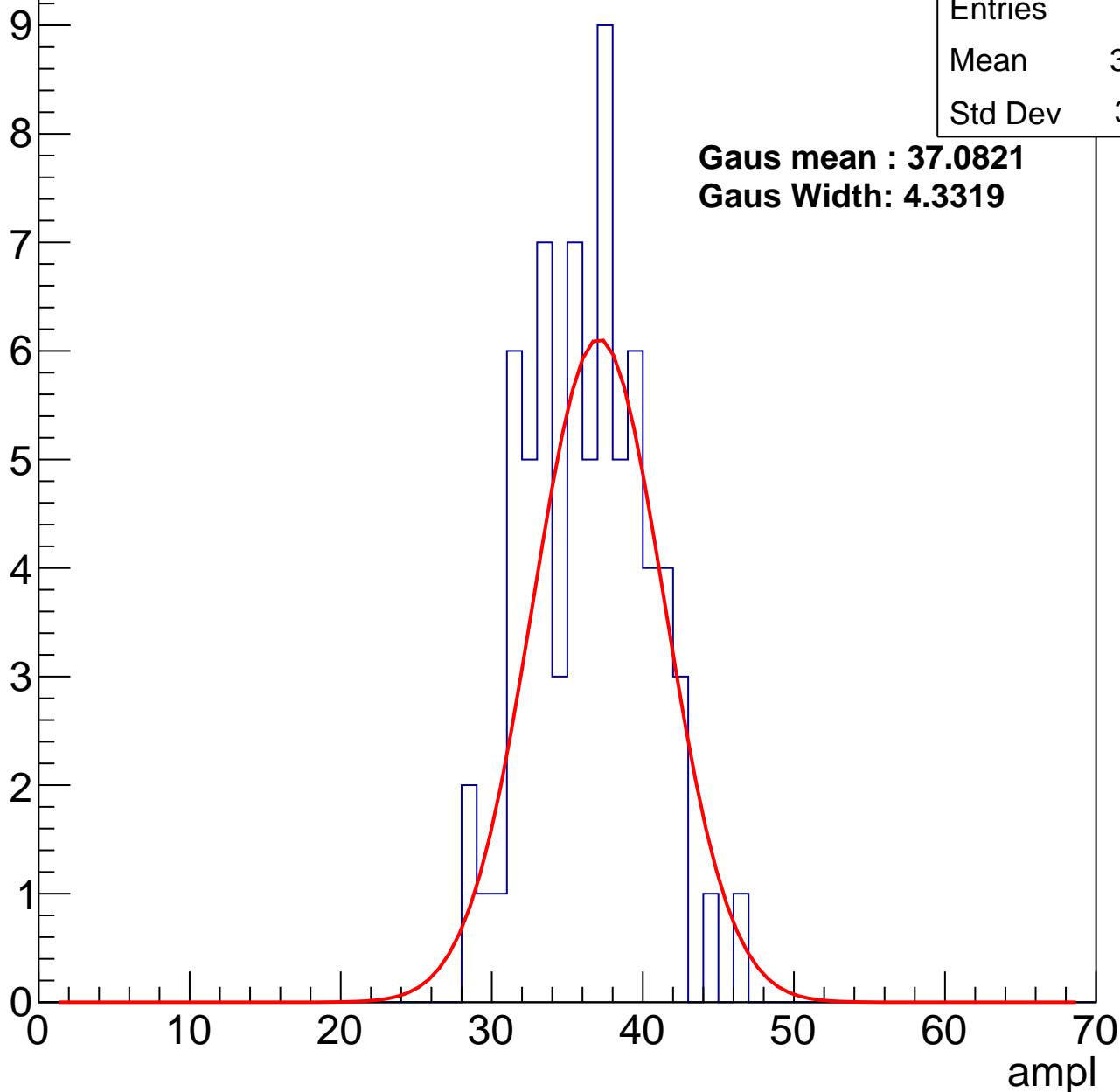
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.94
Std Dev	3.861

Gaus mean : 37.0821

Gaus Width: 4.3319

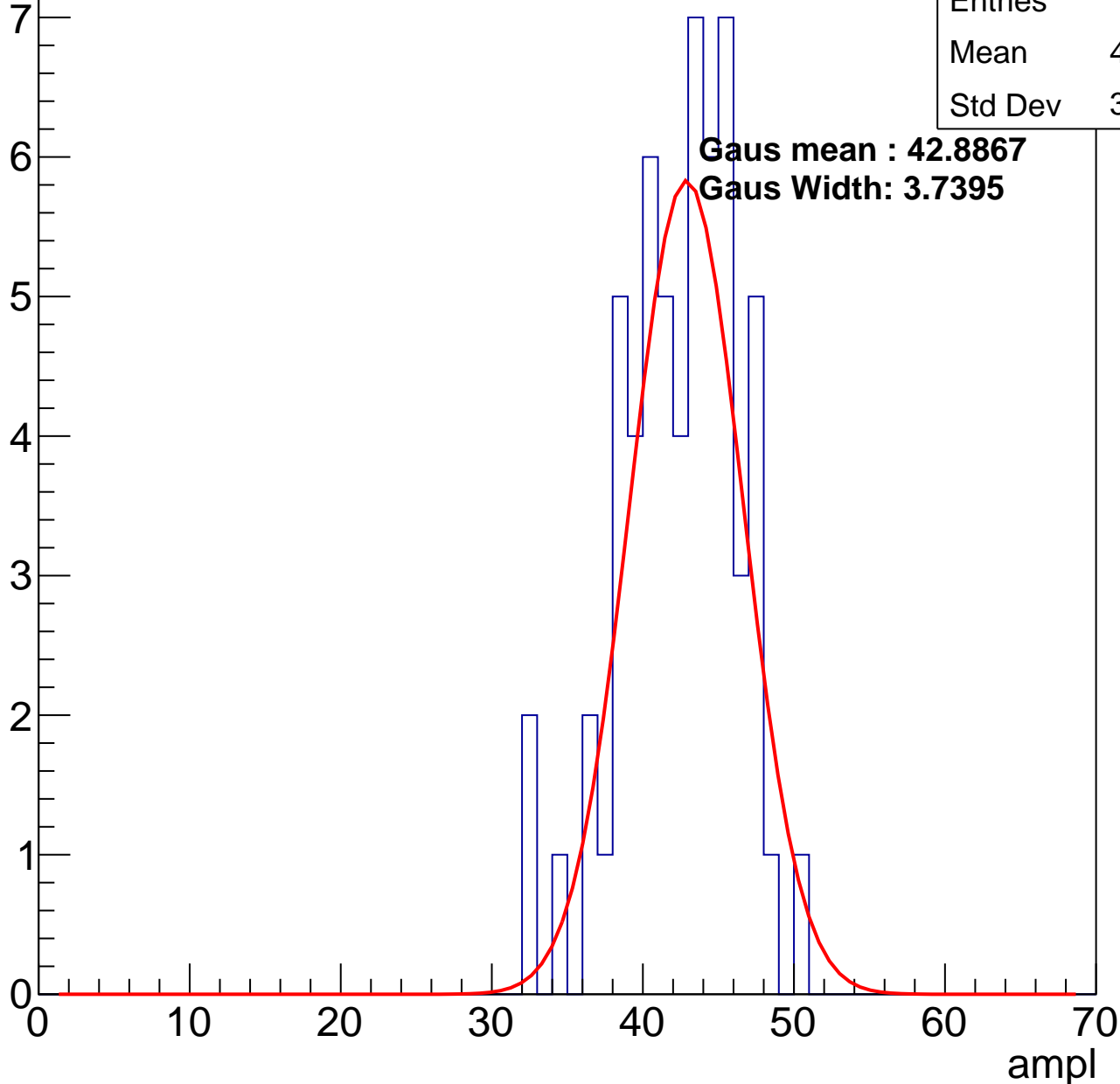


B1L102S, U26-ch124, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.95
Std Dev	3.814

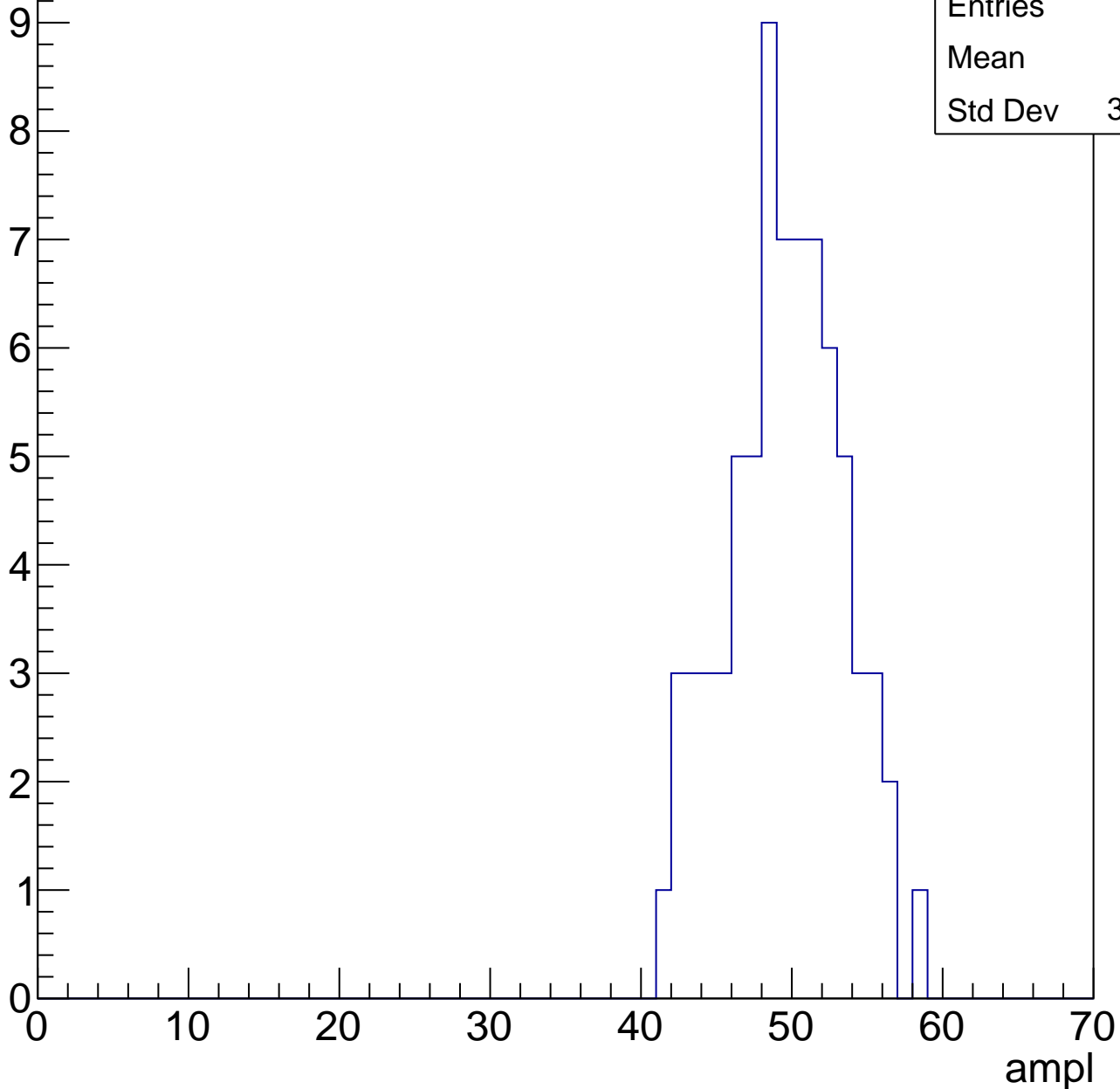


B1L102S, U26-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.1
Std Dev	3.786

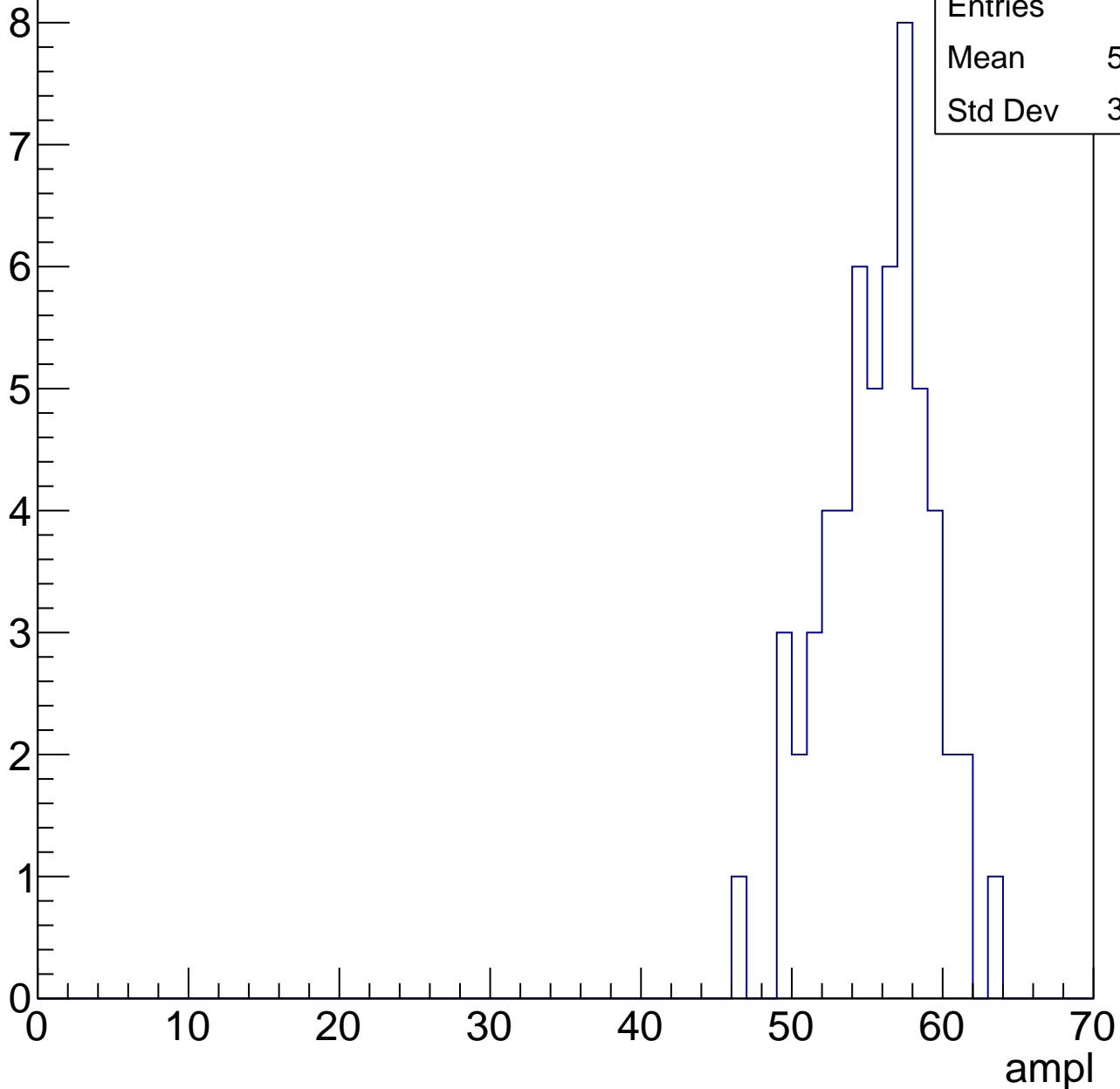


B1L102S, U26-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.14
Std Dev	3.477

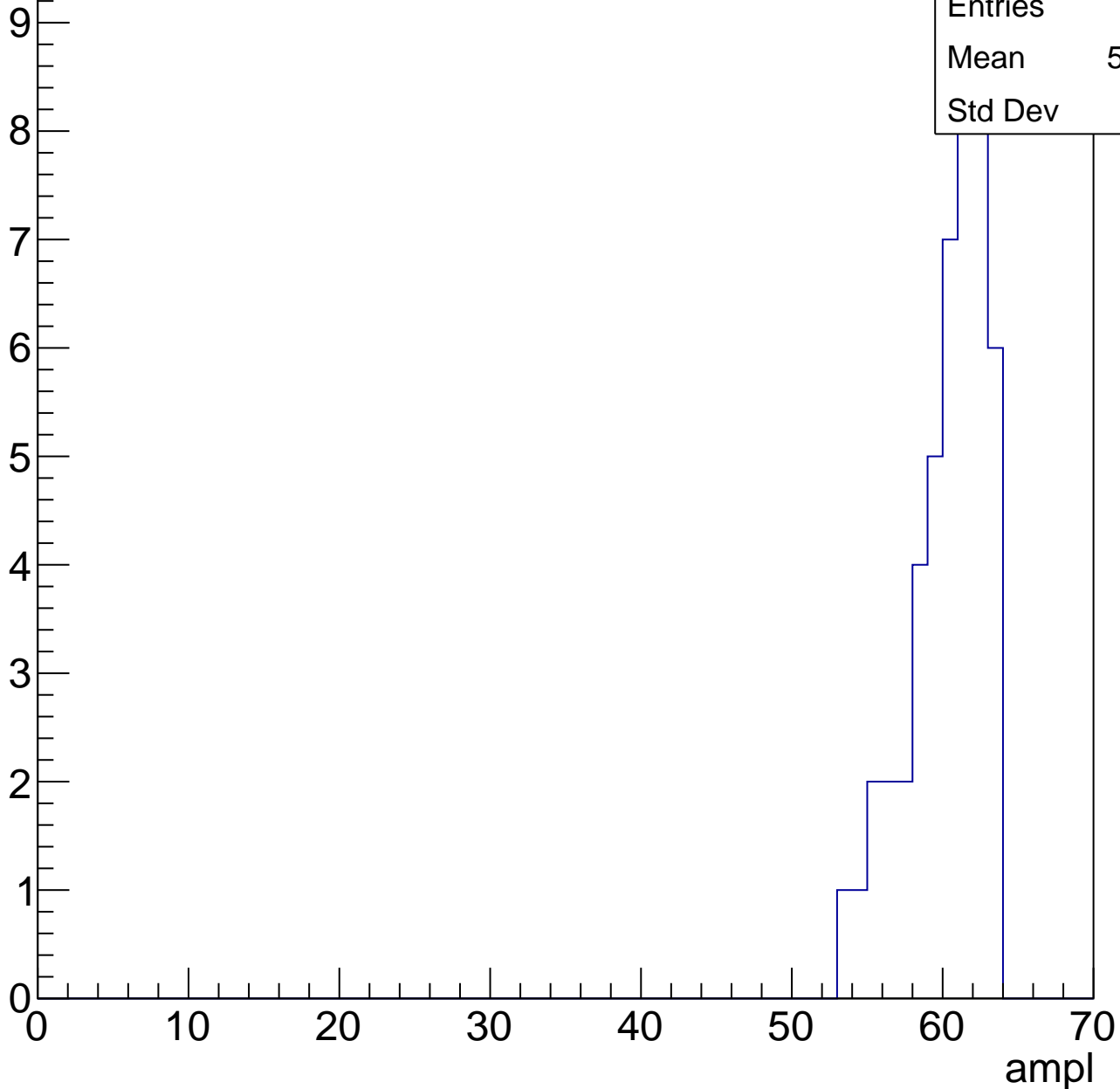


B1L102S, U26-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

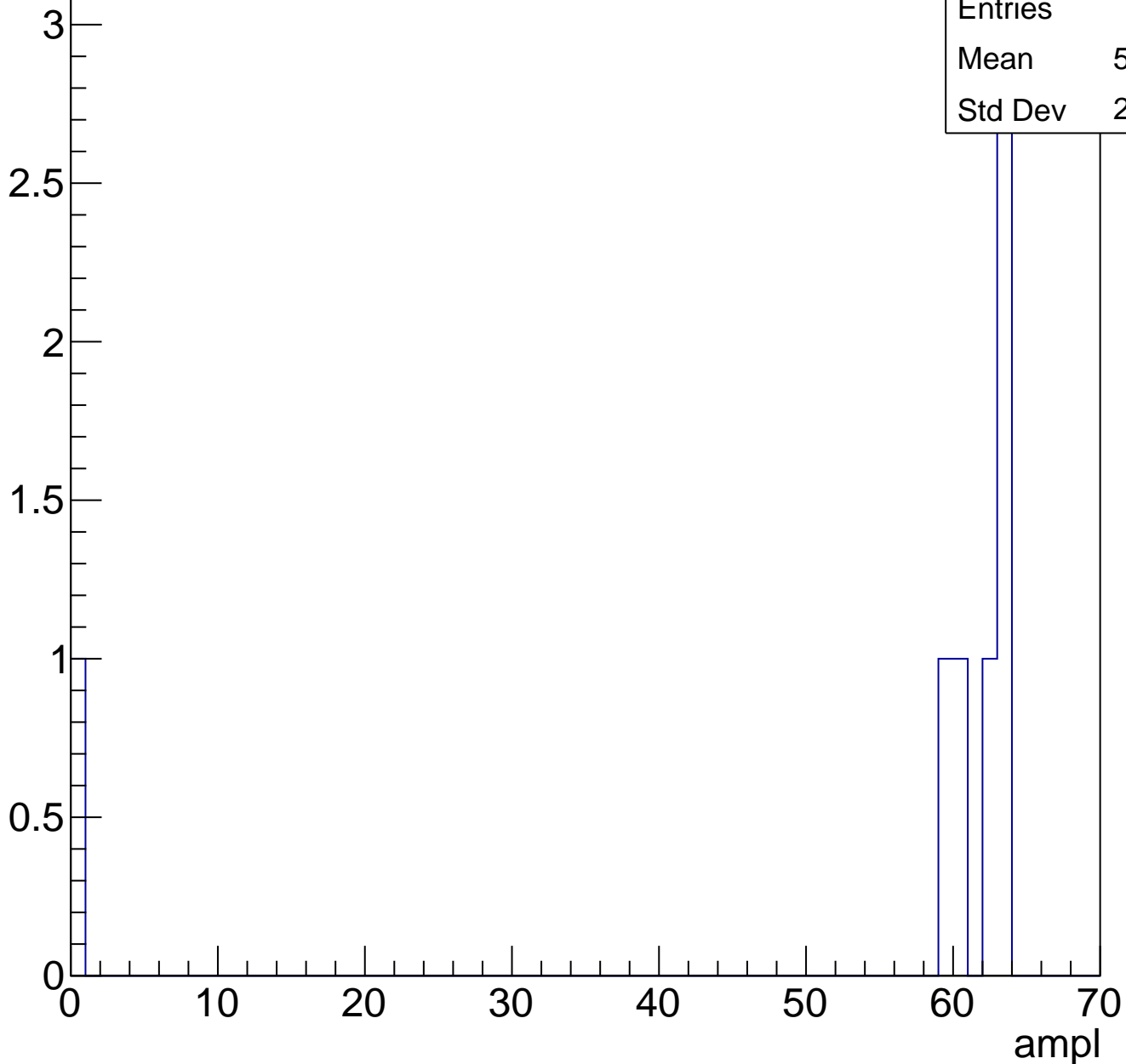
Entries	47
Mean	59.87
Std Dev	2.54



B1L102S, U26-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

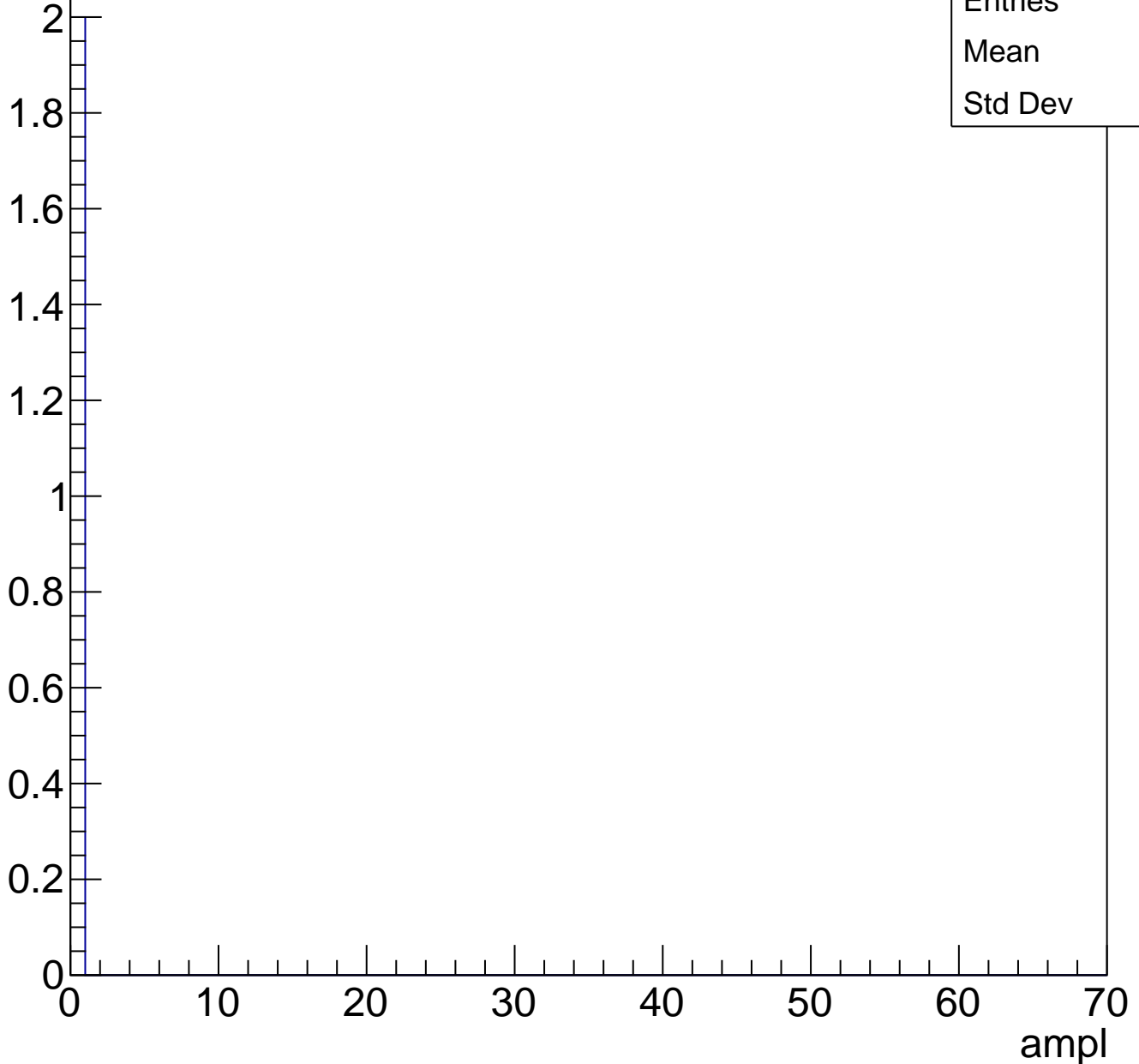
Entry



B1L102S, U26-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch125, adc0

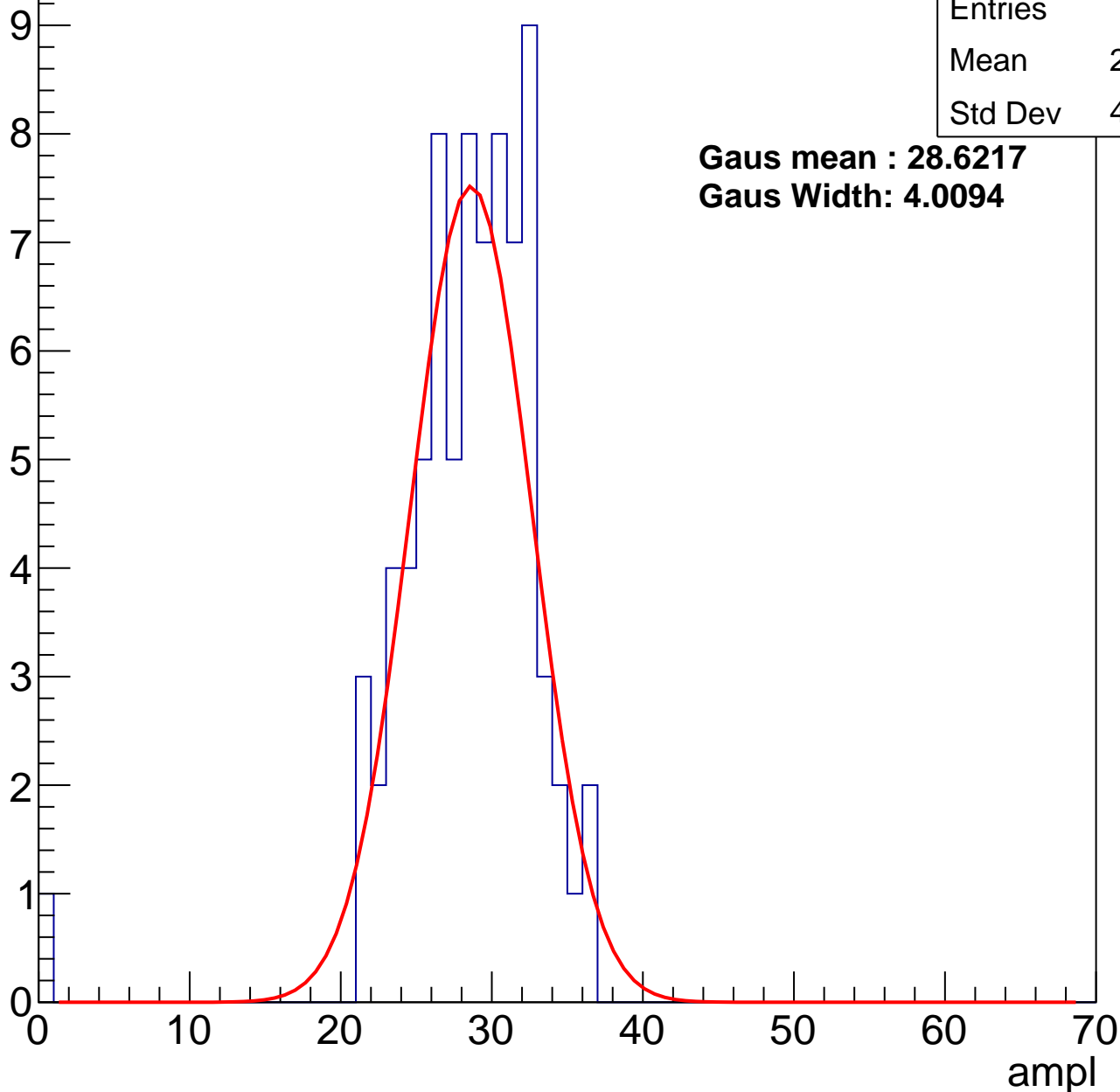
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.96
Std Dev	4.798

Gaus mean : 28.6217

Gaus Width: 4.0094



B1L102S, U26-ch125, adc1

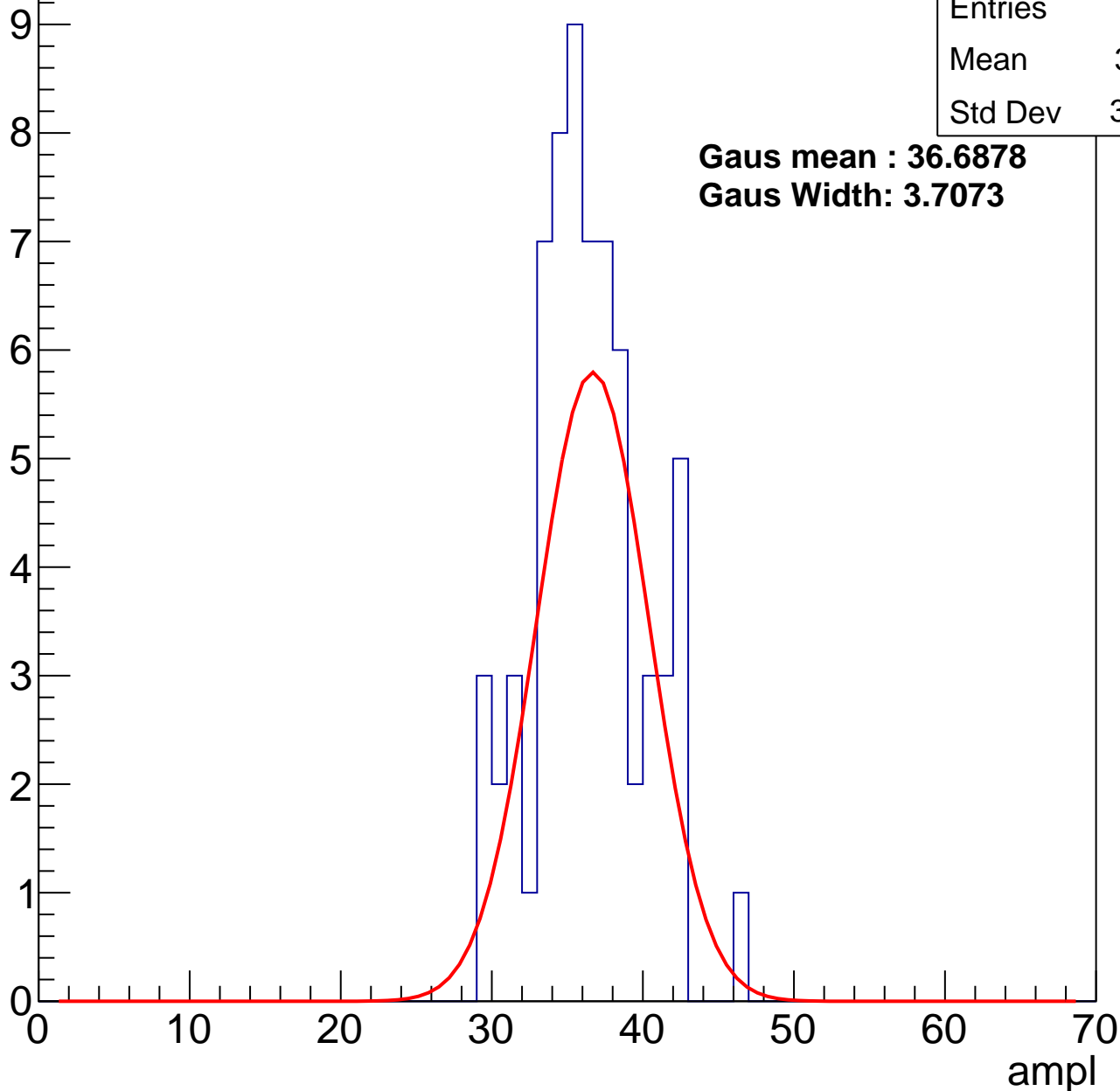
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.91
Std Dev	3.594

Gaus mean : 36.6878

Gaus Width: 3.7073



B1L102S, U26-ch125, adc2

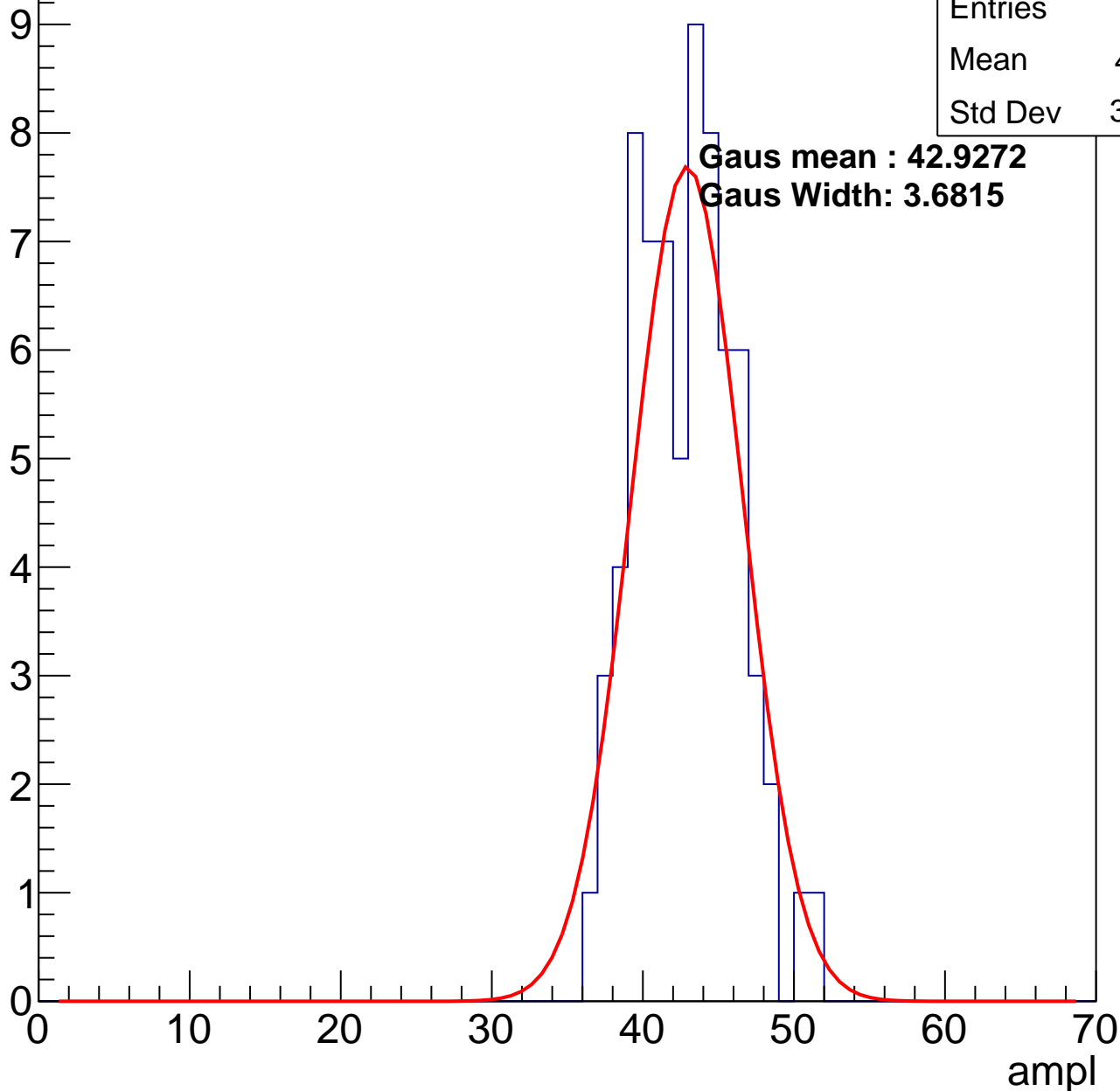
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.41
Std Dev	3.257

Gaus mean : 42.9272

Gaus Width: 3.6815

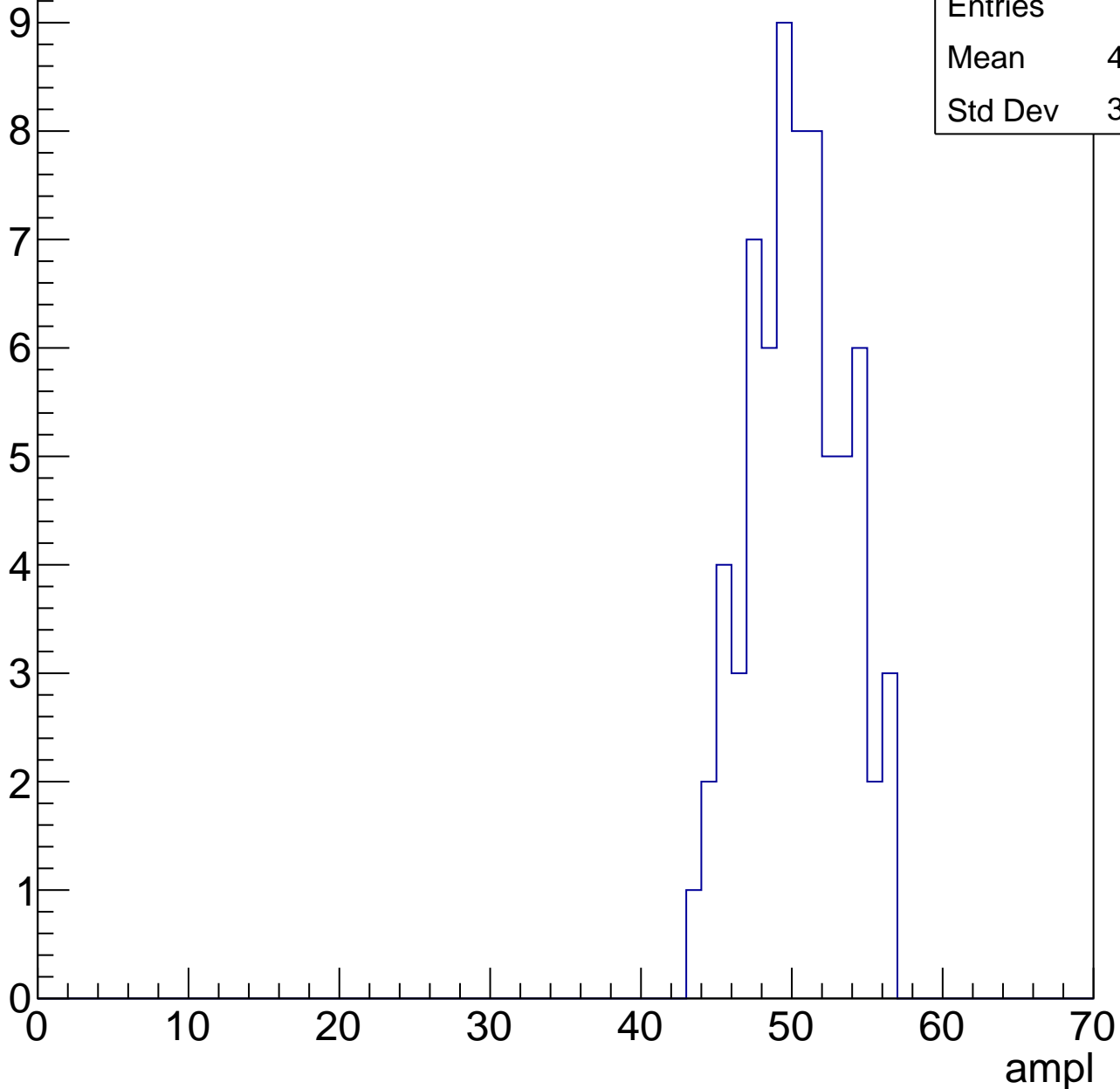


B1L102S, U26-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.88
Std Dev	3.165

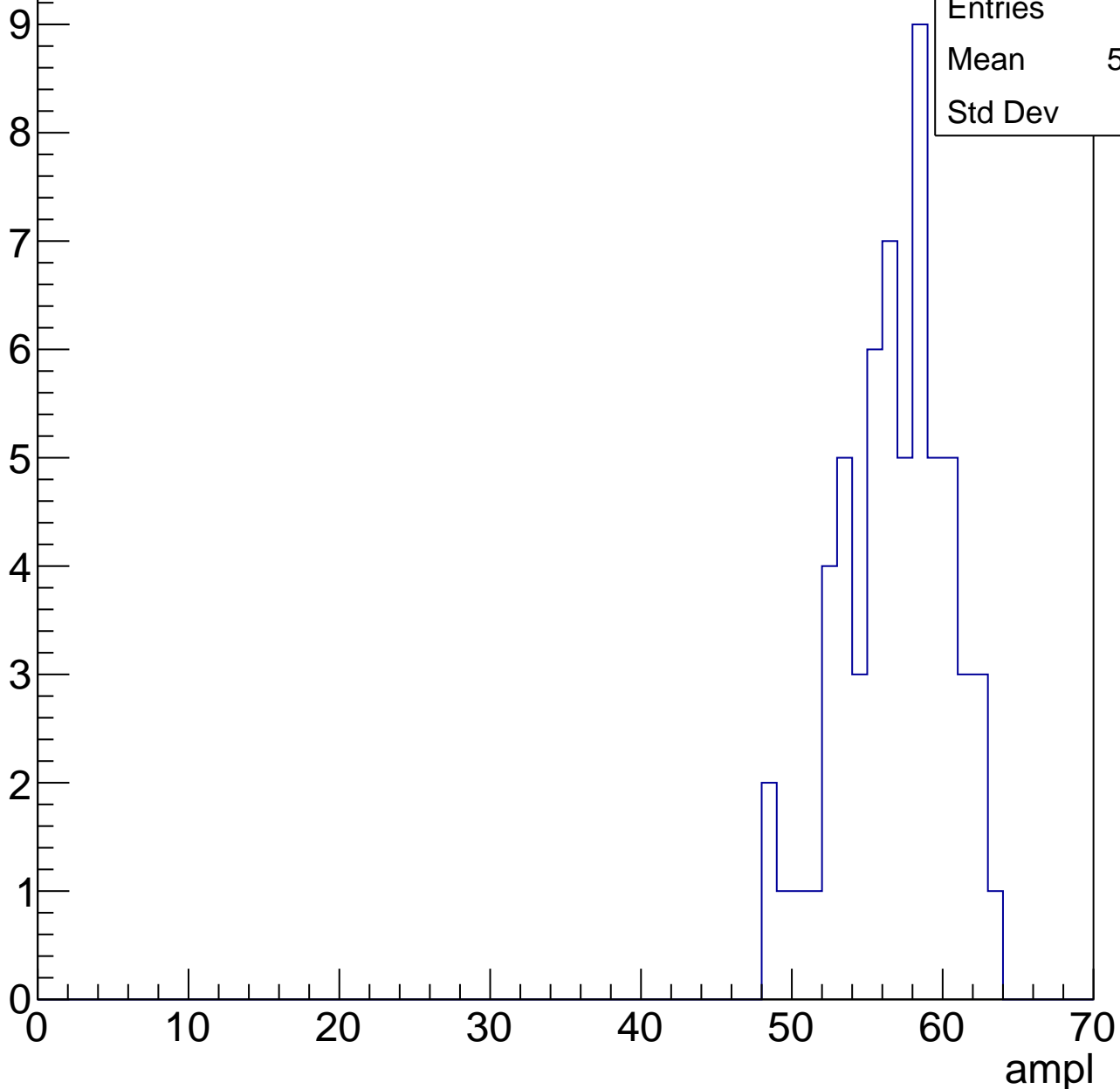


B1L102S, U26-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.34
Std Dev	3.52

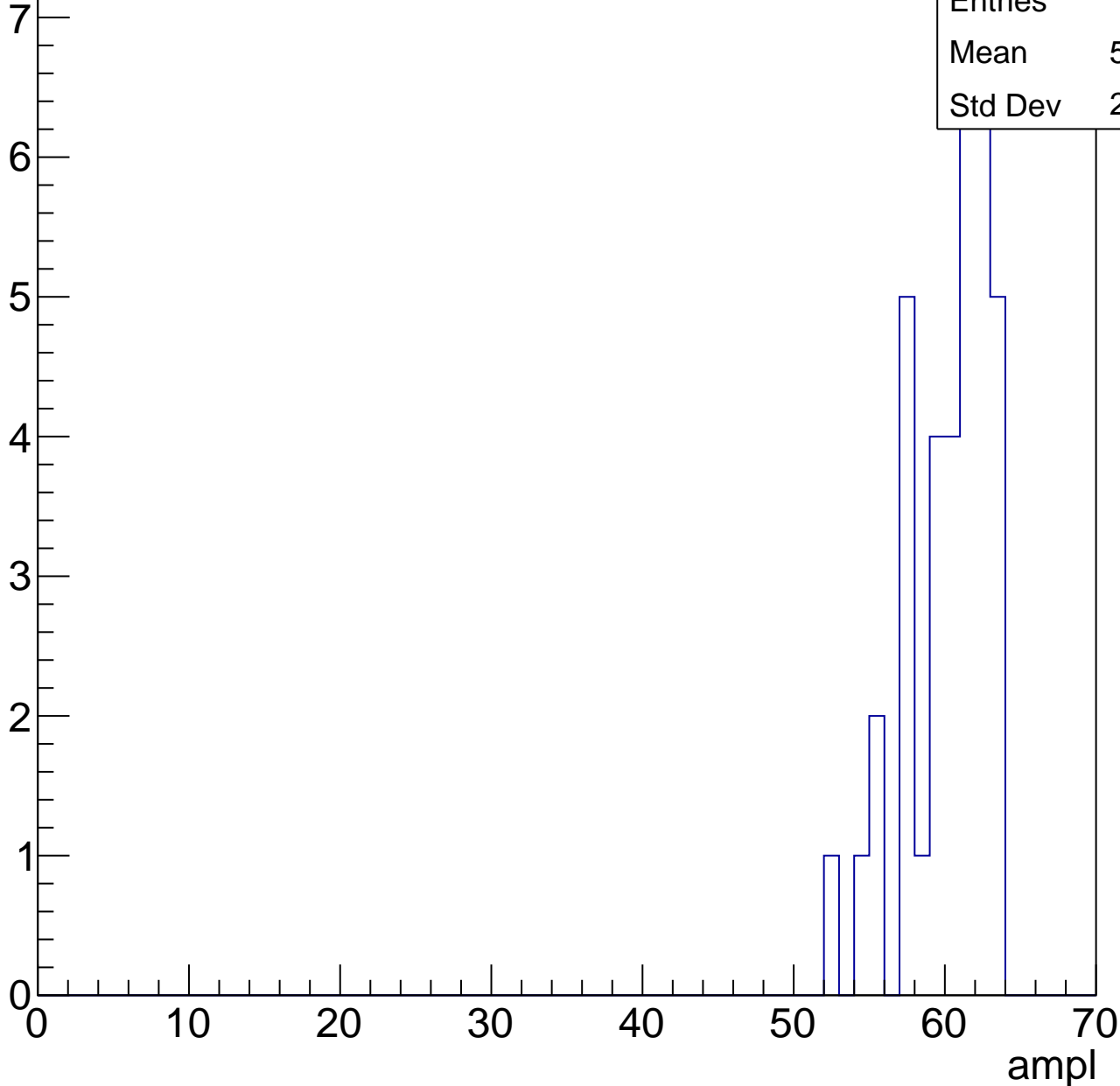


B1L102S, U26-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

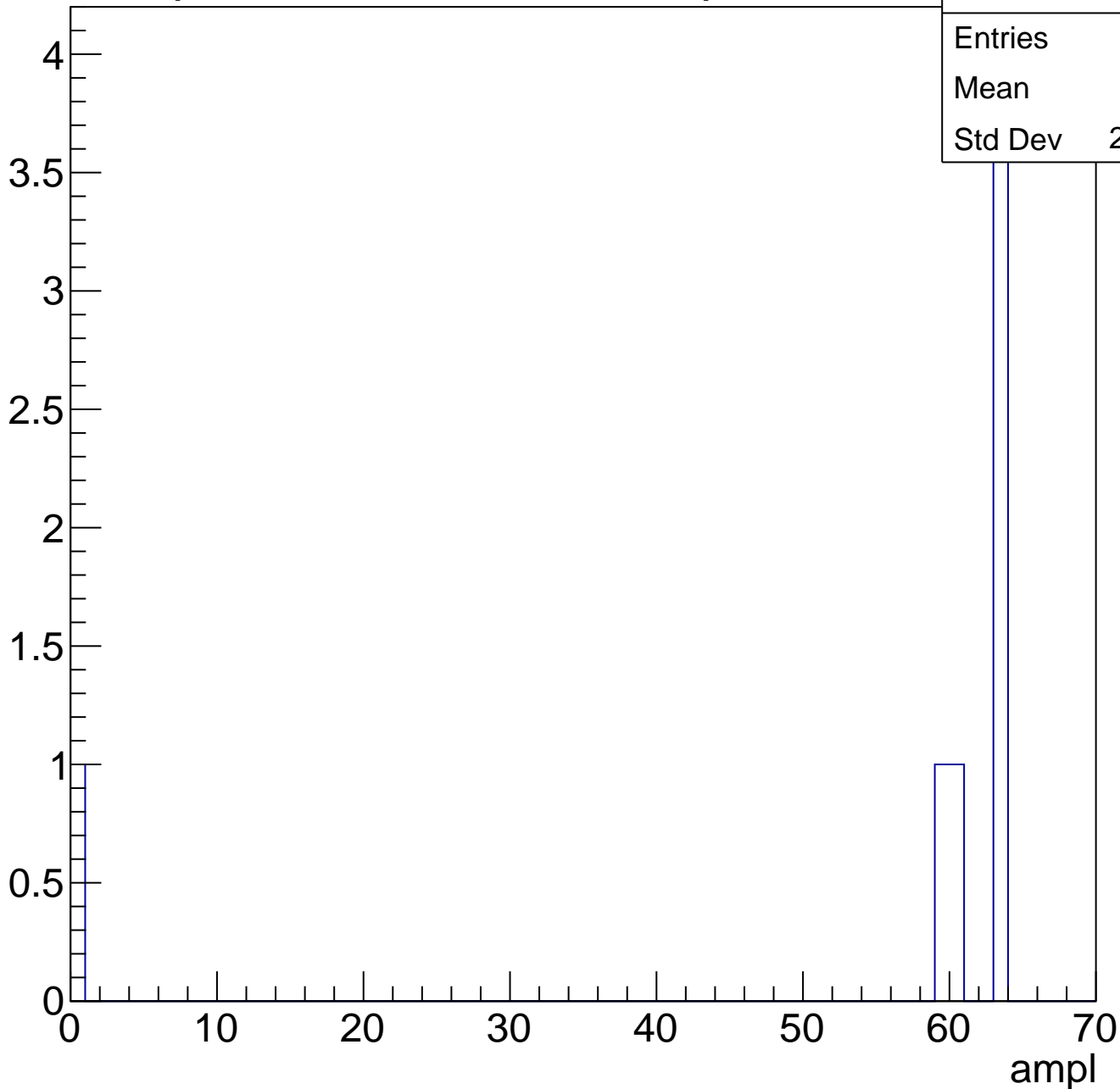
Entries	37
Mean	59.76
Std Dev	2.755



B1L102S, U26-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

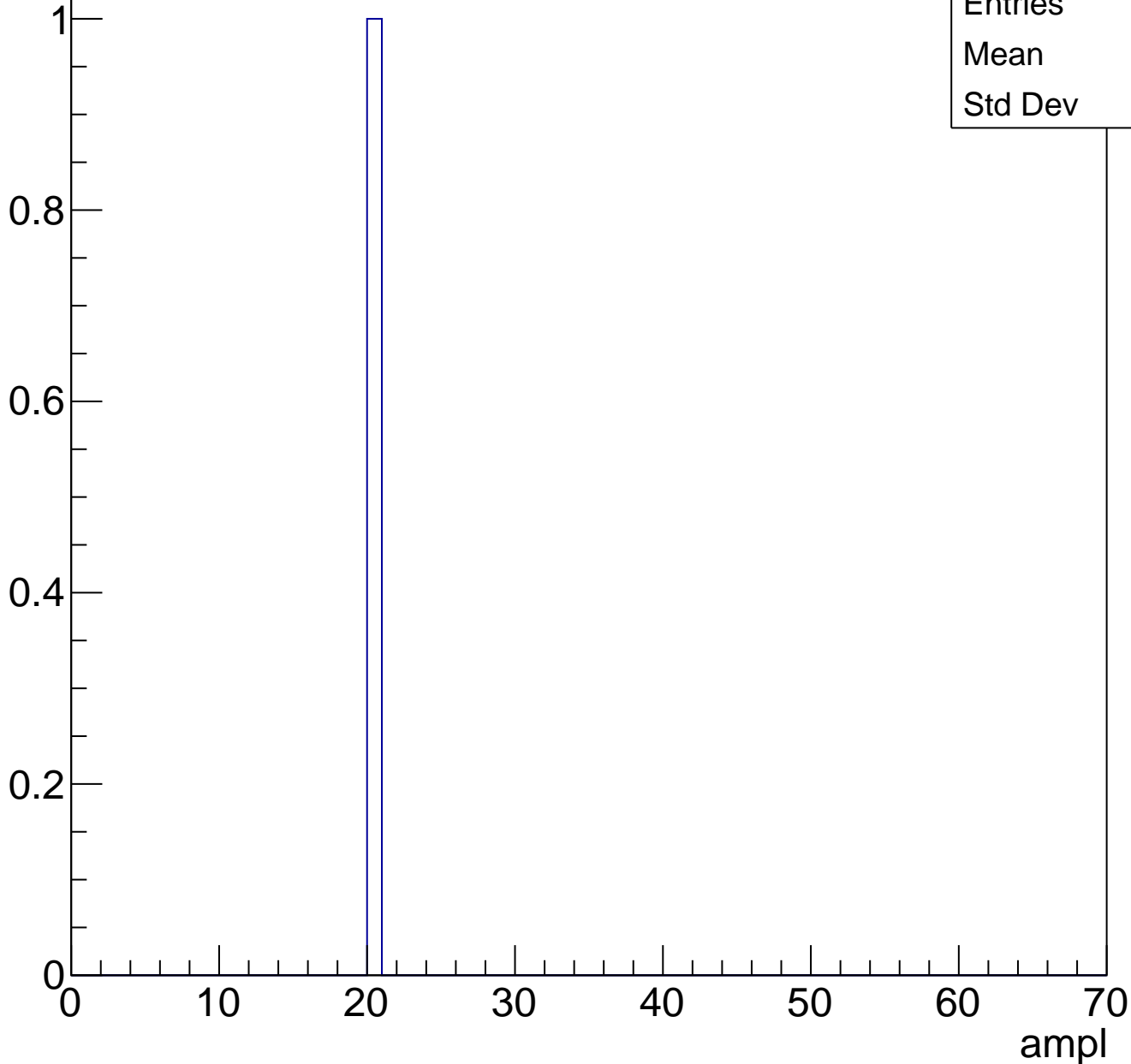
Entry



B1L102S, U26-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch126, adc0

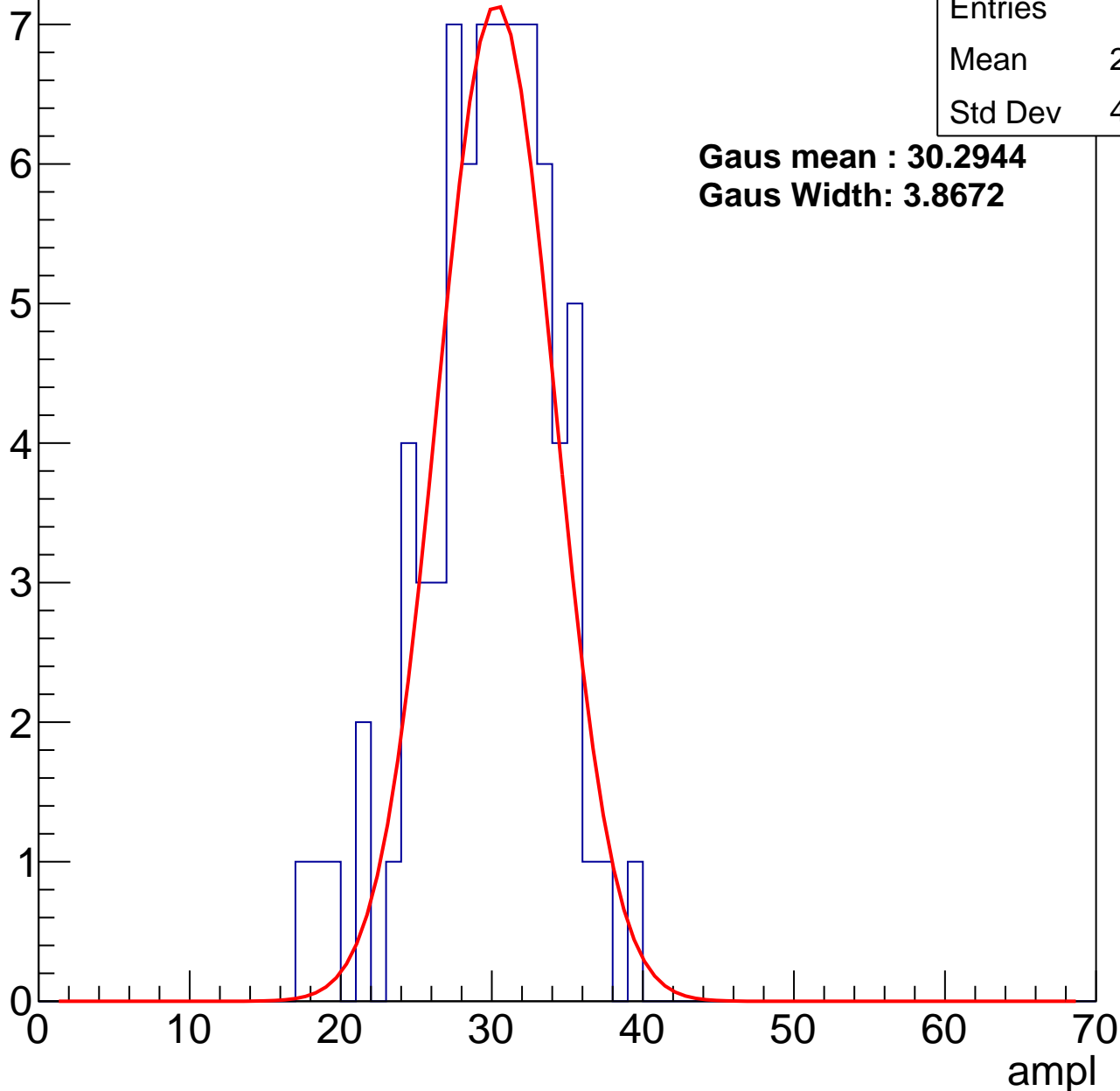
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.33
Std Dev	4.346

Gaus mean : 30.2944

Gaus Width: 3.8672



B1L102S, U26-ch126, adc1

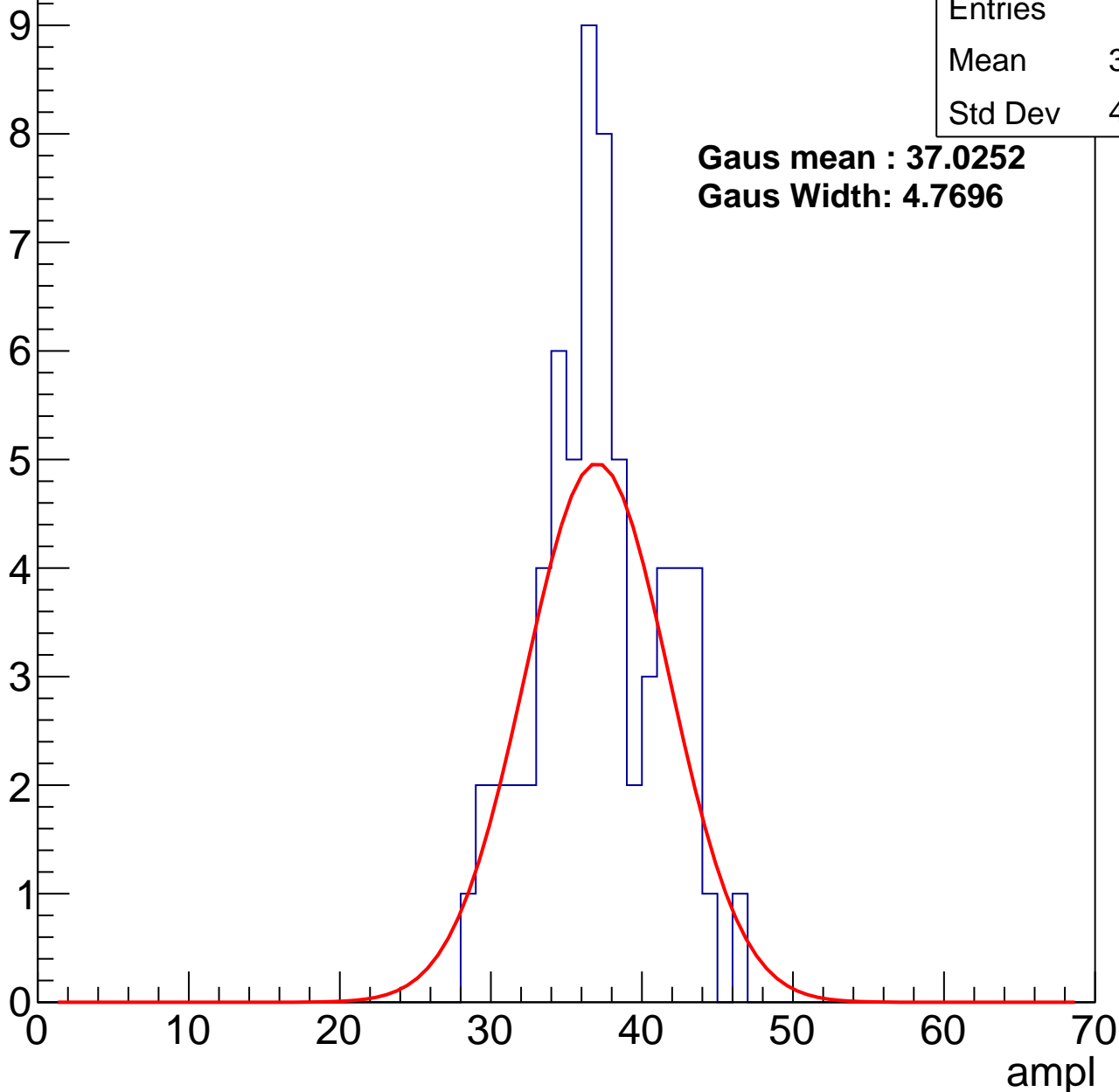
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.69
Std Dev	4.015

Gaus mean : 37.0252

Gaus Width: 4.7696



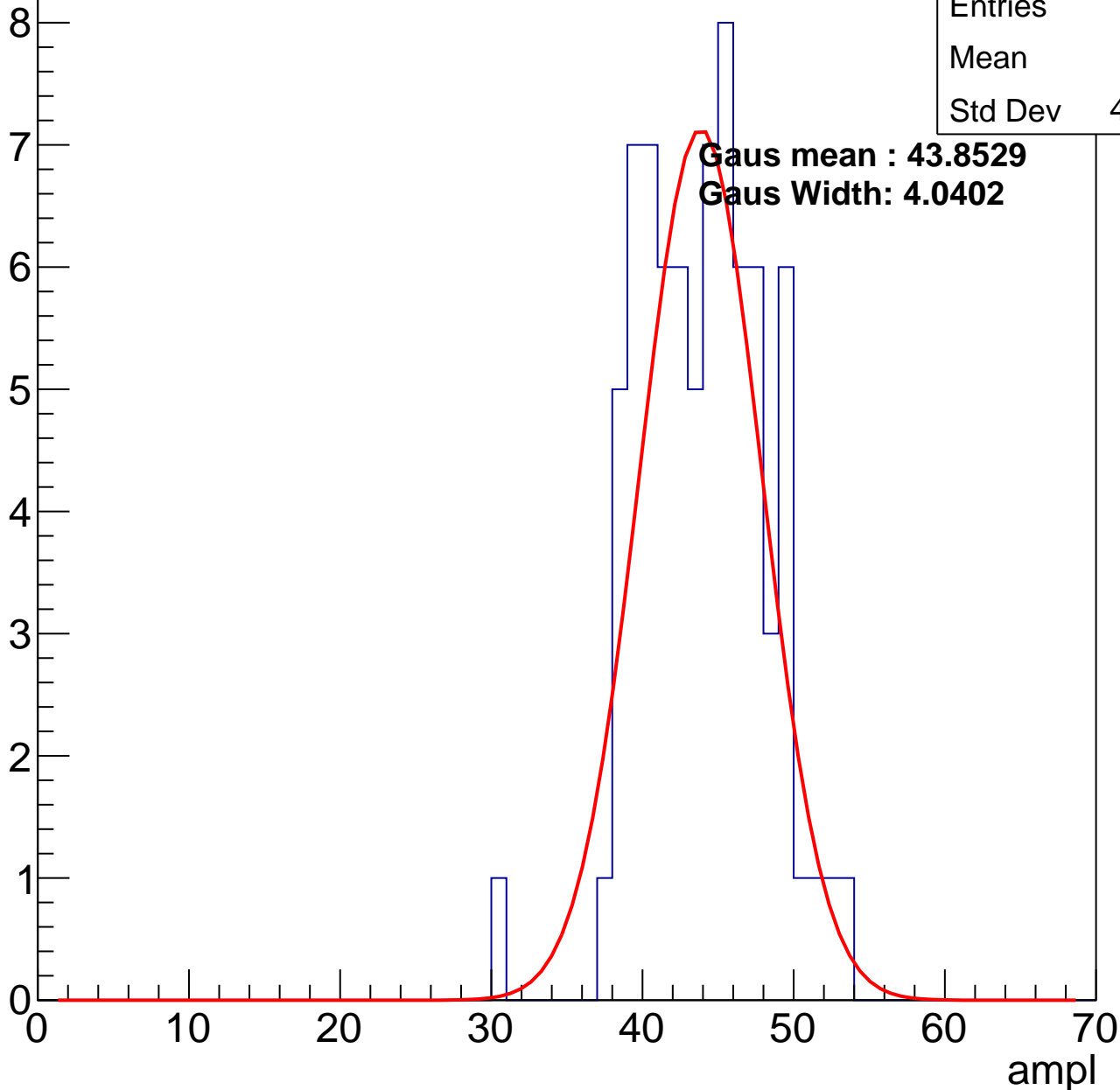
B1L102S, U26-ch126, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	43.5
Std Dev	4.066

Gaus mean : 43.8529
Gaus Width: 4.0402

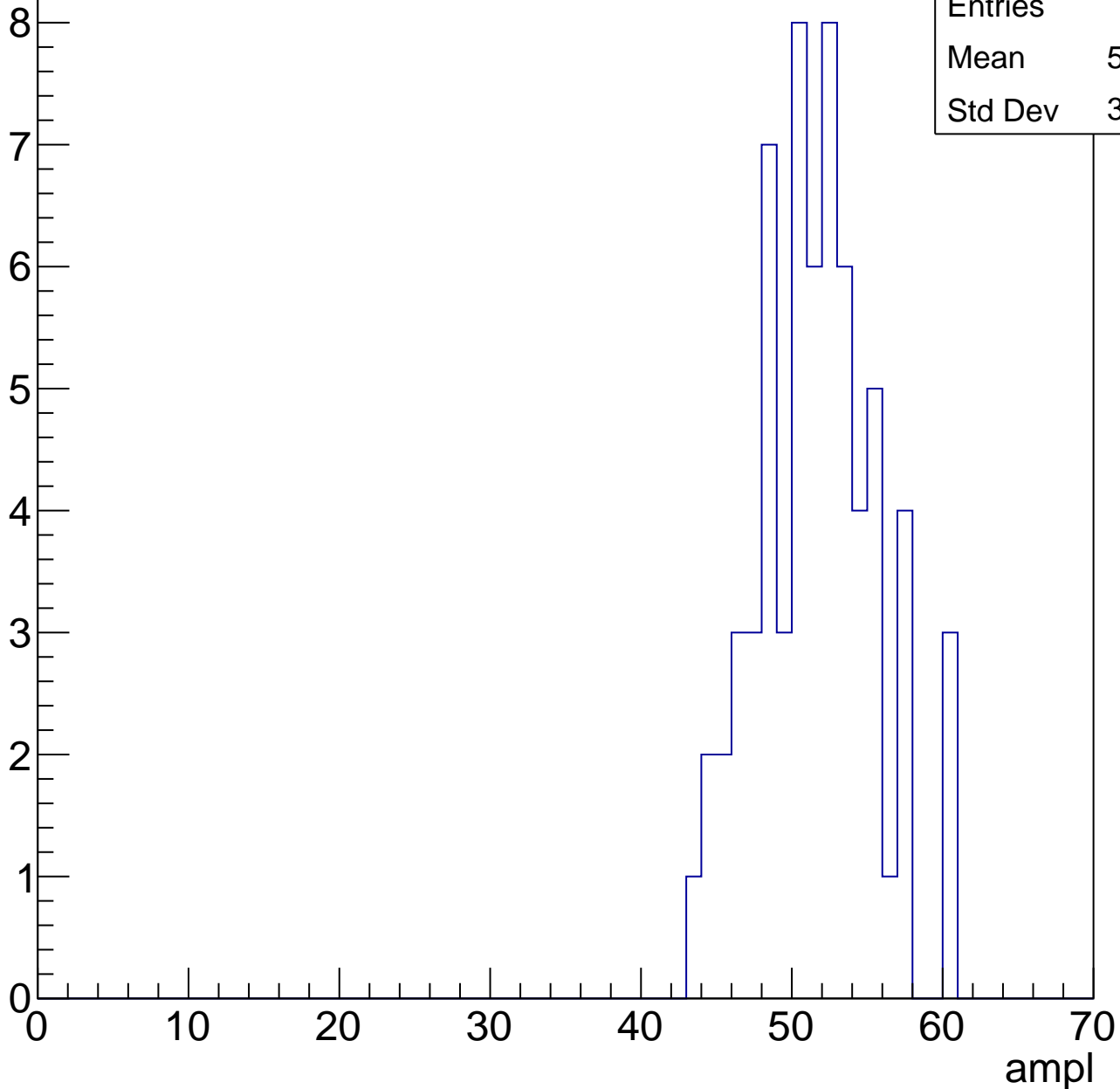


B1L102S, U26-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	51.18
Std Dev	3.888

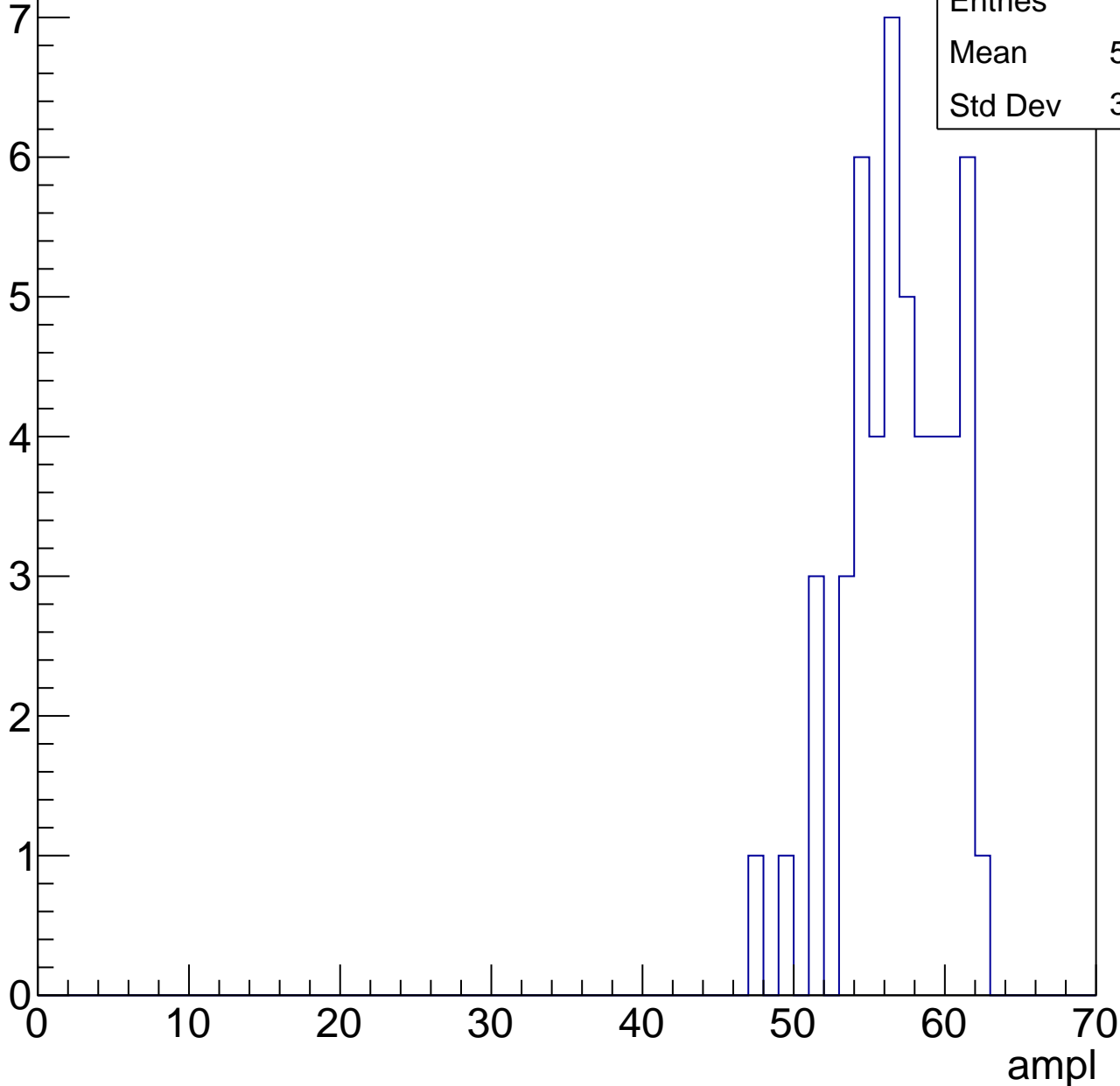


B1L102S, U26-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.43
Std Dev	3.375

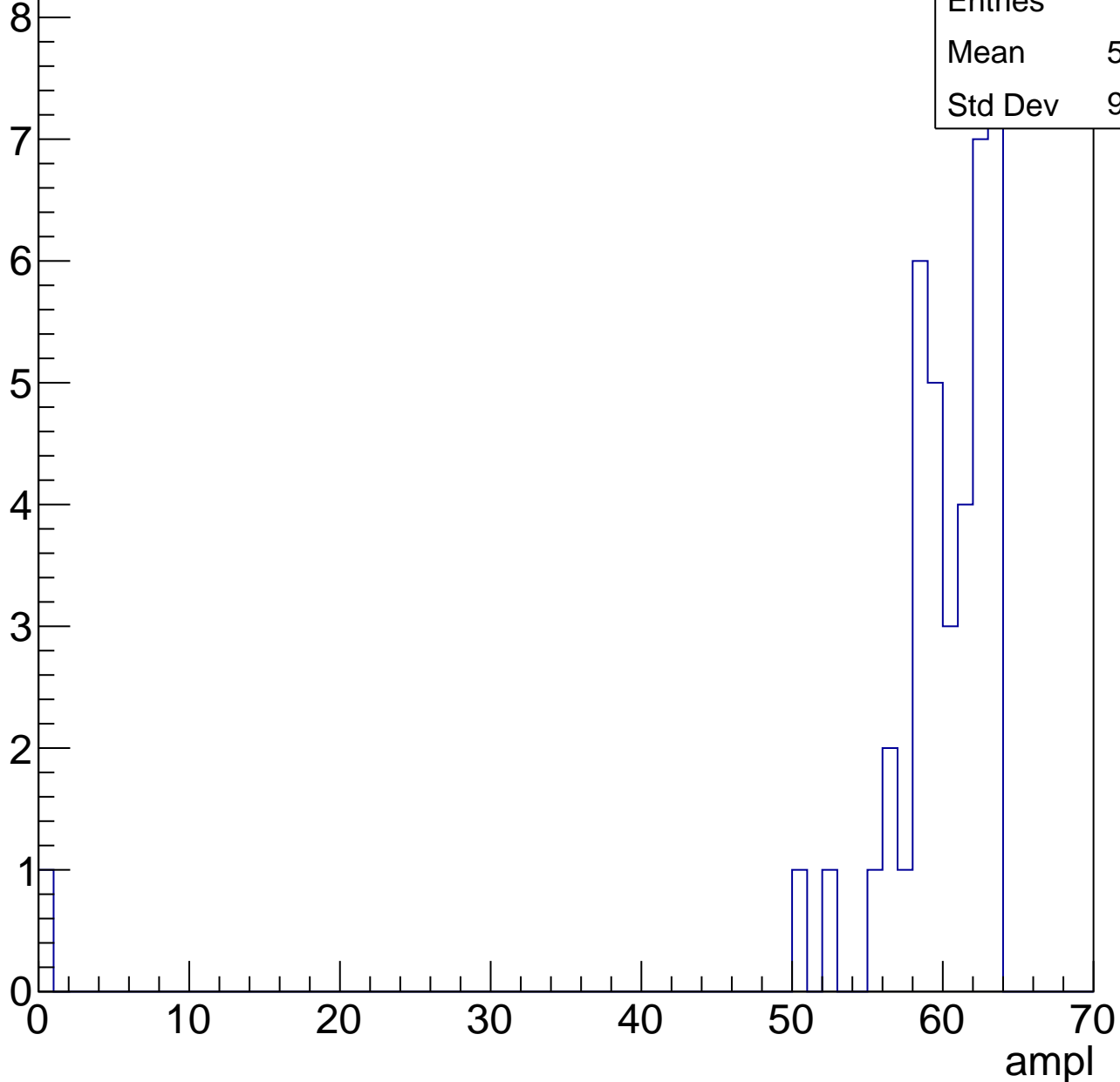


B1L102S, U26-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.27
Std Dev	9.803



B1L102S, U26-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U26-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U26-ch127, adc0

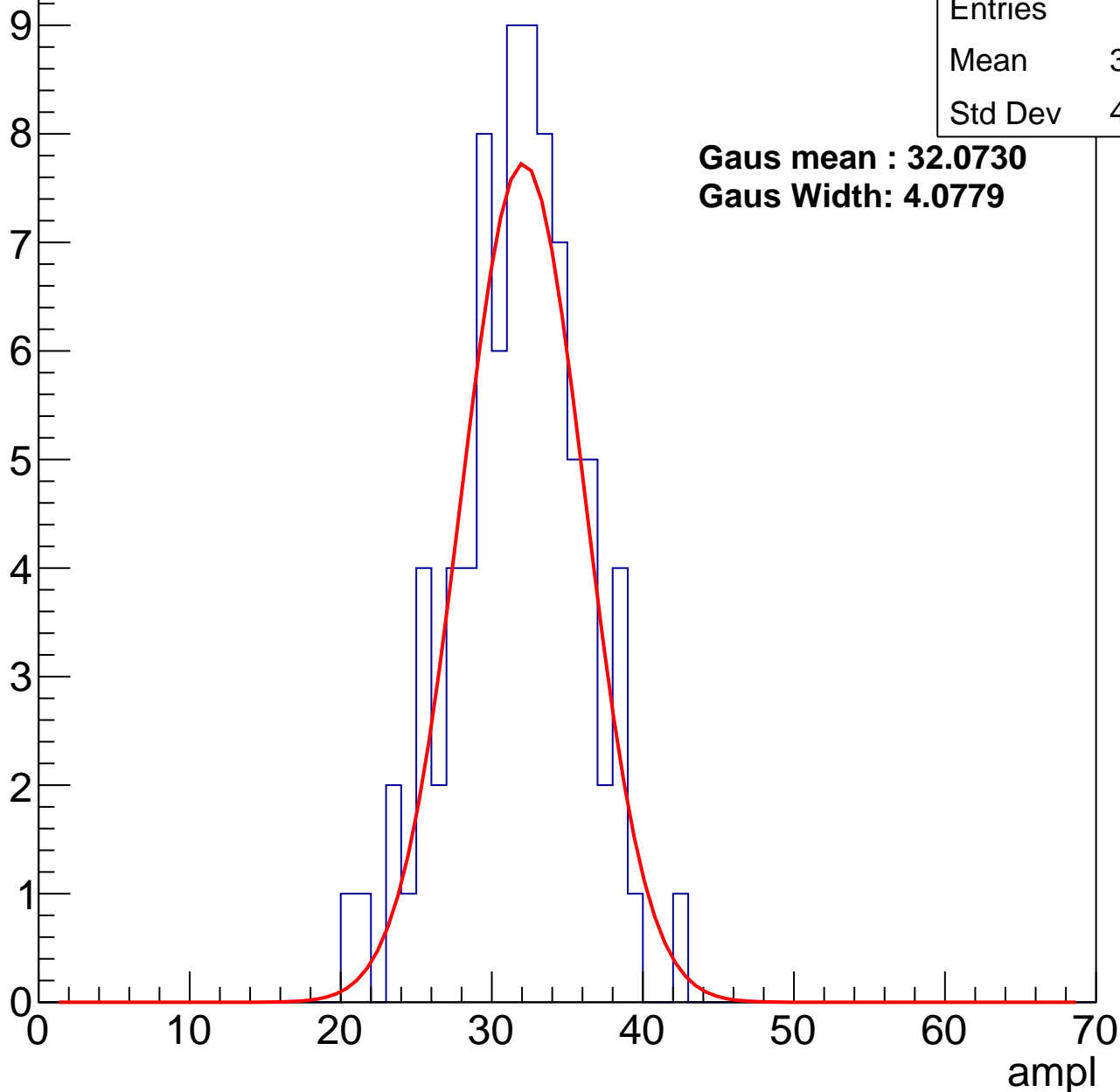
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	31.26
Std Dev	4.206

Gaus mean : 32.0730

Gaus Width: 4.0779



B1L102S, U26-ch127, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	39.75
Std Dev	4.303

Gaus mean : 40.1087

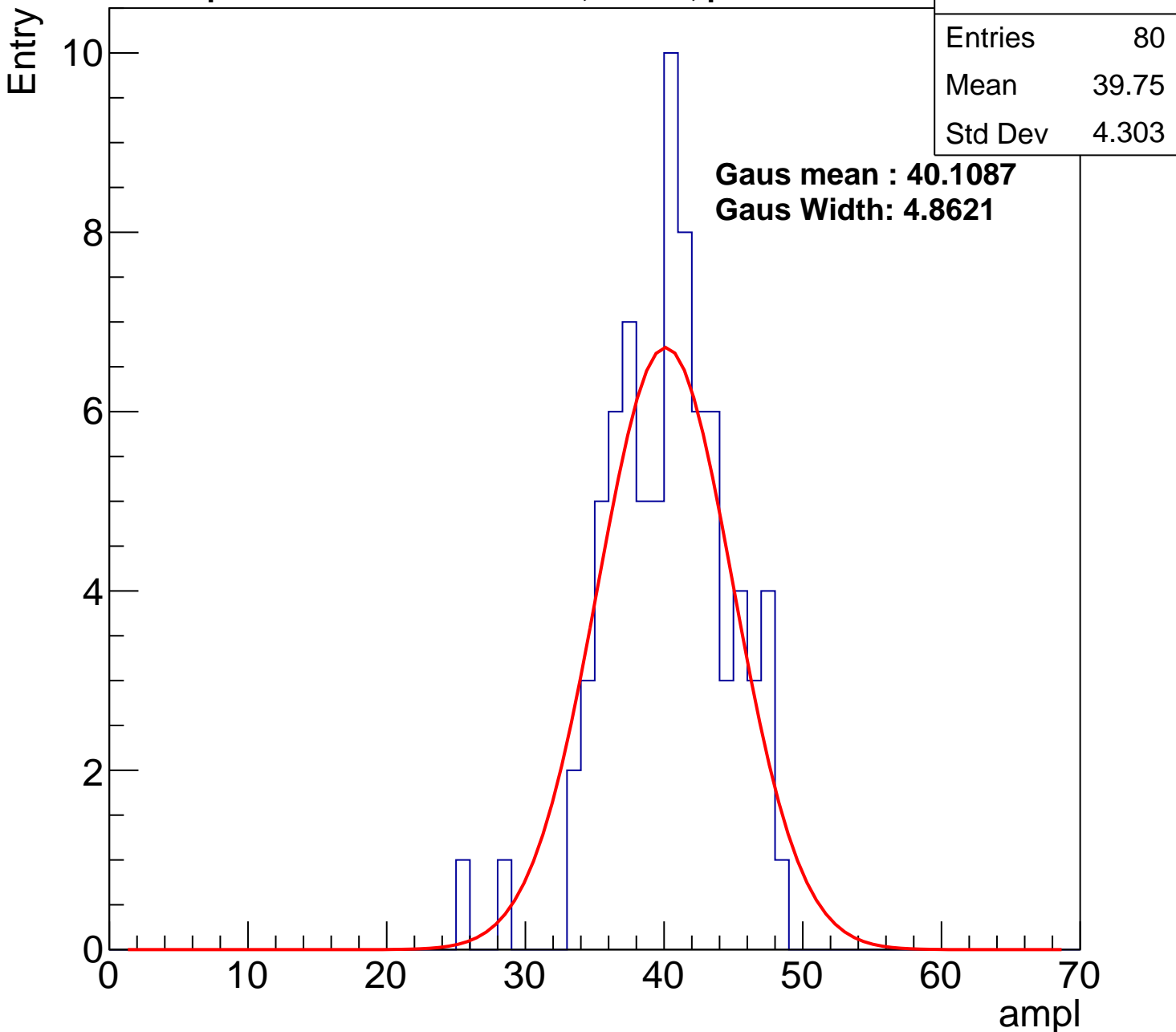
Gaus Width: 4.8621

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch127, adc2

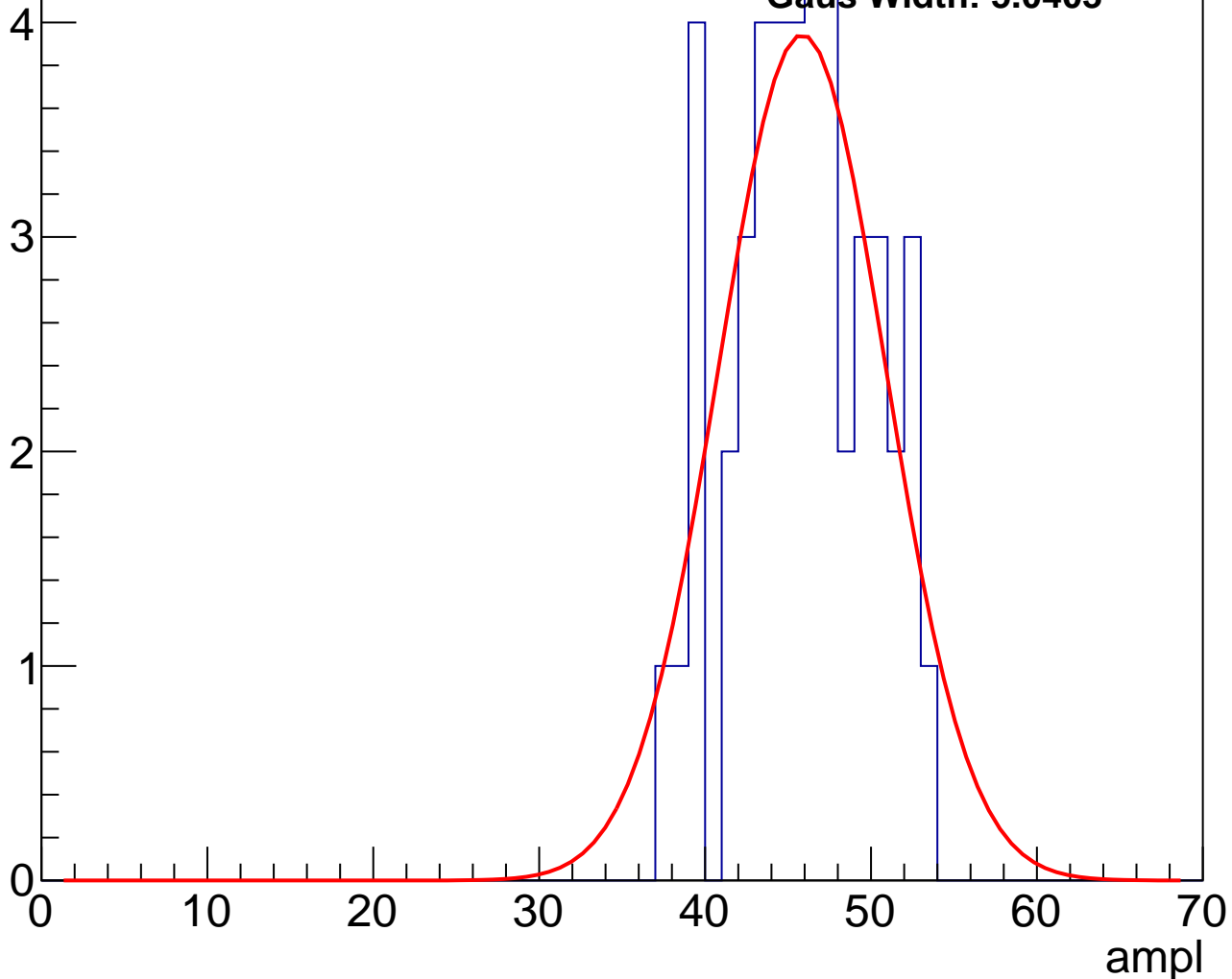
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	45.45
Std Dev	4.062

Gaus mean : 45.8514

Gaus Width: 5.0403

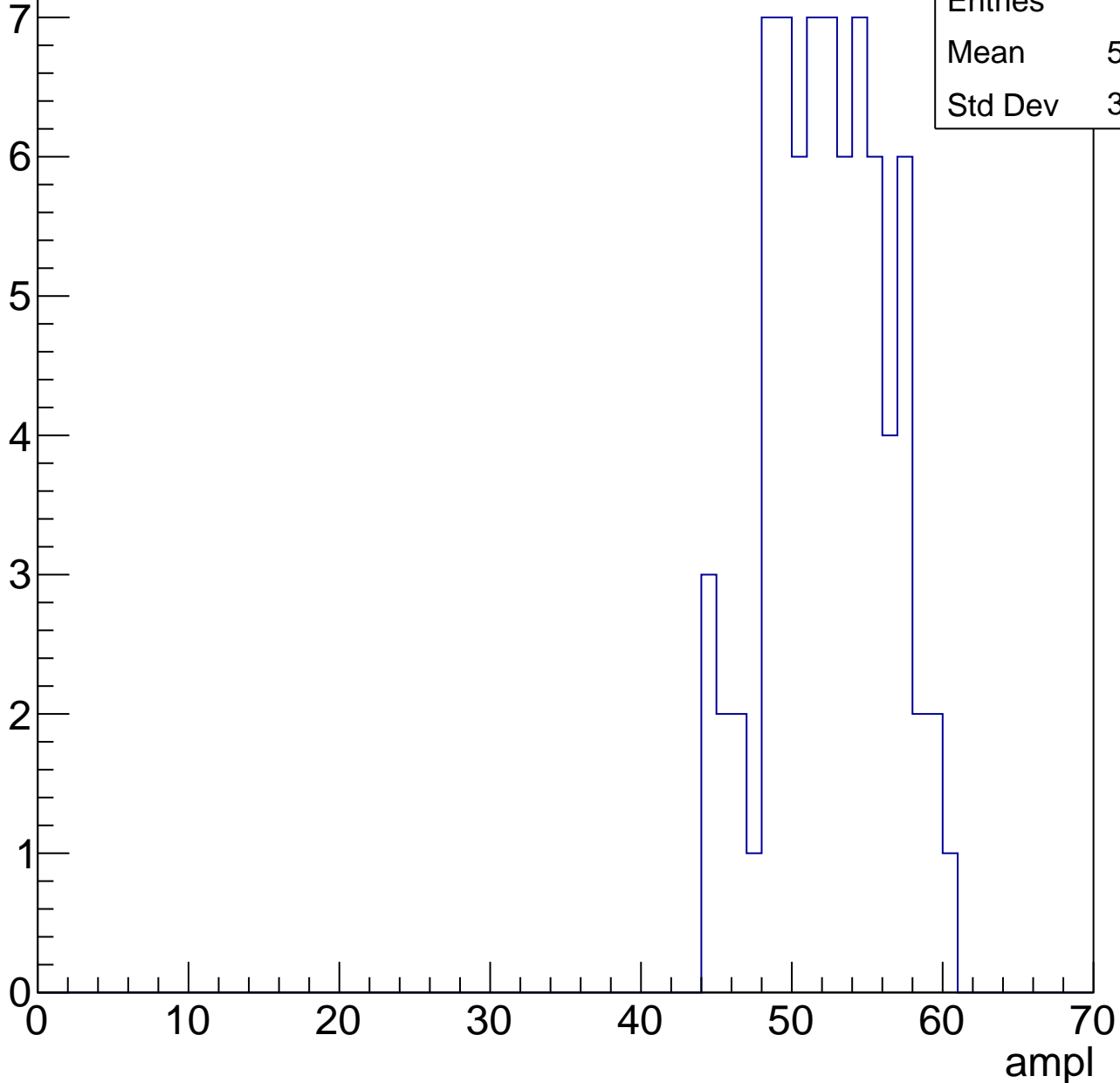


B1L102S, U26-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	51.93
Std Dev	3.854

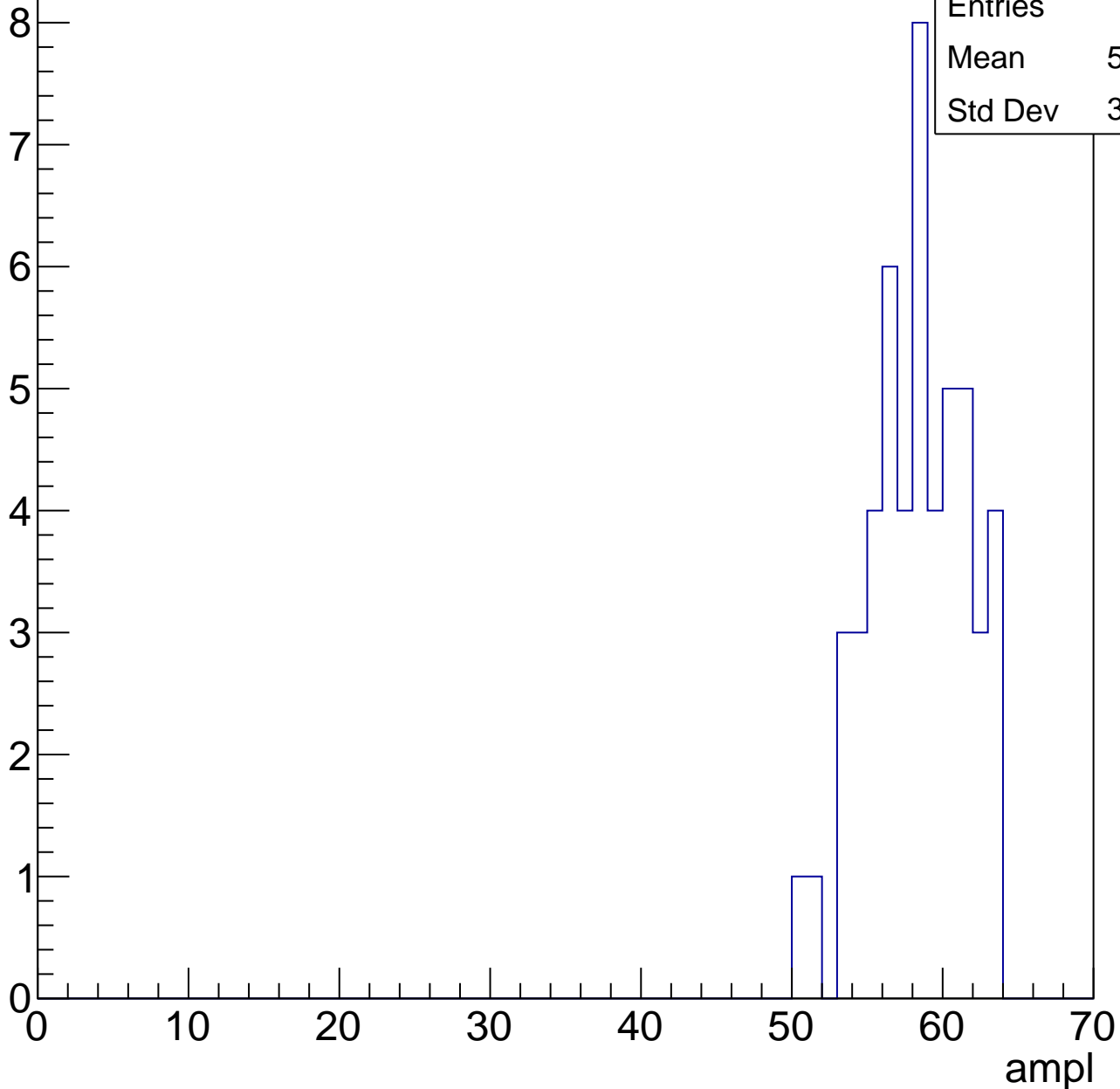


B1L102S, U26-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.82
Std Dev	3.179

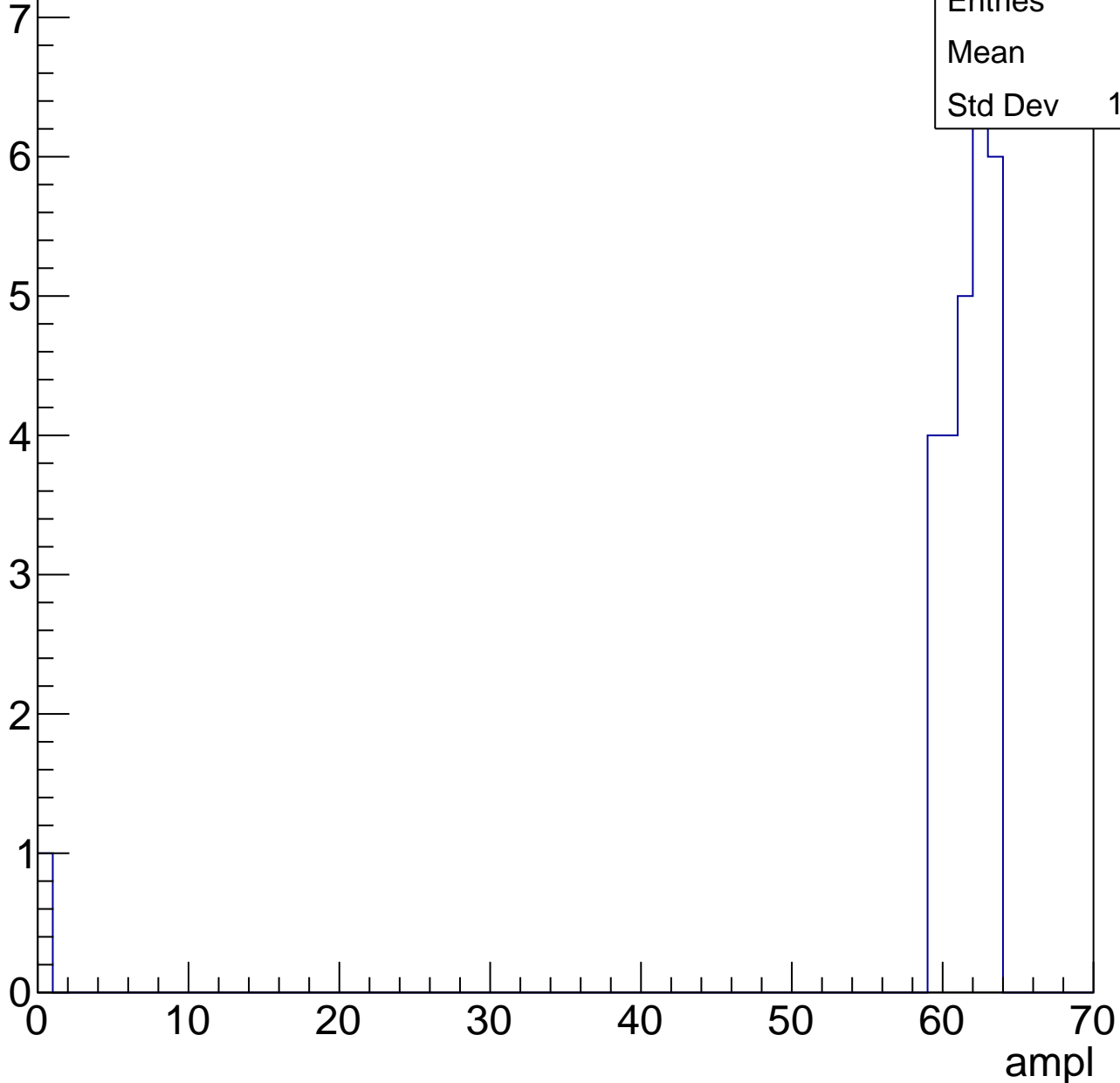


B1L102S, U26-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	27
Mean	59
Std Dev	11.65



B1L102S, U26-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

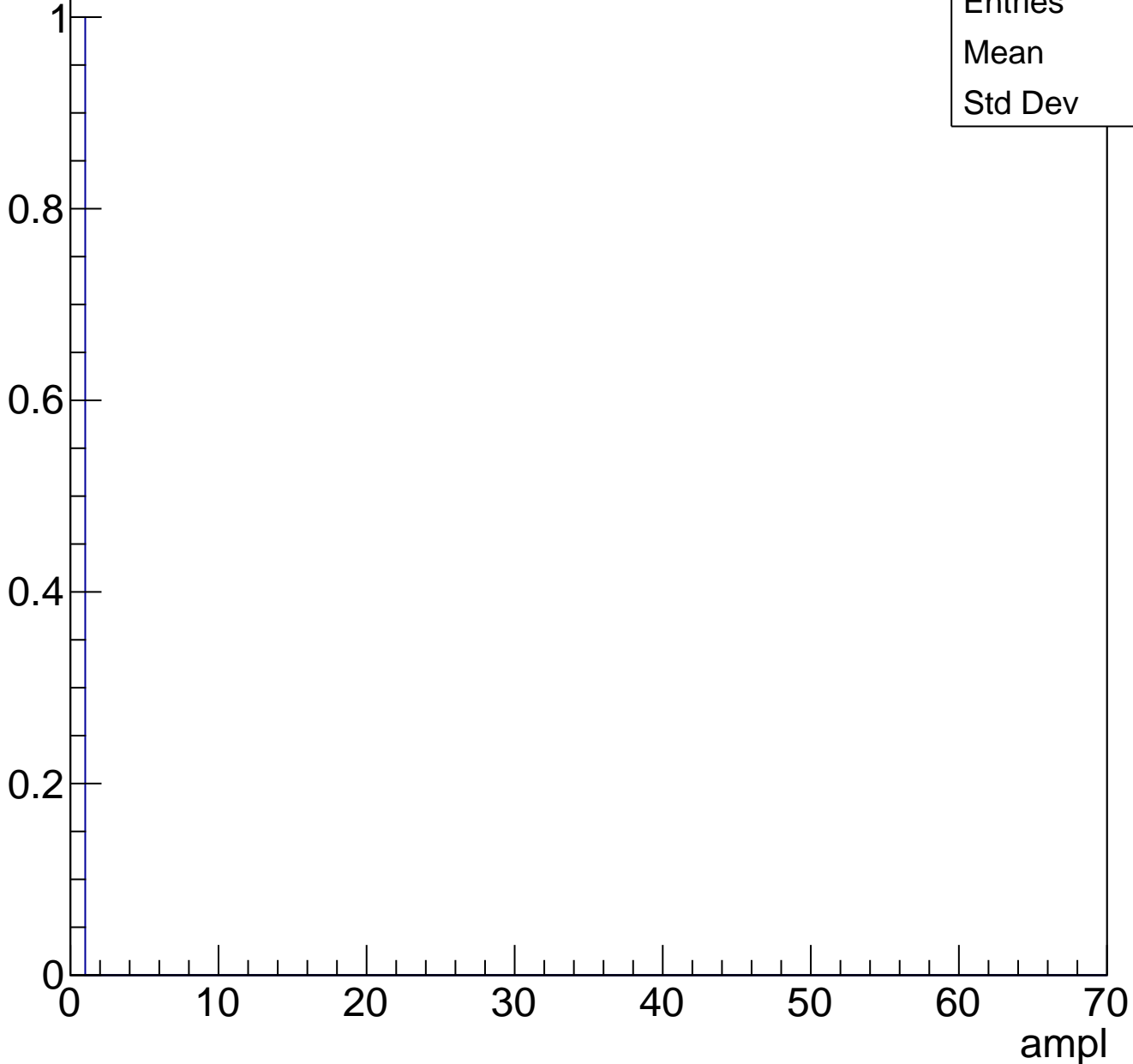
Entry



B1L102S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0