

B1L003S, U13-ch0, adc0

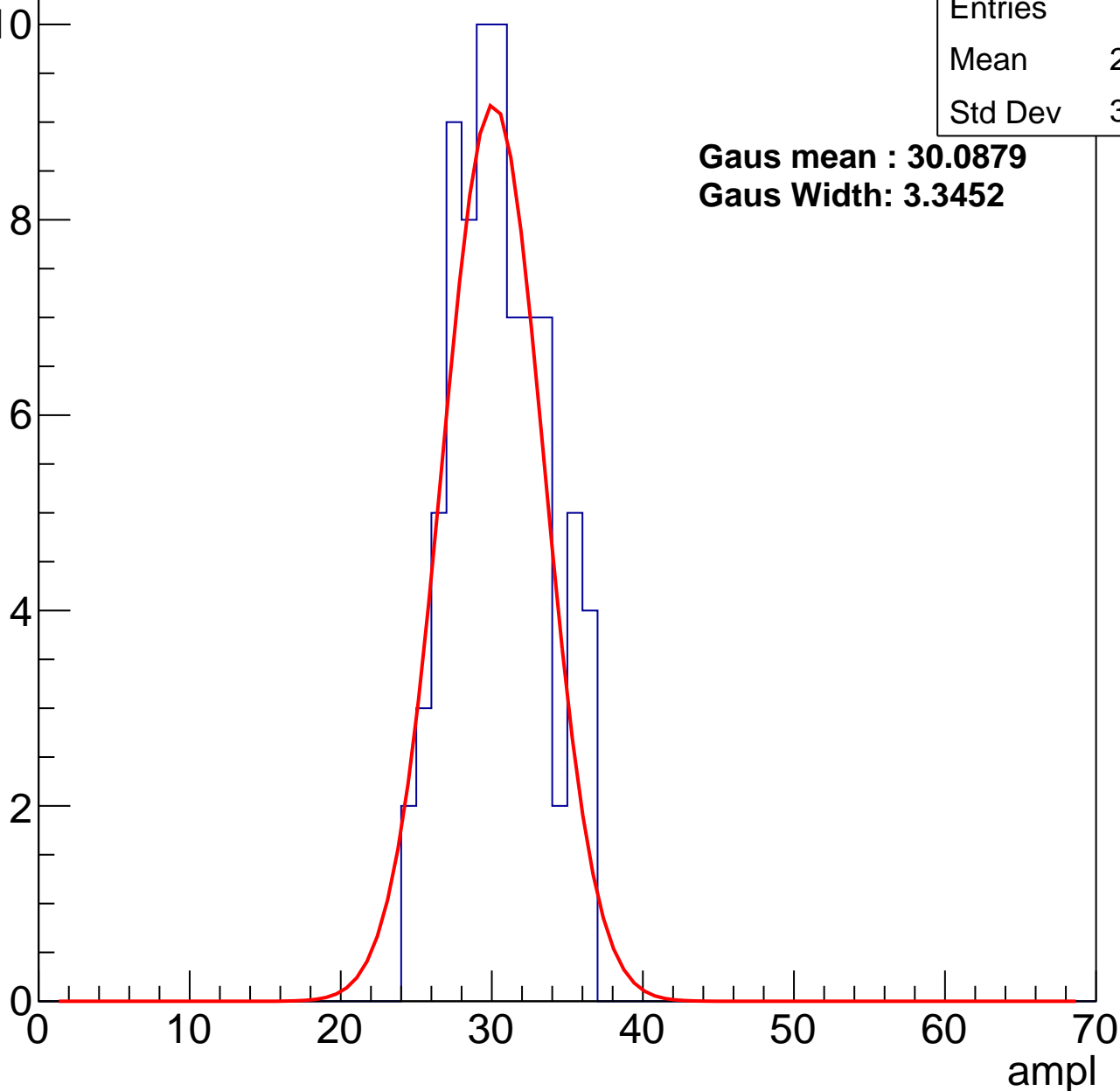
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	29.99
Std Dev	3.079

Gaus mean : 30.0879

Gaus Width: 3.3452



B1L003S, U13-ch0, adc1

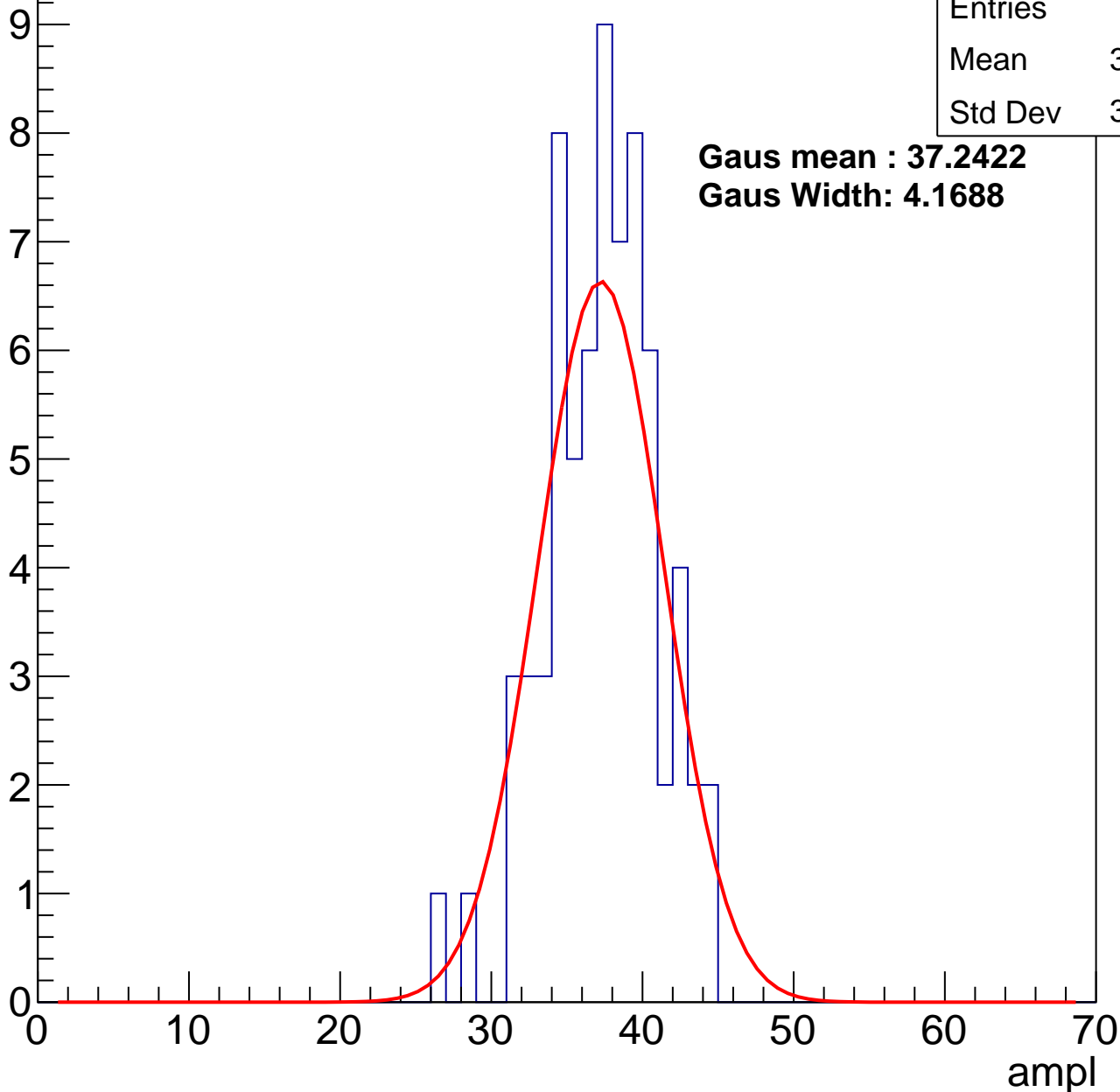
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.86
Std Dev	3.634

Gaus mean : 37.2422

Gaus Width: 4.1688



B1L003S, U13-ch0, adc2

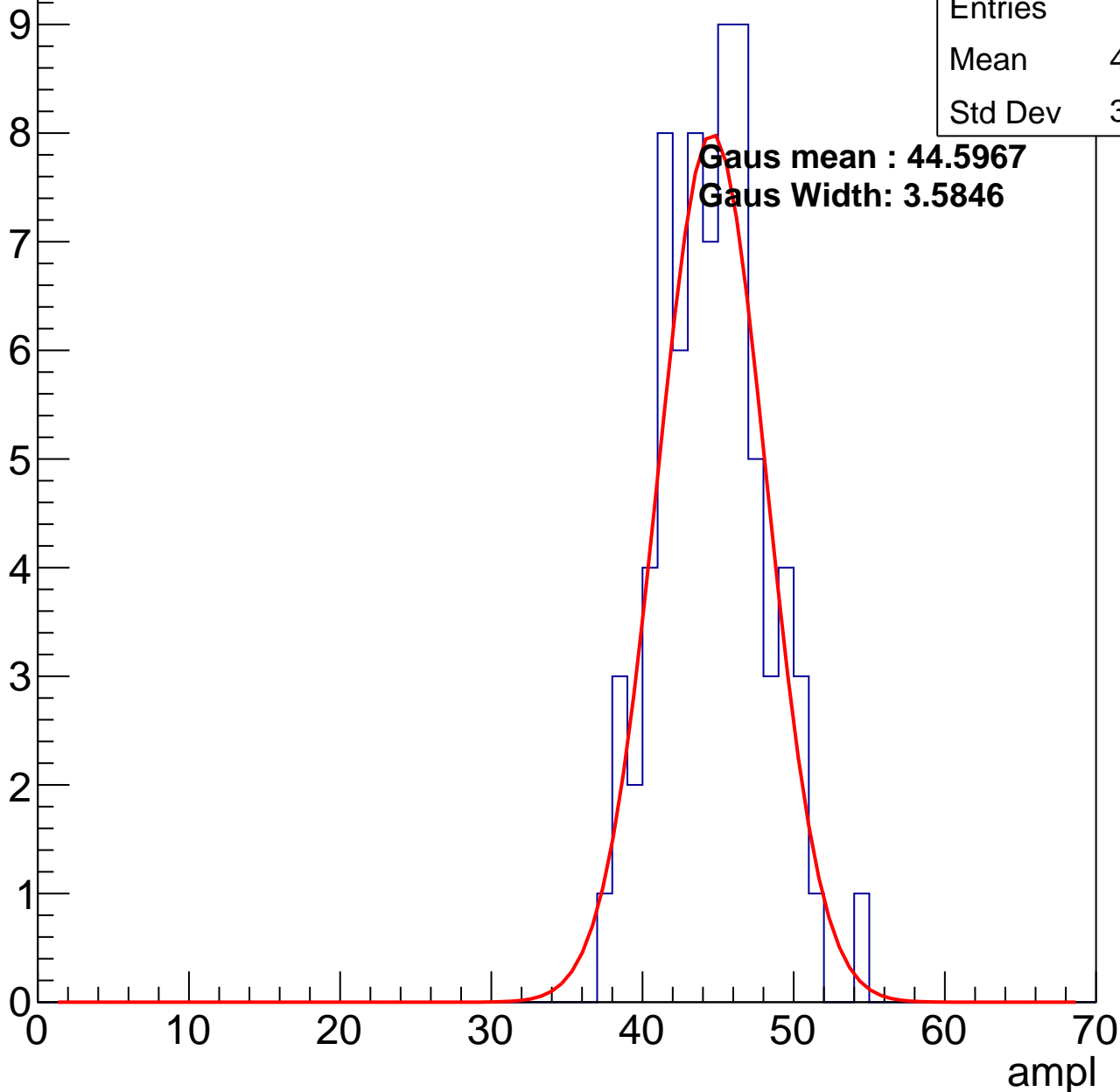
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	44.19
Std Dev	3.424

Gaus mean : 44.5967

Gaus Width: 3.5846

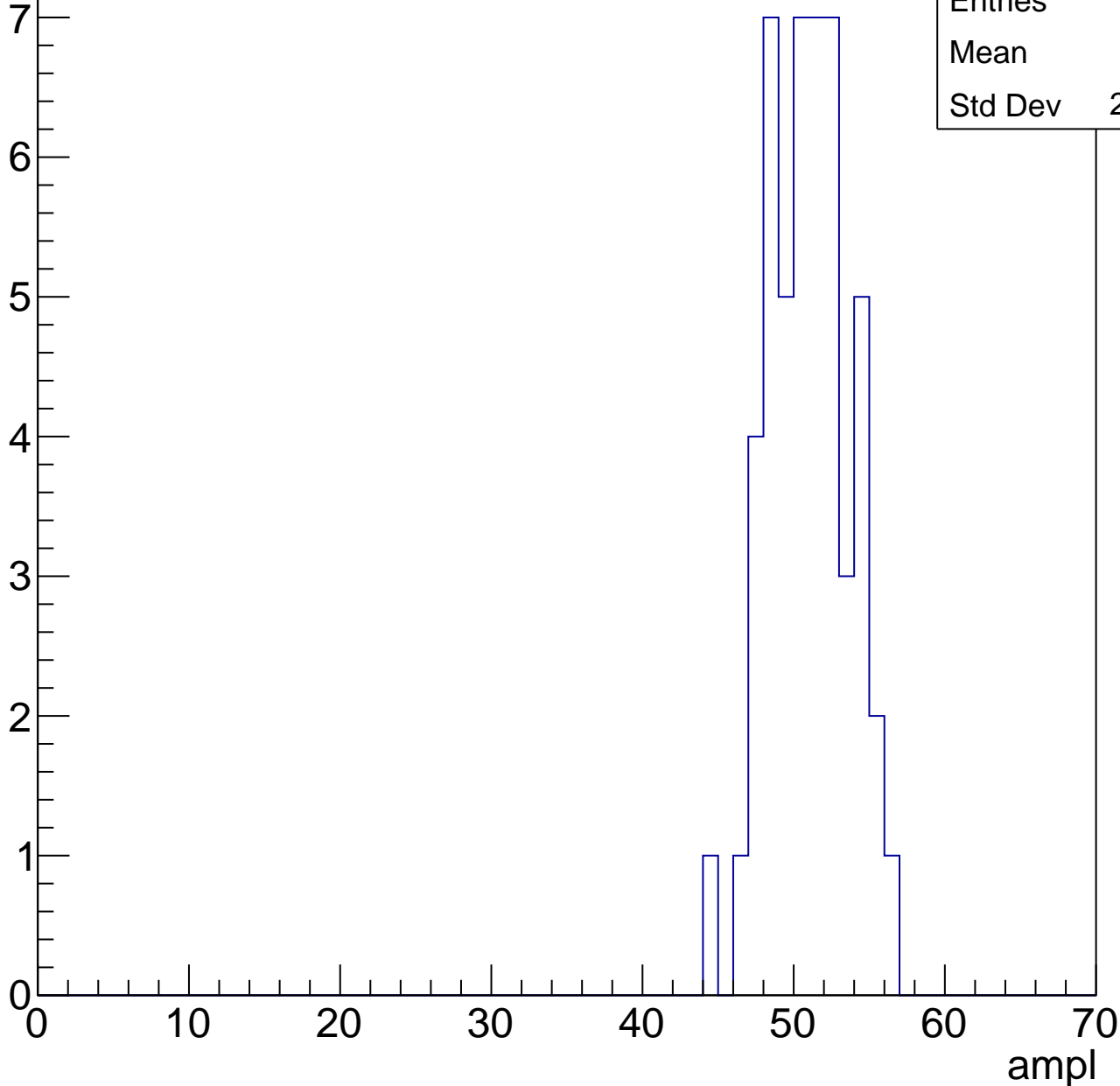


B1L003S, U13-ch0, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	50.5
Std Dev	2.594

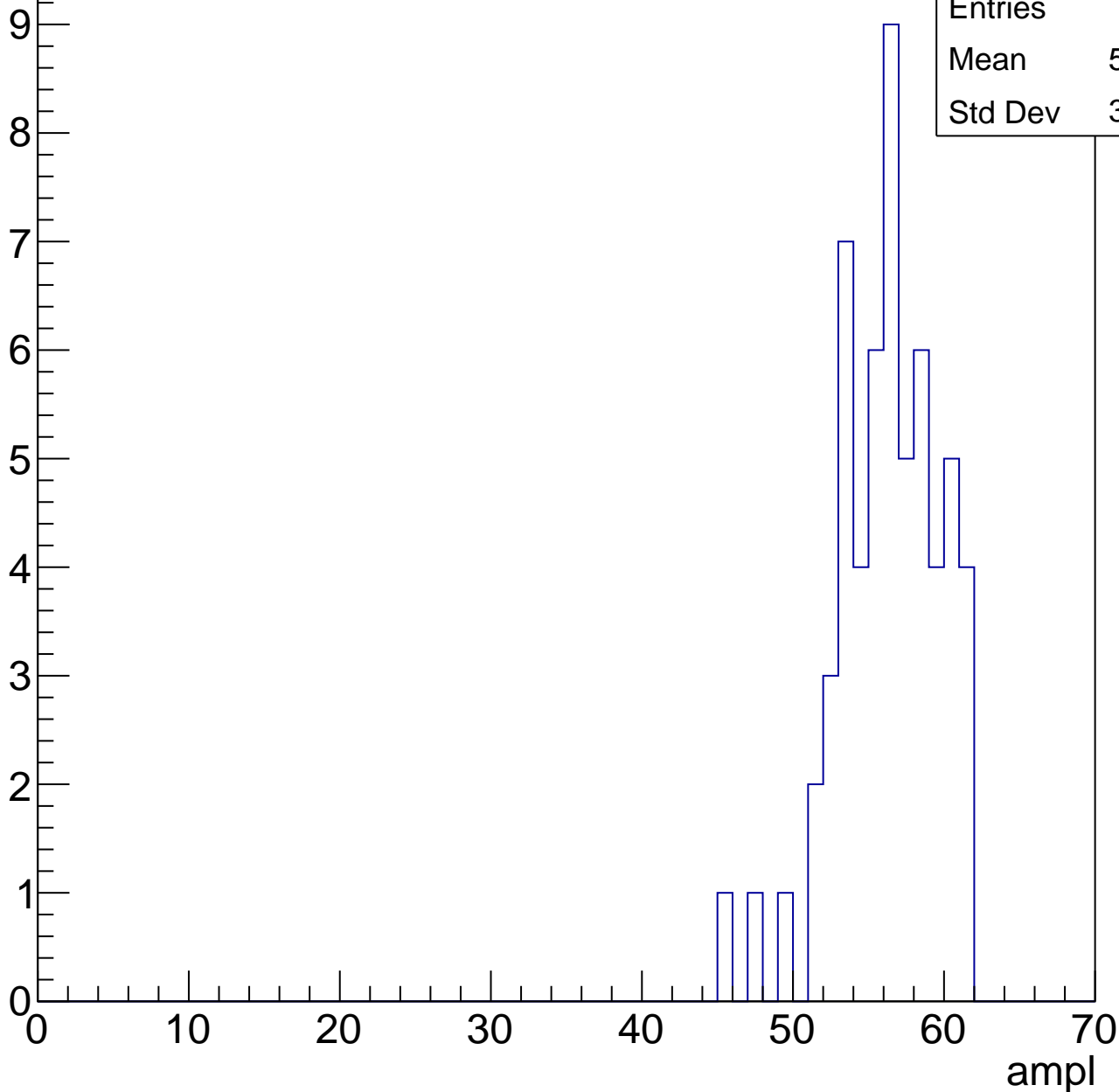


B1L003S, U13-ch0, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.74
Std Dev	3.412

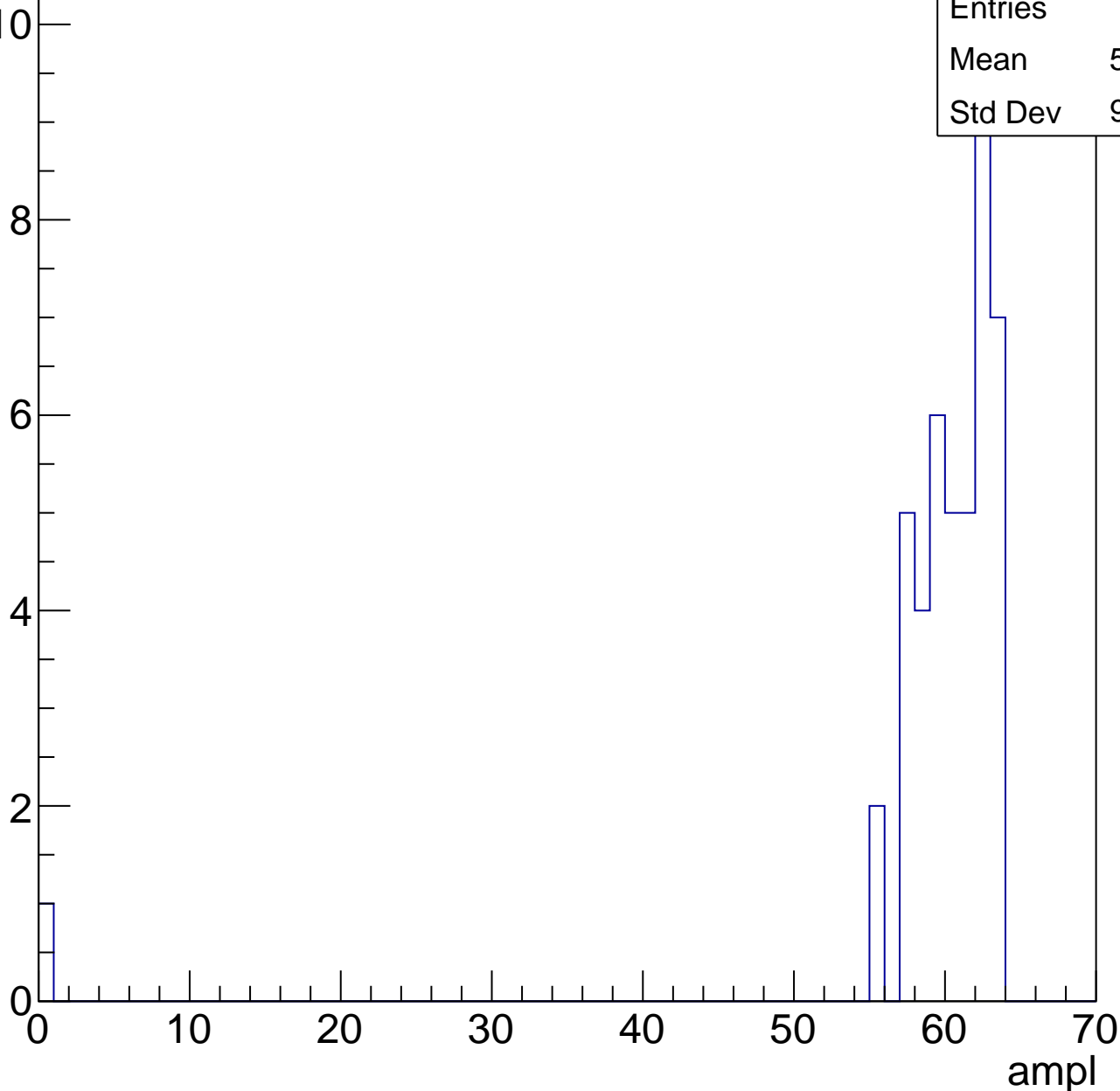


B1L003S, U13-ch0, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

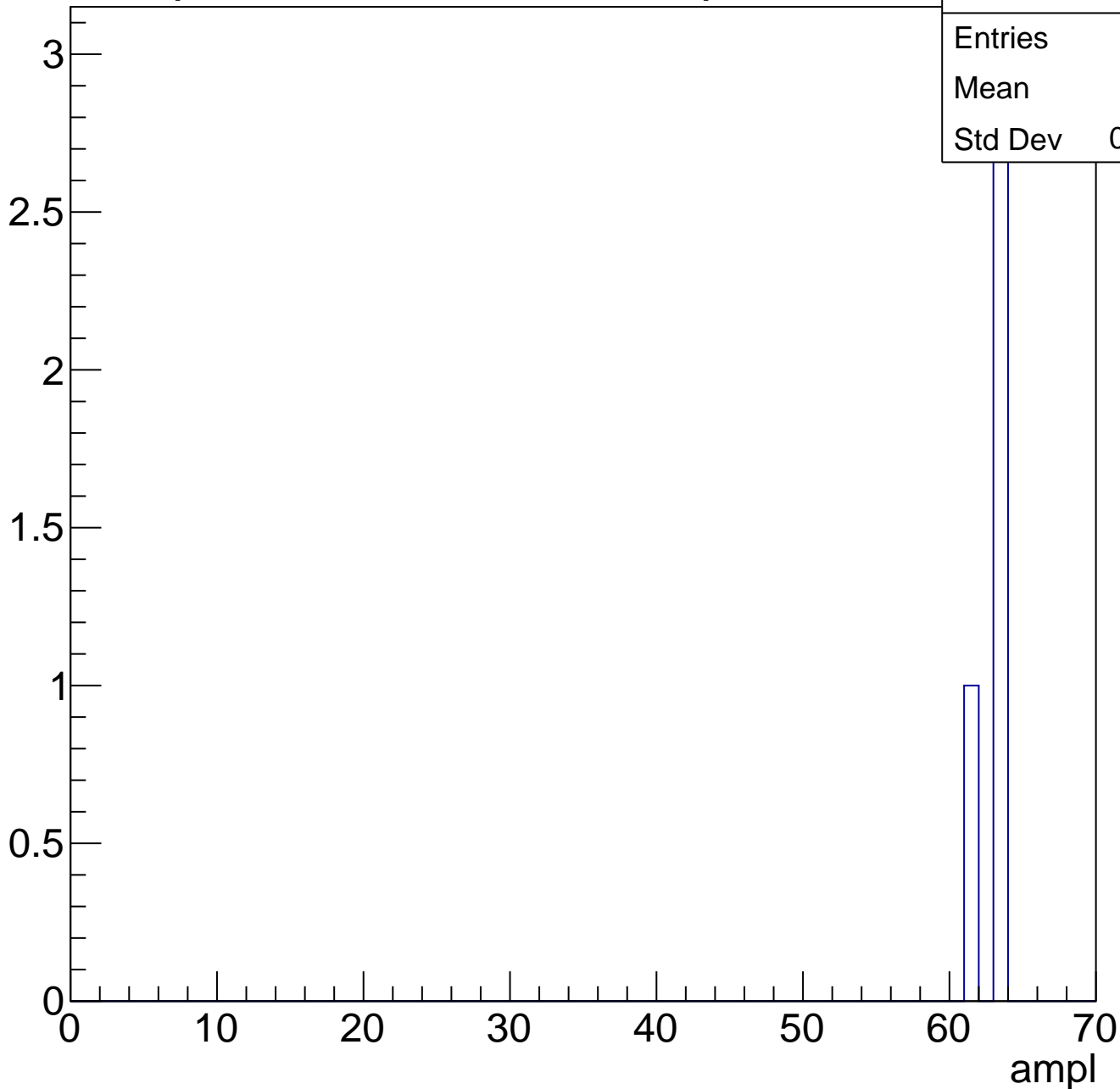
Entries	45
Mean	58.82
Std Dev	9.144



B1L003S, U13-ch0, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch0, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L003S, U13-ch1, adc0

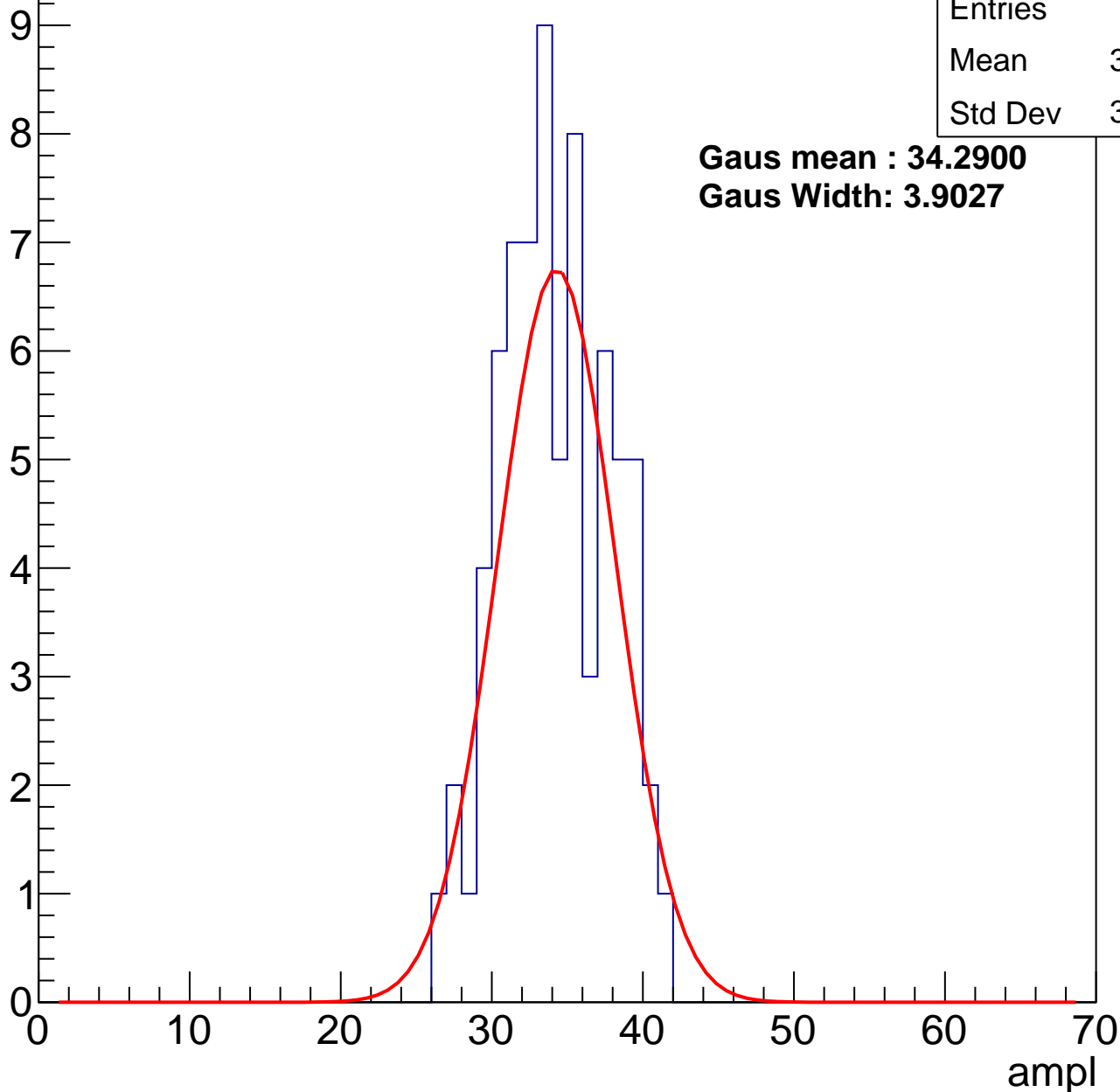
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	33.72
Std Dev	3.513

Gaus mean : 34.2900

Gaus Width: 3.9027



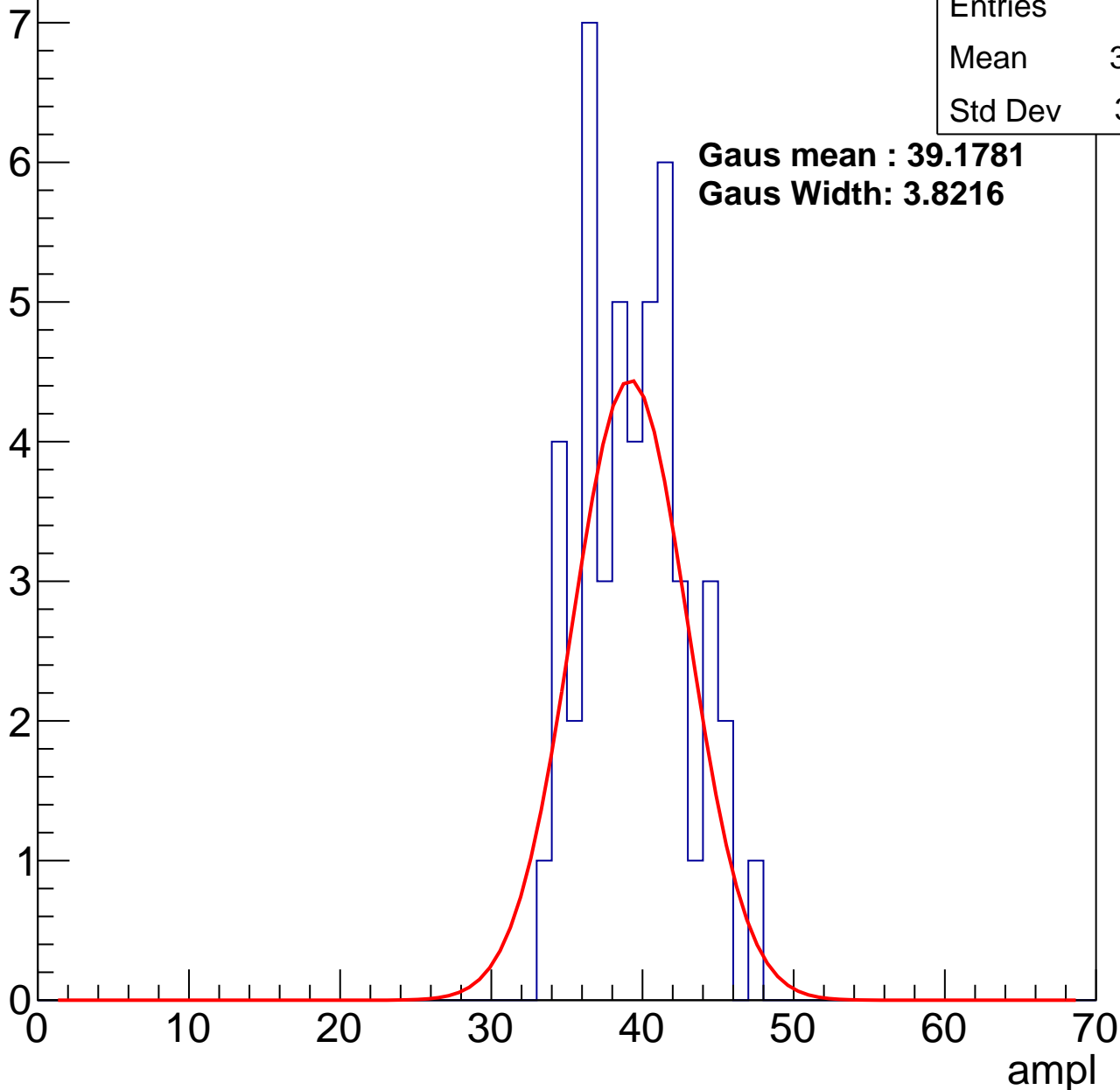
B1L003S, U13-ch1, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	38.98
Std Dev	3.361

Gaus mean : 39.1781
Gaus Width: 3.8216



B1L003S, U13-ch1, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	46.05
Std Dev	3.694

Gaus mean : 46.4449

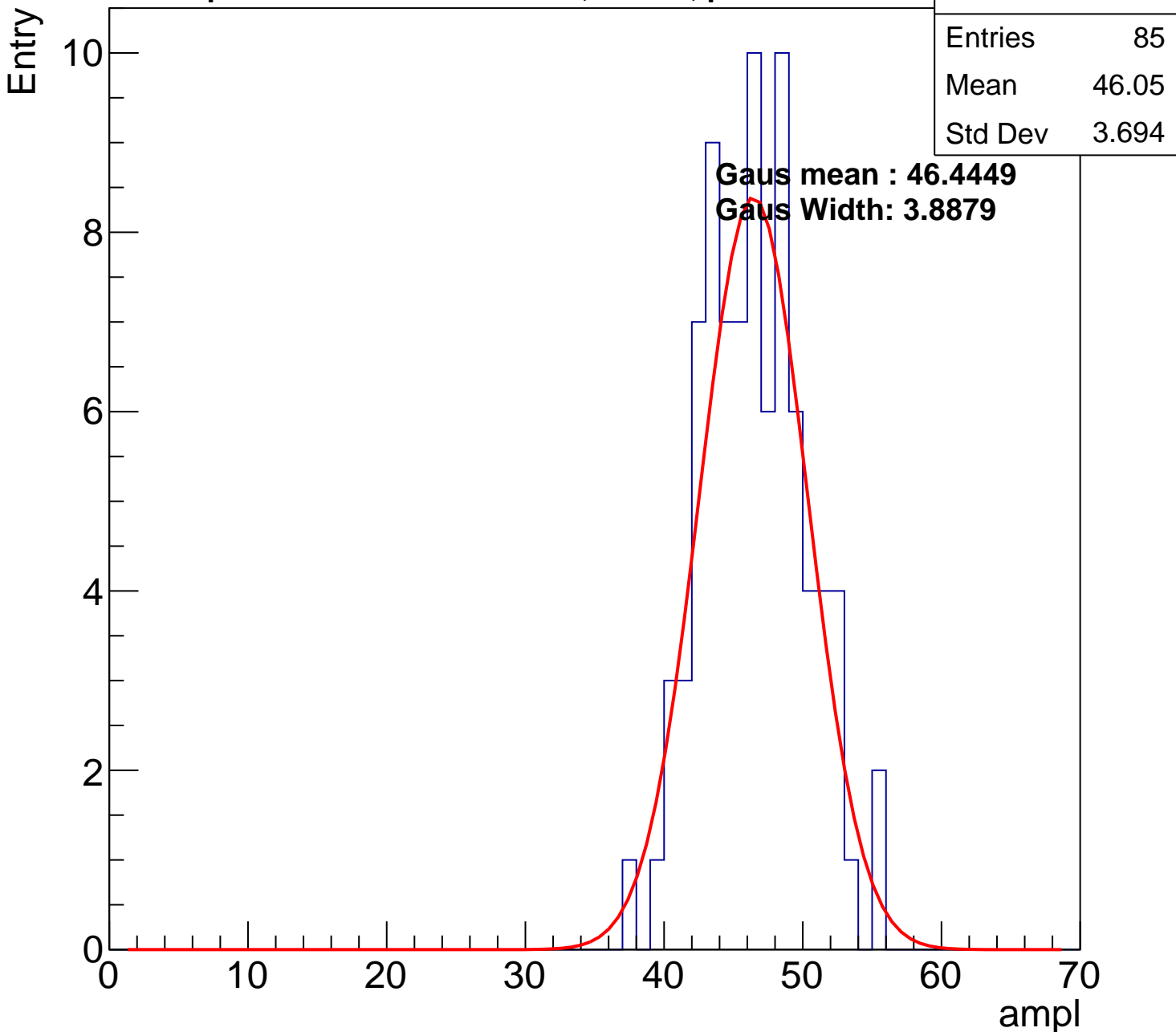
Gaus Width: 3.8879

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

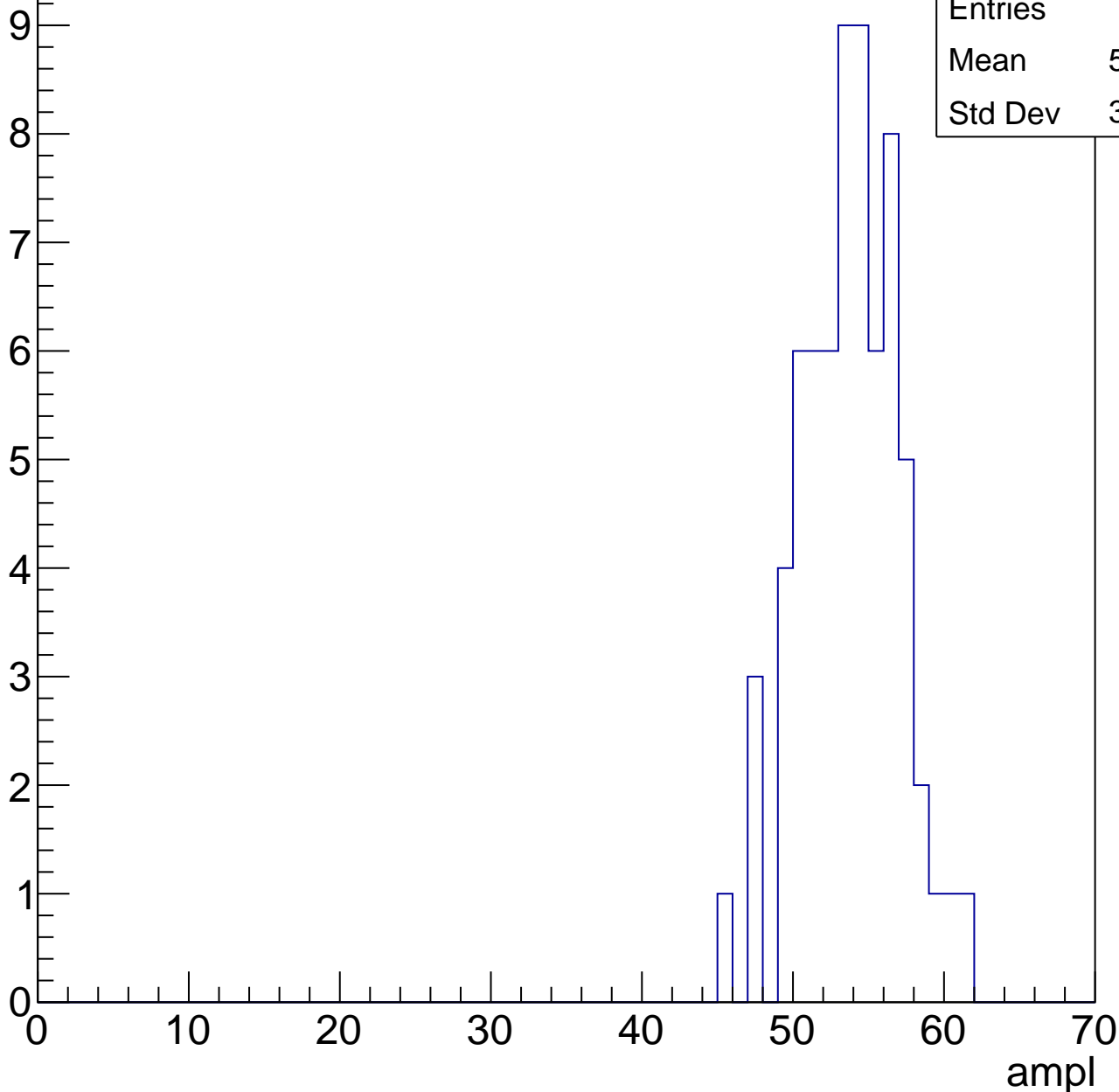


B1L003S, U13-ch1, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

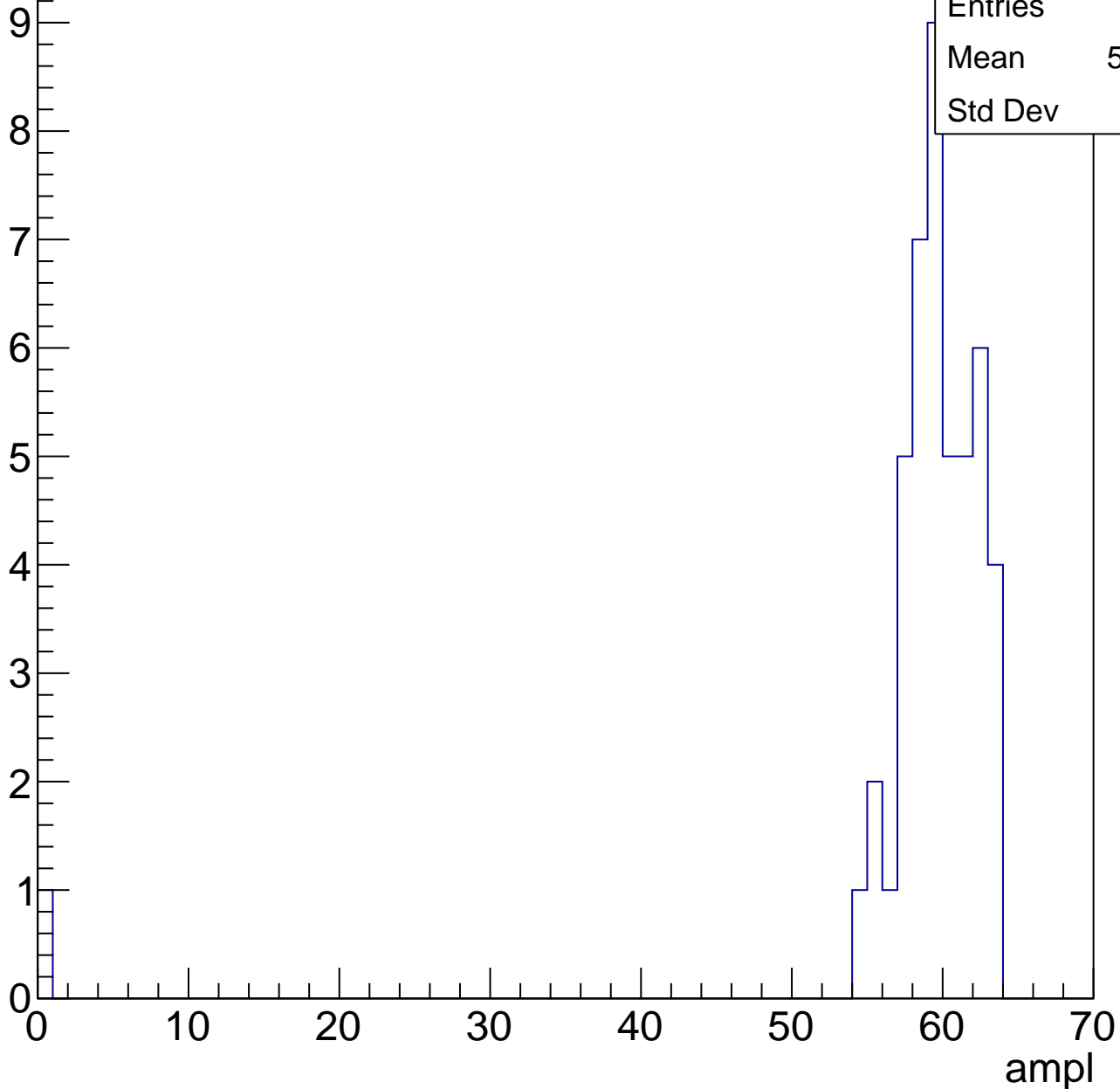
Entries	68
Mean	53.26
Std Dev	3.207



B1L003S, U13-ch1, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

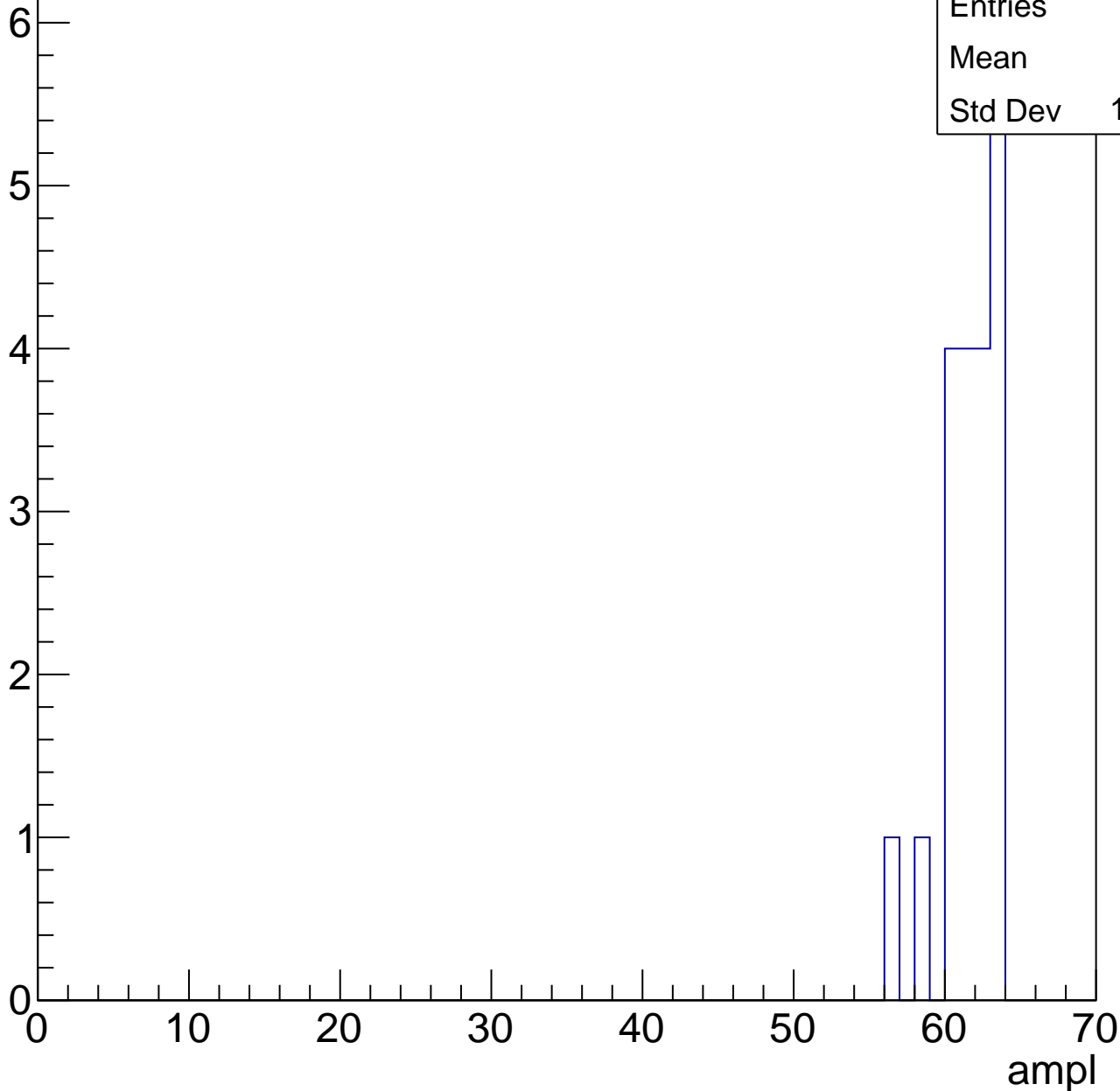


B1L003S, U13-ch1, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	61.2
Std Dev	1.806



B1L003S, U13-ch1, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

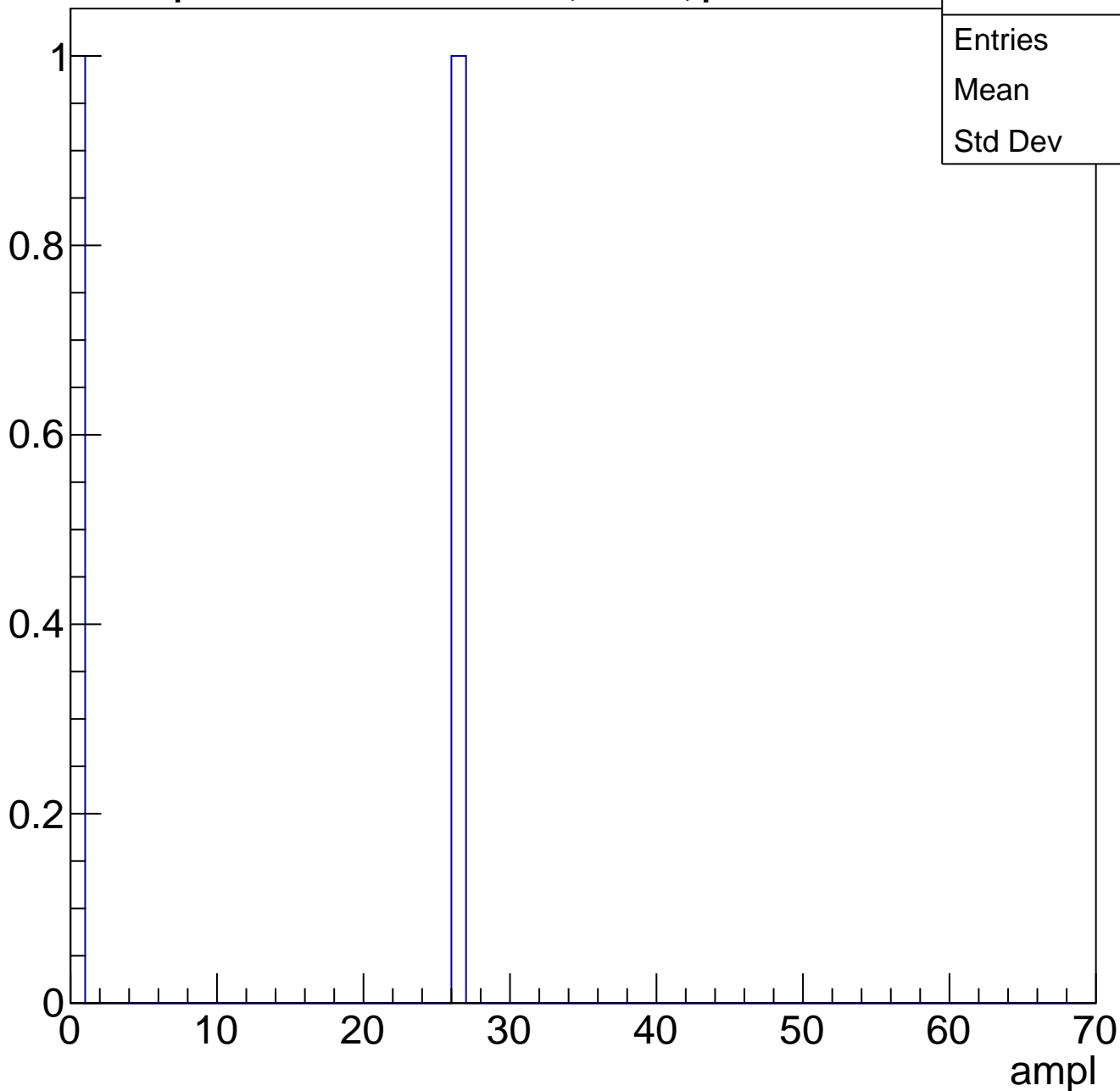


Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch1, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	13
Std Dev	13

B1L003S, U13-ch2, adc0

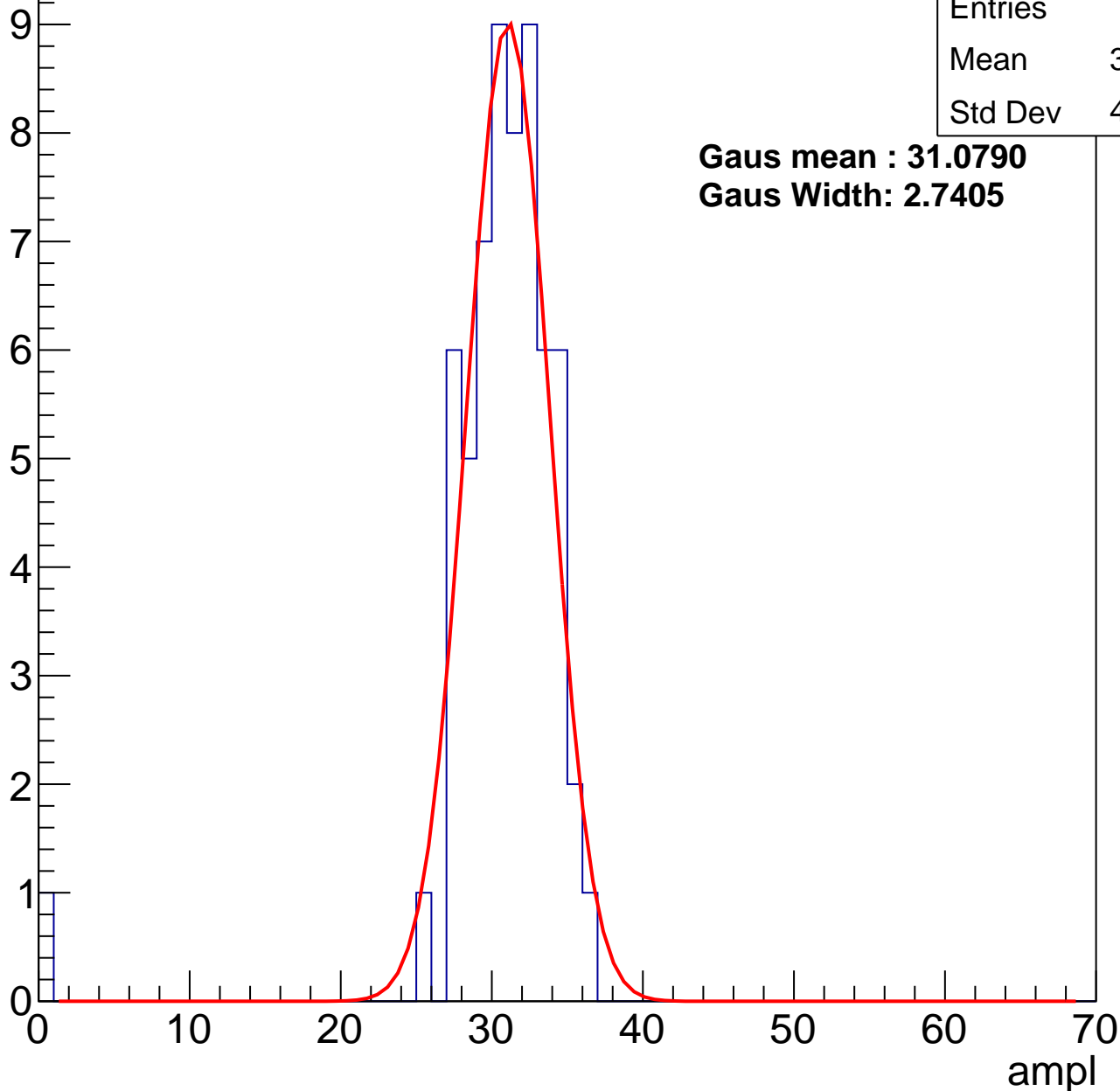
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	30.23
Std Dev	4.586

Gaus mean : 31.0790

Gaus Width: 2.7405



B1L003S, U13-ch2, adc1

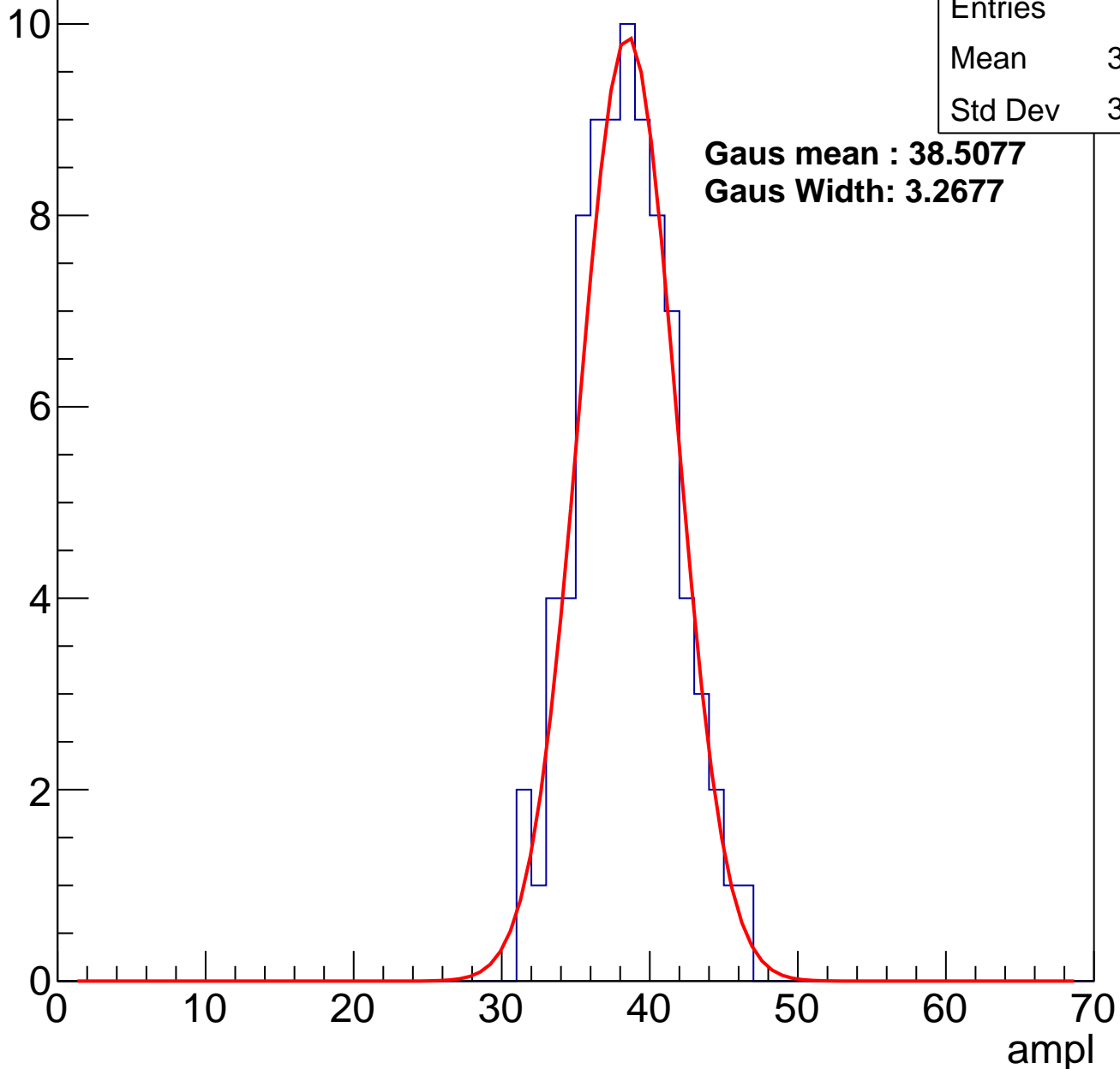
calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	37.96
Std Dev	3.206

Gaus mean : 38.5077

Gaus Width: 3.2677

Entry



B1L003S, U13-ch2, adc2

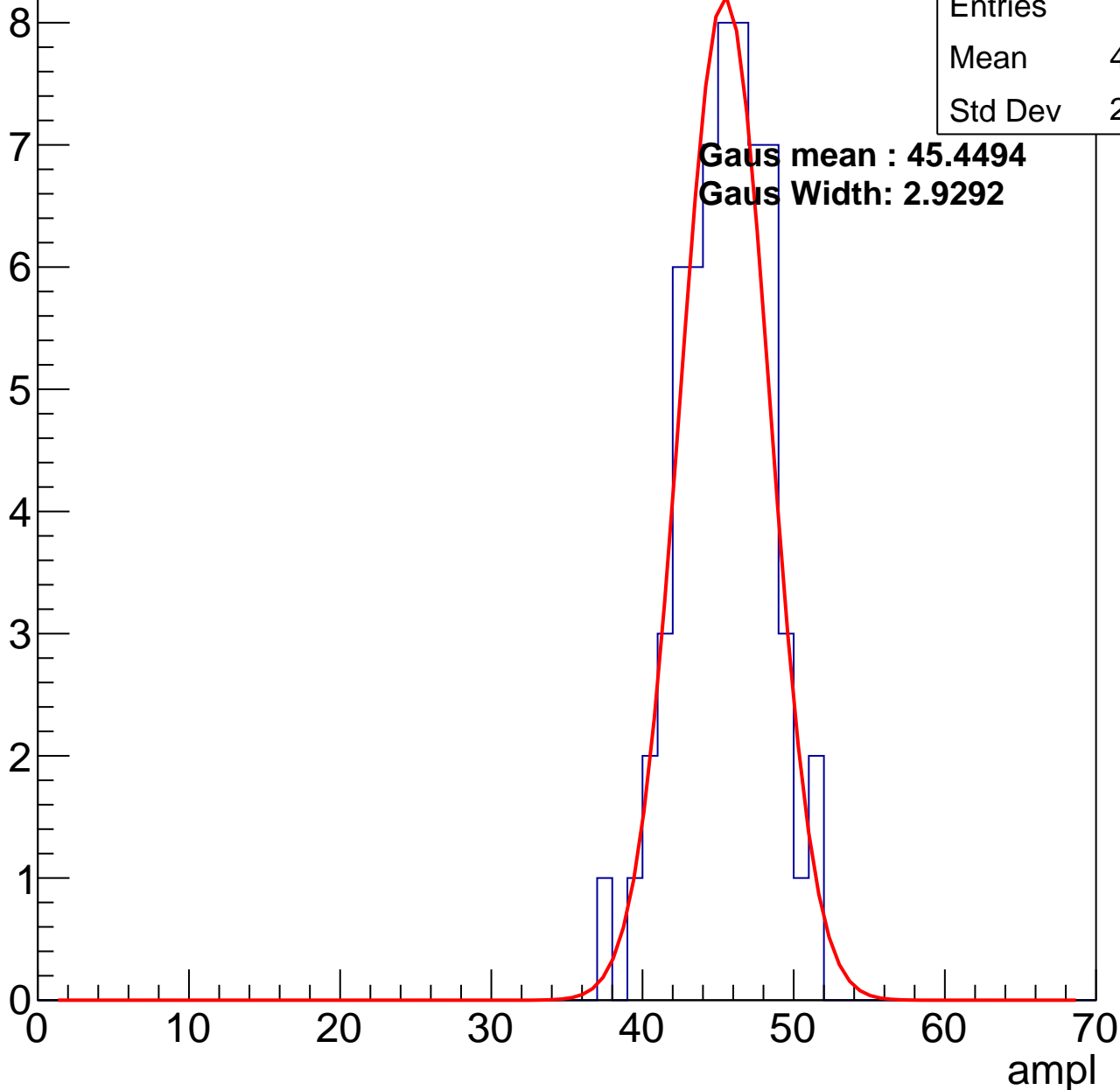
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	44.98
Std Dev	2.915

Gaus mean : 45.4494

Gaus Width: 2.9292

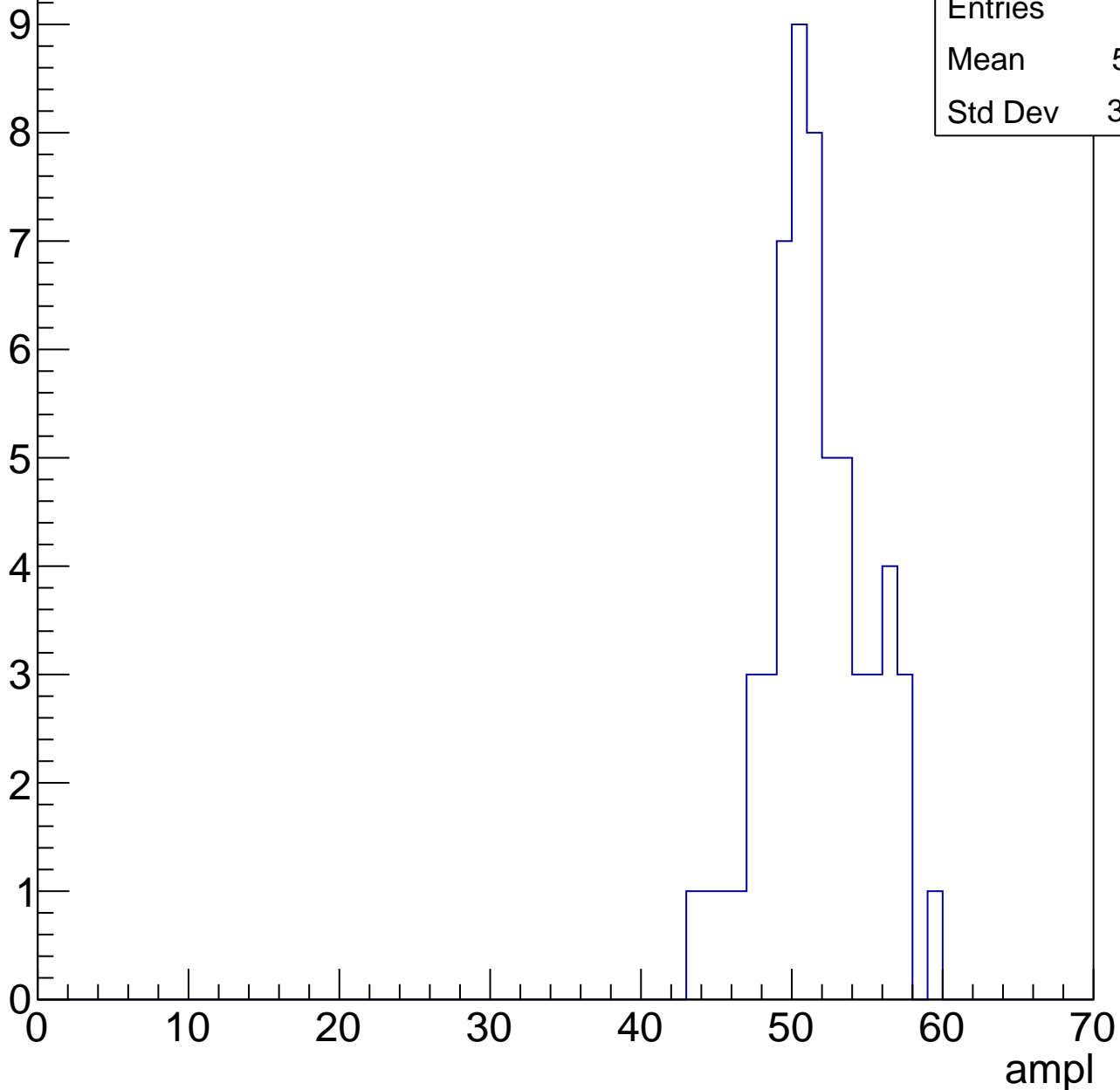


B1L003S, U13-ch2, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	51.21
Std Dev	3.362

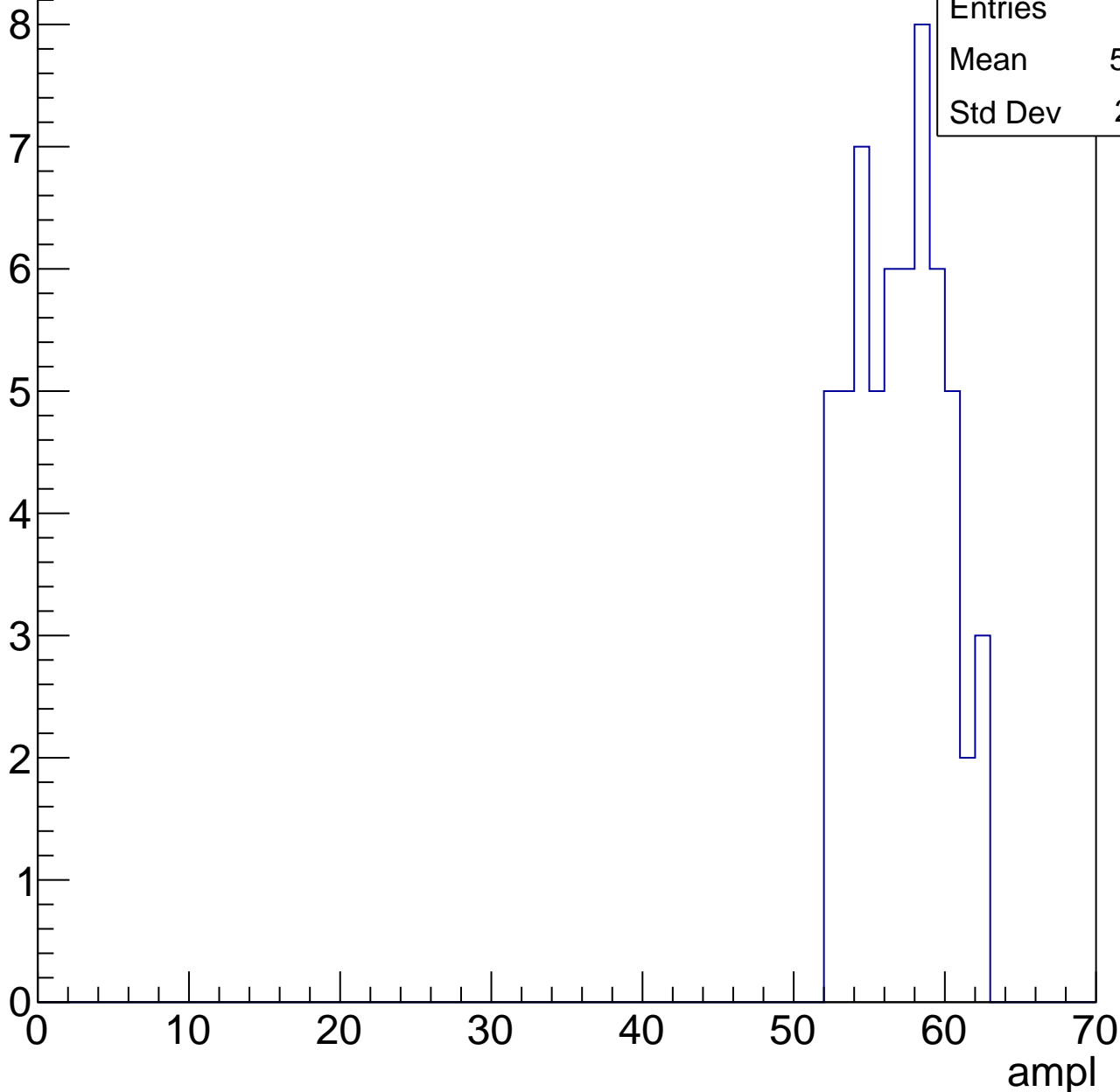


B1L003S, U13-ch2, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	56.59
Std Dev	2.841

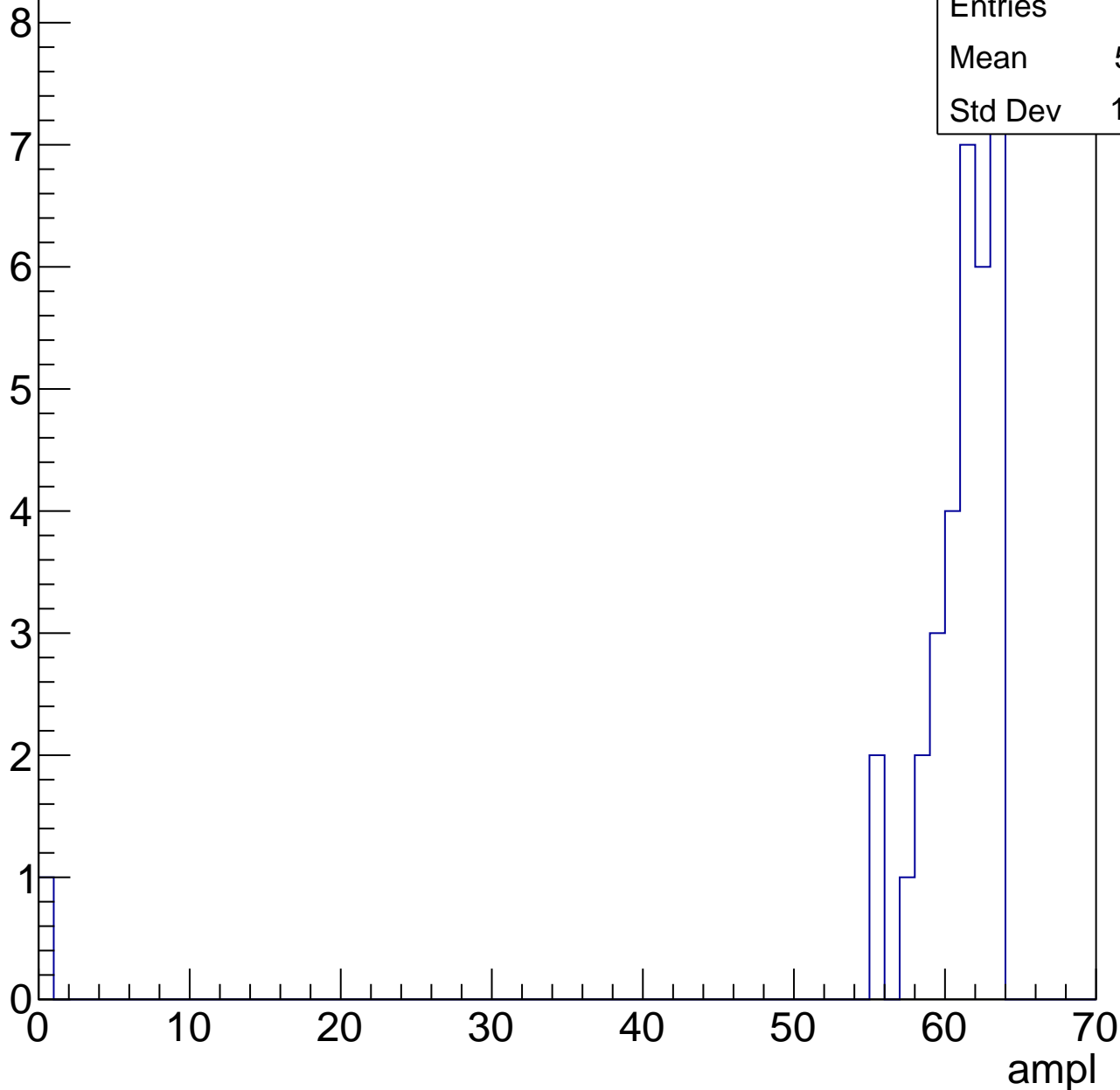


B1L003S, U13-ch2, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	58.91
Std Dev	10.48



B1L003S, U13-ch2, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	61.8
Std Dev	1.166

B1L003S, U13-ch2, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch3, adc0

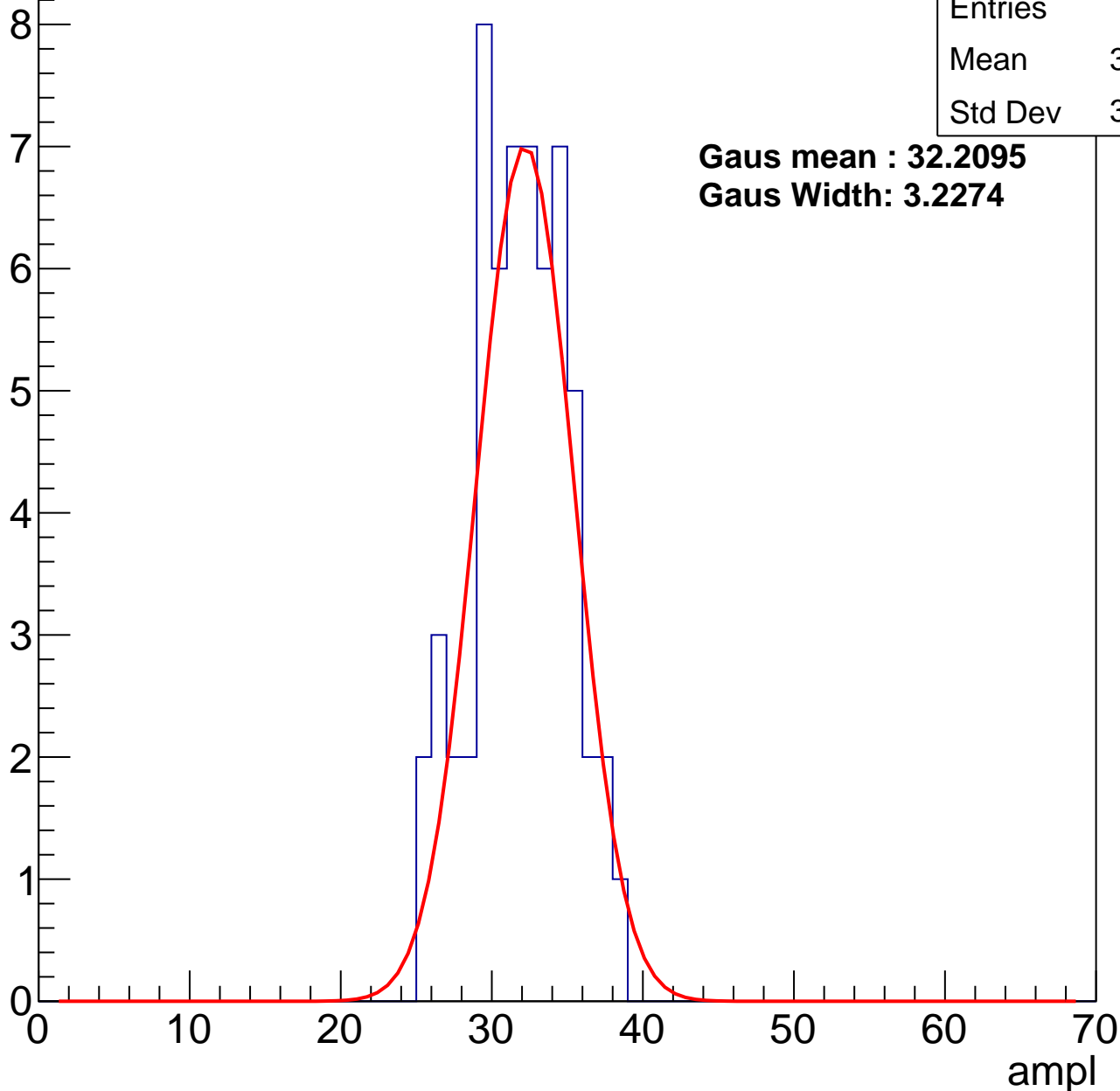
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	31.43
Std Dev	3.079

Gaus mean : 32.2095

Gaus Width: 3.2274



B1L003S, U13-ch3, adc1

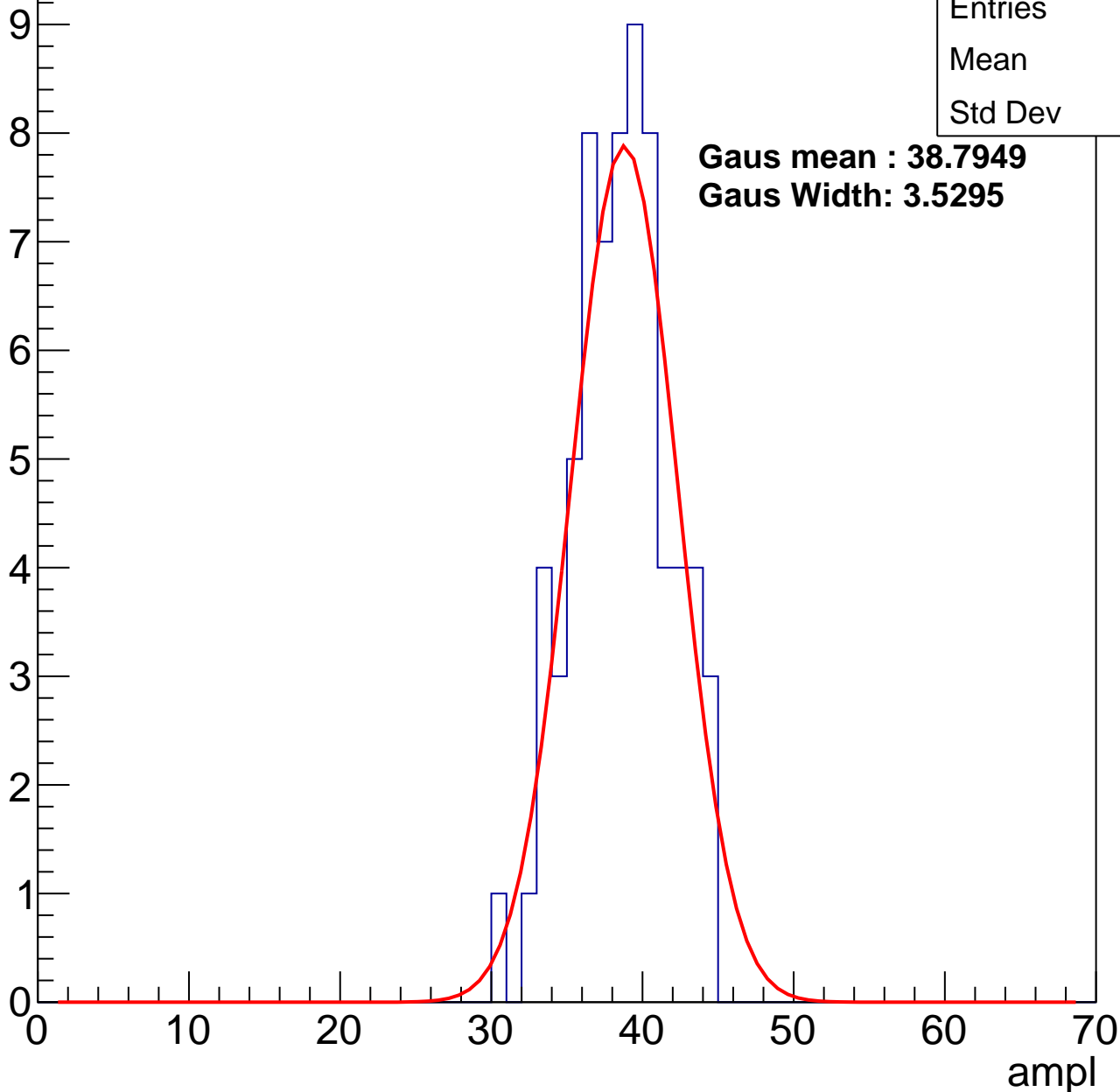
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	38.1
Std Dev	3.14

Gaus mean : 38.7949

Gaus Width: 3.5295



B1L003S, U13-ch3, adc2

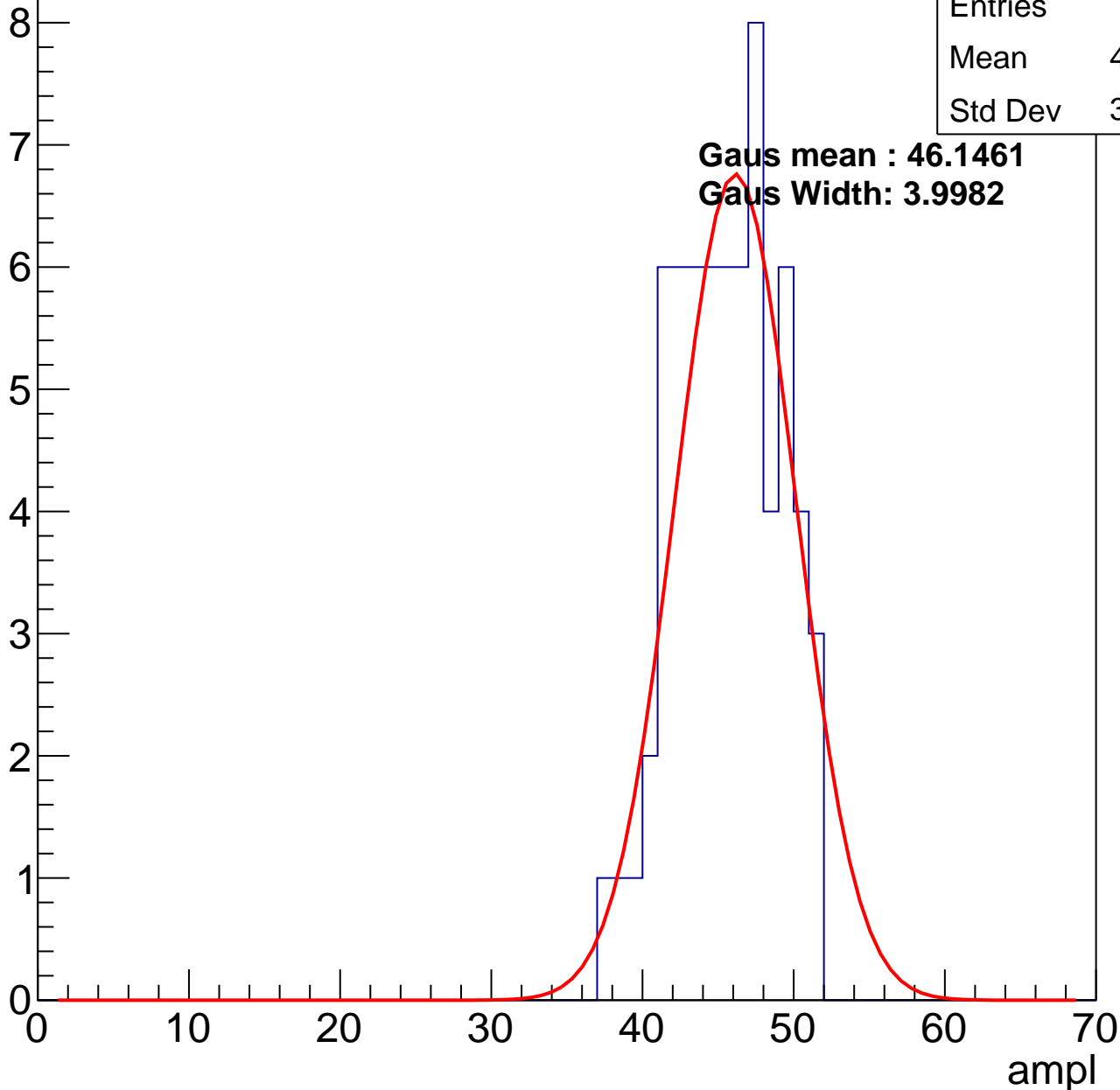
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	45.08
Std Dev	3.386

Gaus mean : 46.1461

Gaus Width: 3.9982

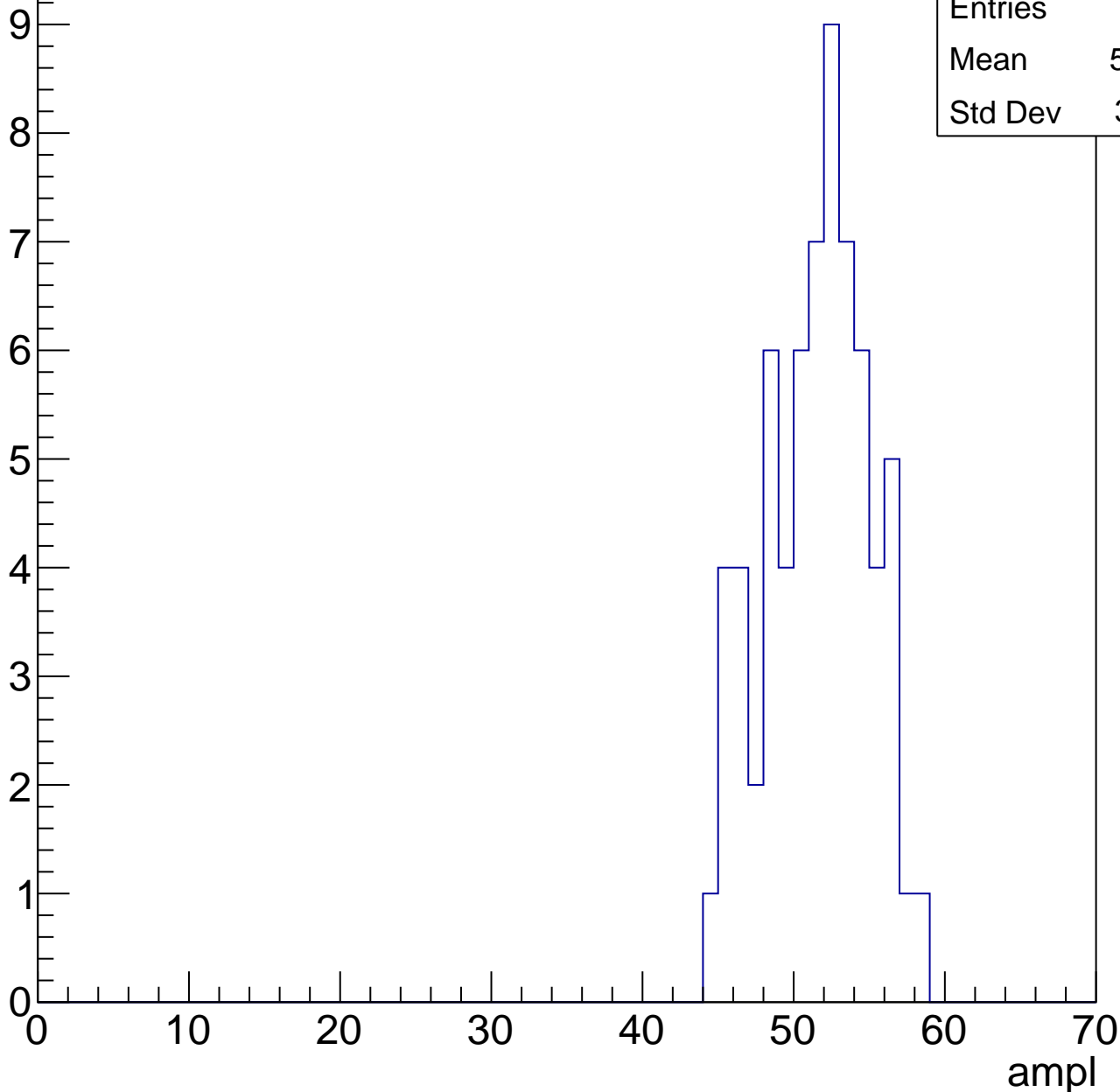


B1L003S, U13-ch3, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	51.06
Std Dev	3.381



B1L003S, U13-ch3, adc4

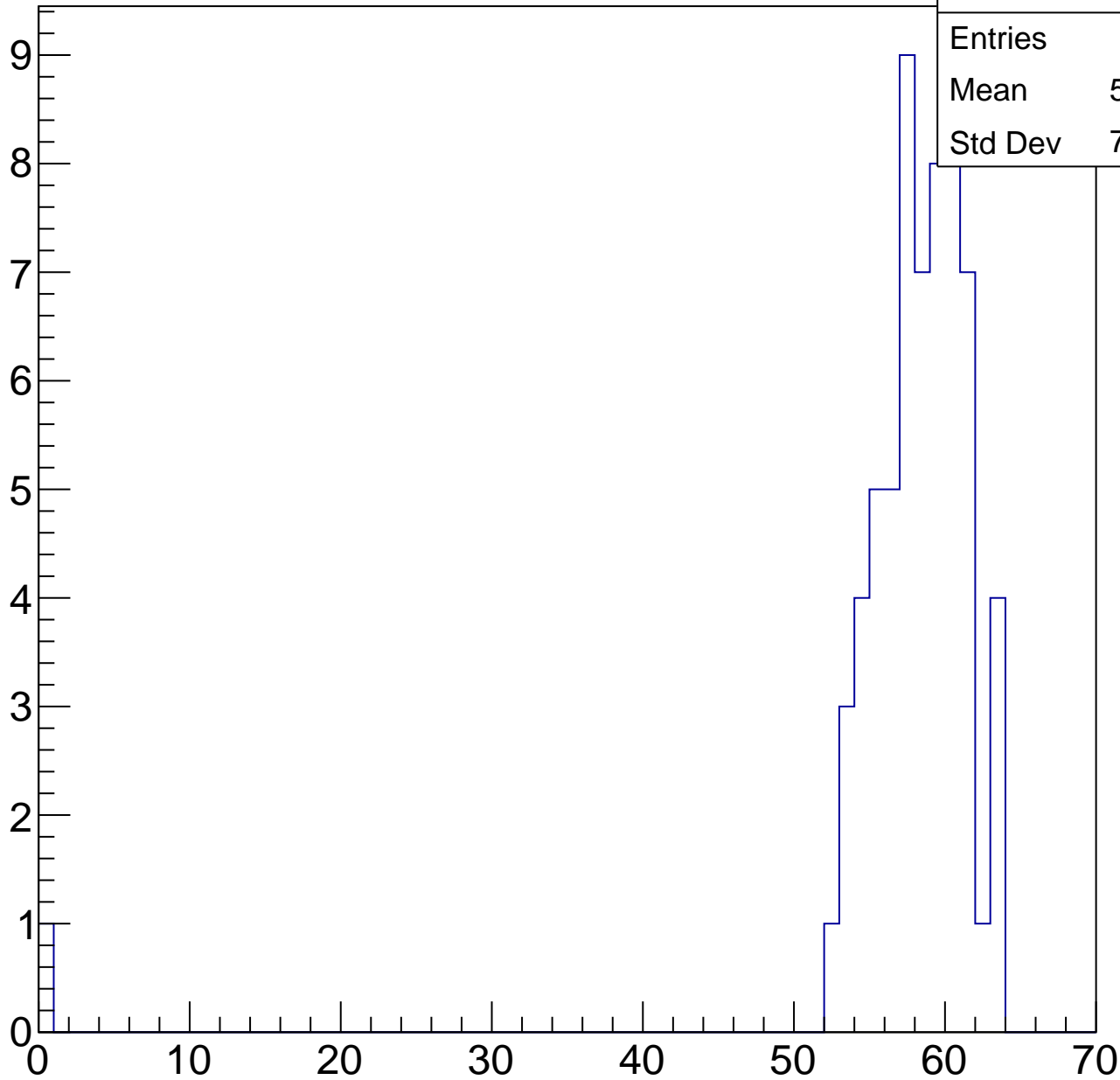
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	57.09
Std Dev	7.689

ampl

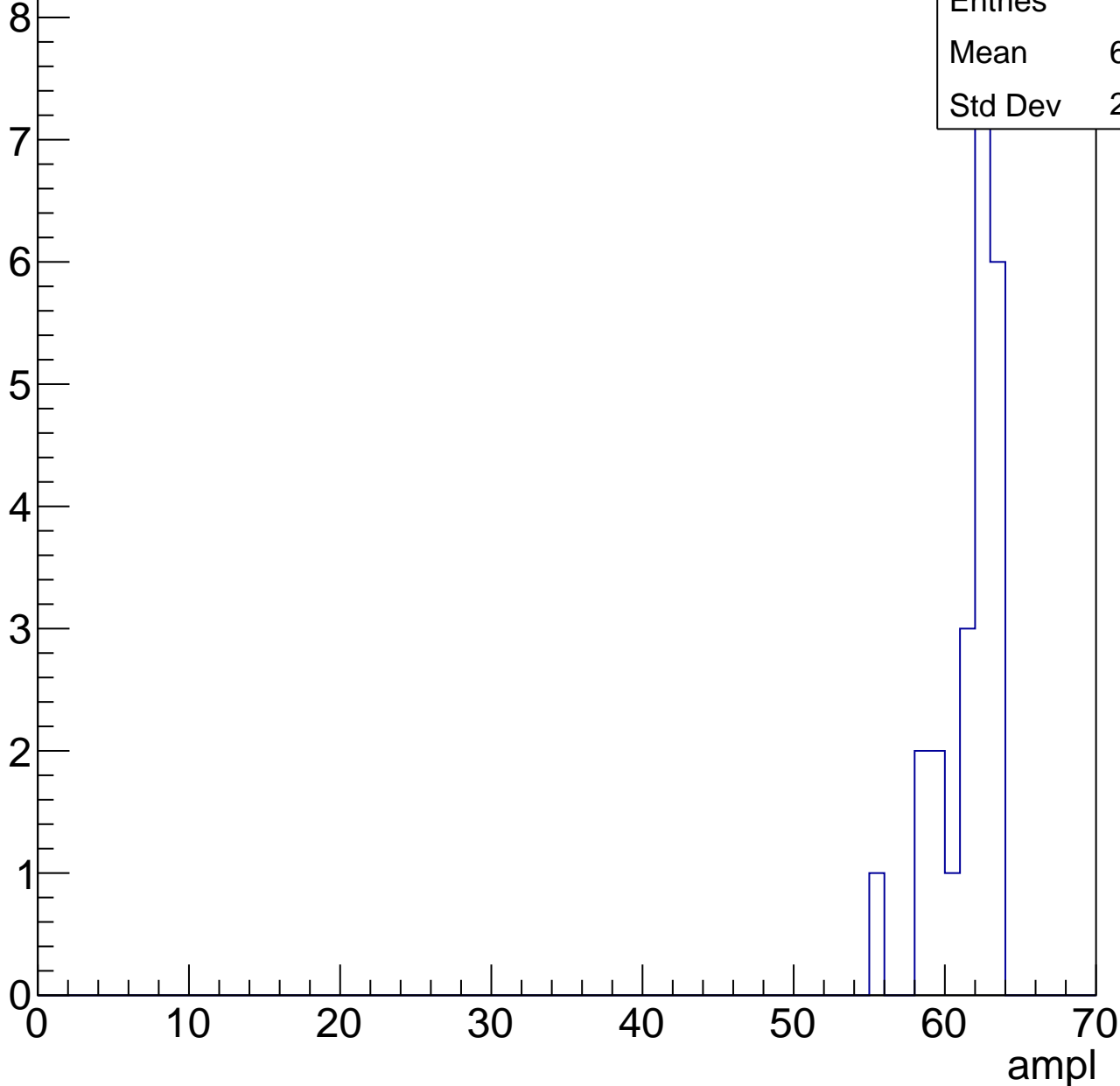


B1L003S, U13-ch3, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	61.13
Std Dev	2.028



B1L003S, U13-ch3, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch3, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch4, adc0

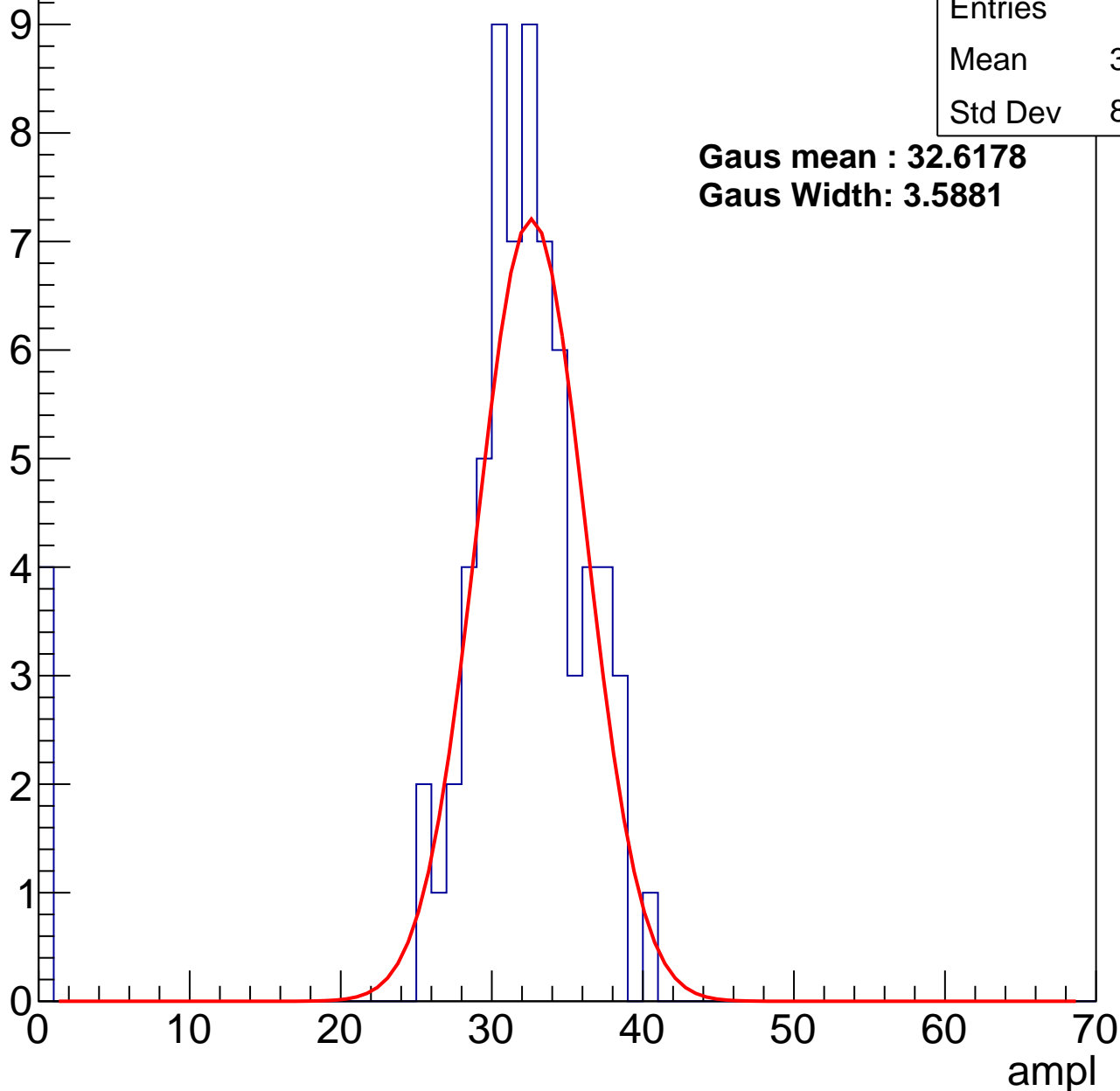
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.25
Std Dev	8.057

Gaus mean : 32.6178

Gaus Width: 3.5881



B1L003S, U13-ch4, adc1

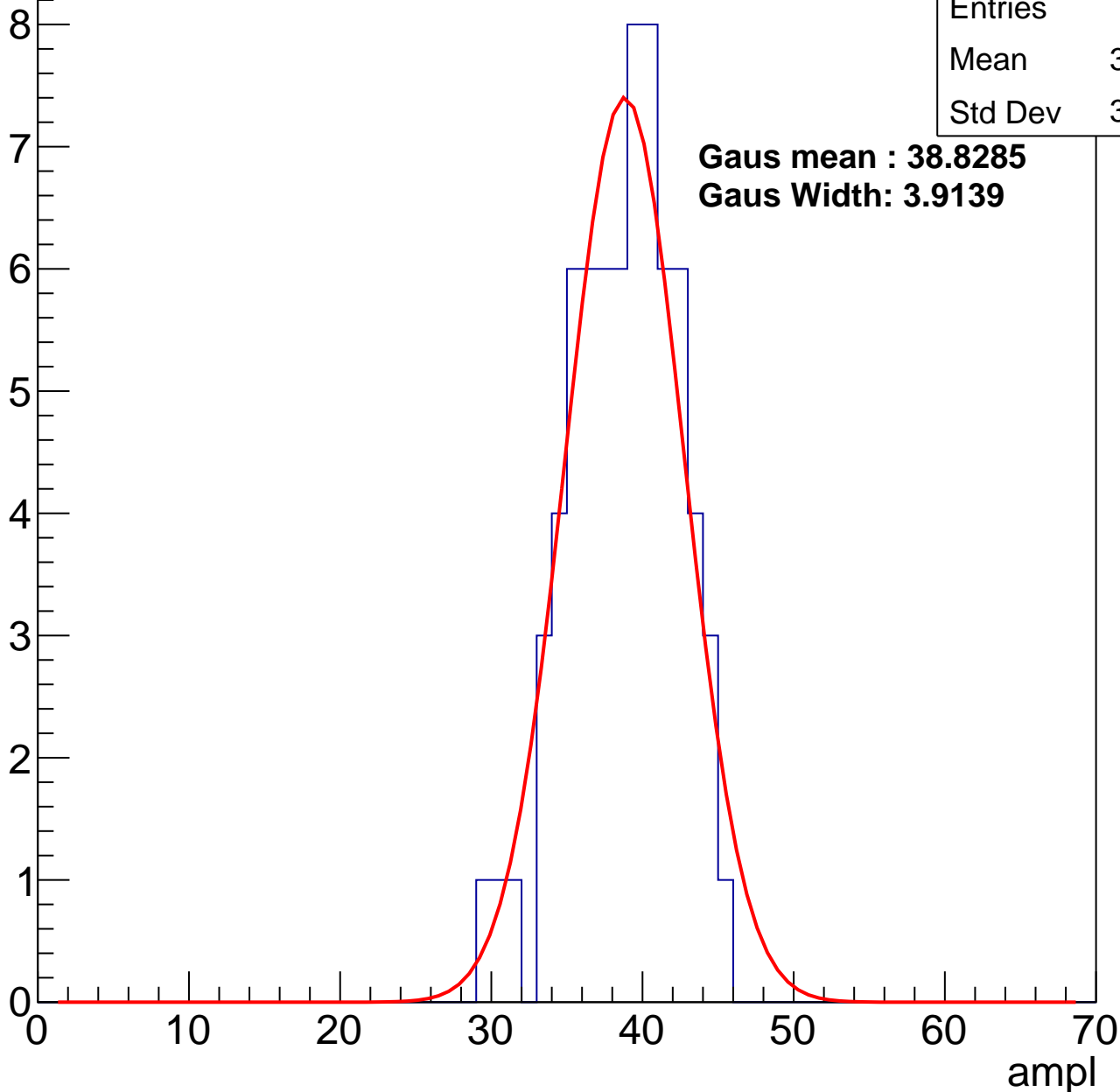
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	38.29
Std Dev	3.502

Gaus mean : 38.8285

Gaus Width: 3.9139



B1L003S, U13-ch4, adc2

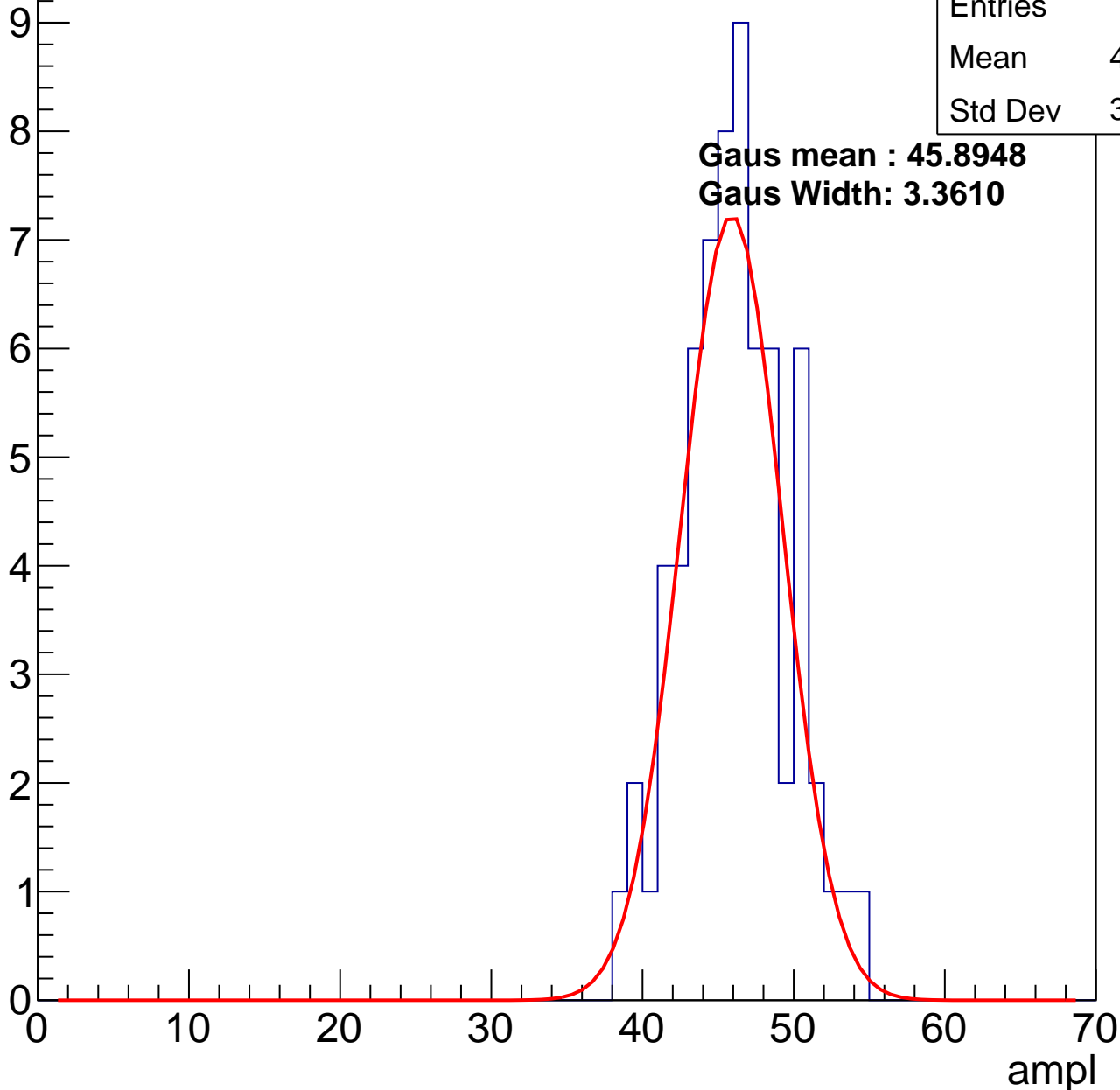
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	45.63
Std Dev	3.424

Gaus mean : 45.8948

Gaus Width: 3.3610

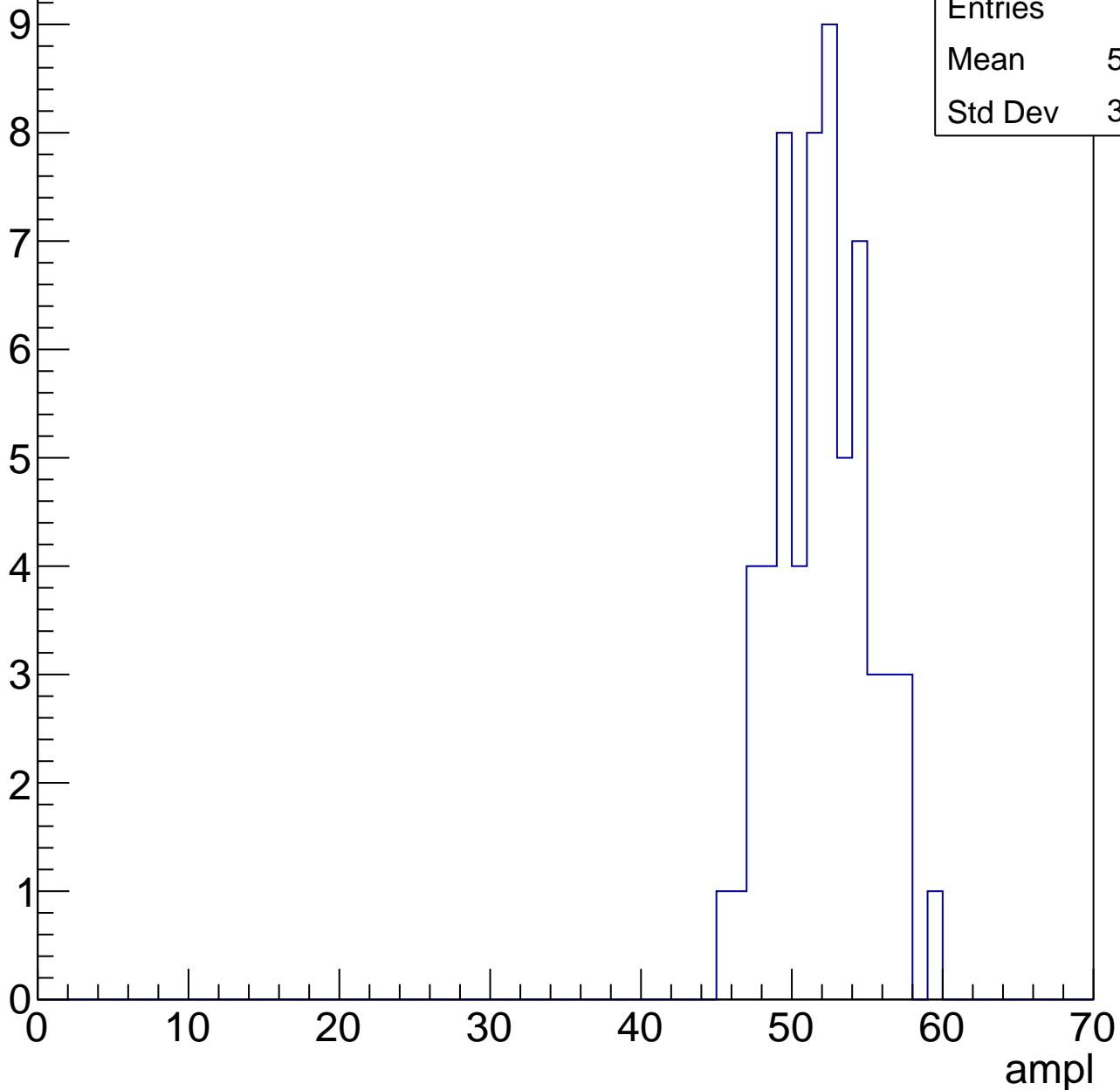


B1L003S, U13-ch4, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	51.56
Std Dev	3.044

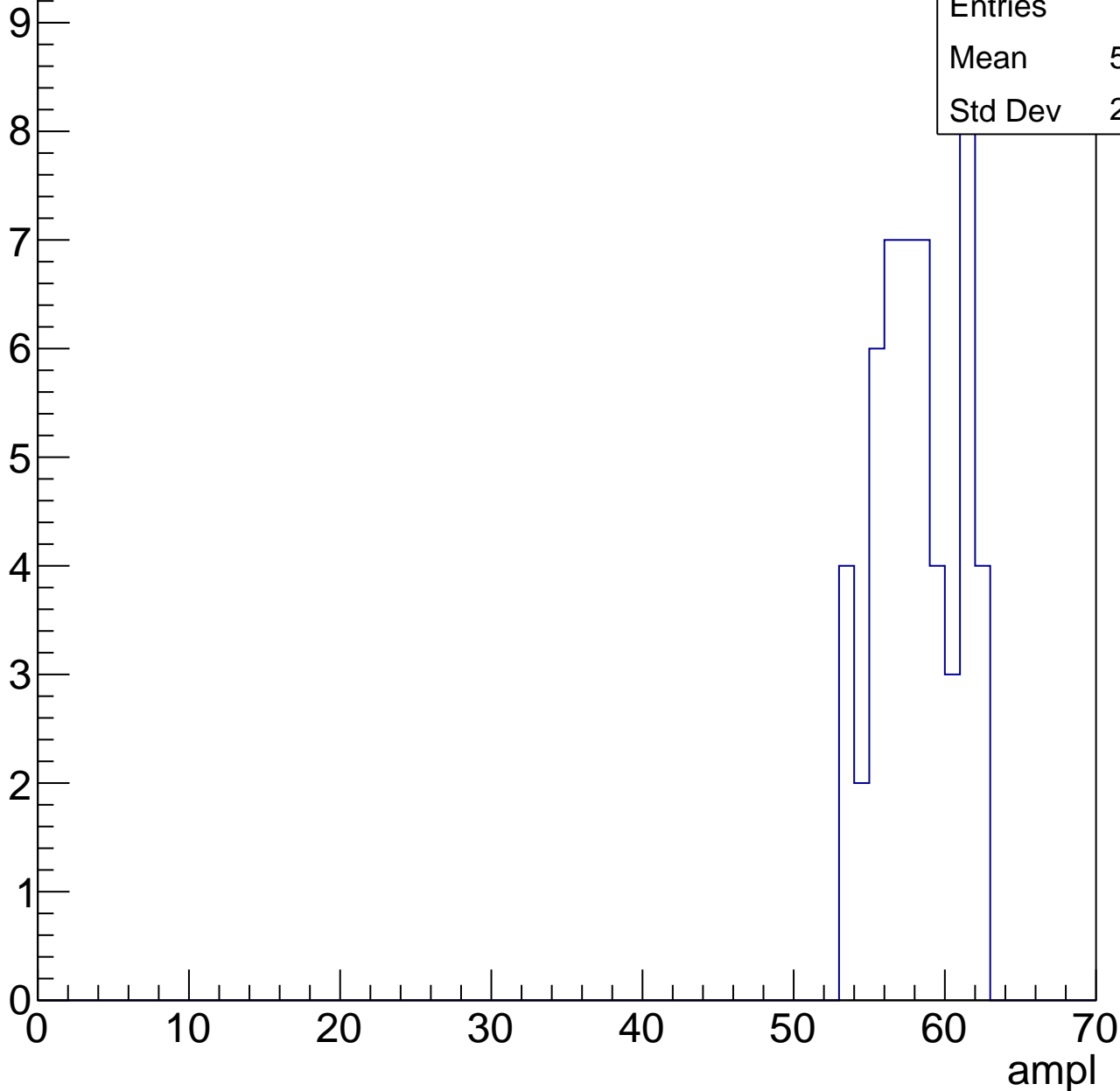


B1L003S, U13-ch4, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	57.74
Std Dev	2.672

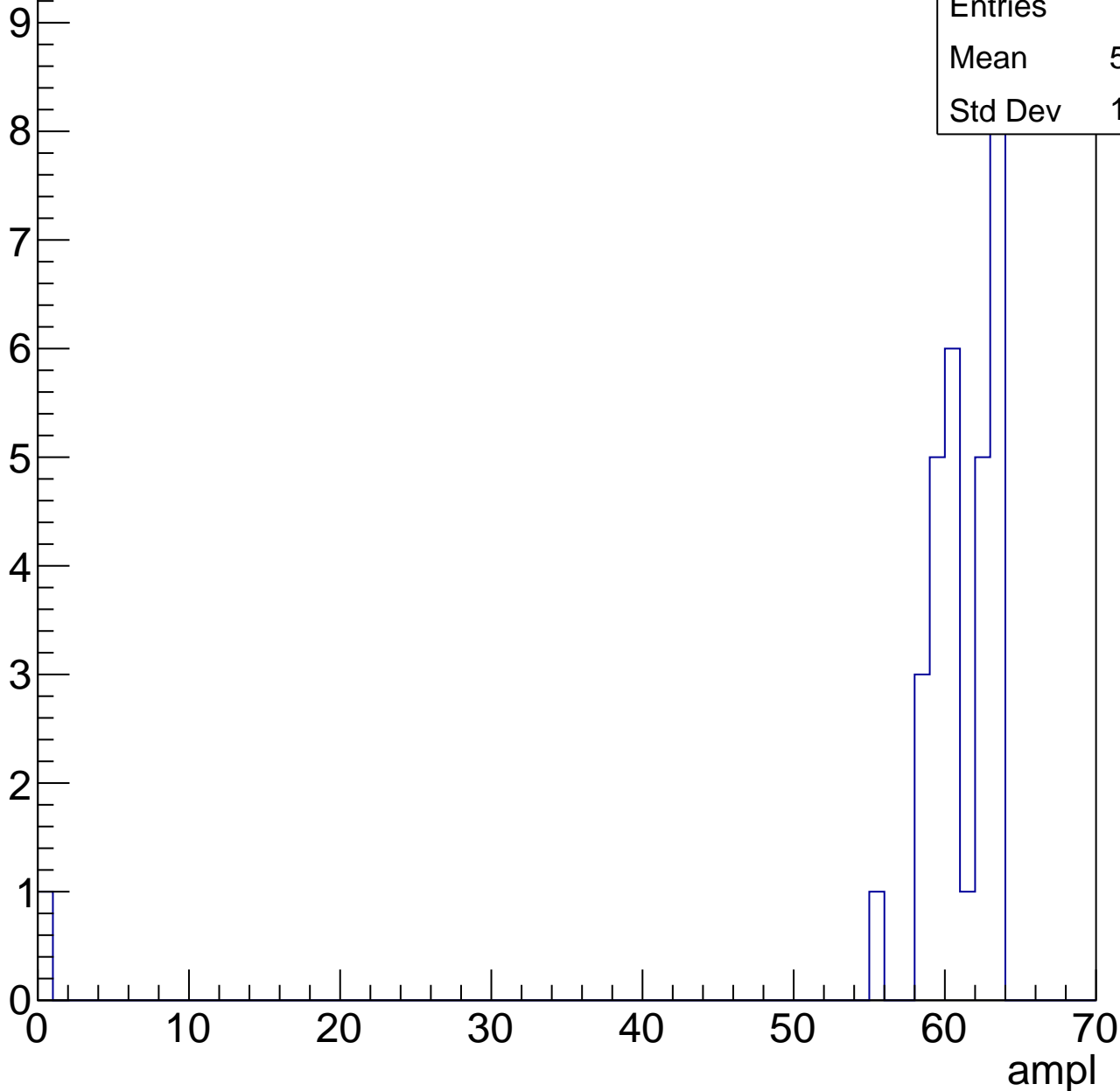


B1L003S, U13-ch4, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

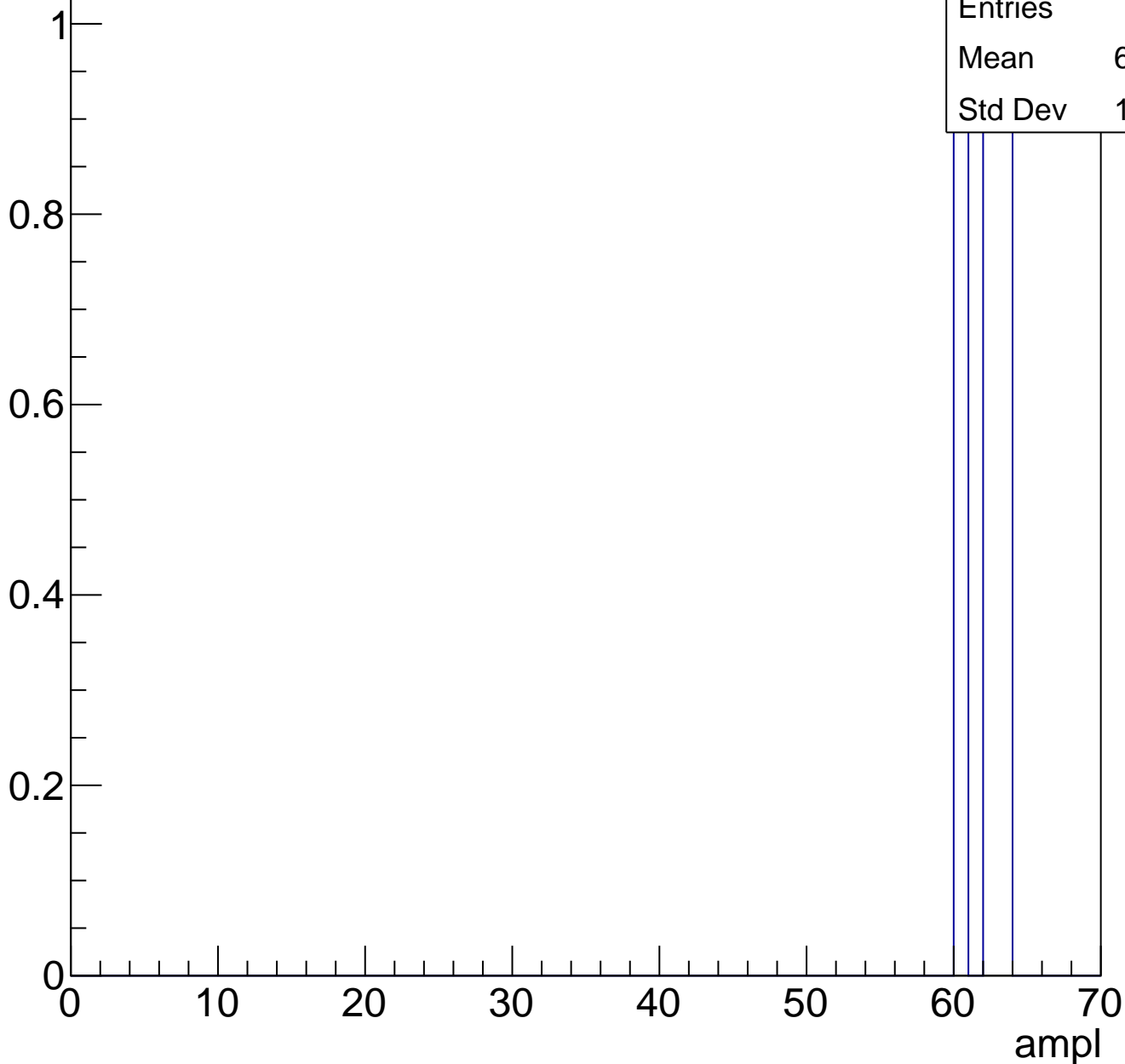
Entries	31
Mean	58.77
Std Dev	10.92



B1L003S, U13-ch4, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch4, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch5, adc0

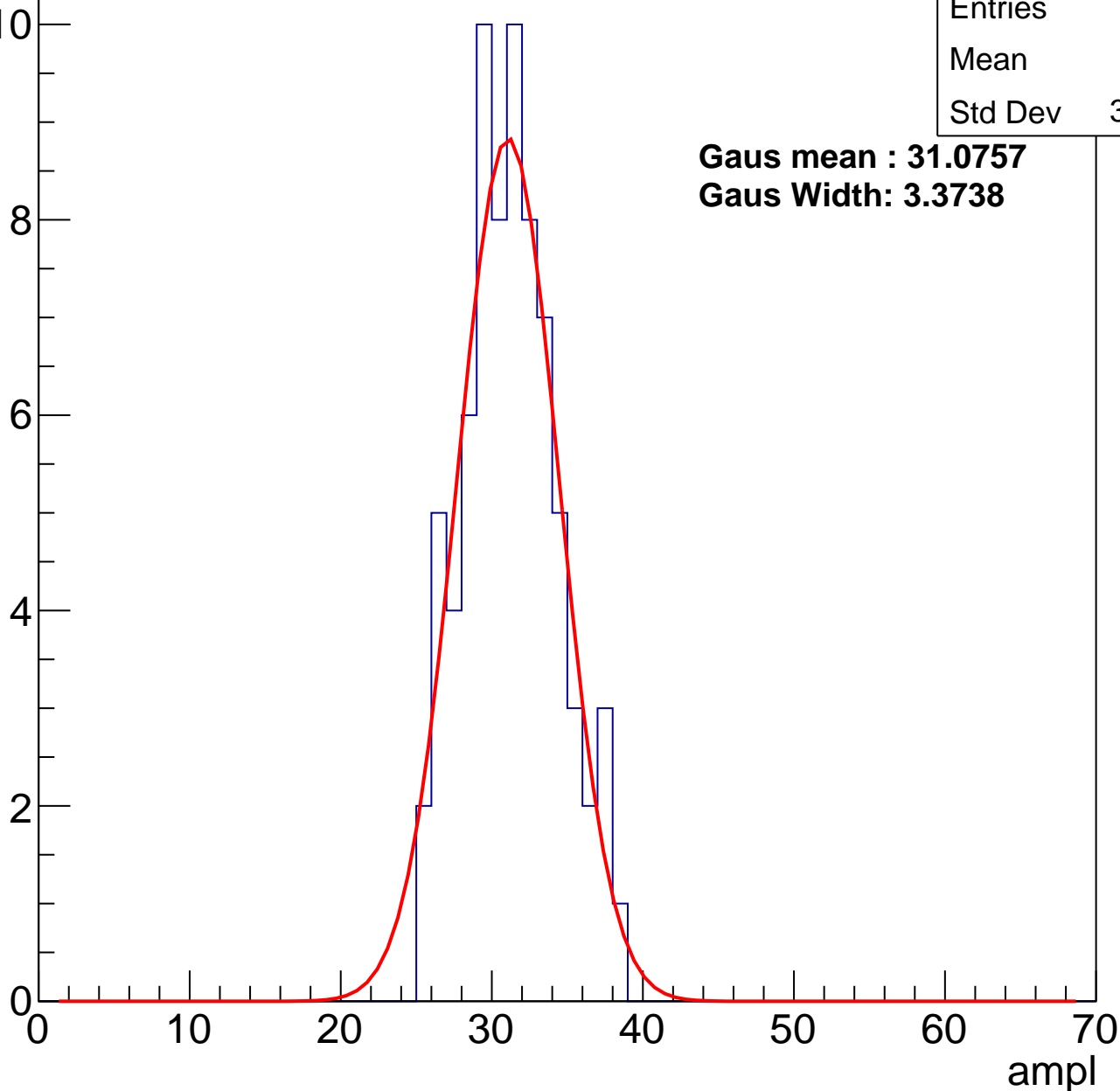
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.8
Std Dev	3.067

Gaus mean : 31.0757

Gaus Width: 3.3738



B1L003S, U13-ch5, adc1

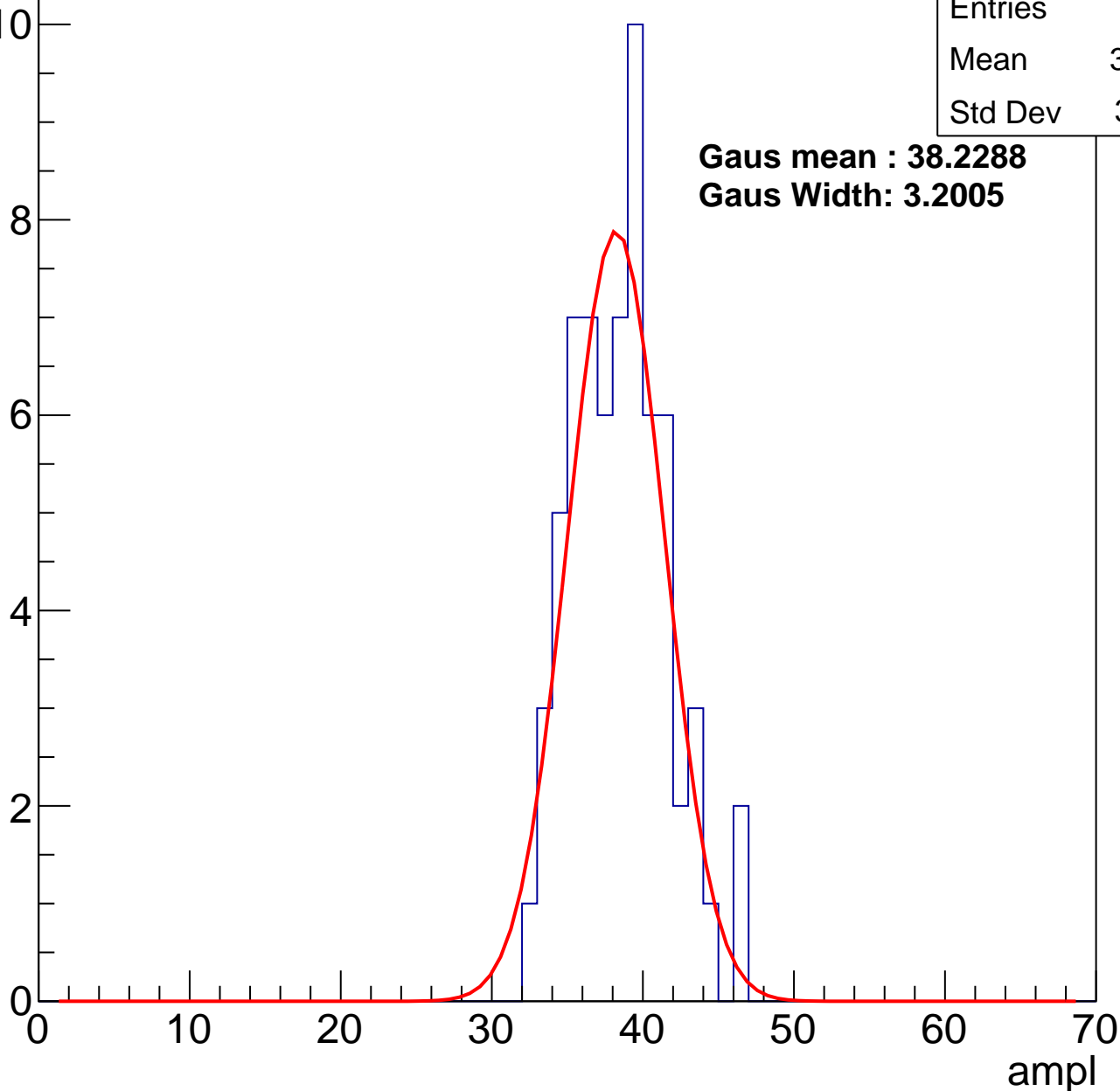
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	38.05
Std Dev	3.131

Gaus mean : 38.2288

Gaus Width: 3.2005



B1L003S, U13-ch5, adc2

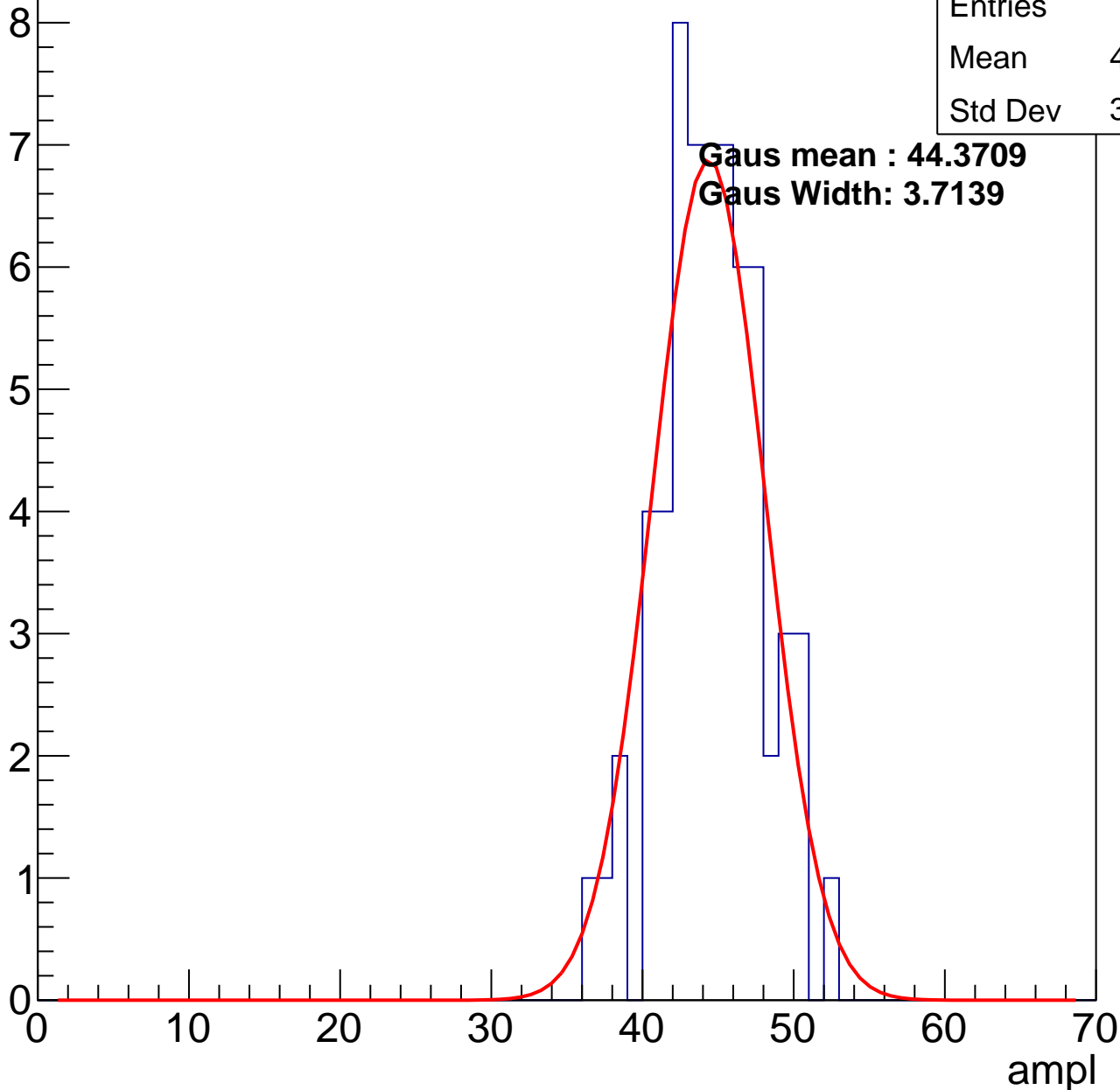
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	44.13
Std Dev	3.329

Gaus mean : 44.3709

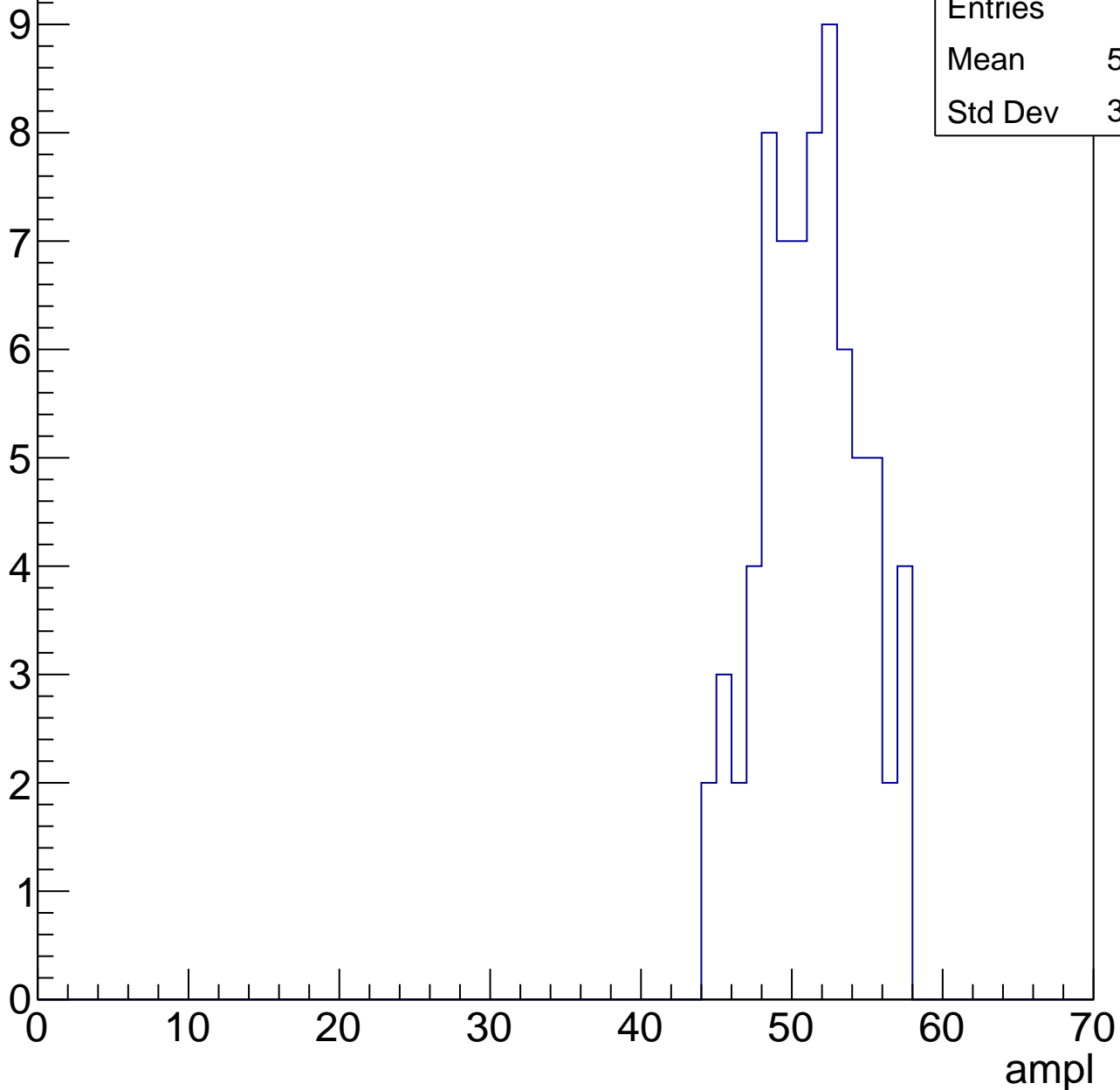
Gaus Width: 3.7139



B1L003S, U13-ch5, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

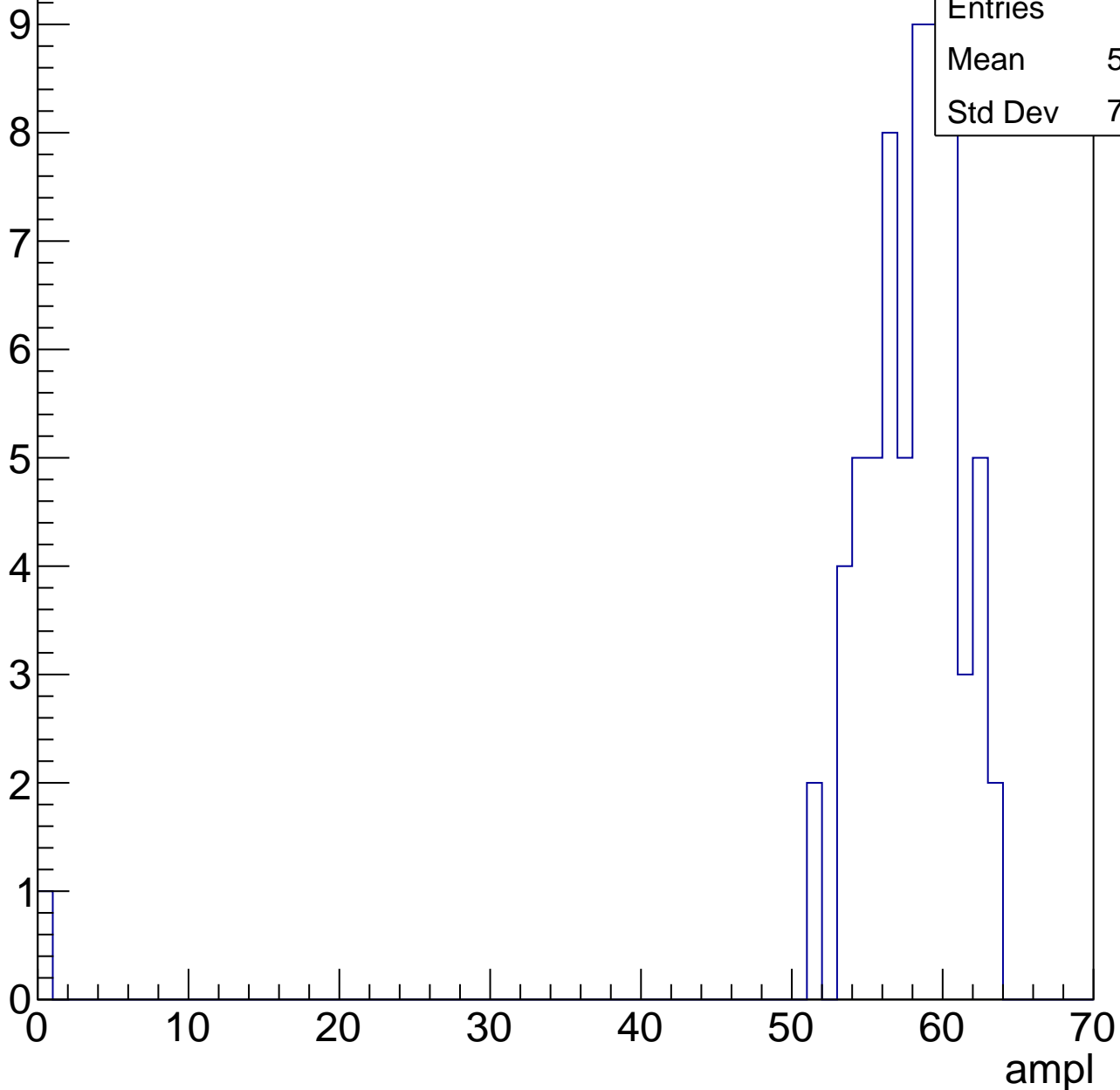


B1L003S, U13-ch5, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

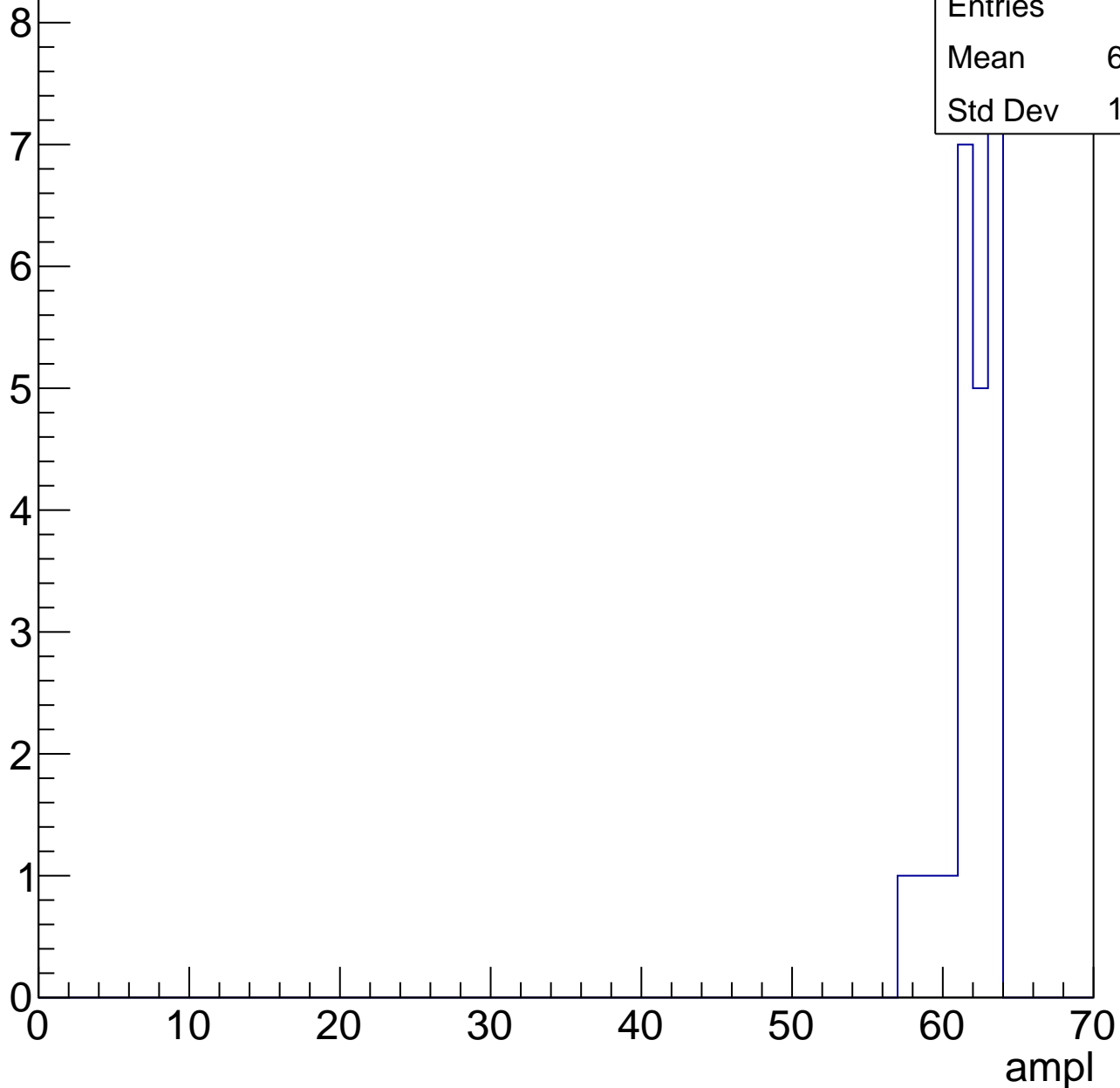
Entries	67
Mean	56.78
Std Dev	7.556



B1L003S, U13-ch5, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch5, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

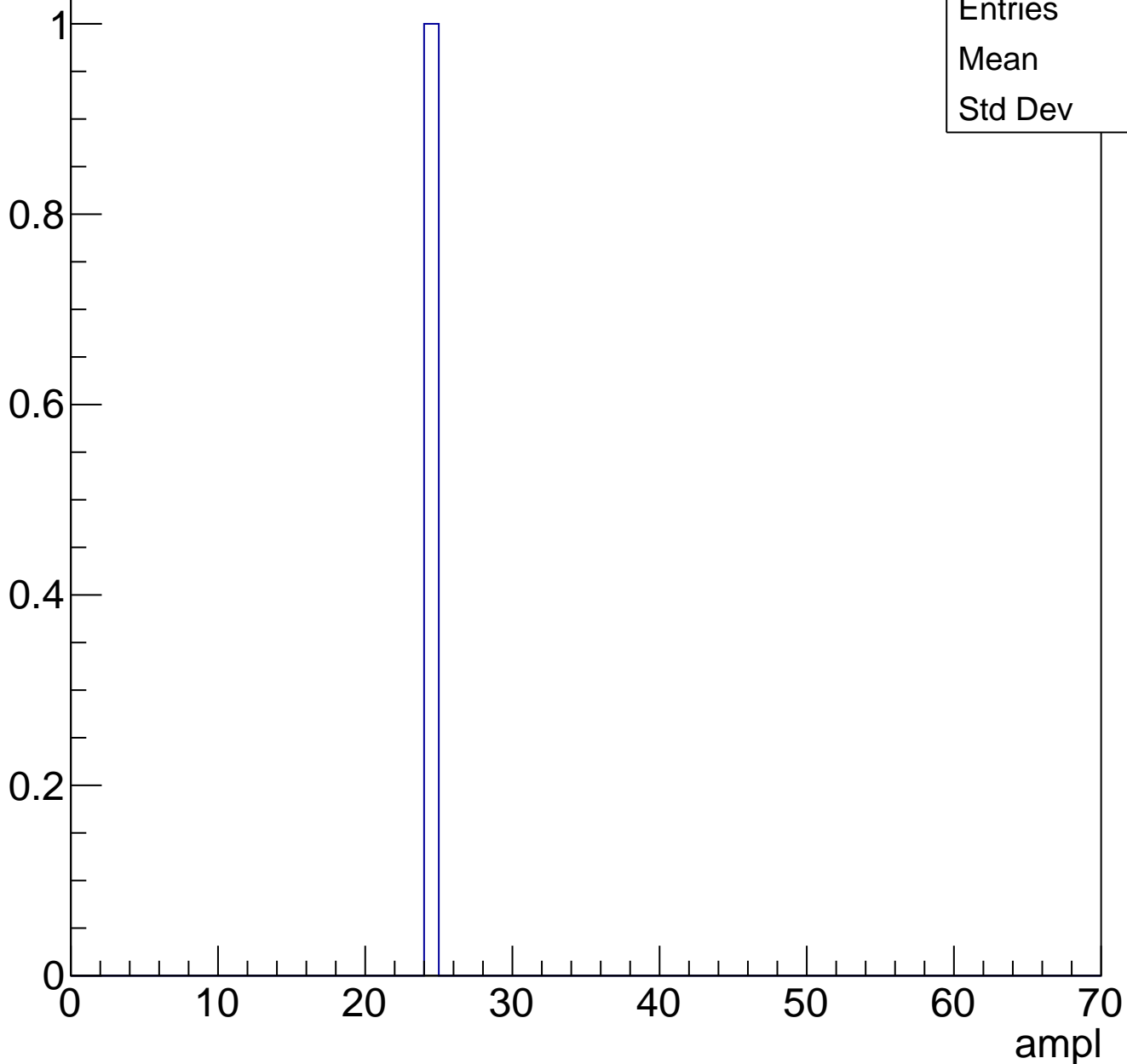


Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch5, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	24
Std Dev	0

B1L003S, U13-ch6, adc0

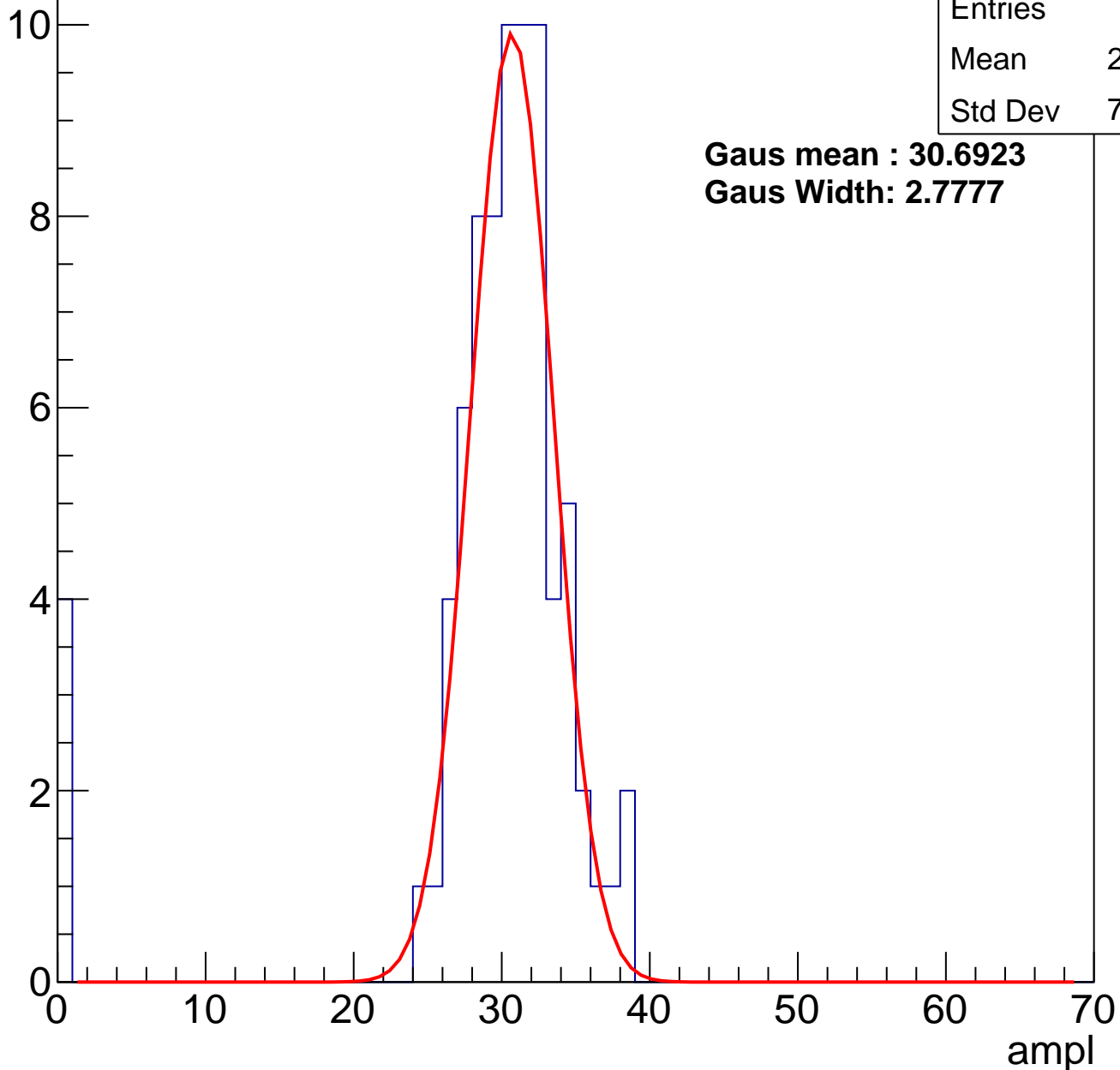
calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	28.86
Std Dev	7.339

Gaus mean : 30.6923

Gaus Width: 2.7777

Entry



B1L003S, U13-ch6, adc1

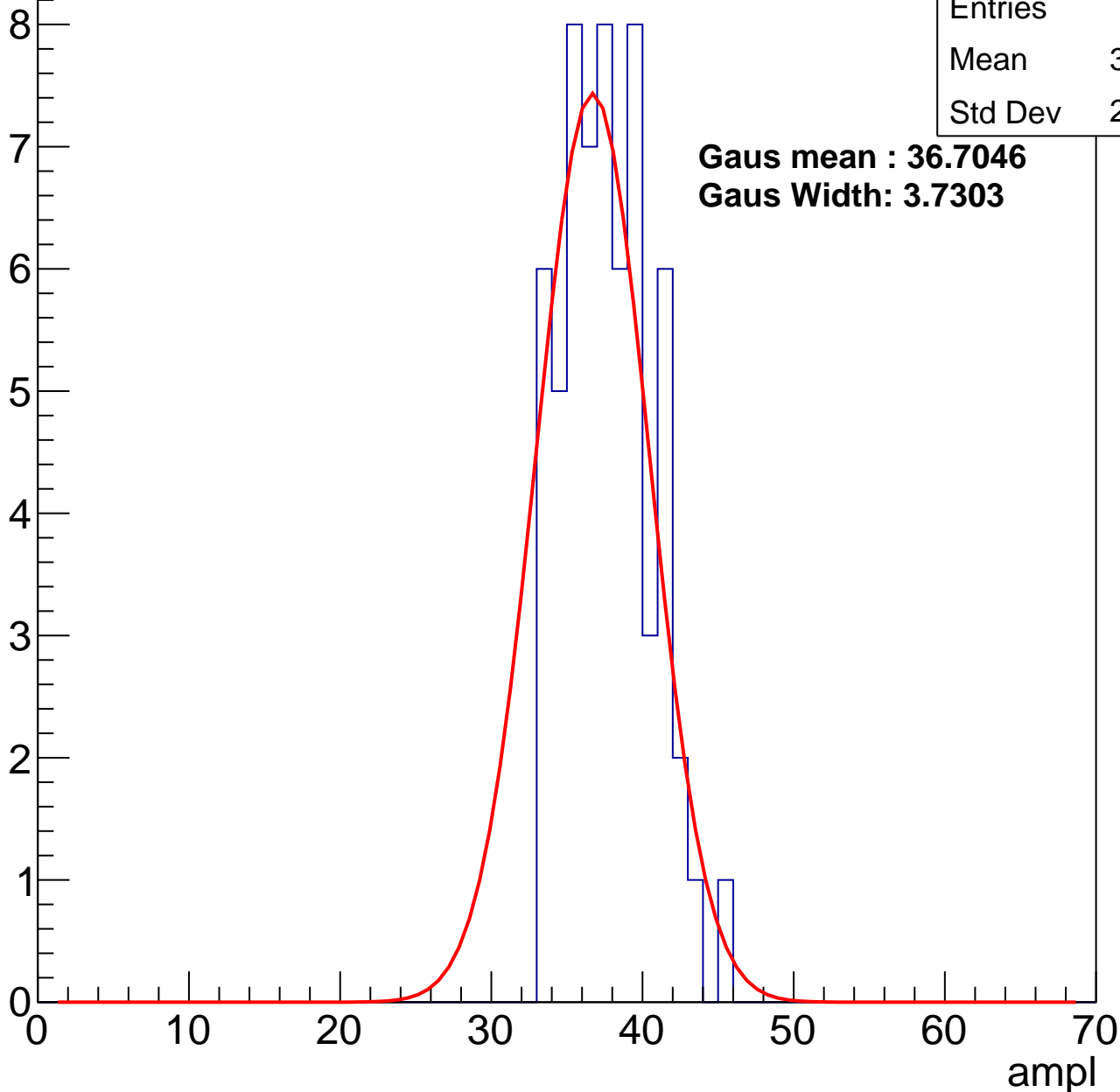
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	37.28
Std Dev	2.823

Gaus mean : 36.7046

Gaus Width: 3.7303



B1L003S, U13-ch6, adc2

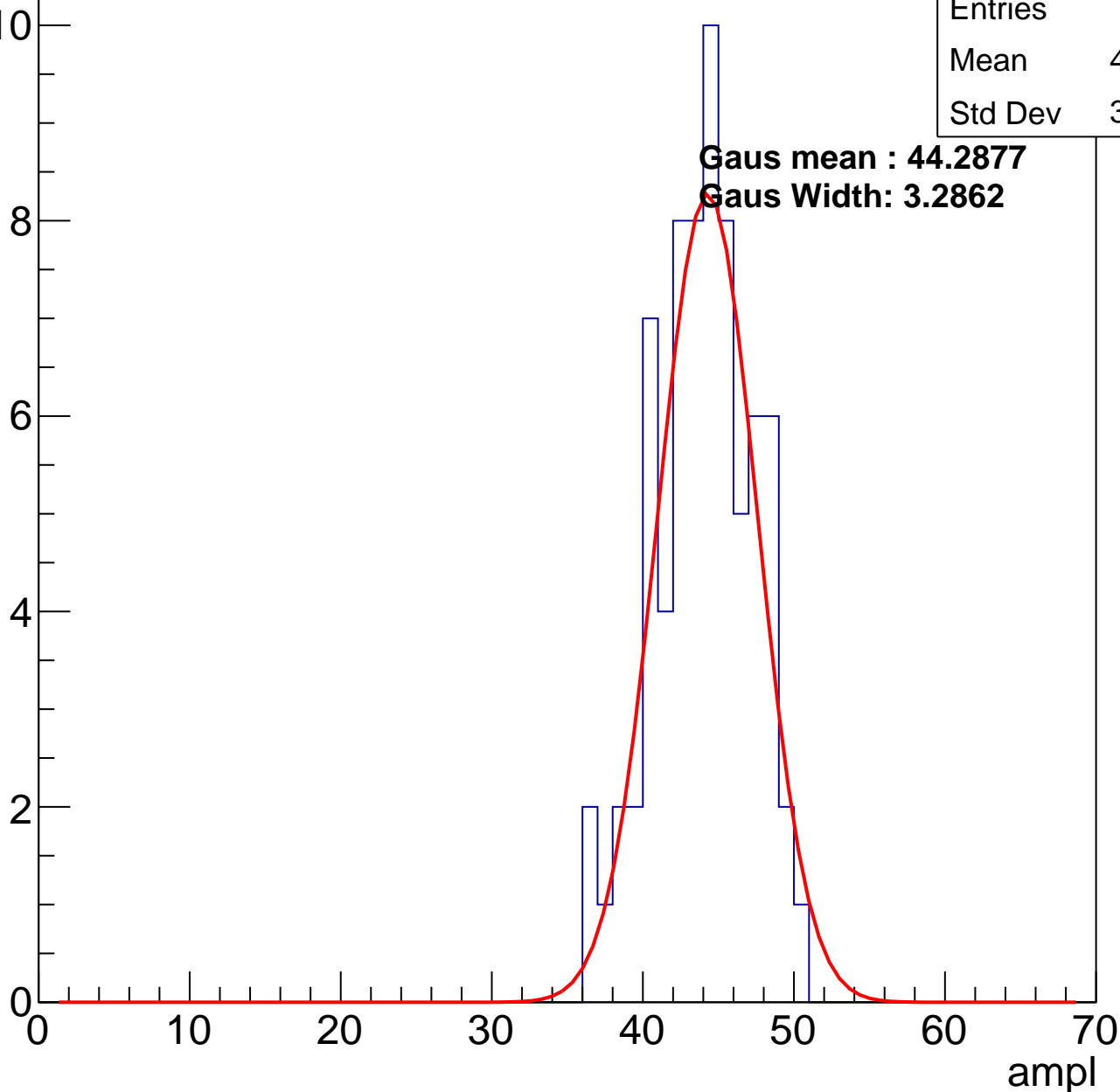
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.54
Std Dev	3.197

Gaus mean : 44.2877

Gaus Width: 3.2862

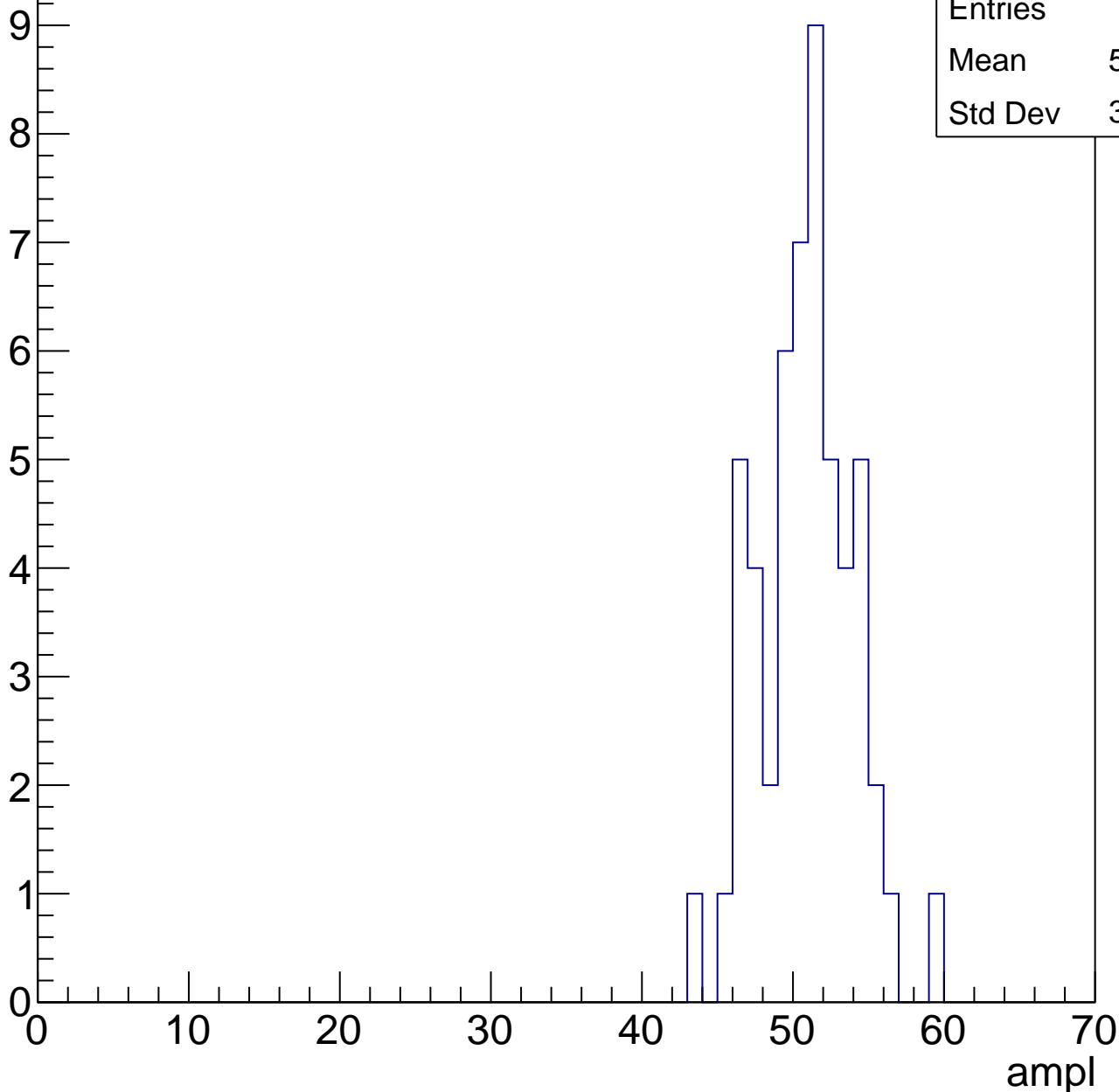


B1L003S, U13-ch6, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

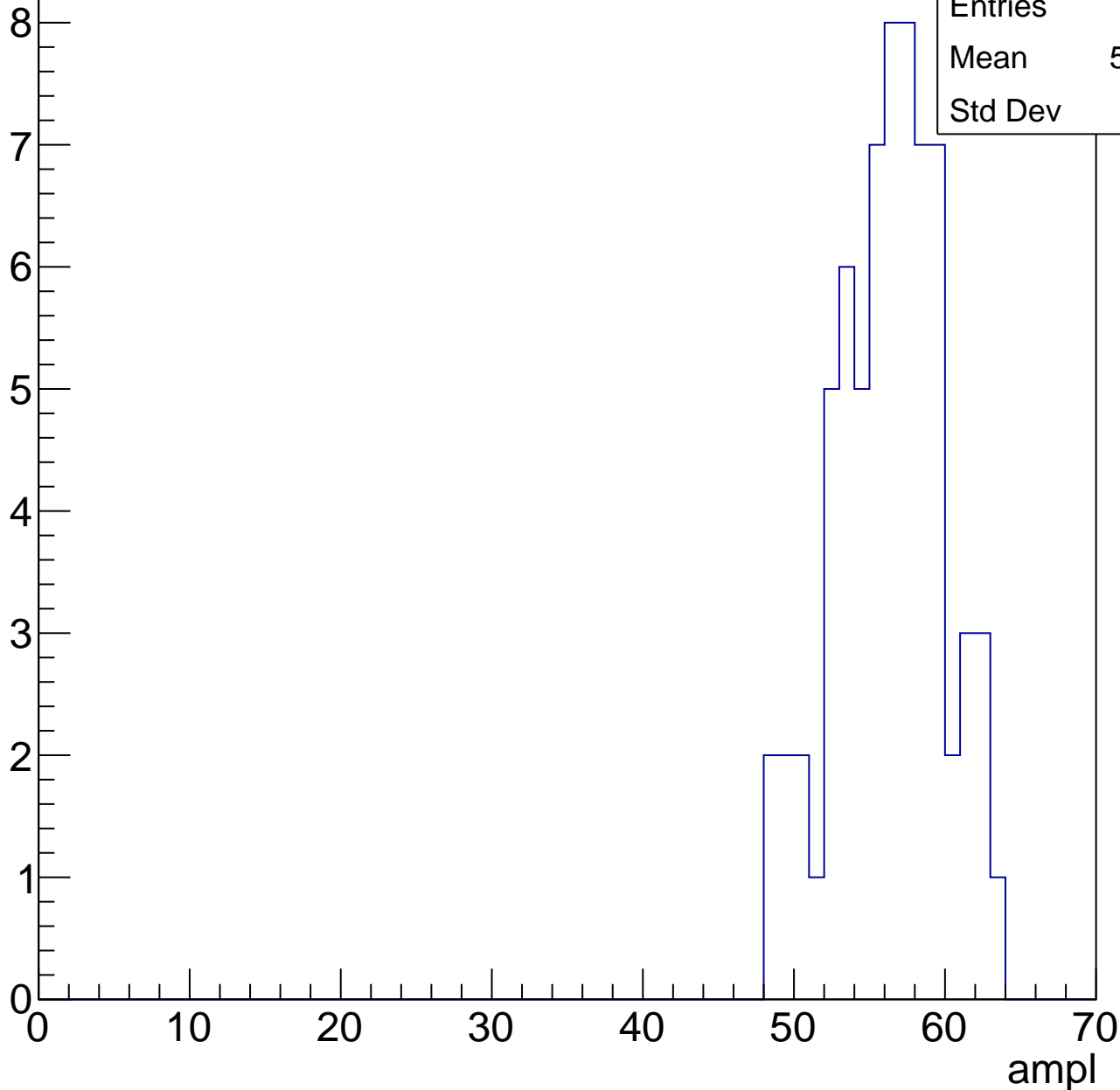
Entries	53
Mean	50.42
Std Dev	3.092



B1L003S, U13-ch6, adc4

calib_packv5_042523_0143.root, FC#13, port D2

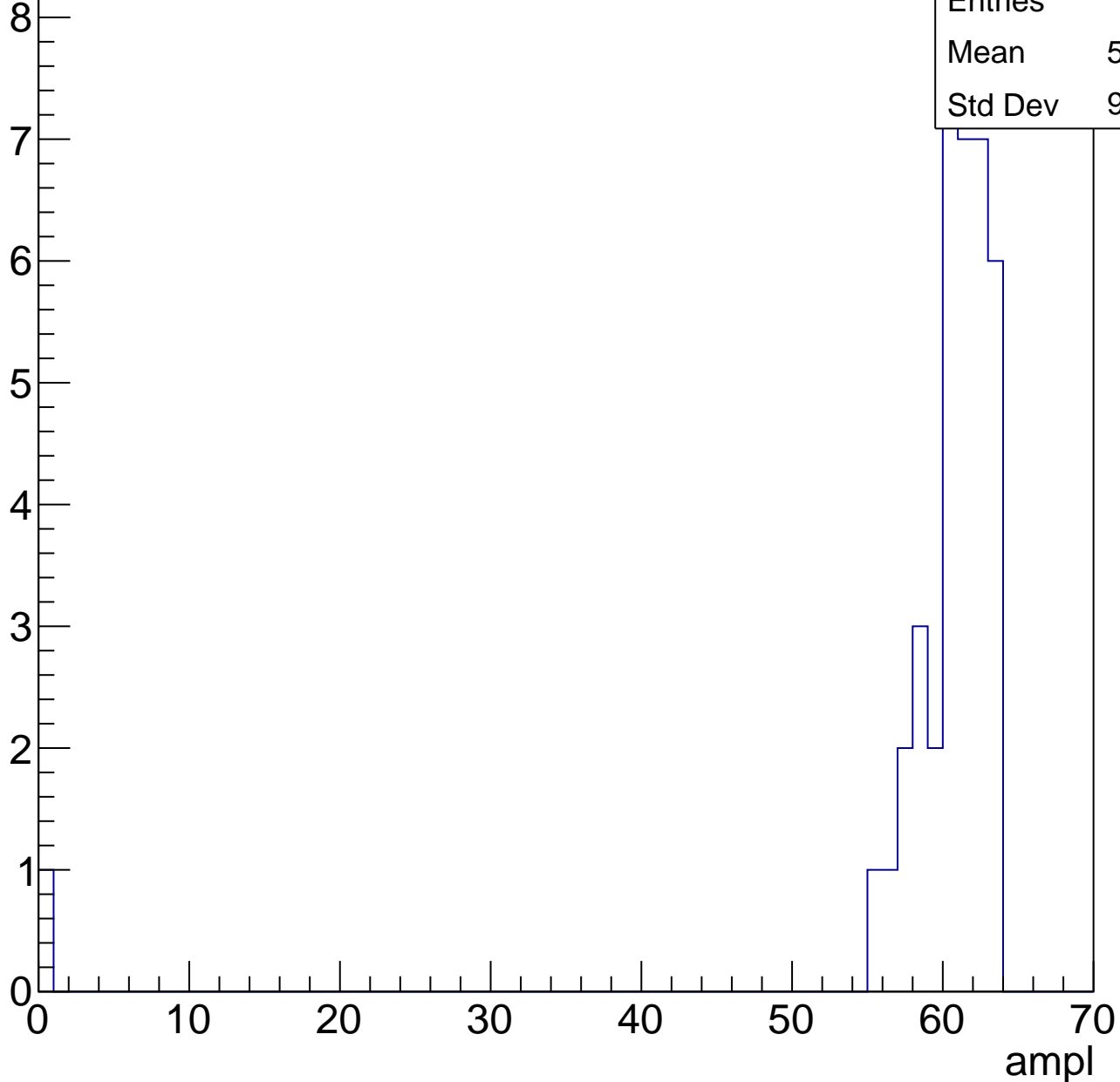
Entry



B1L003S, U13-ch6, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch6, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch6, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch7, adc0

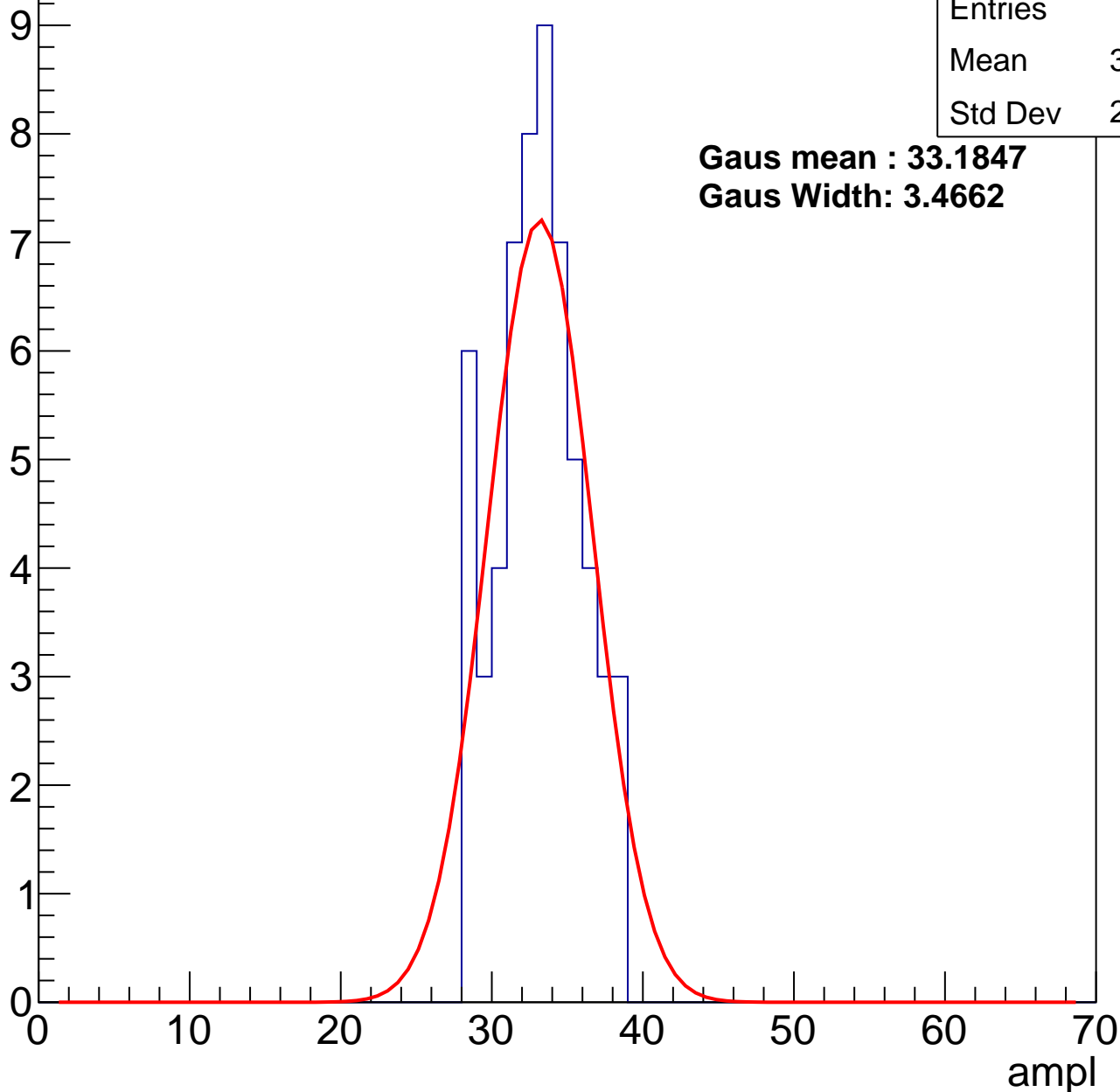
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	32.66
Std Dev	2.759

Gaus mean : 33.1847

Gaus Width: 3.4662



B1L003S, U13-ch7, adc1

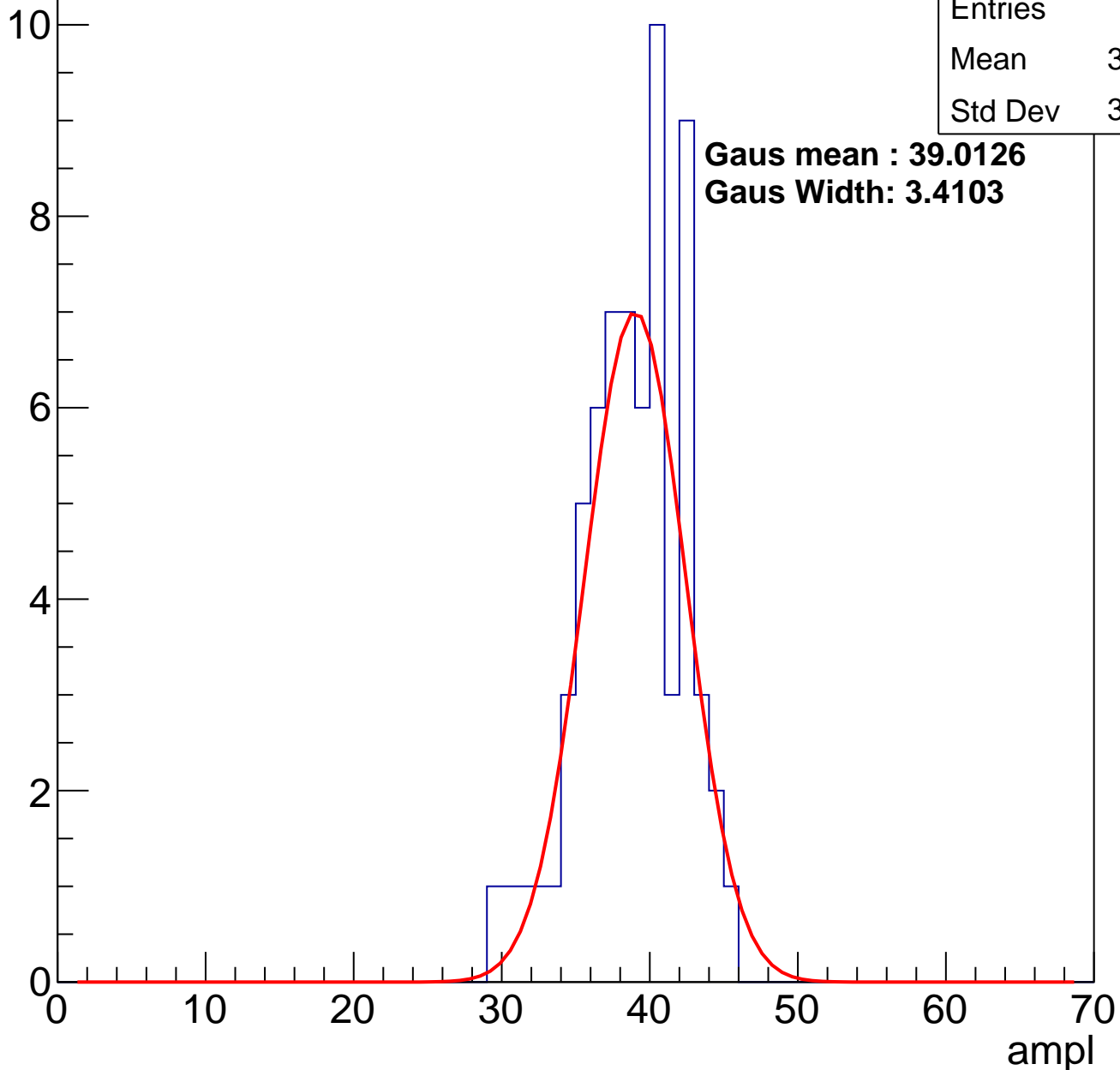
calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	38.36
Std Dev	3.419

Gaus mean : 39.0126

Gaus Width: 3.4103

Entry



B1L003S, U13-ch7, adc2

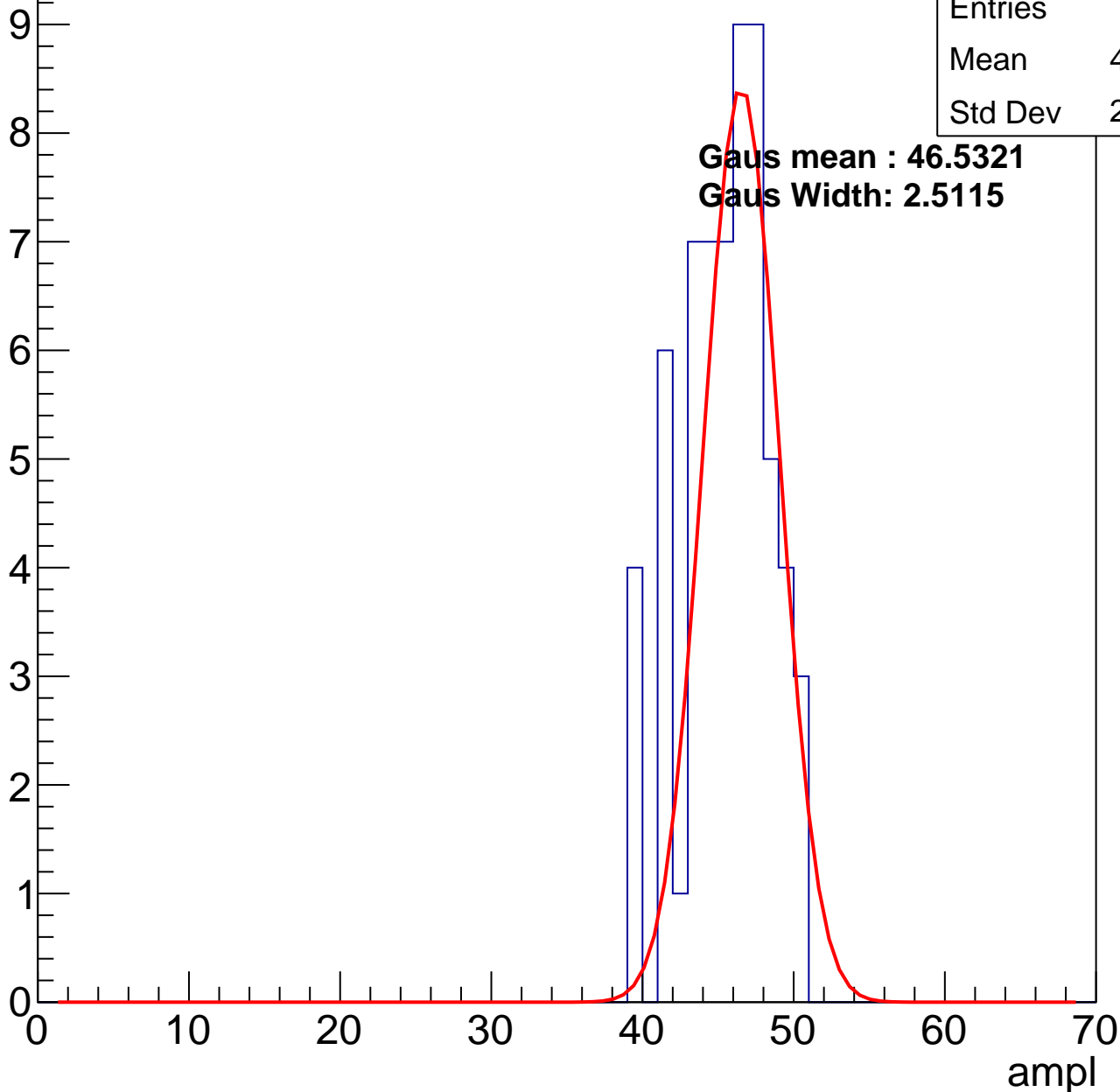
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	45.02
Std Dev	2.876

Gaus mean : 46.5321

Gaus Width: 2.5115



B1L003S, U13-ch7, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	51.82
Std Dev	3.382

Entry

10

8

6

4

2

0

0

10

20

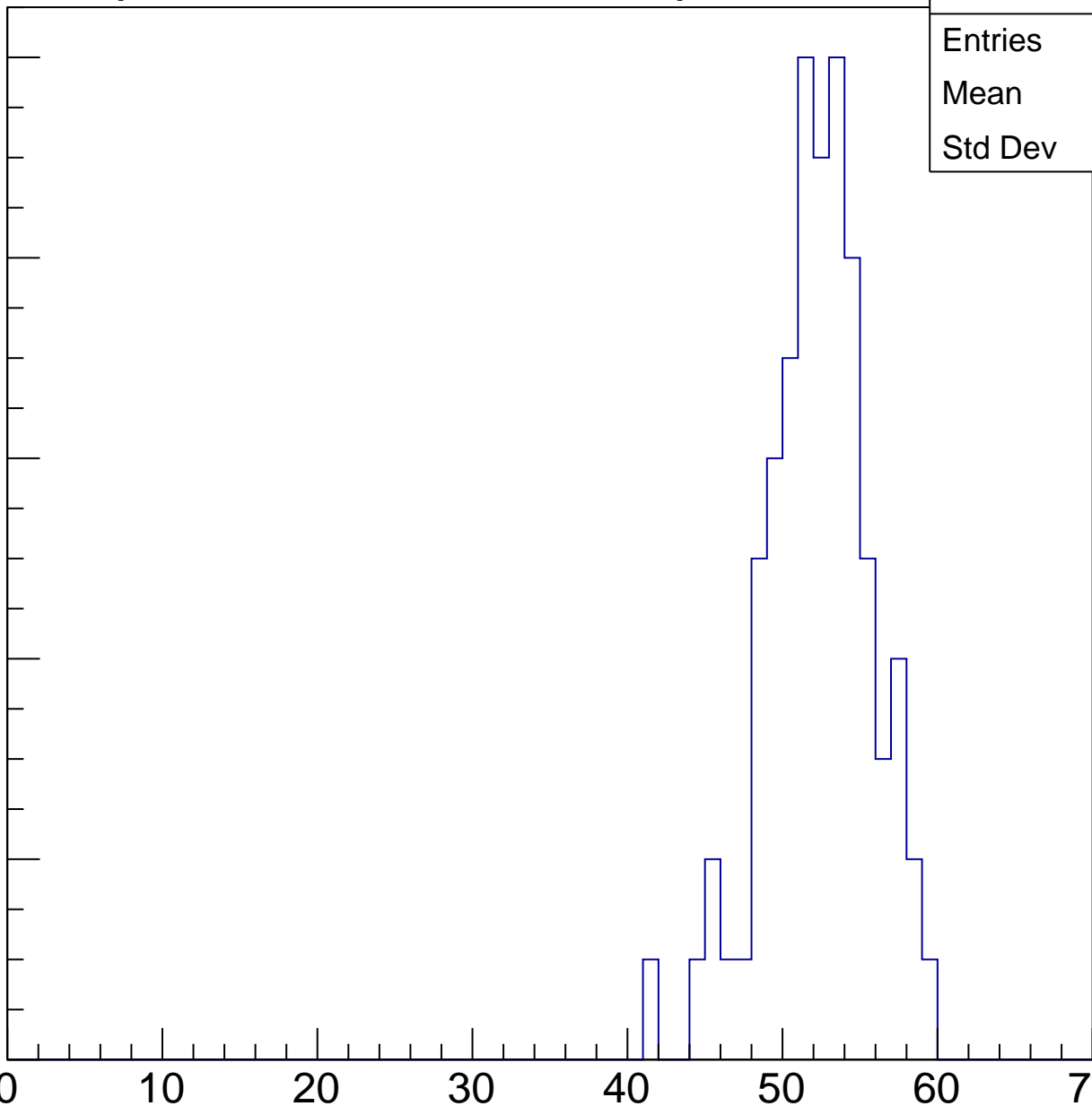
30

40

50

60

ampl

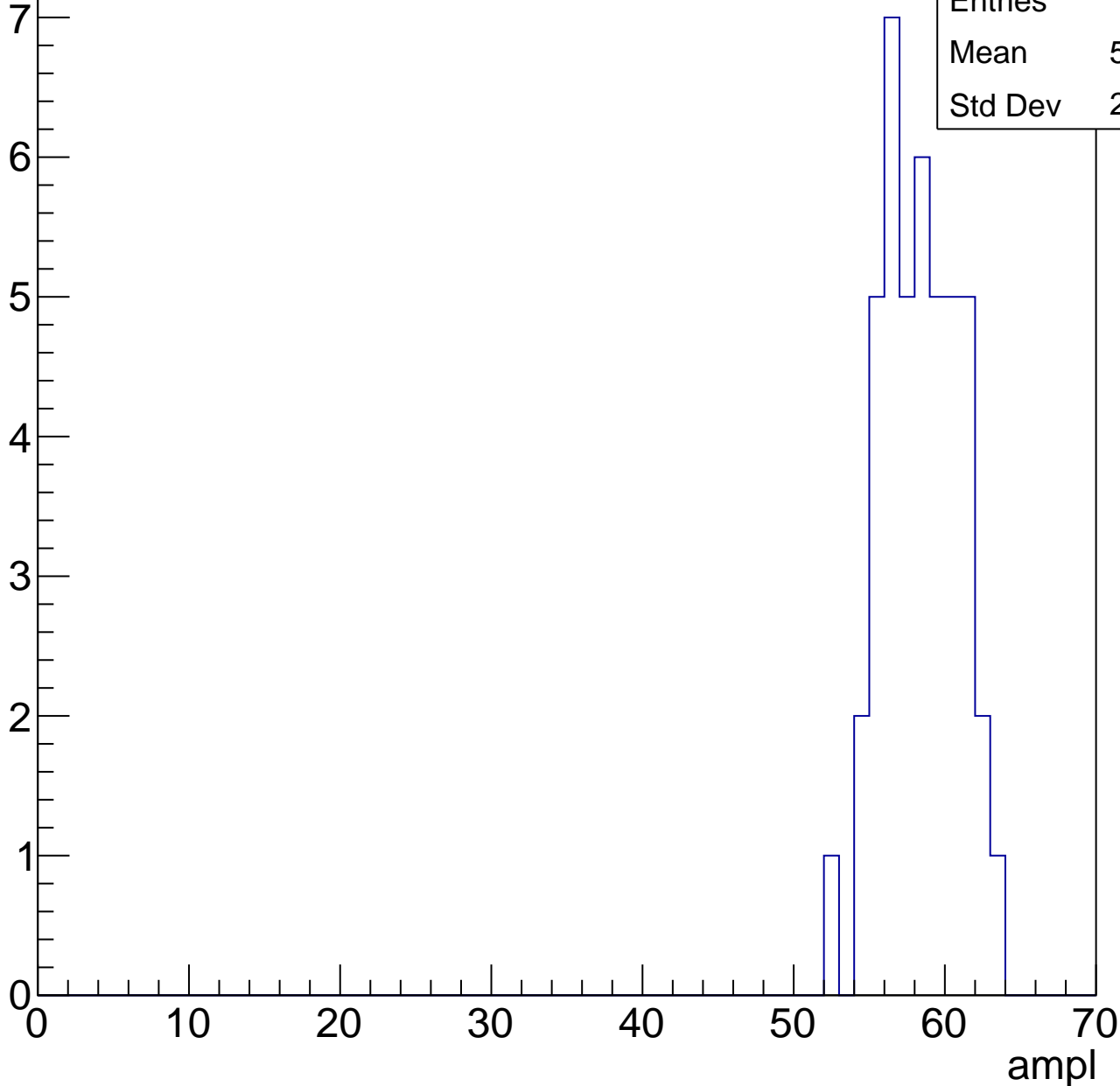


B1L003S, U13-ch7, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

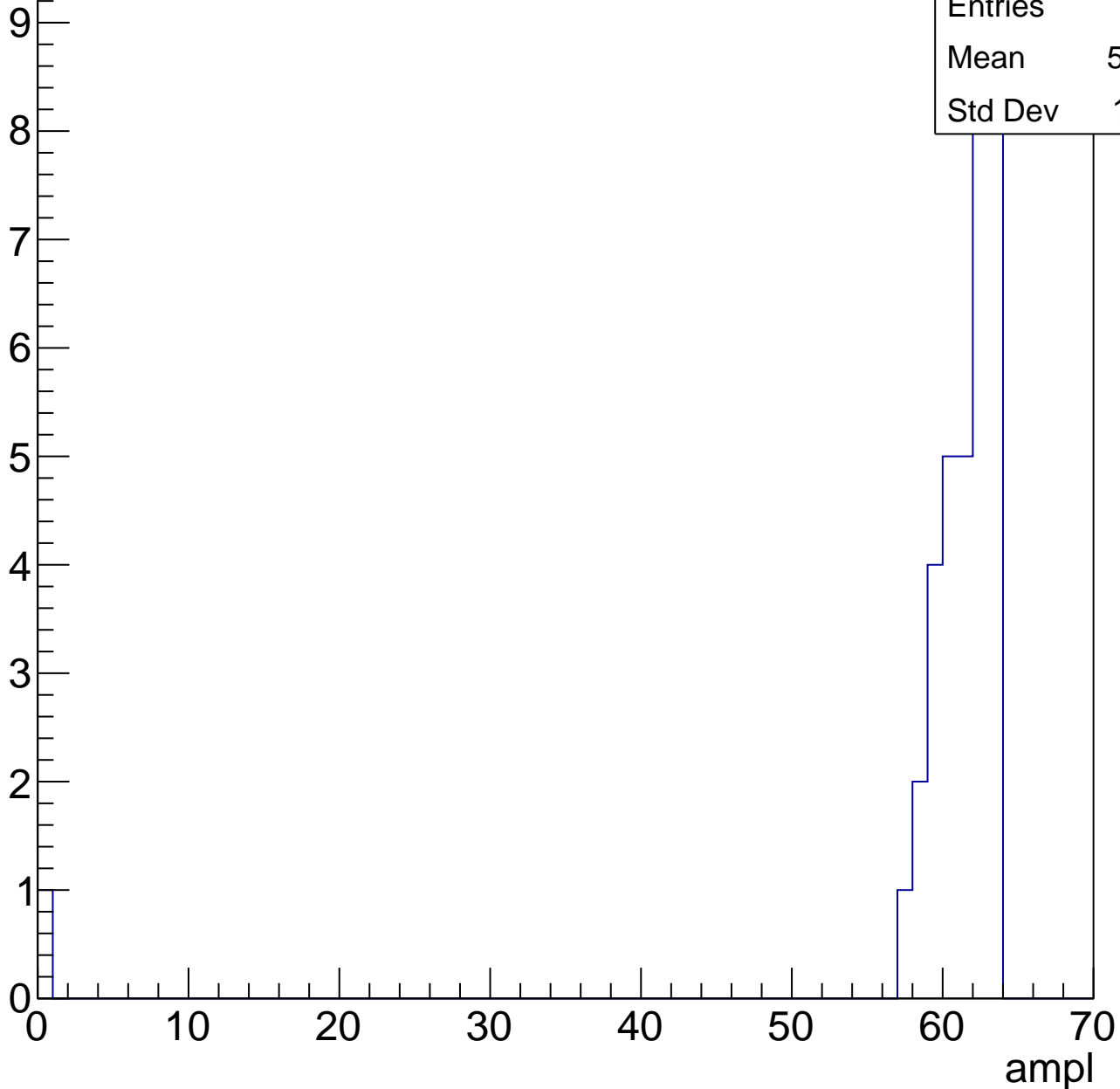
Entries	44
Mean	57.89
Std Dev	2.488



B1L003S, U13-ch7, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch7, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

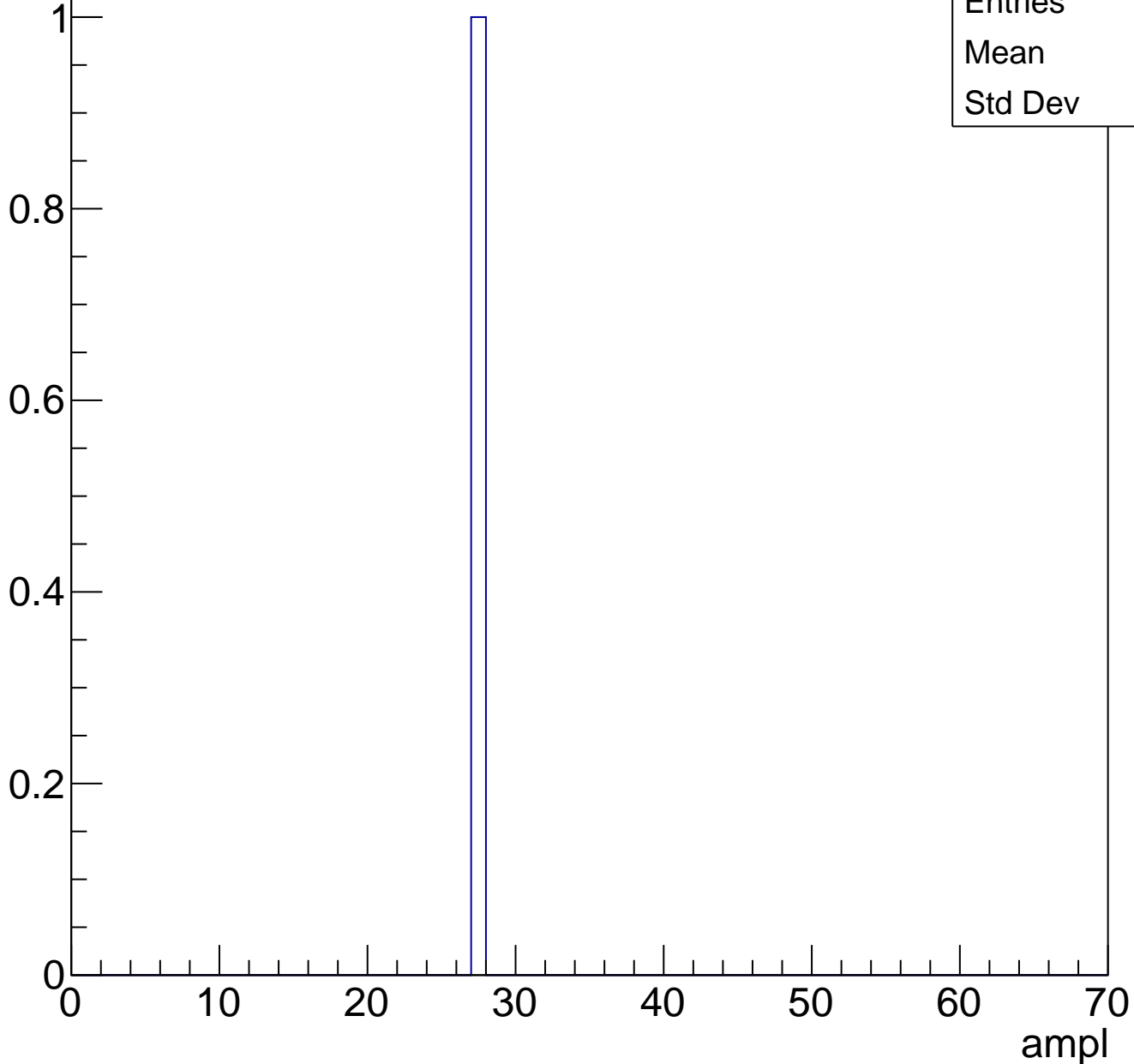


Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch7, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch8, adc0

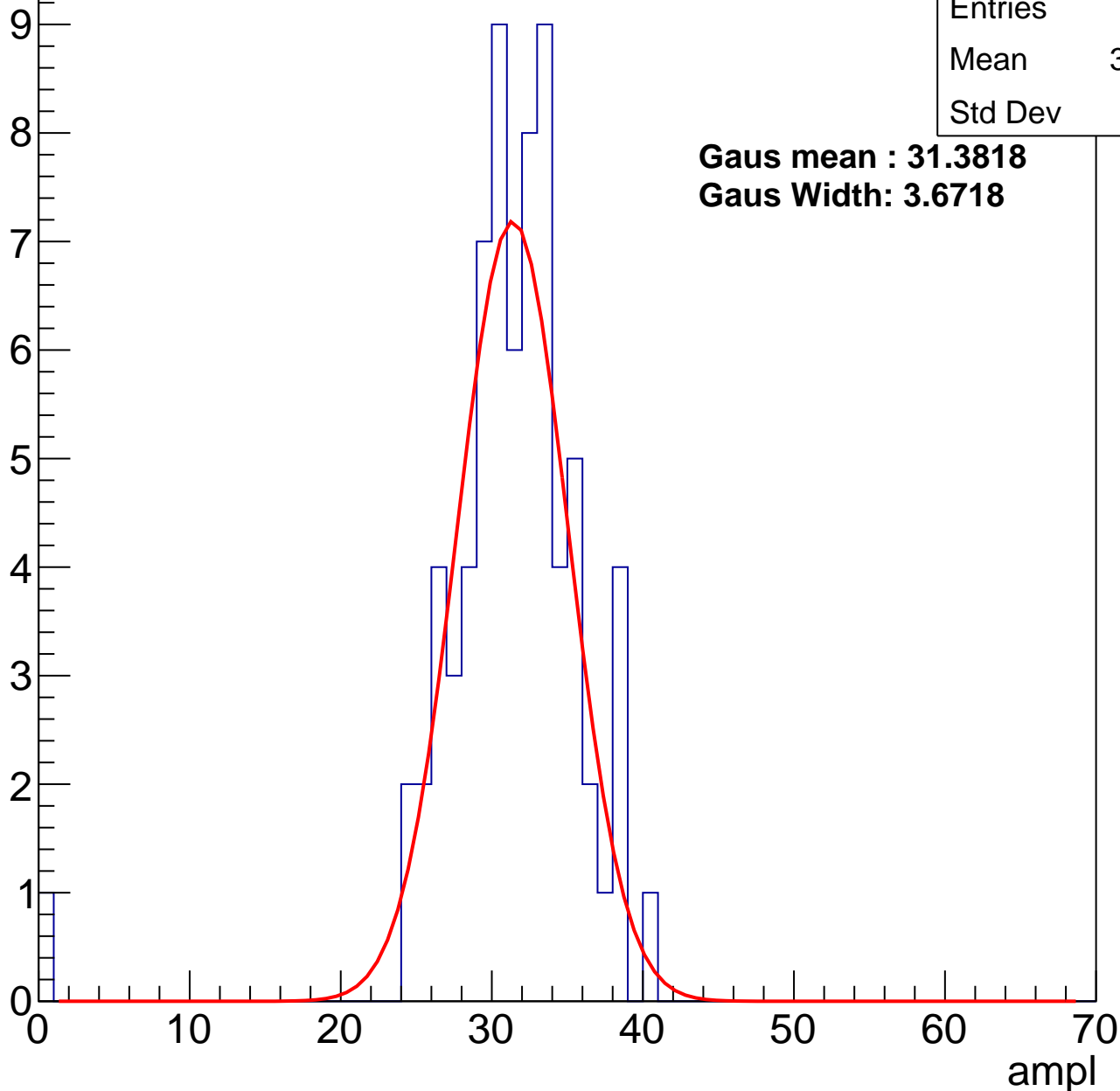
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.82
Std Dev	5.1

Gaus mean : 31.3818

Gaus Width: 3.6718



B1L003S, U13-ch8, adc1

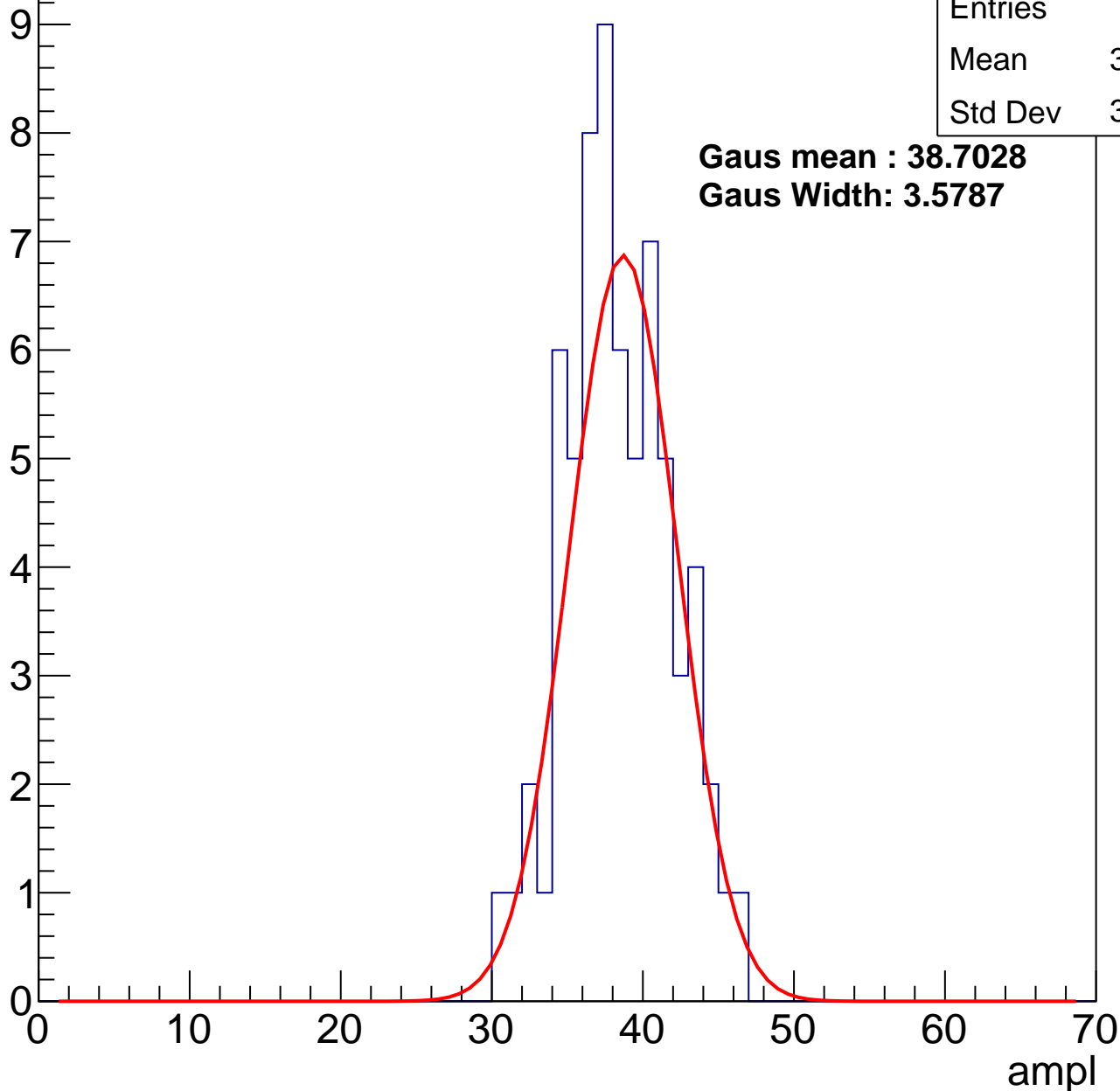
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.96
Std Dev	3.462

Gaus mean : 38.7028

Gaus Width: 3.5787

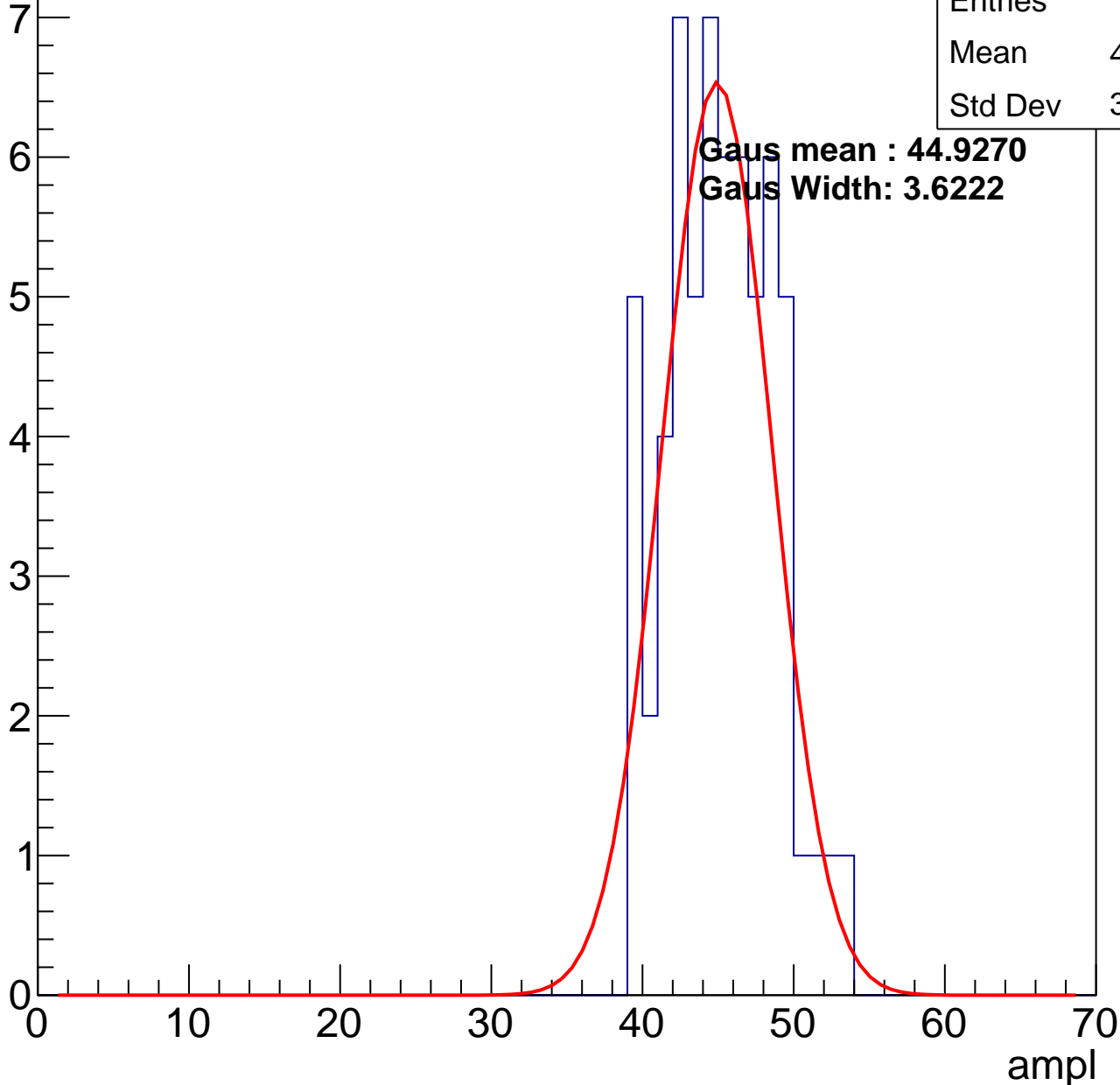


B1L003S, U13-ch8, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	44.77
Std Dev	3.396

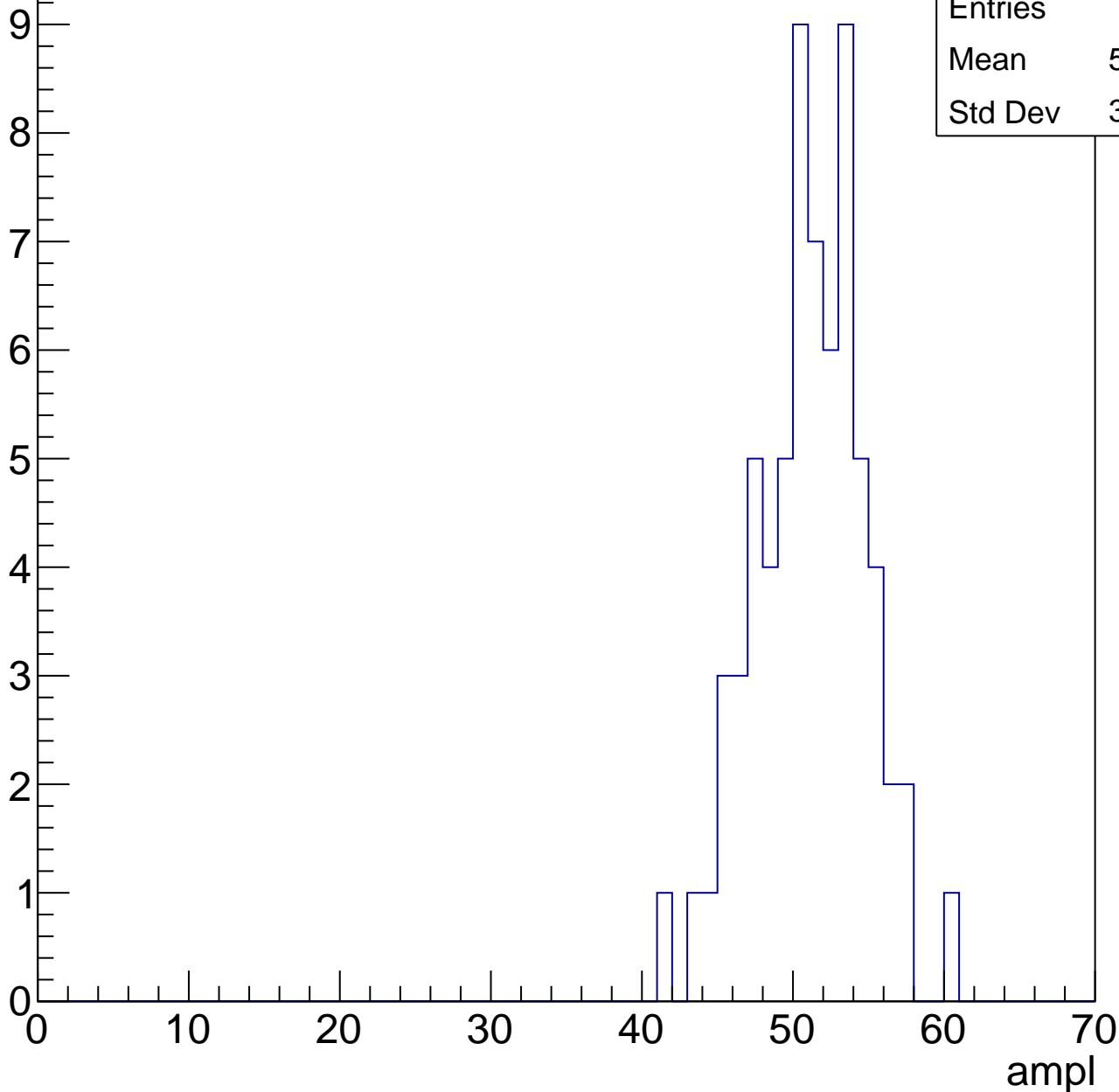


B1L003S, U13-ch8, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.66
Std Dev	3.612

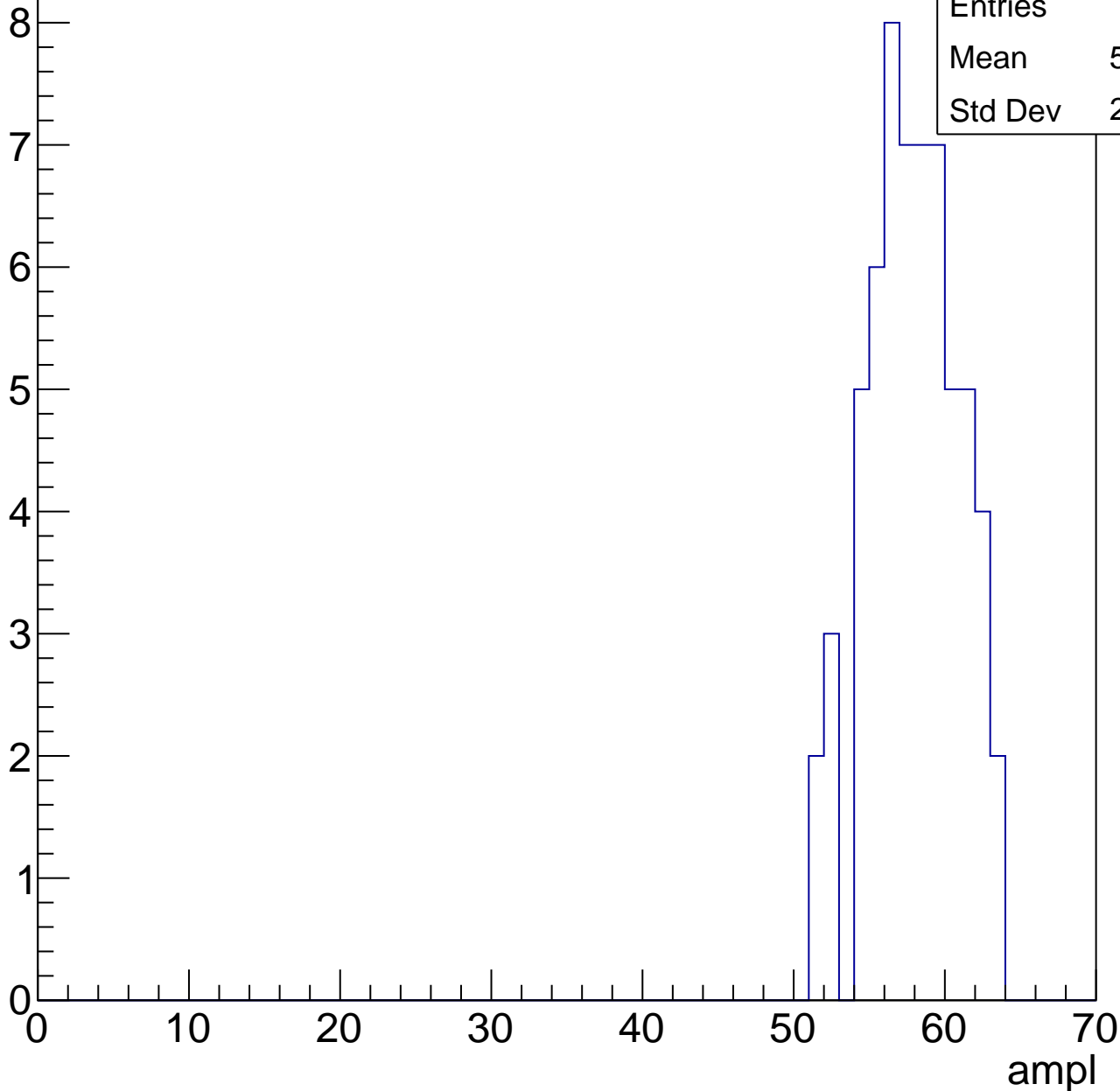


B1L003S, U13-ch8, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

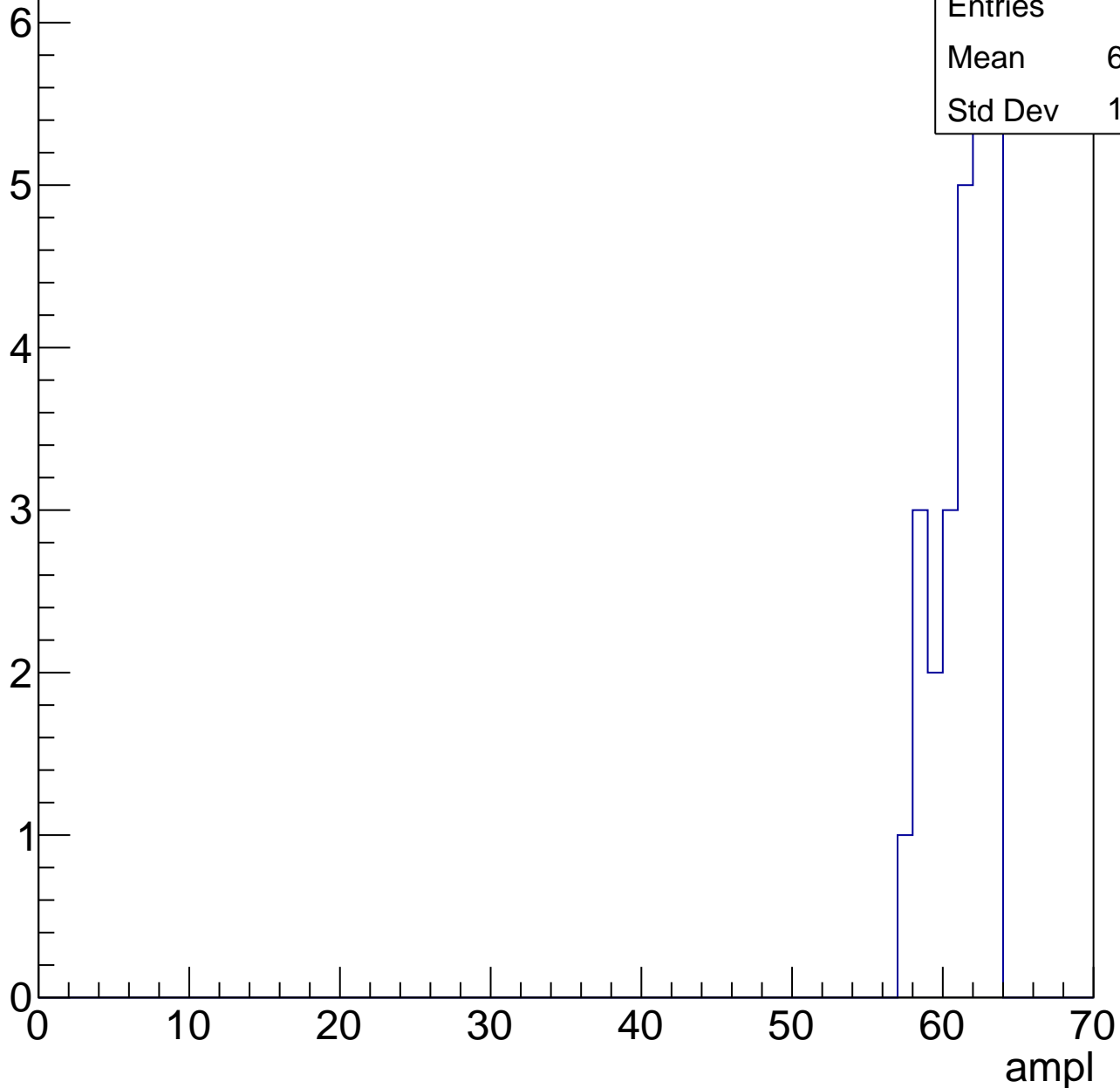
Entries	61
Mean	57.43
Std Dev	2.989



B1L003S, U13-ch8, adc5

calib_packv5_042523_0143.root, FC#13, port D2

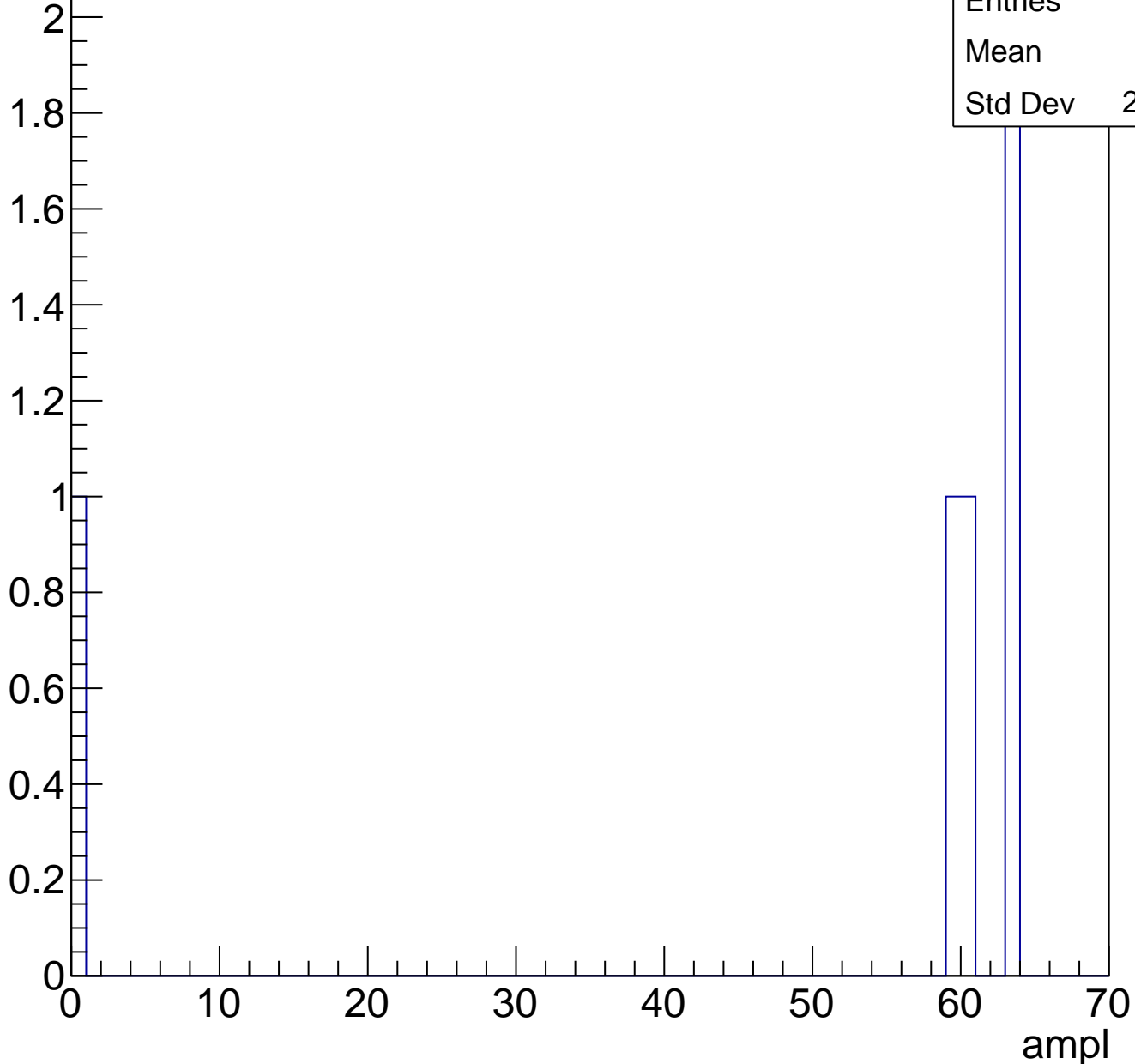
Entry



B1L003S, U13-ch8, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch8, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch9, adc0

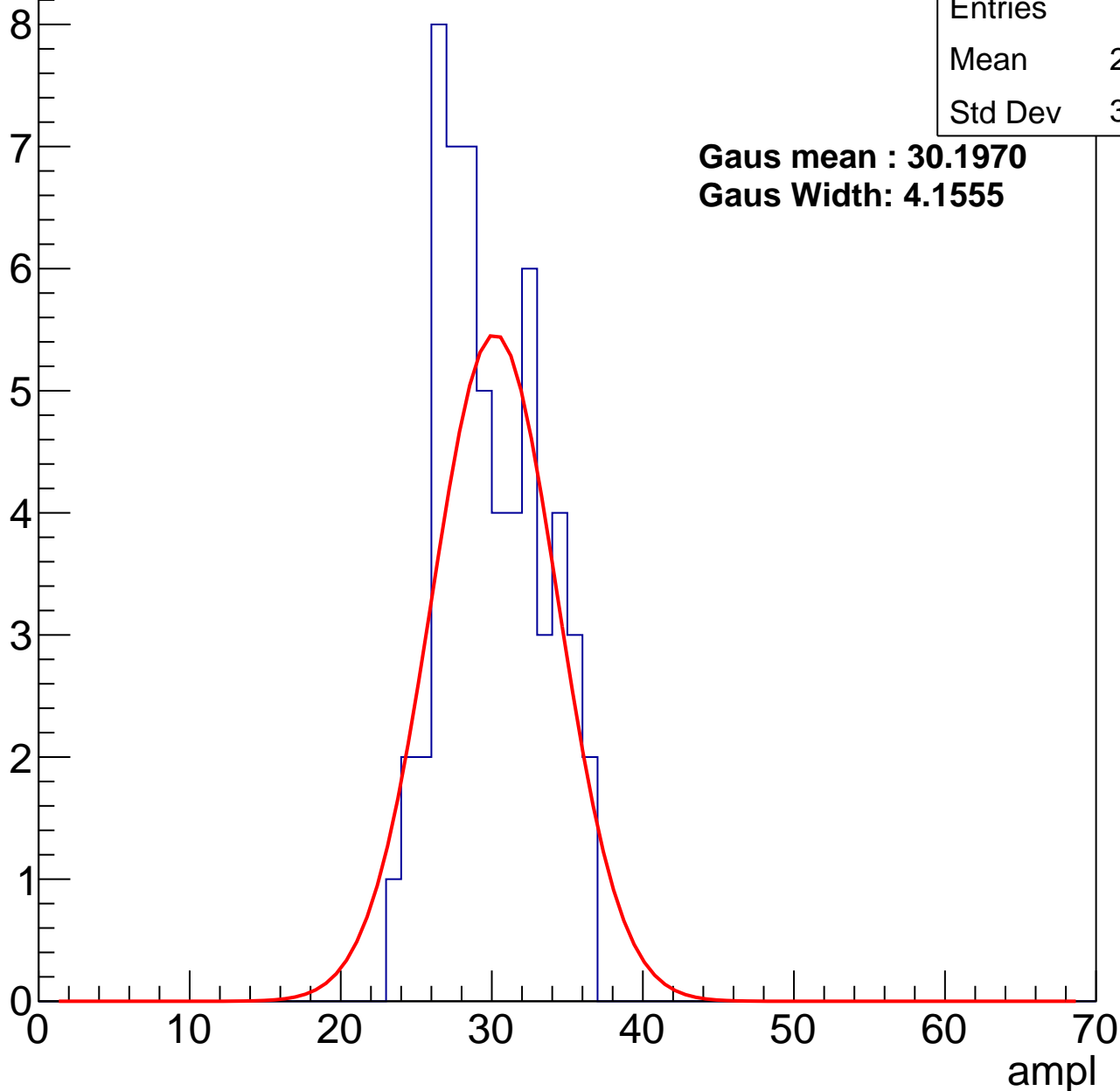
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29.43
Std Dev	3.328

Gaus mean : 30.1970

Gaus Width: 4.1555



B1L003S, U13-ch9, adc1

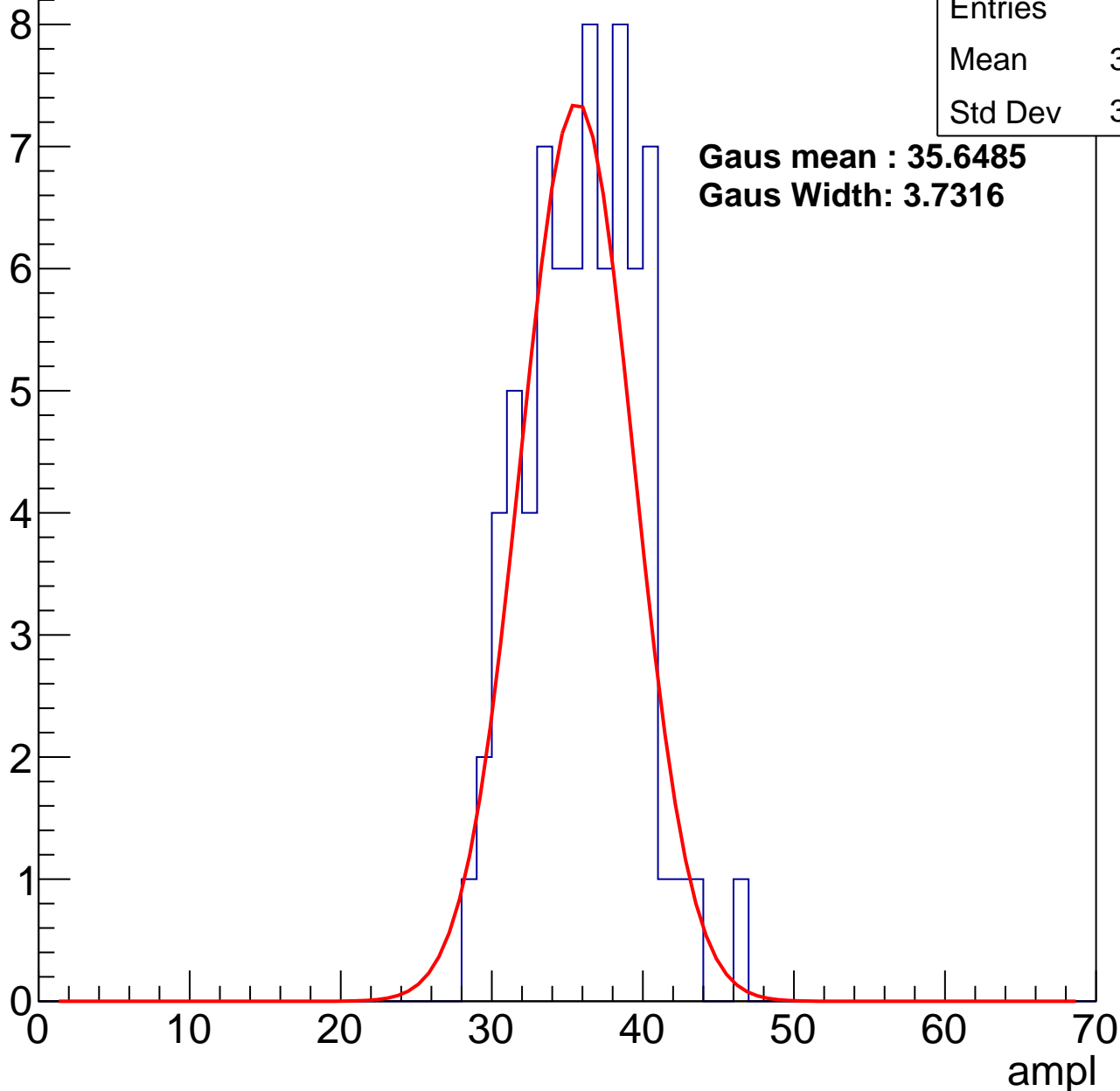
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.59
Std Dev	3.657

Gaus mean : 35.6485

Gaus Width: 3.7316



B1L003S, U13-ch9, adc2

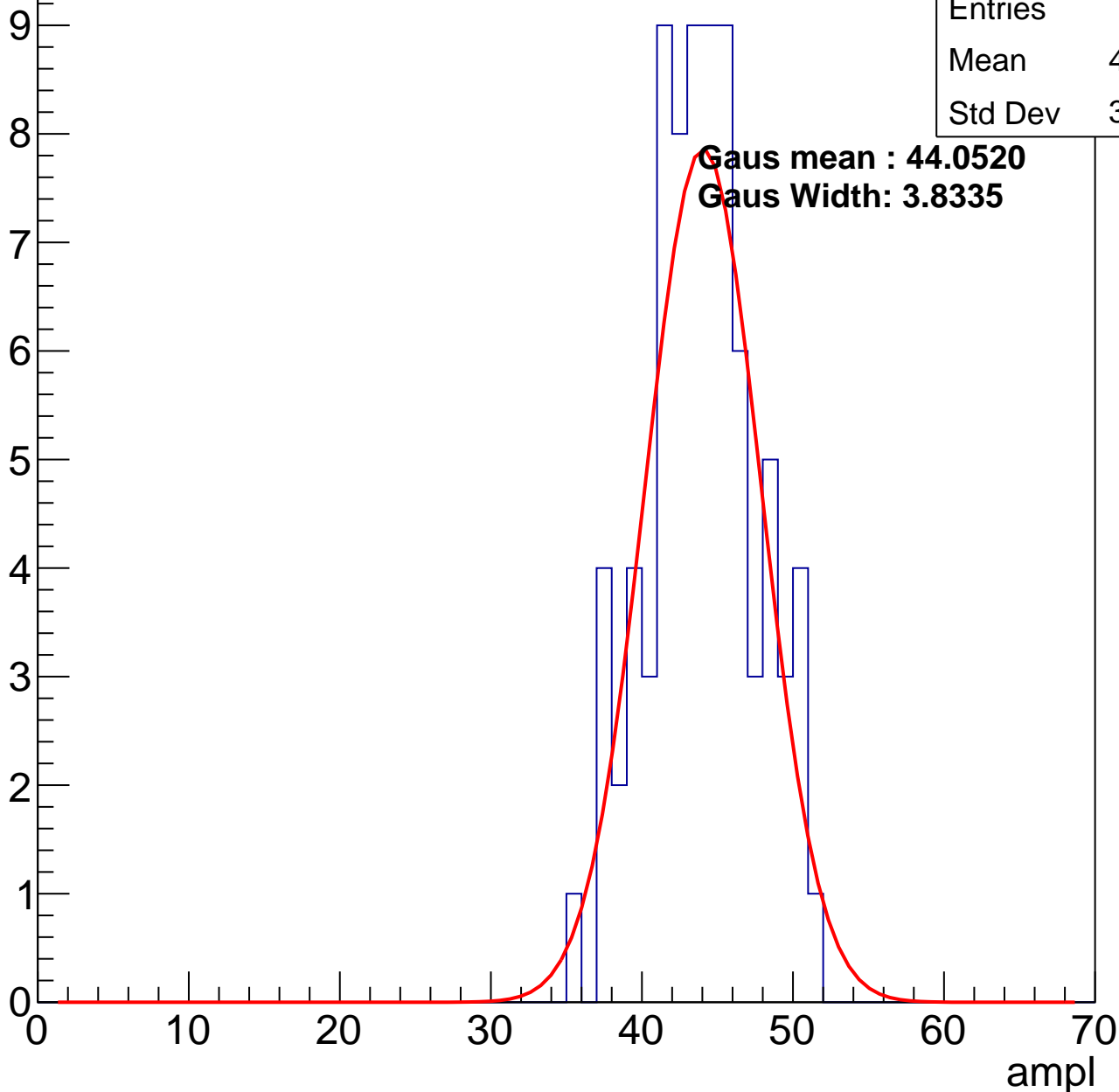
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	43.54
Std Dev	3.553

Gaus mean : 44.0520

Gaus Width: 3.8335

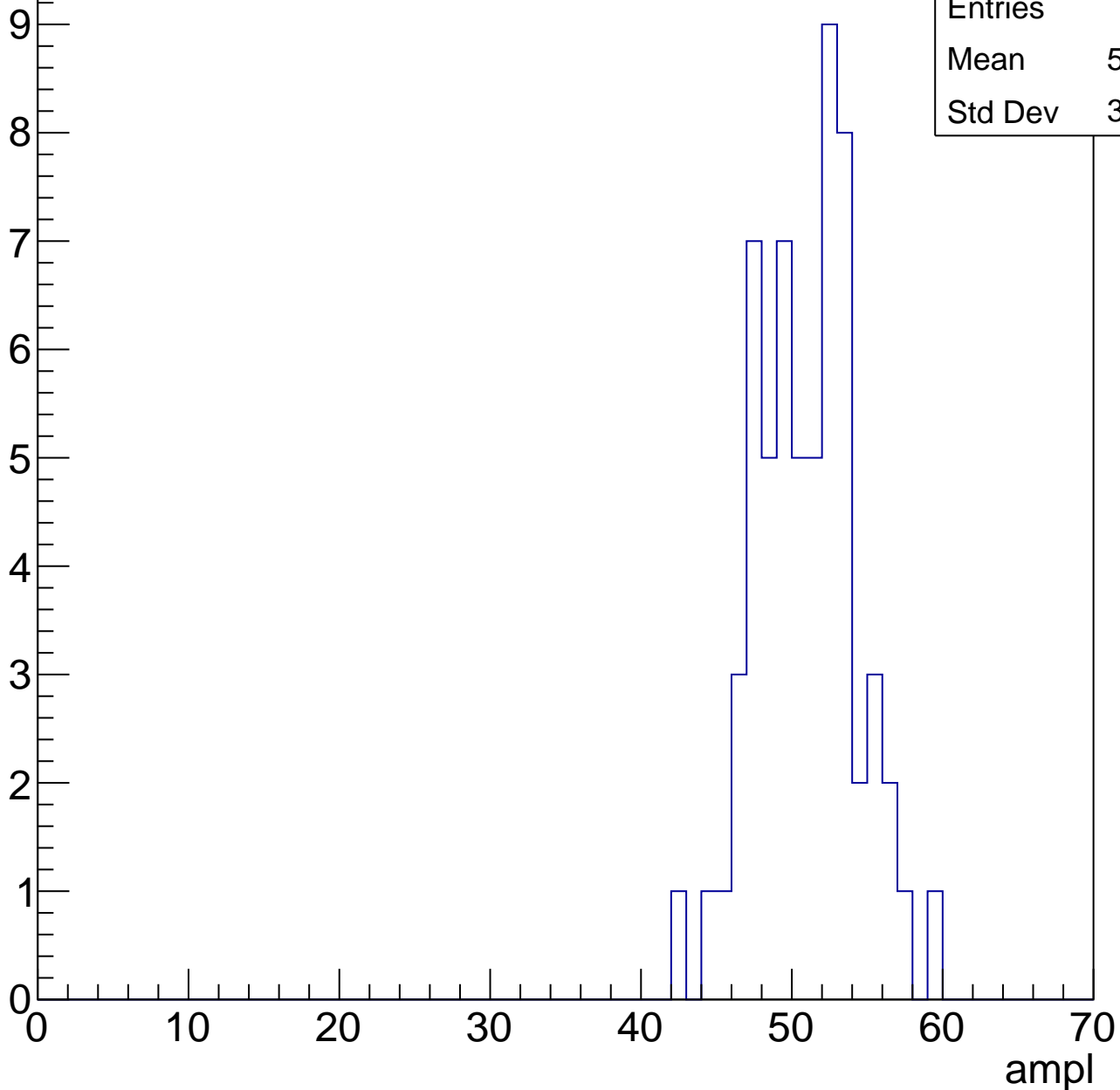


B1L003S, U13-ch9, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	50.48
Std Dev	3.317

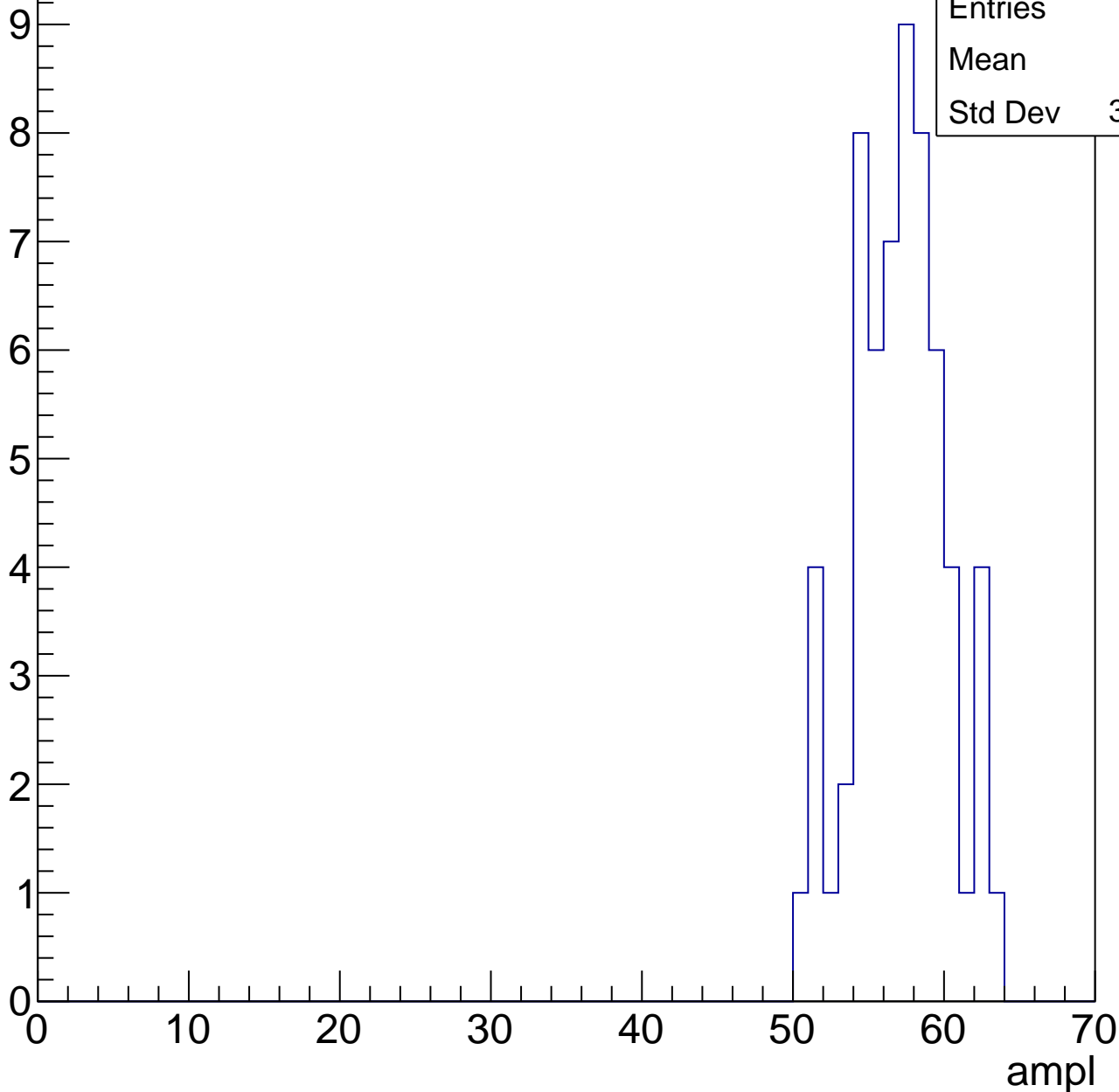


B1L003S, U13-ch9, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	56.6
Std Dev	3.013

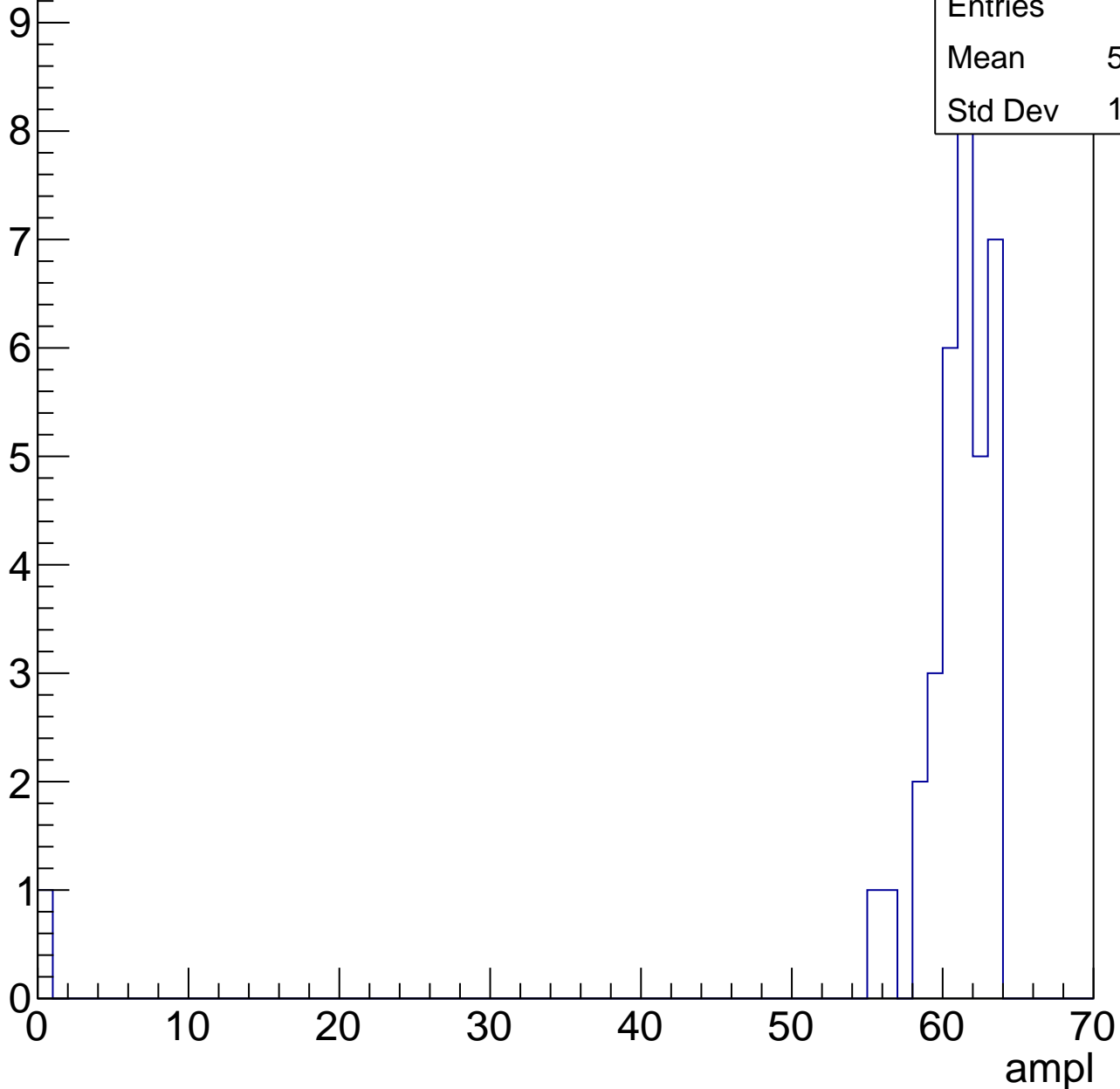


B1L003S, U13-ch9, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	58.97
Std Dev	10.29



B1L003S, U13-ch9, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch9, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L003S, U13-ch10, adc0

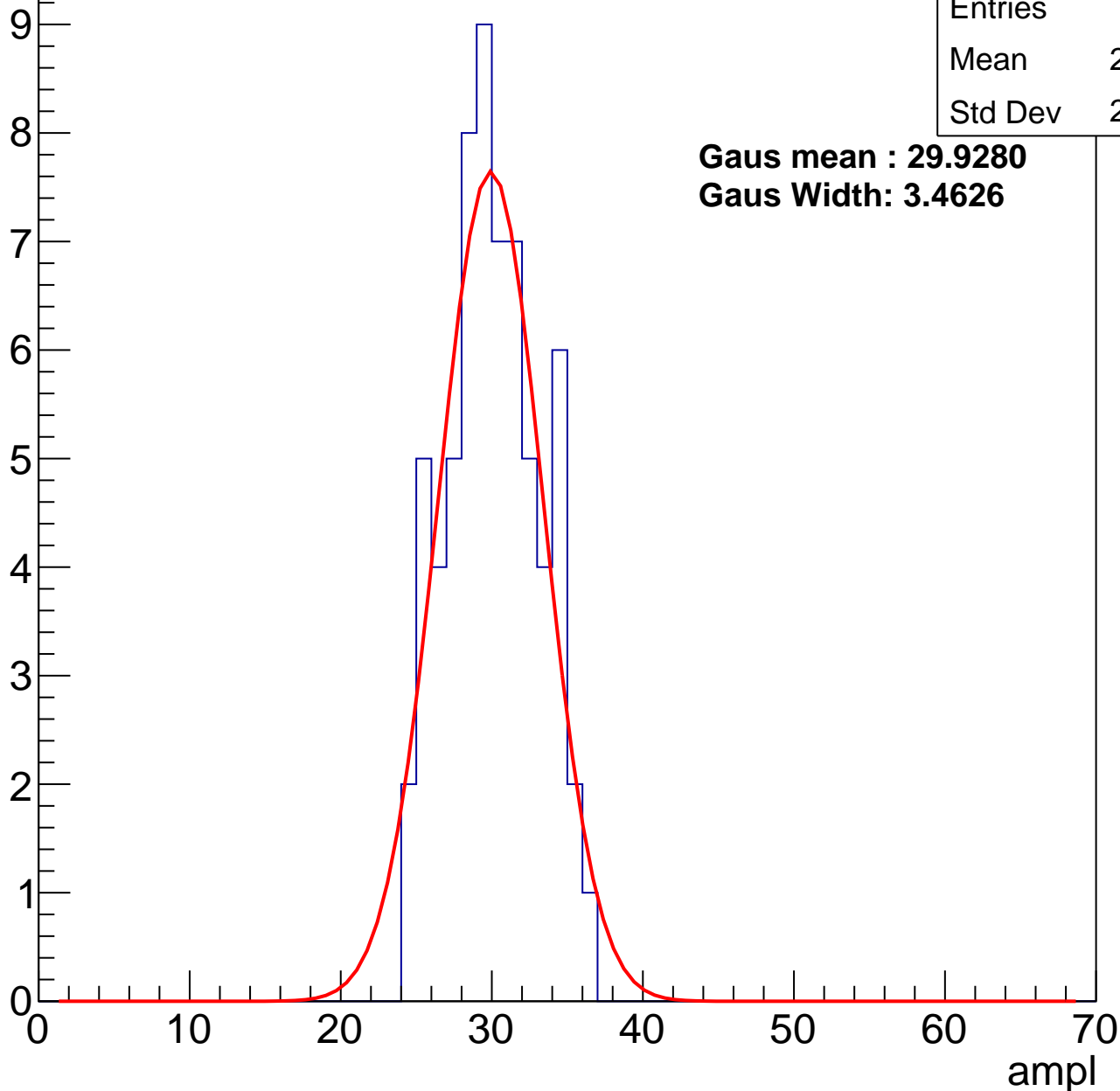
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.63
Std Dev	2.995

Gaus mean : 29.9280

Gaus Width: 3.4626



B1L003S, U13-ch10, adc1

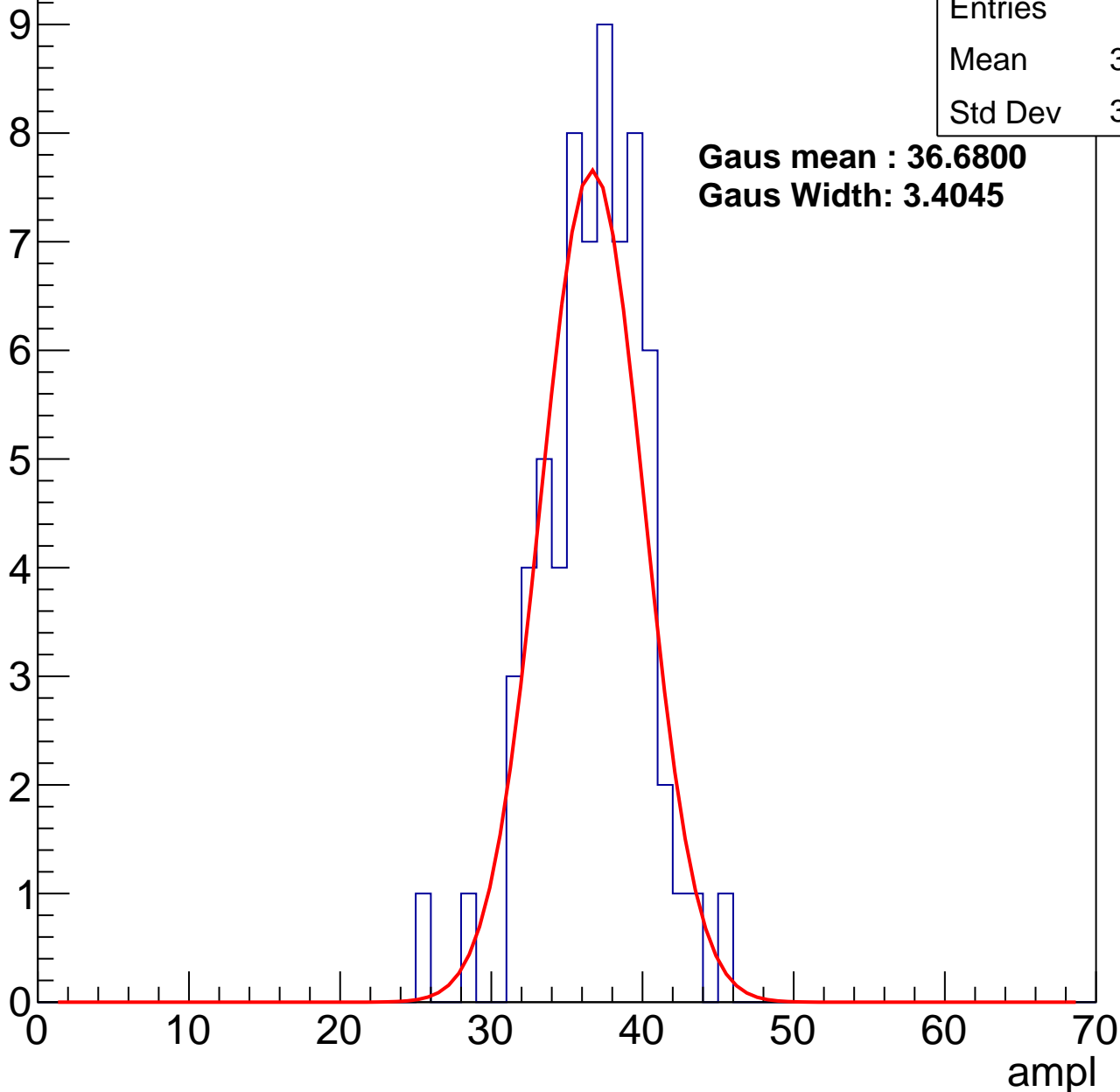
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.32
Std Dev	3.445

Gaus mean : 36.6800

Gaus Width: 3.4045



B1L003S, U13-ch10, adc2

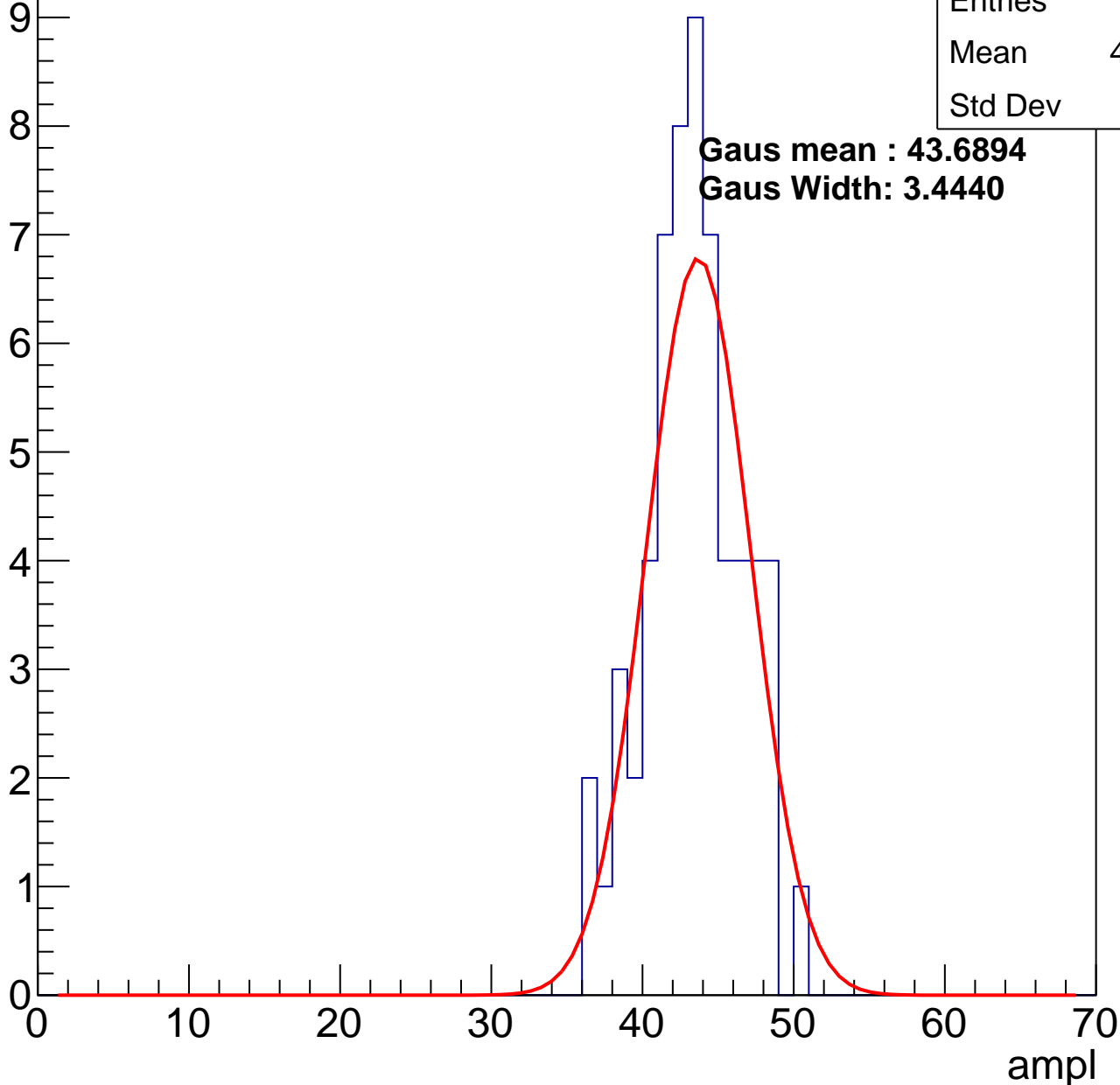
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.88
Std Dev	3.12

Gaus mean : 43.6894

Gaus Width: 3.4440

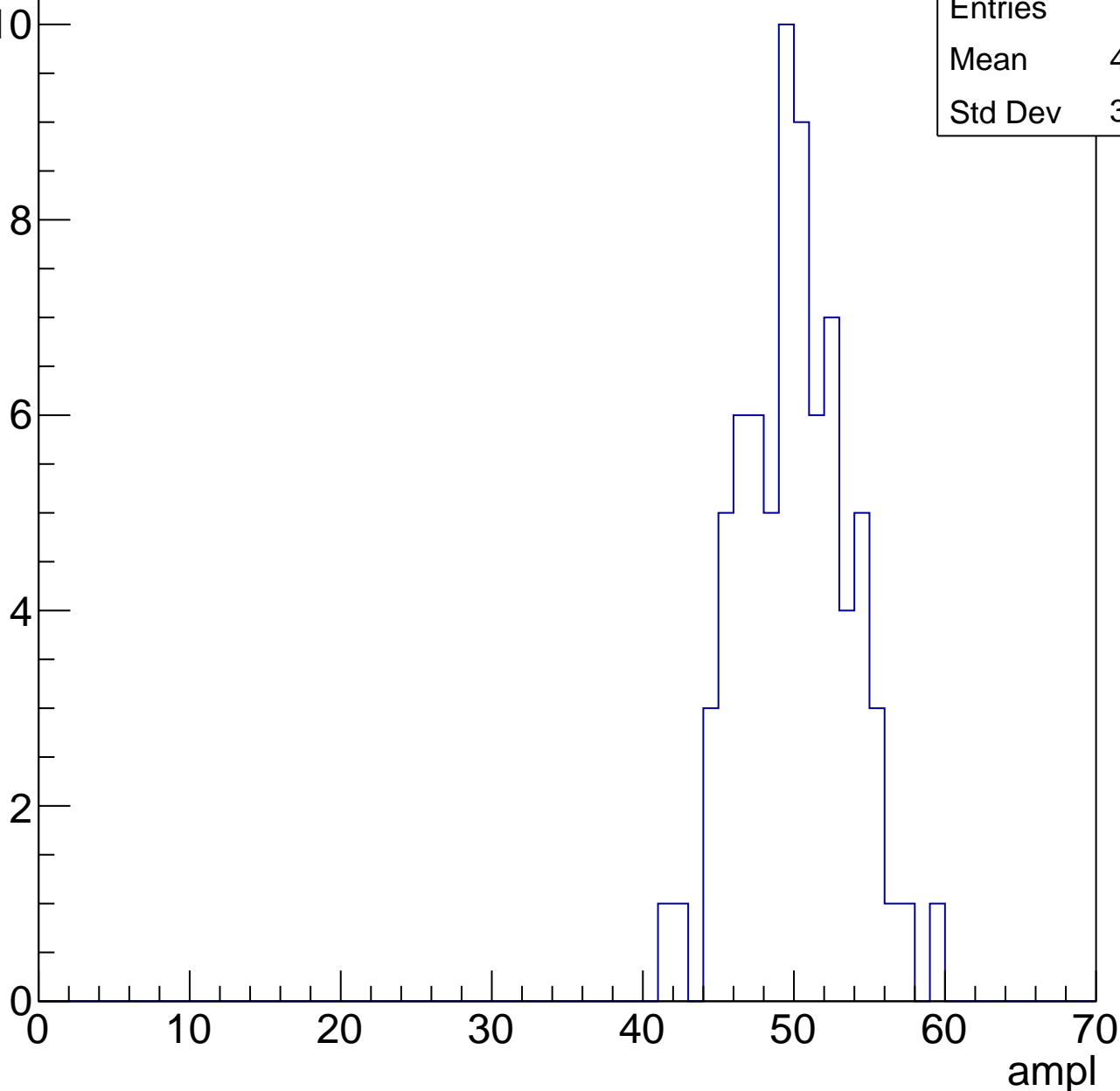


B1L003S, U13-ch10, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.55
Std Dev	3.553

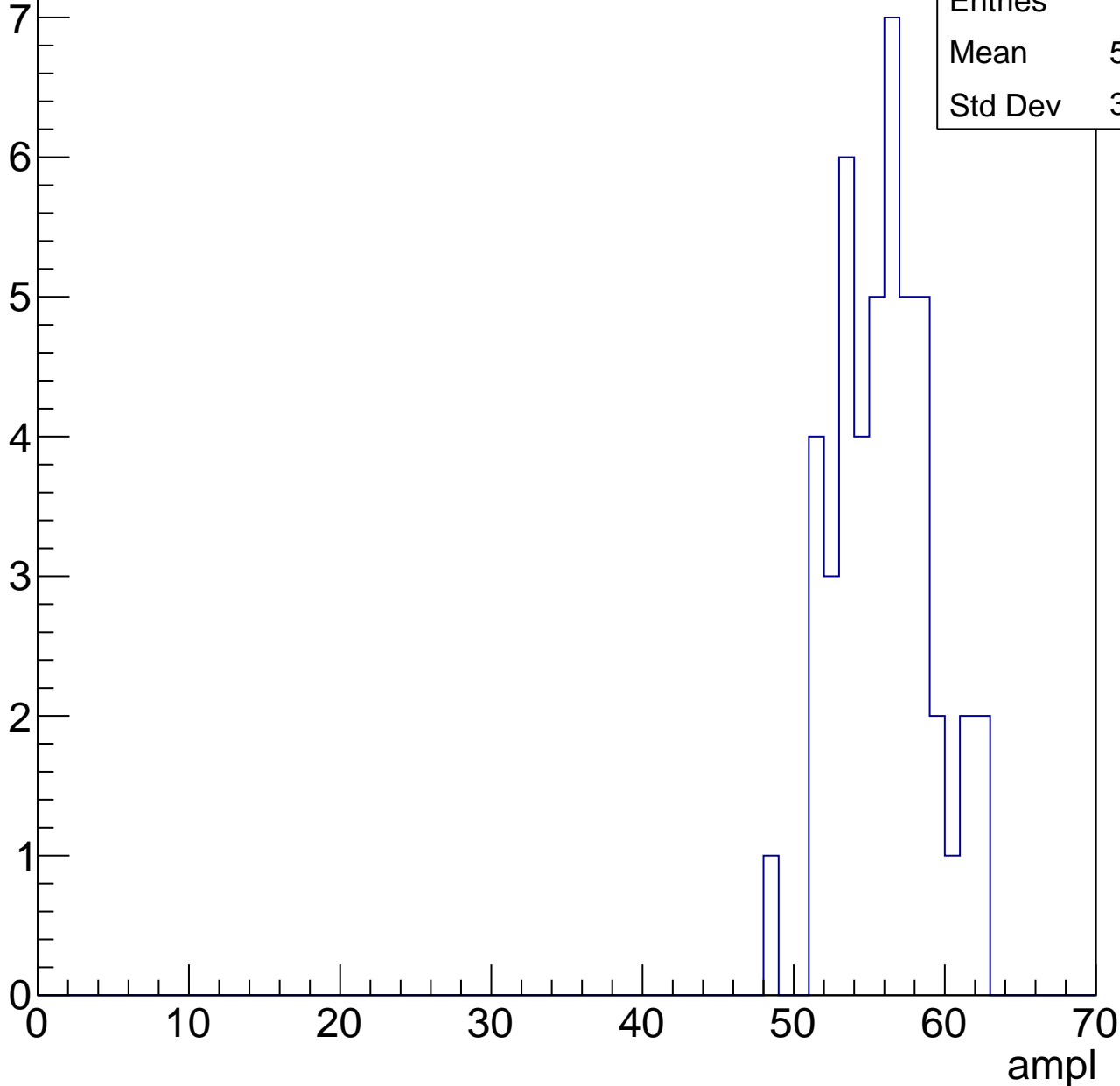


B1L003S, U13-ch10, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

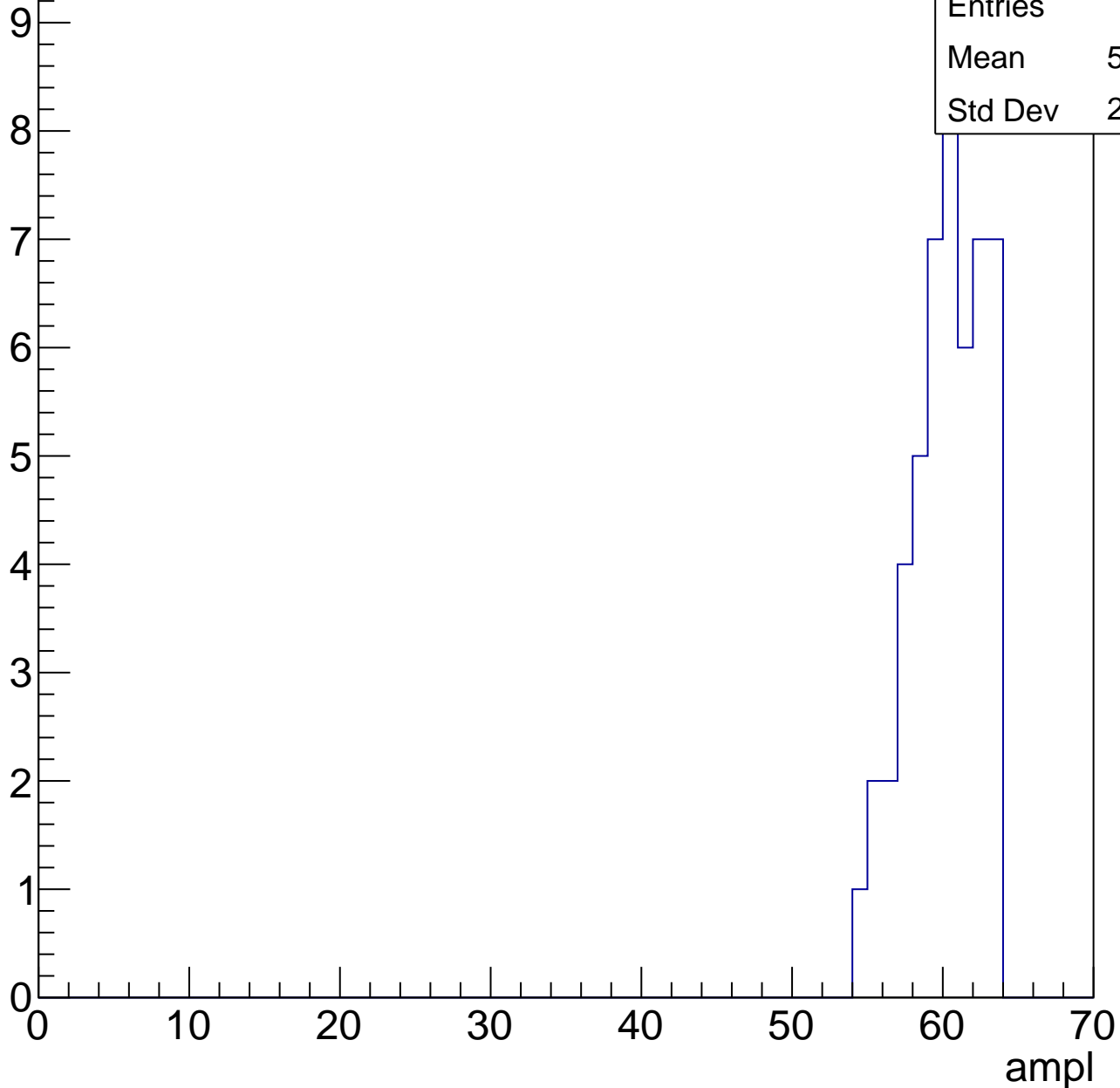
Entries	47
Mean	55.49
Std Dev	3.114



B1L003S, U13-ch10, adc5

calib_packv5_042523_0143.root, FC#13, port D2

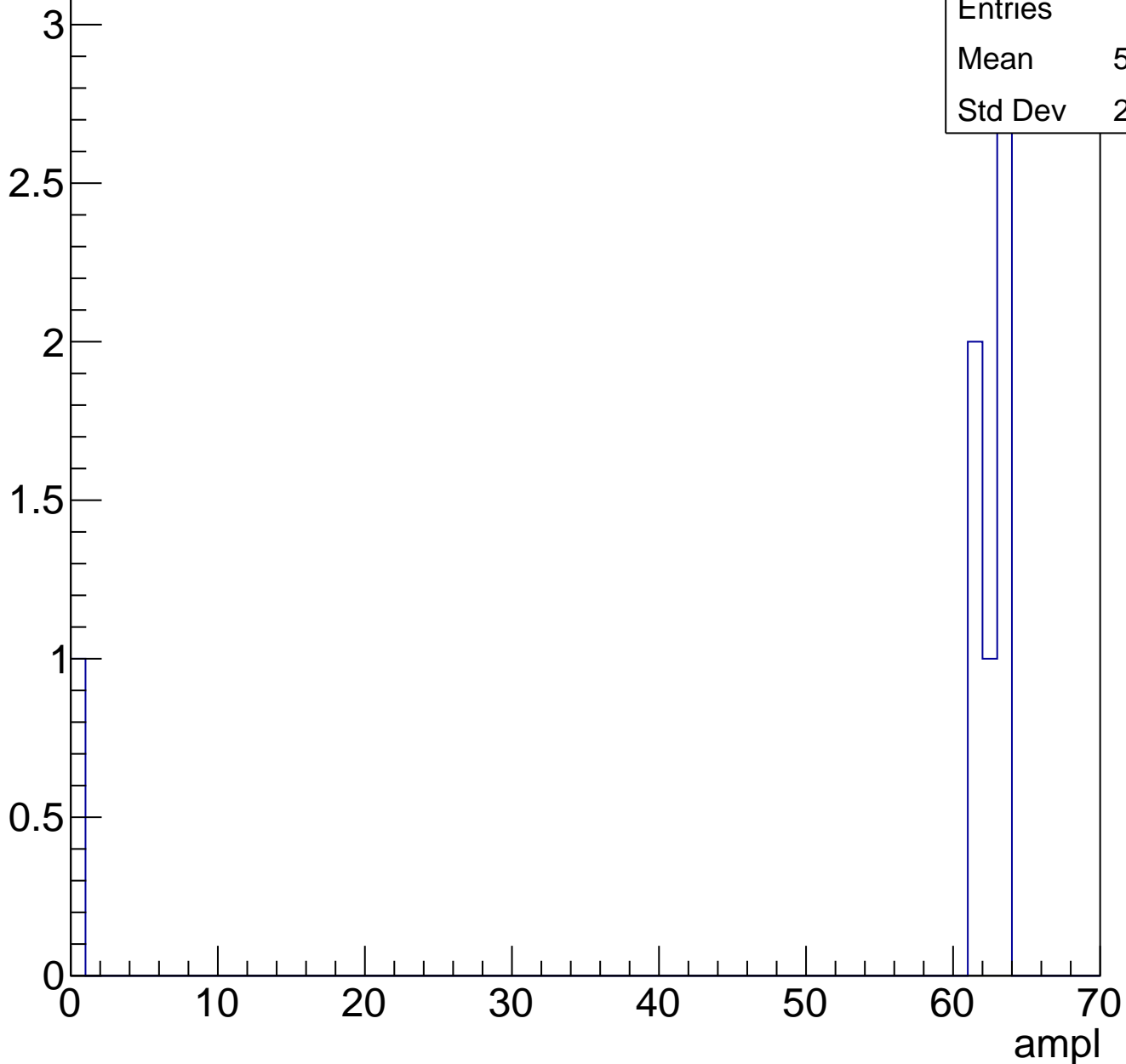
Entry



B1L003S, U13-ch10, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch10, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch11, adc0

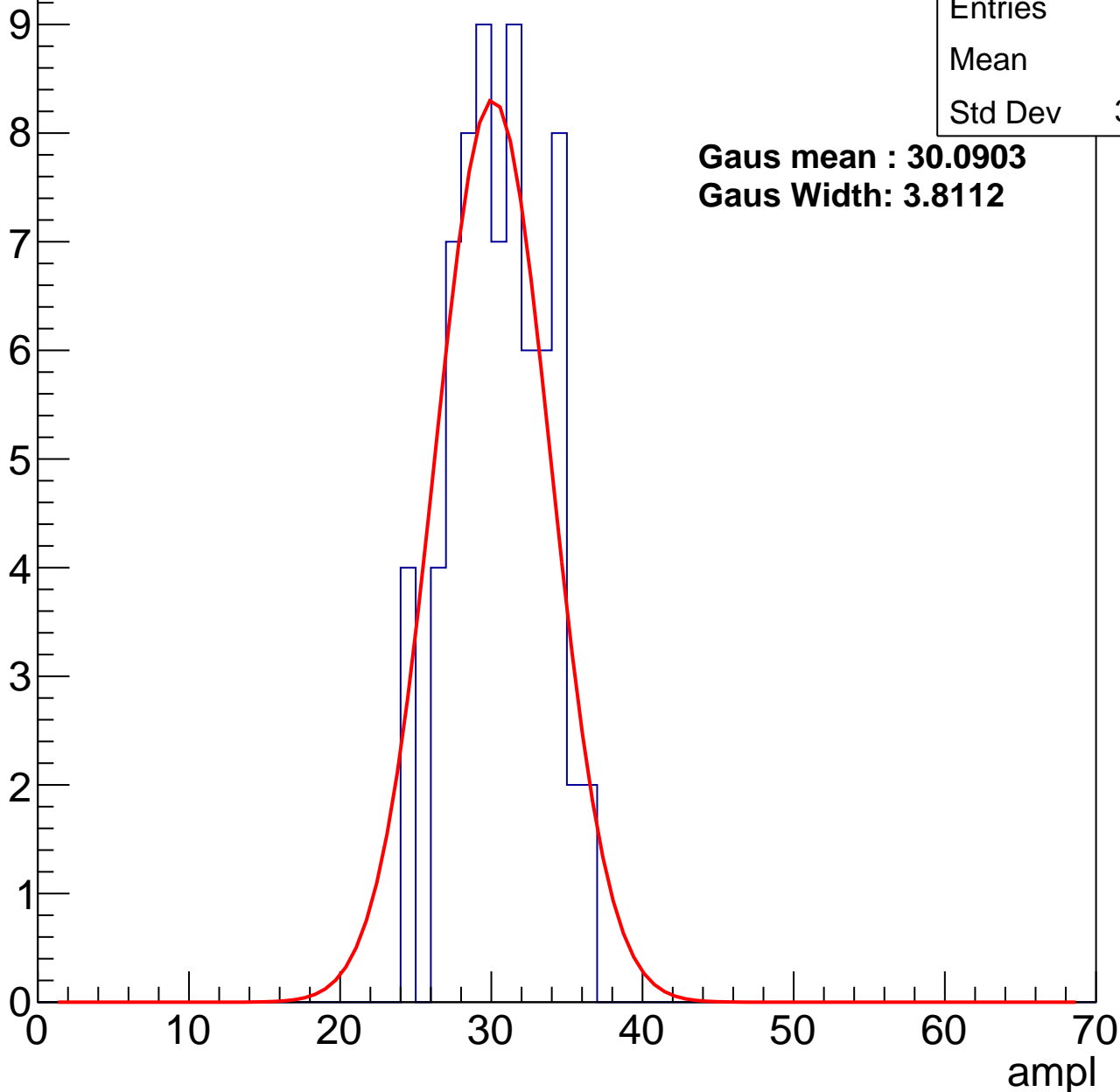
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.1
Std Dev	3.001

Gaus mean : 30.0903

Gaus Width: 3.8112



B1L003S, U13-ch11, adc1

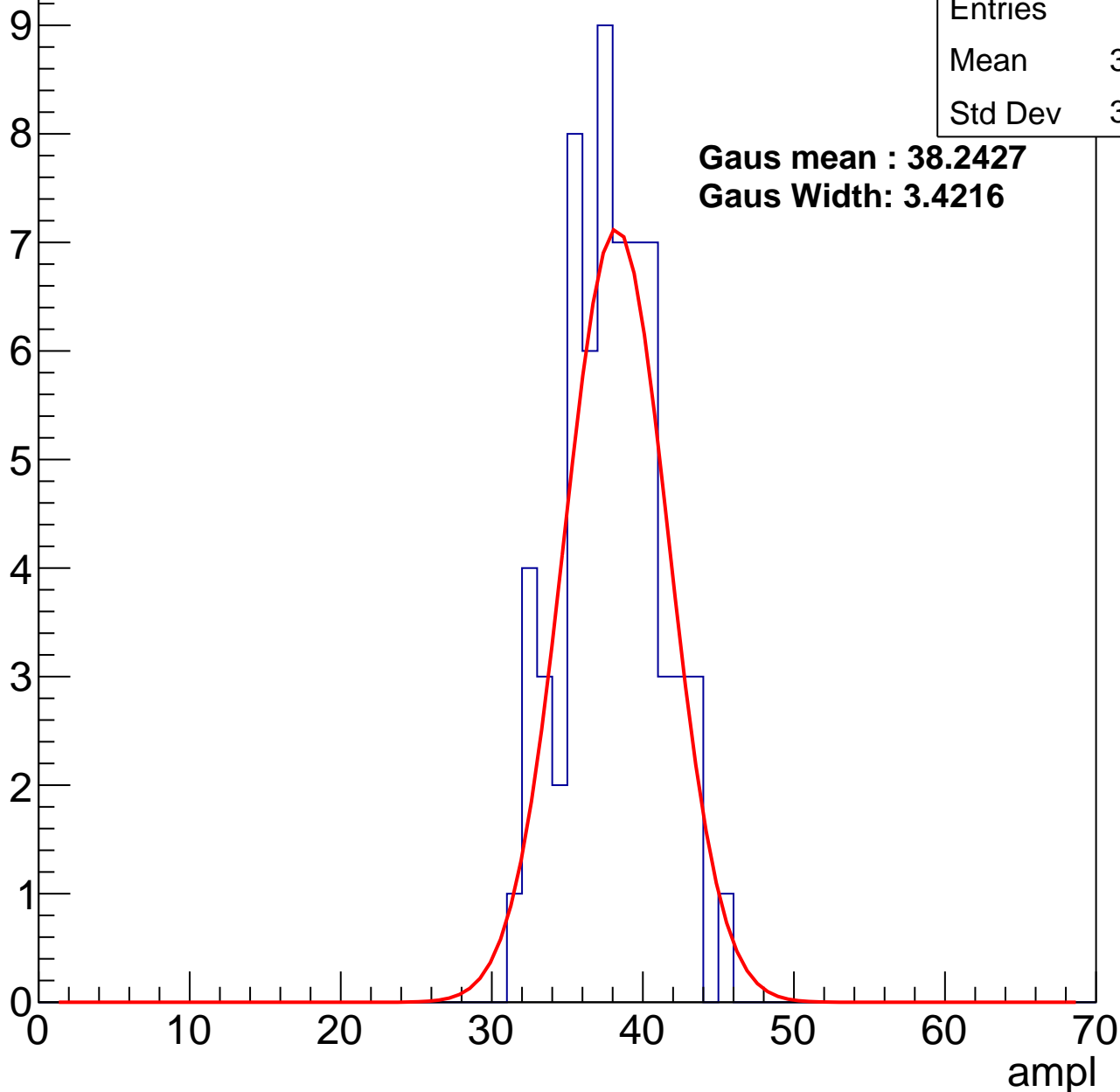
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.45
Std Dev	3.112

Gaus mean : 38.2427

Gaus Width: 3.4216



B1L003S, U13-ch11, adc2

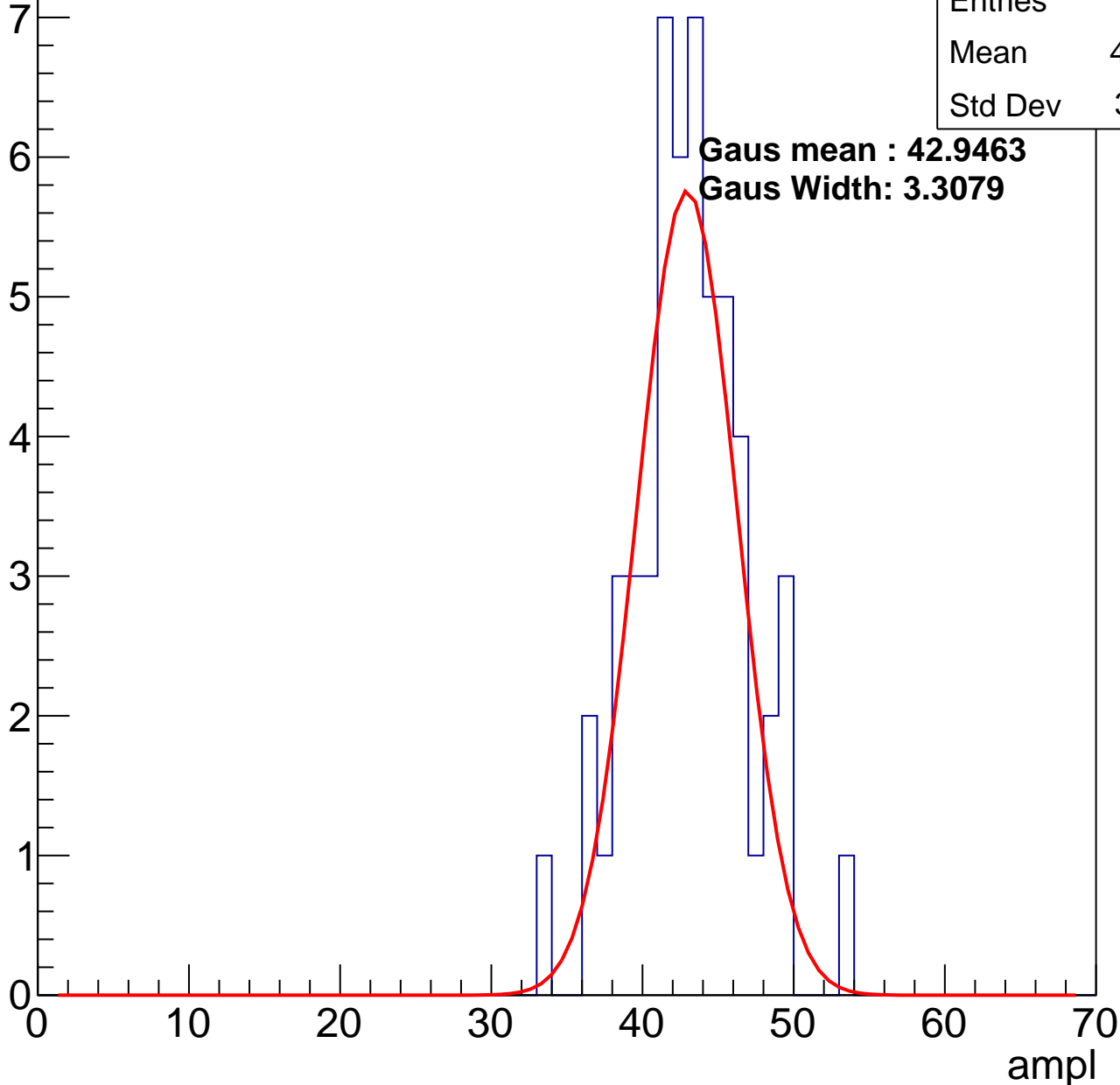
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.69
Std Dev	3.721

Gaus mean : 42.9463

Gaus Width: 3.3079

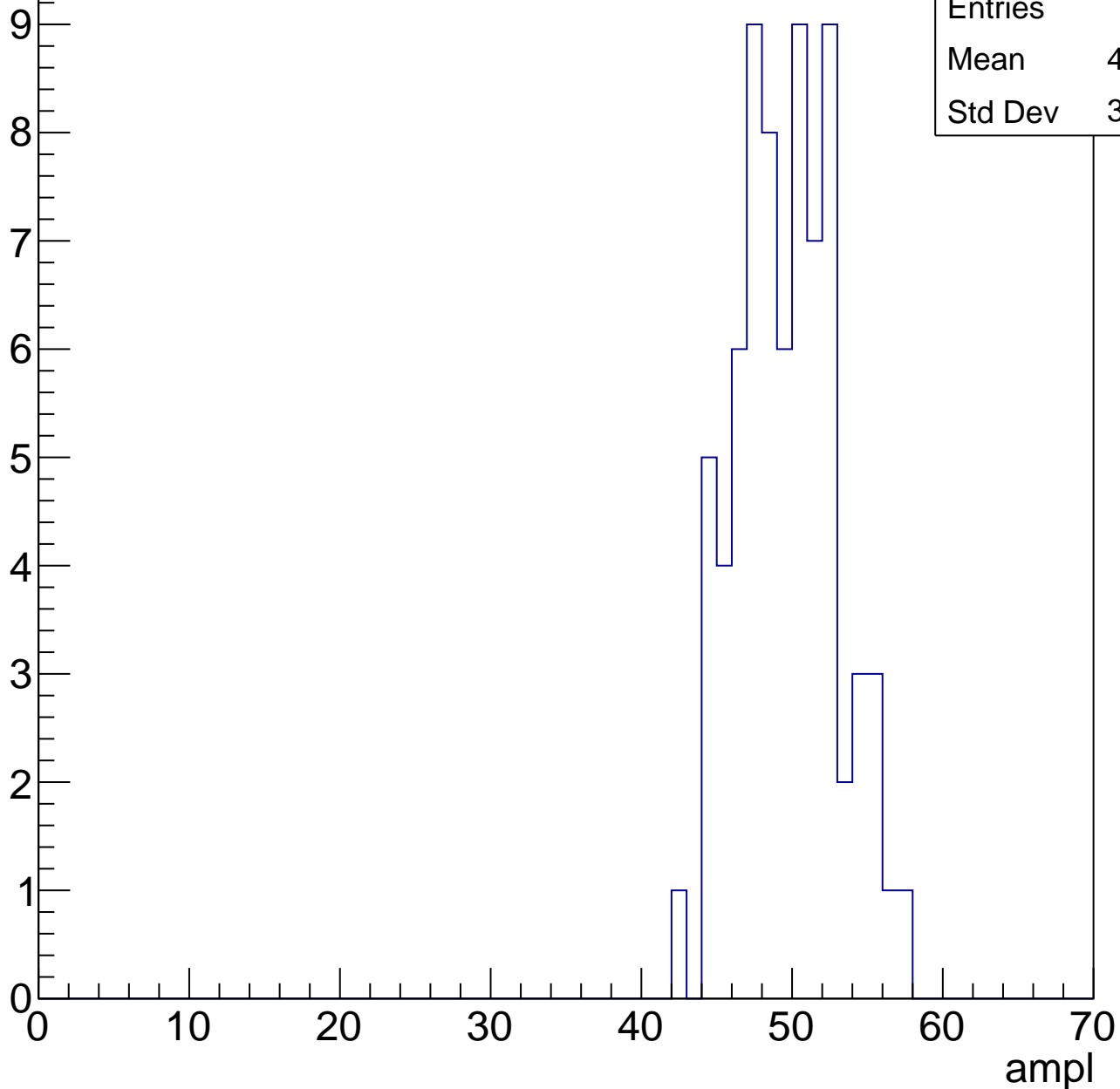


B1L003S, U13-ch11, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.19
Std Dev	3.245



B1L003S, U13-ch11, adc4

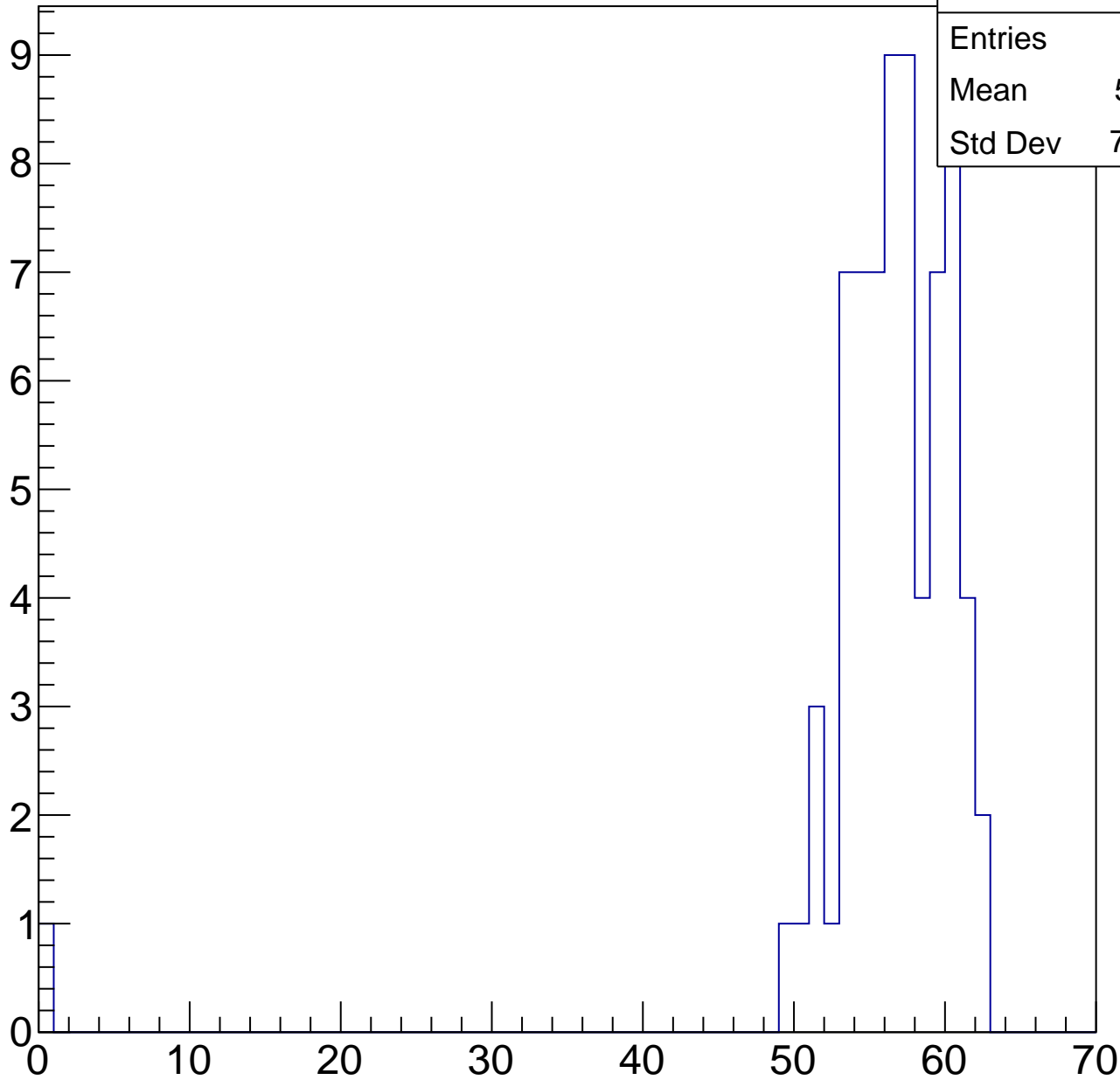
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	55.61
Std Dev	7.303

ampl

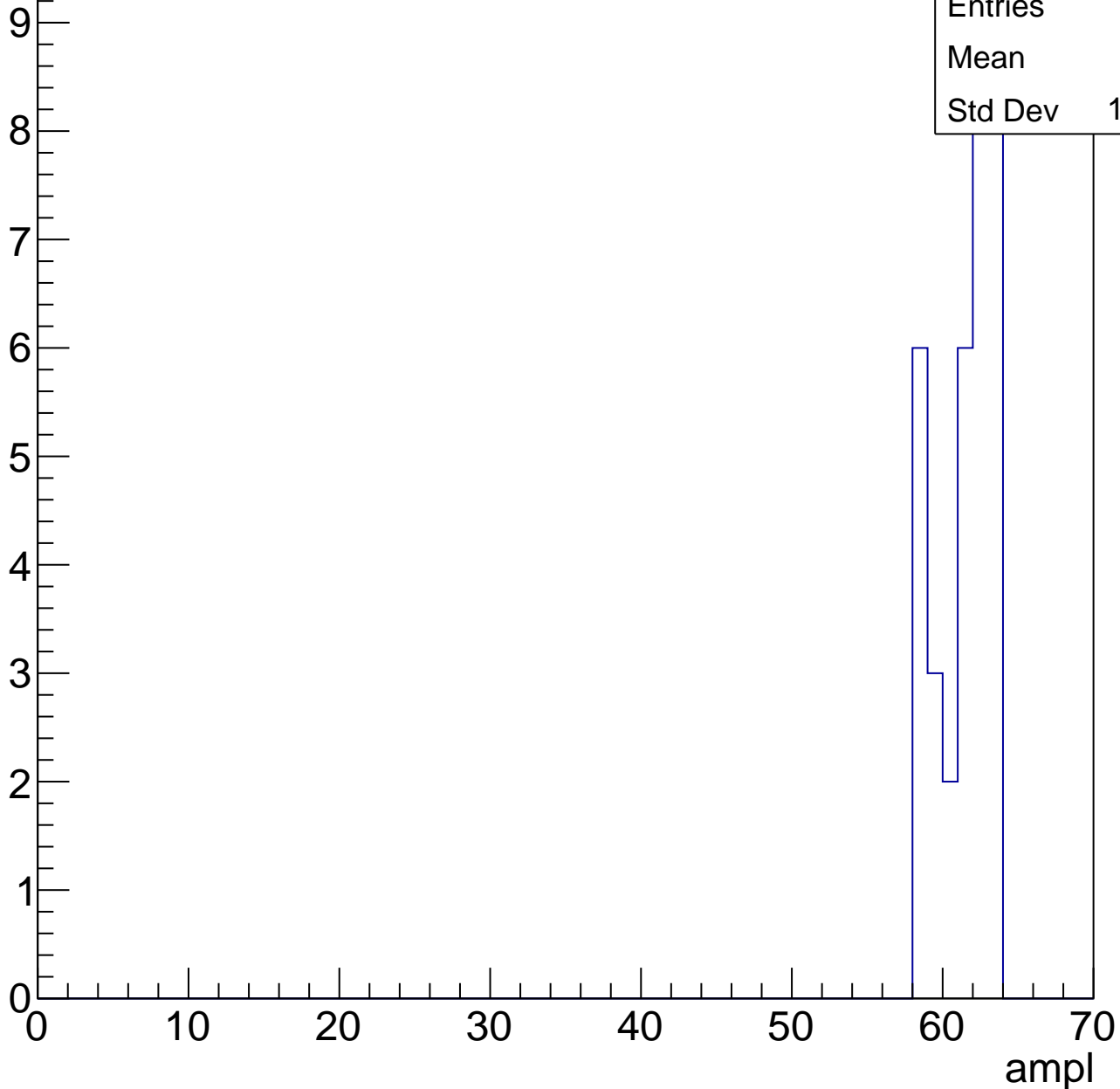


B1L003S, U13-ch11, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	61
Std Dev	1.815



B1L003S, U13-ch11, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch11, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L003S, U13-ch12, adc0

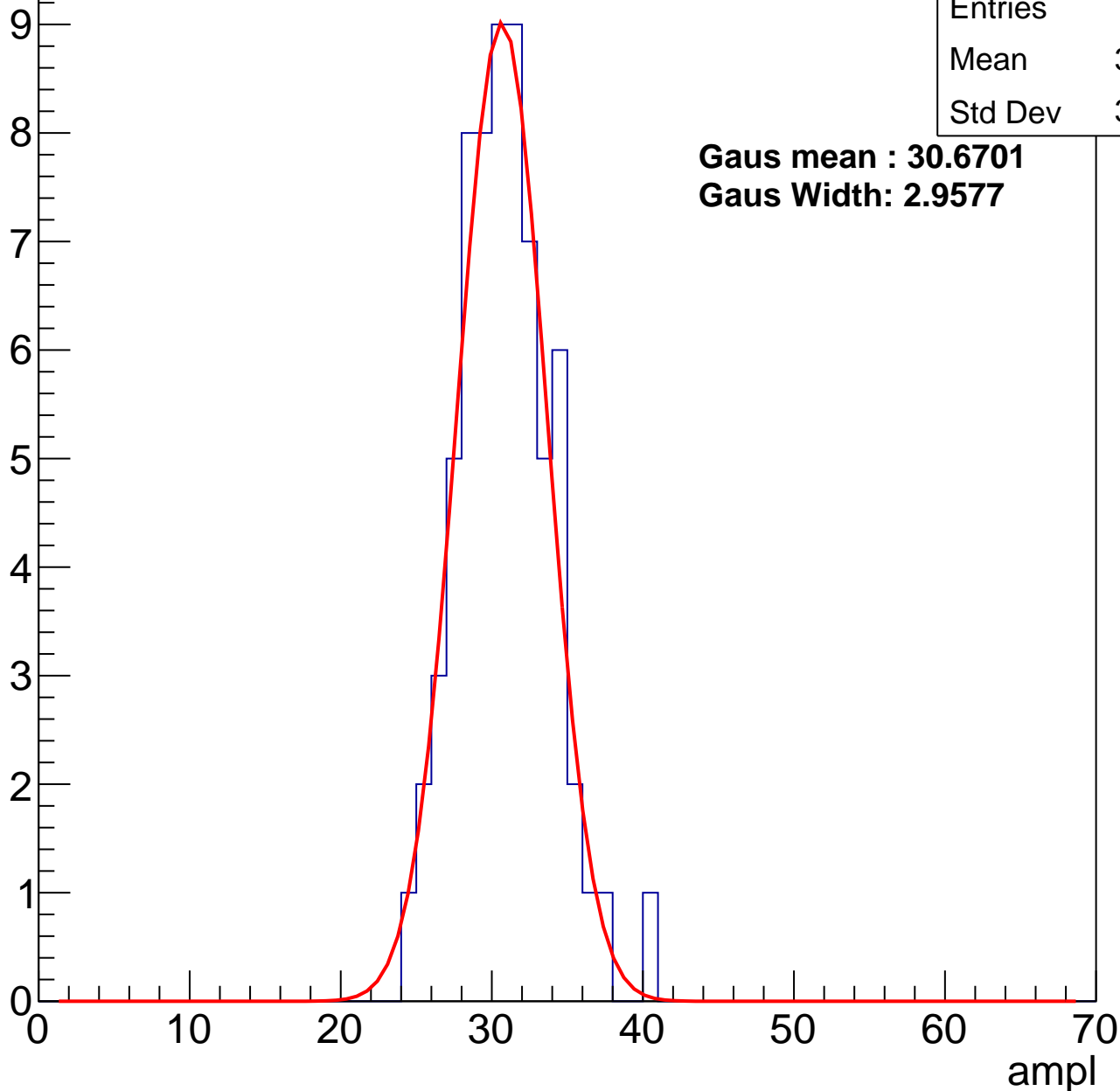
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.41
Std Dev	3.021

Gaus mean : 30.6701

Gaus Width: 2.9577



B1L003S, U13-ch12, adc1

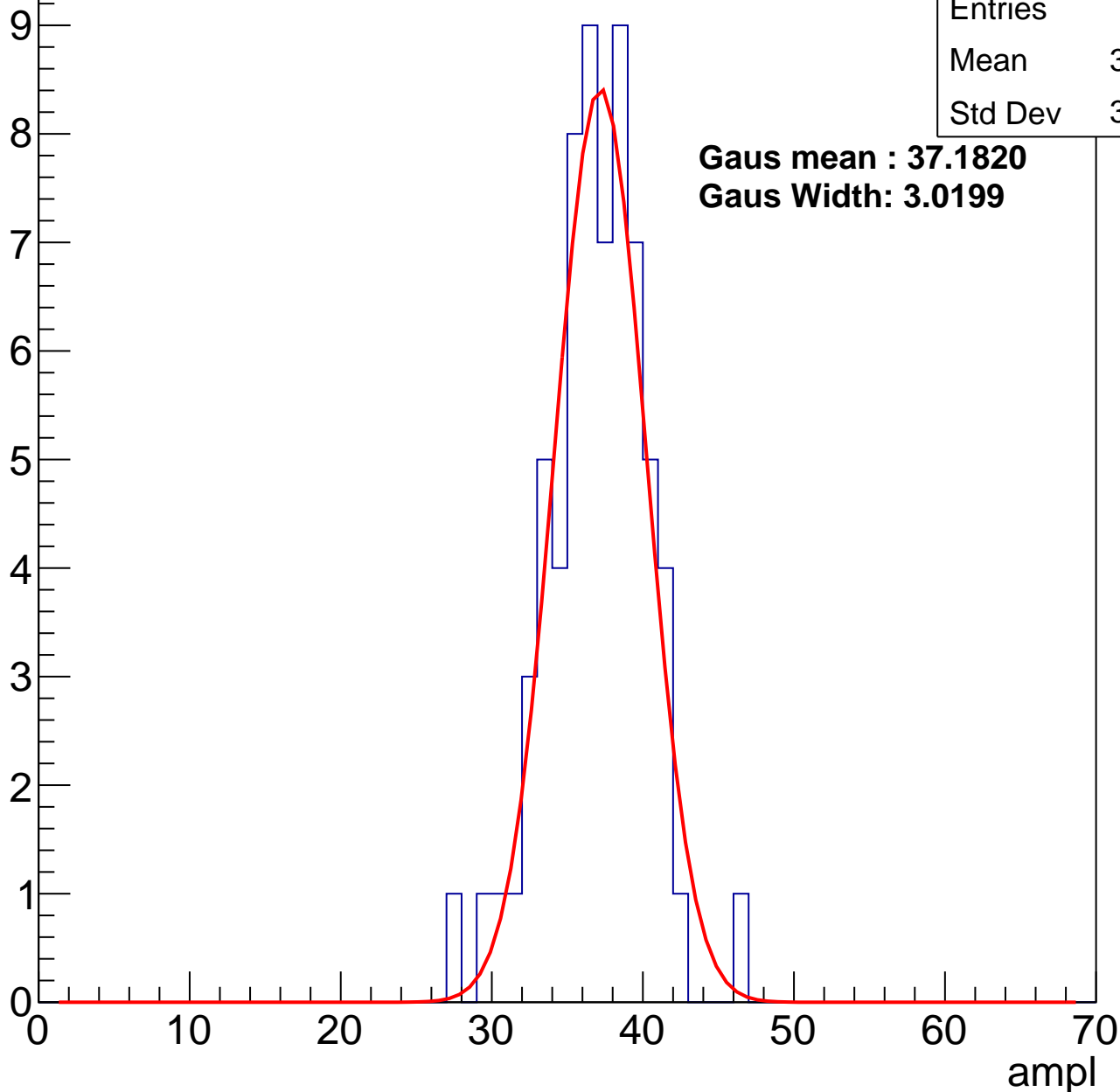
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.48
Std Dev	3.266

Gaus mean : 37.1820

Gaus Width: 3.0199



B1L003S, U13-ch12, adc2

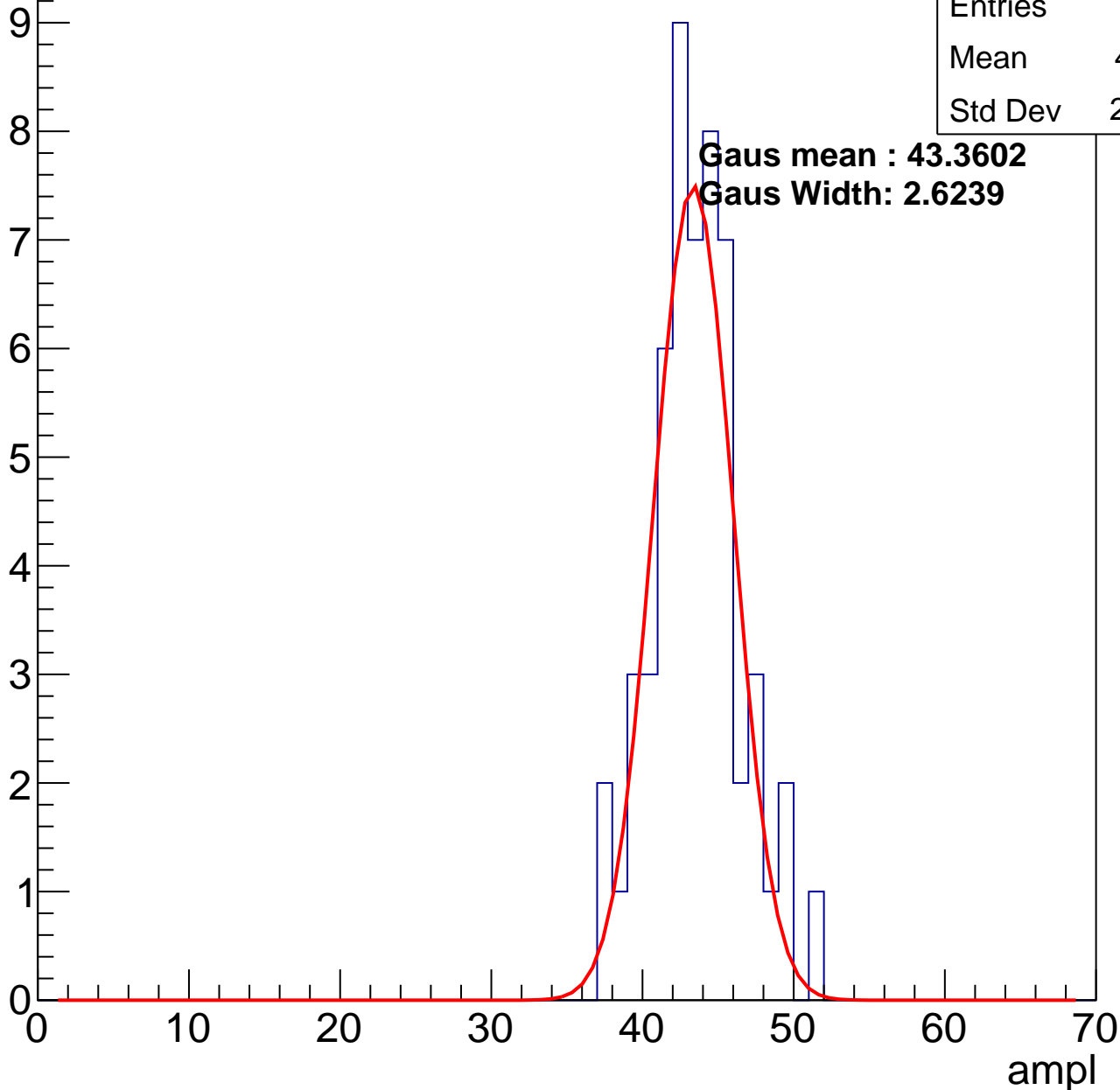
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.11
Std Dev	2.915

Gaus mean : 43.3602

Gaus Width: 2.6239

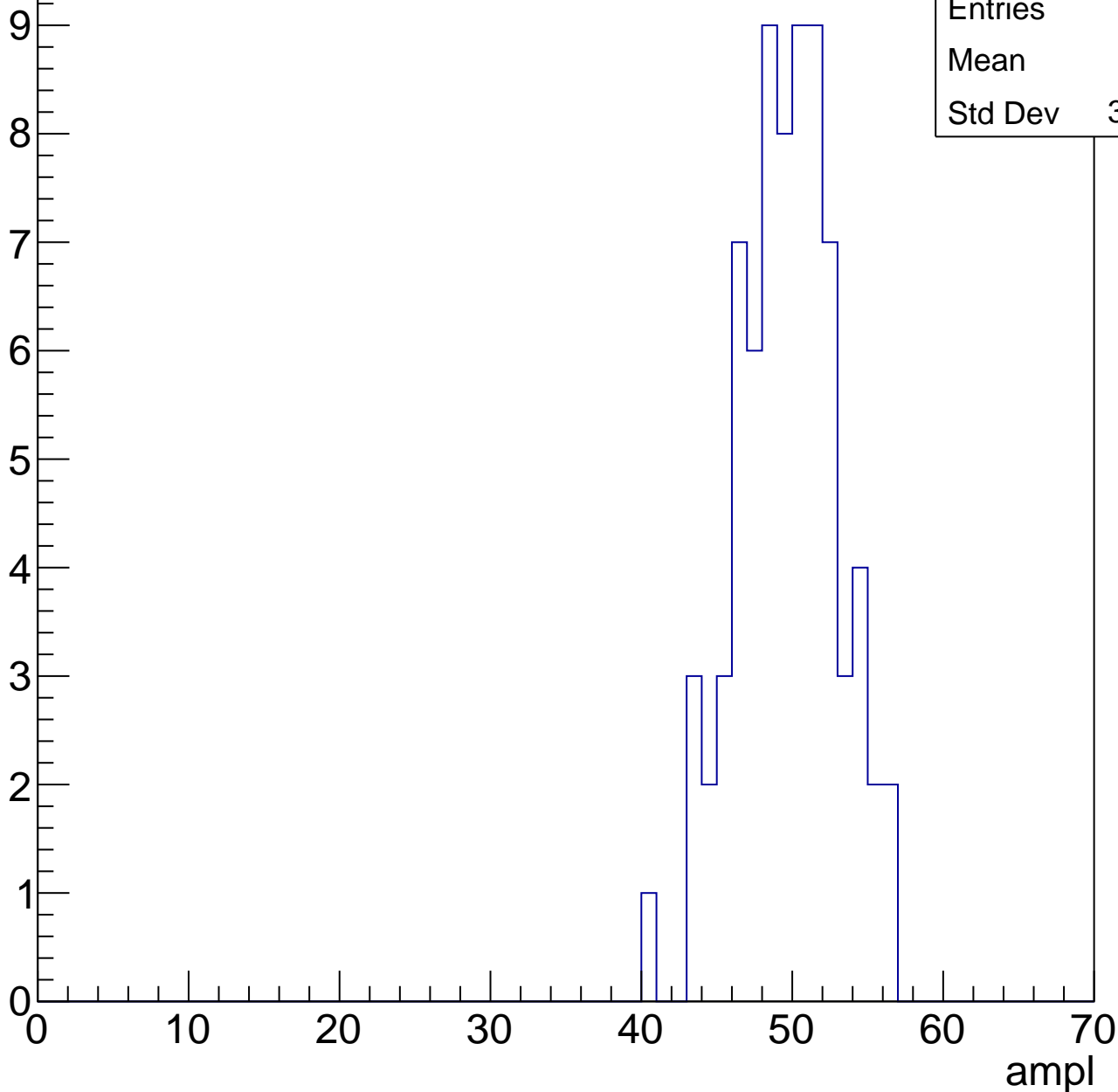


B1L003S, U13-ch12, adc3

calib_packv5_042523_0143.root, FC#13, port D2

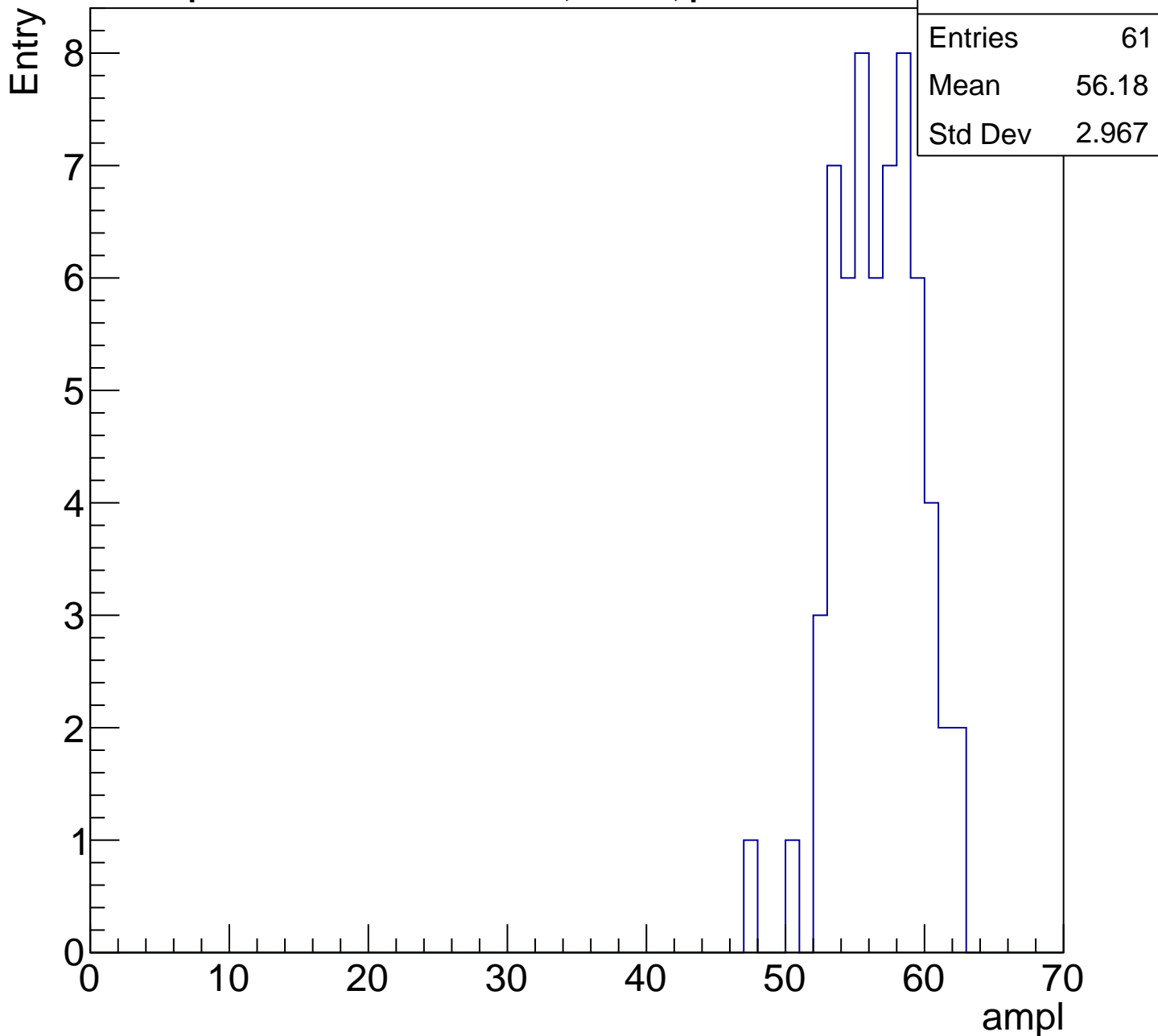
Entry

Entries	75
Mean	49.2
Std Dev	3.278



B1L003S, U13-ch12, adc4

calib_packv5_042523_0143.root, FC#13, port D2

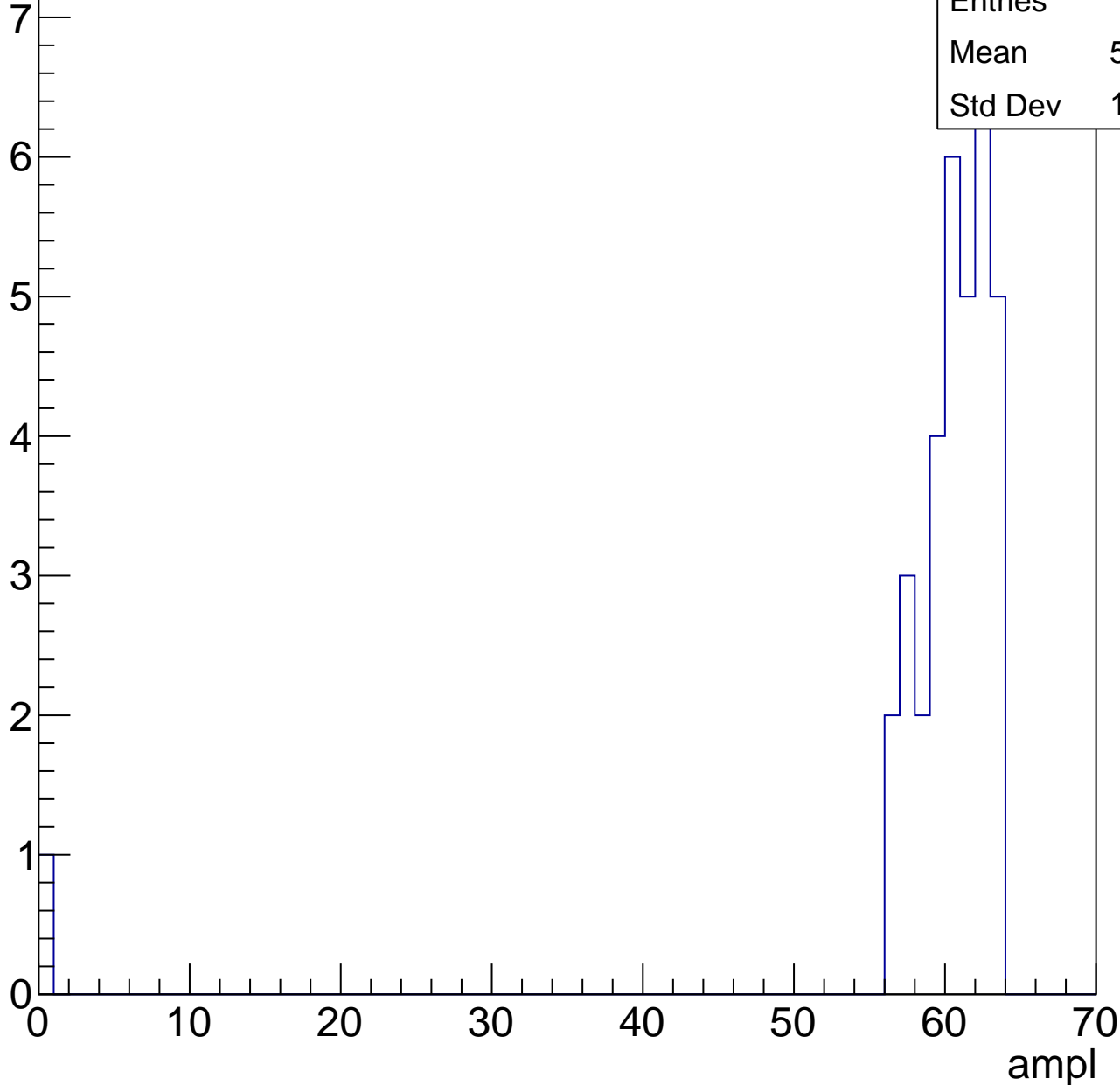


B1L003S, U13-ch12, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

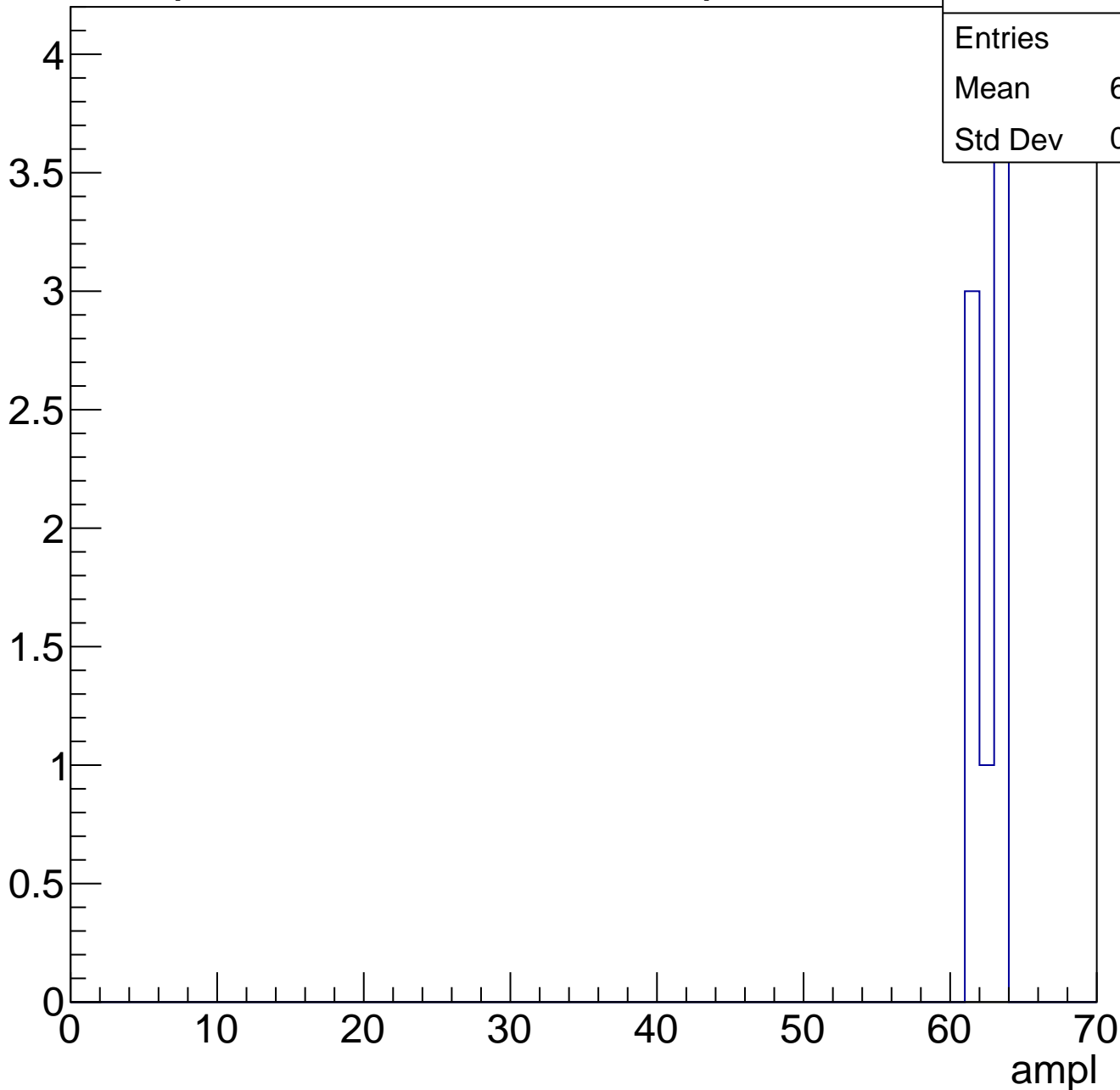
Entries	35
Mean	58.54
Std Dev	10.25



B1L003S, U13-ch12, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch12, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch13, adc0

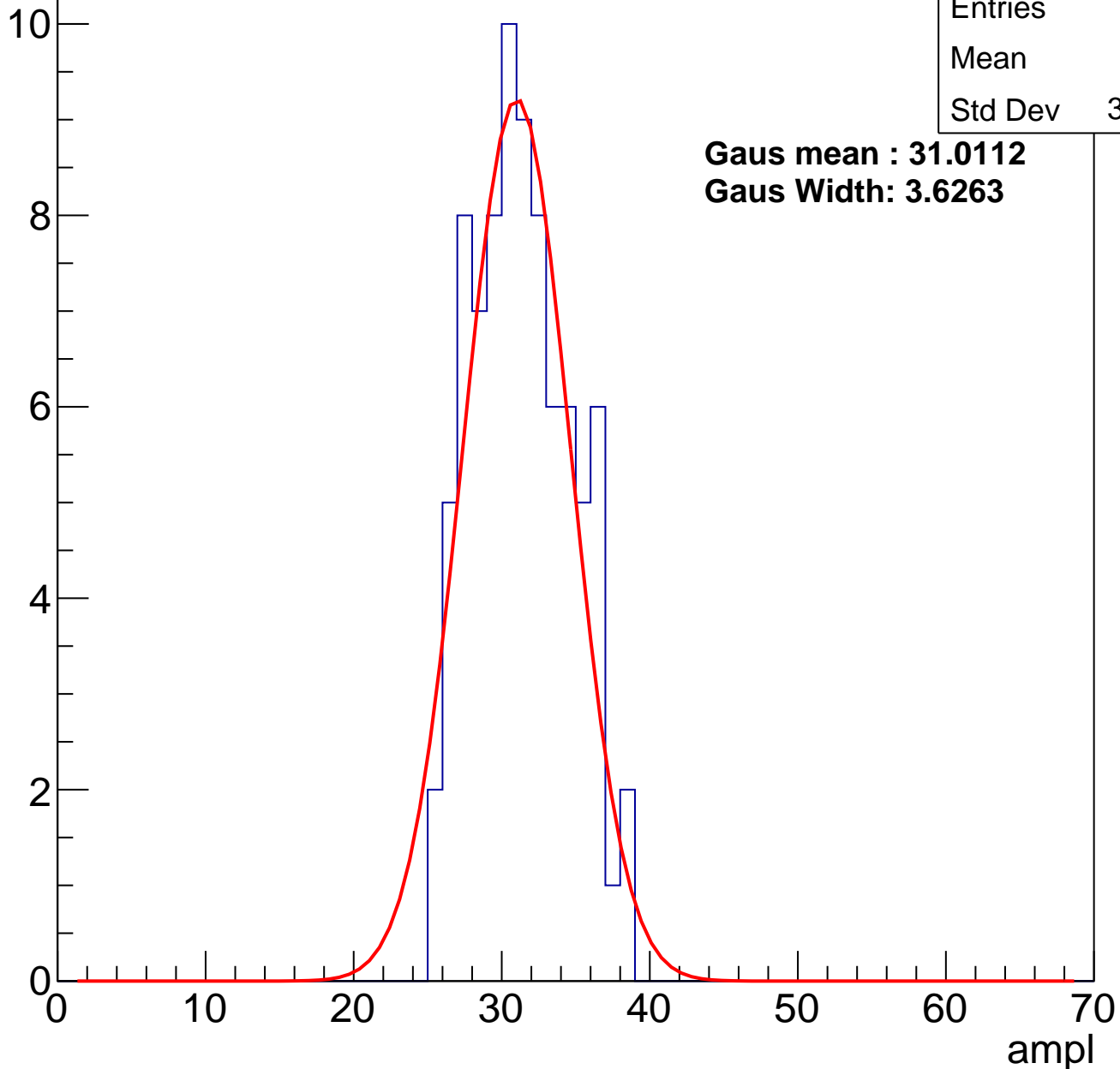
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	30.9
Std Dev	3.255

Gaus mean : 31.0112

Gaus Width: 3.6263

Entry



B1L003S, U13-ch13, adc1

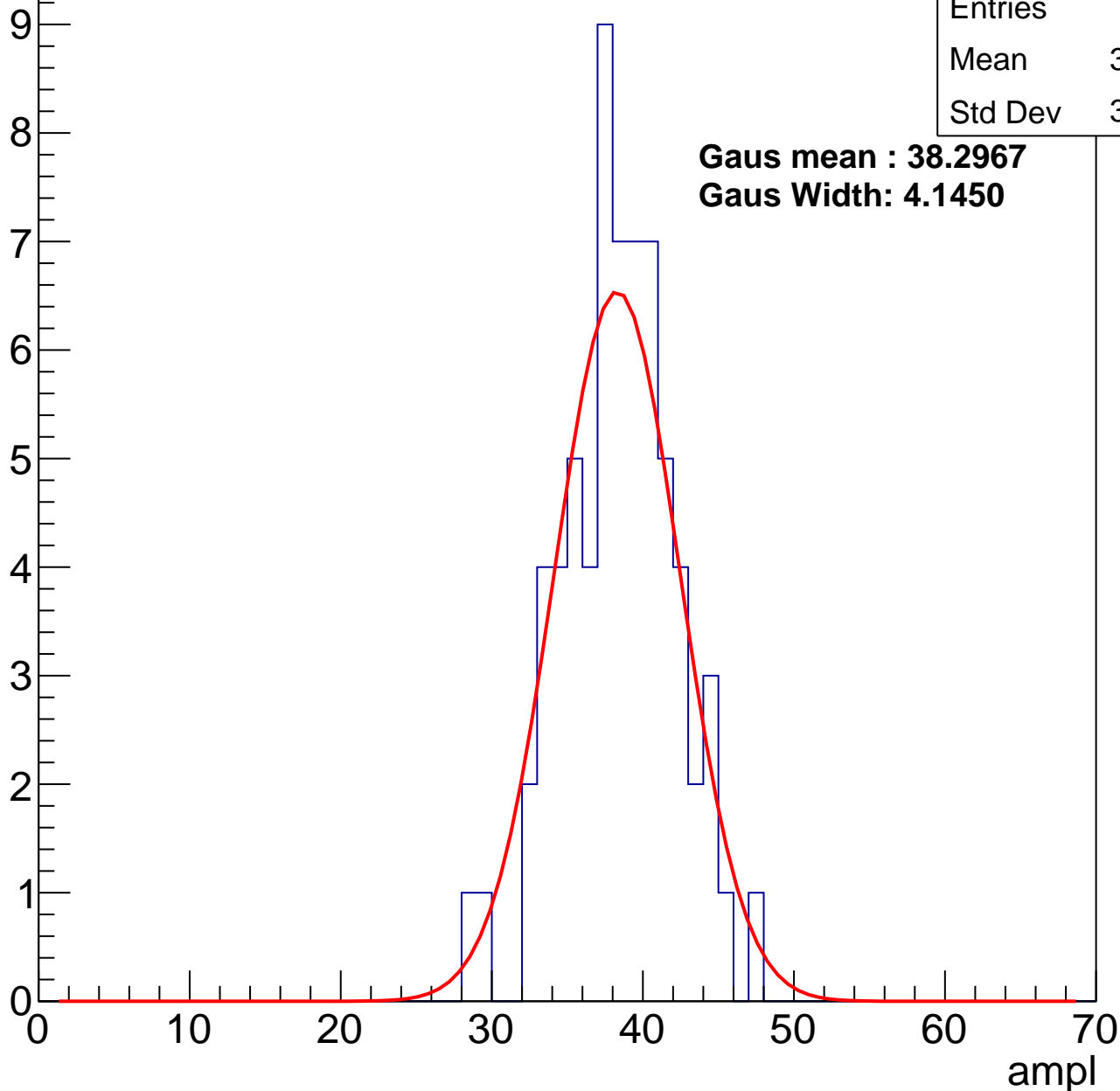
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.96
Std Dev	3.703

Gaus mean : 38.2967

Gaus Width: 4.1450



B1L003S, U13-ch13, adc2

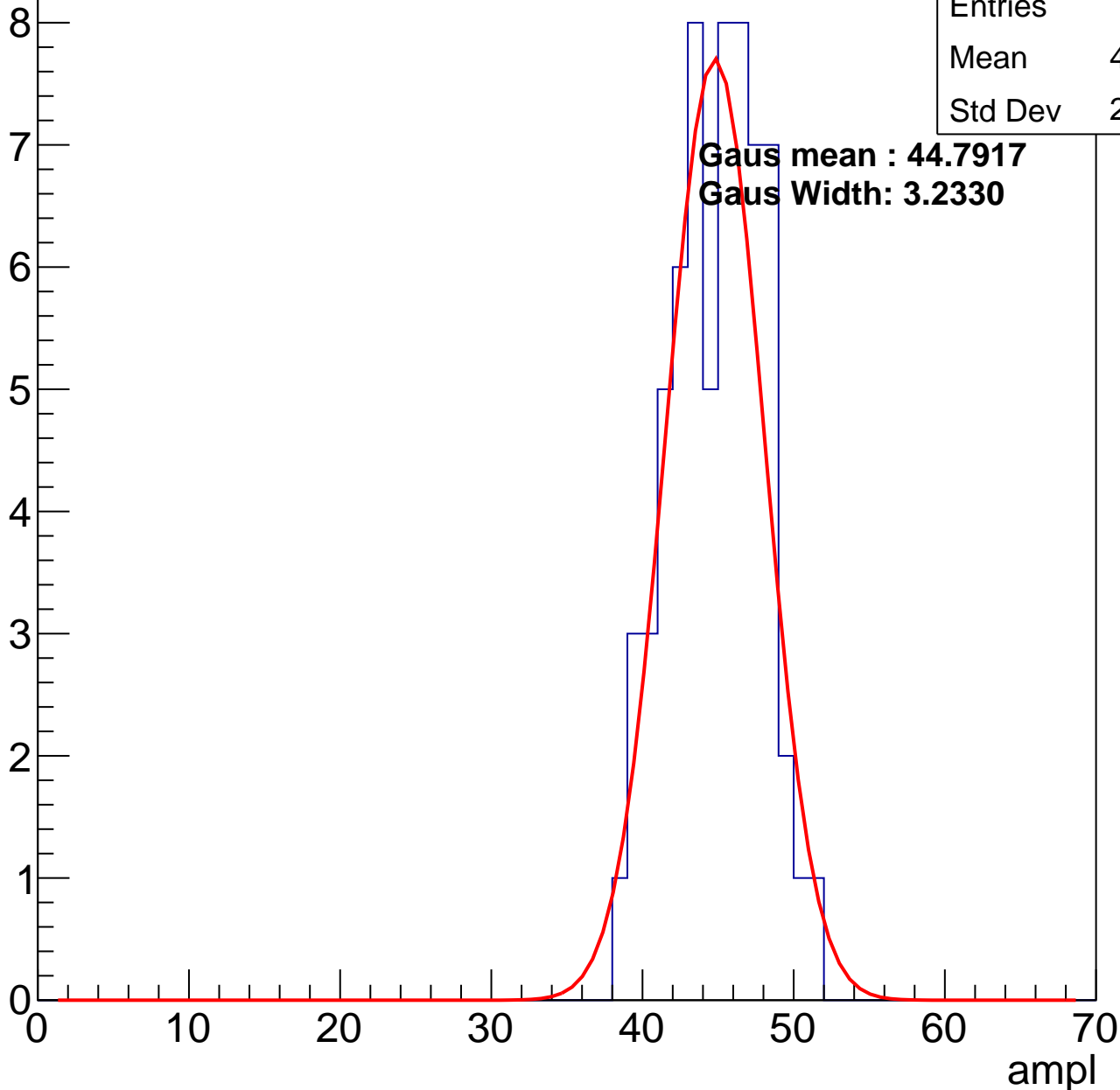
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	44.43
Std Dev	2.972

Gaus mean : 44.7917

Gaus Width: 3.2330

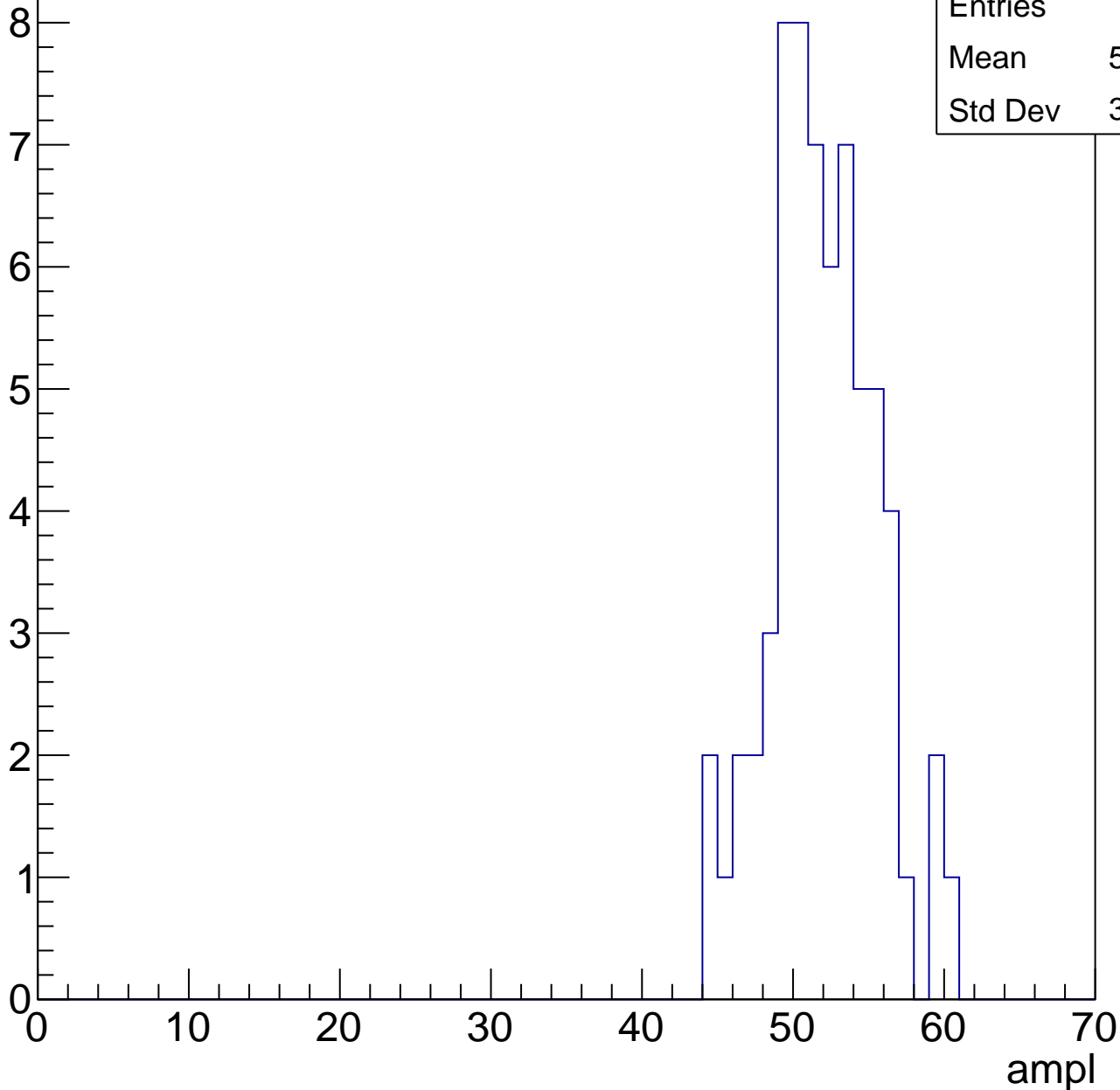


B1L003S, U13-ch13, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

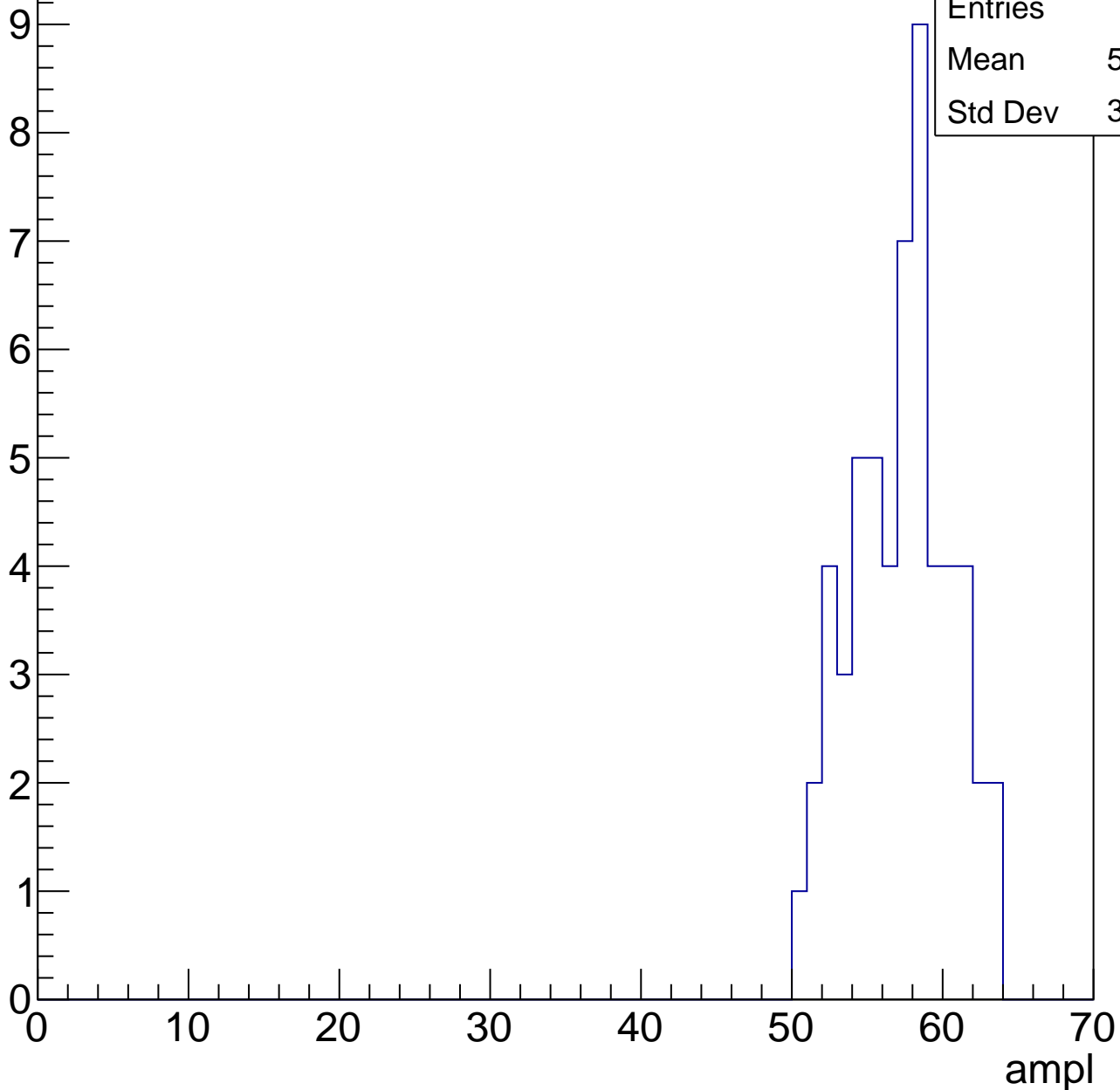
Entries	64
Mean	51.55
Std Dev	3.464



B1L003S, U13-ch13, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

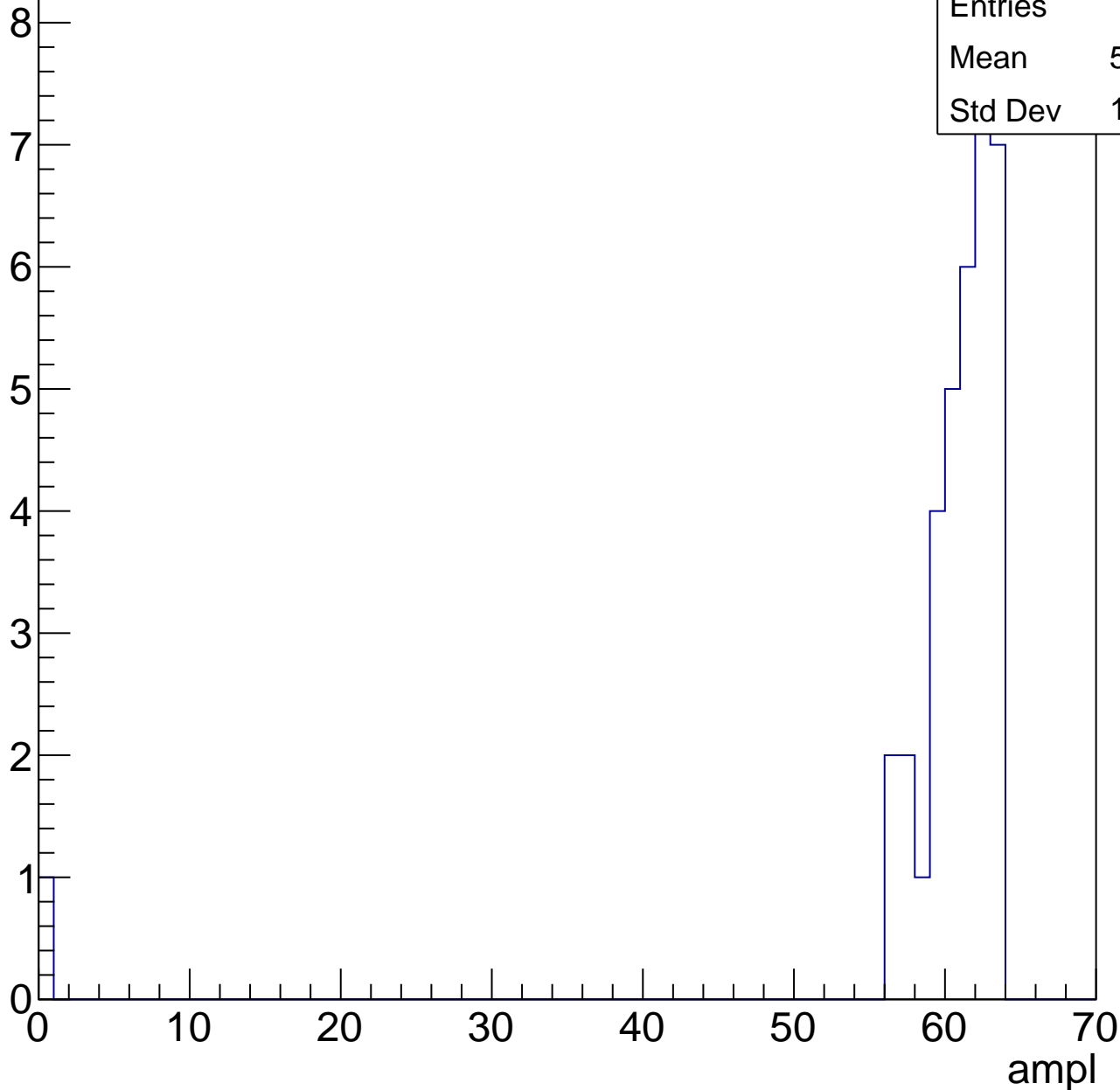


B1L003S, U13-ch13, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	58.97
Std Dev	10.17



B1L003S, U13-ch13, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U13-ch13, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch14, adc0

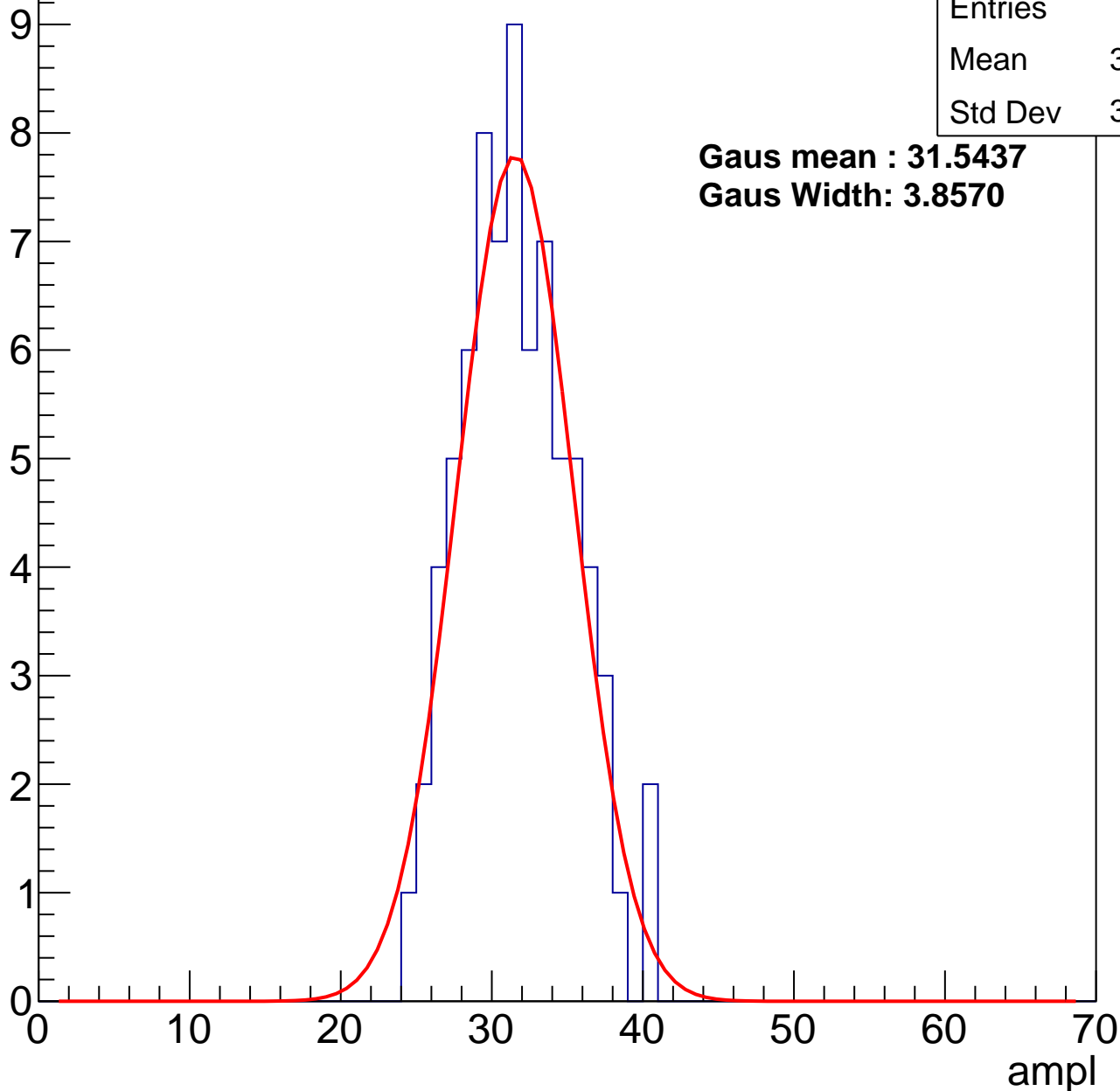
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	31.24
Std Dev	3.592

Gaus mean : 31.5437

Gaus Width: 3.8570



B1L003S, U13-ch14, adc1

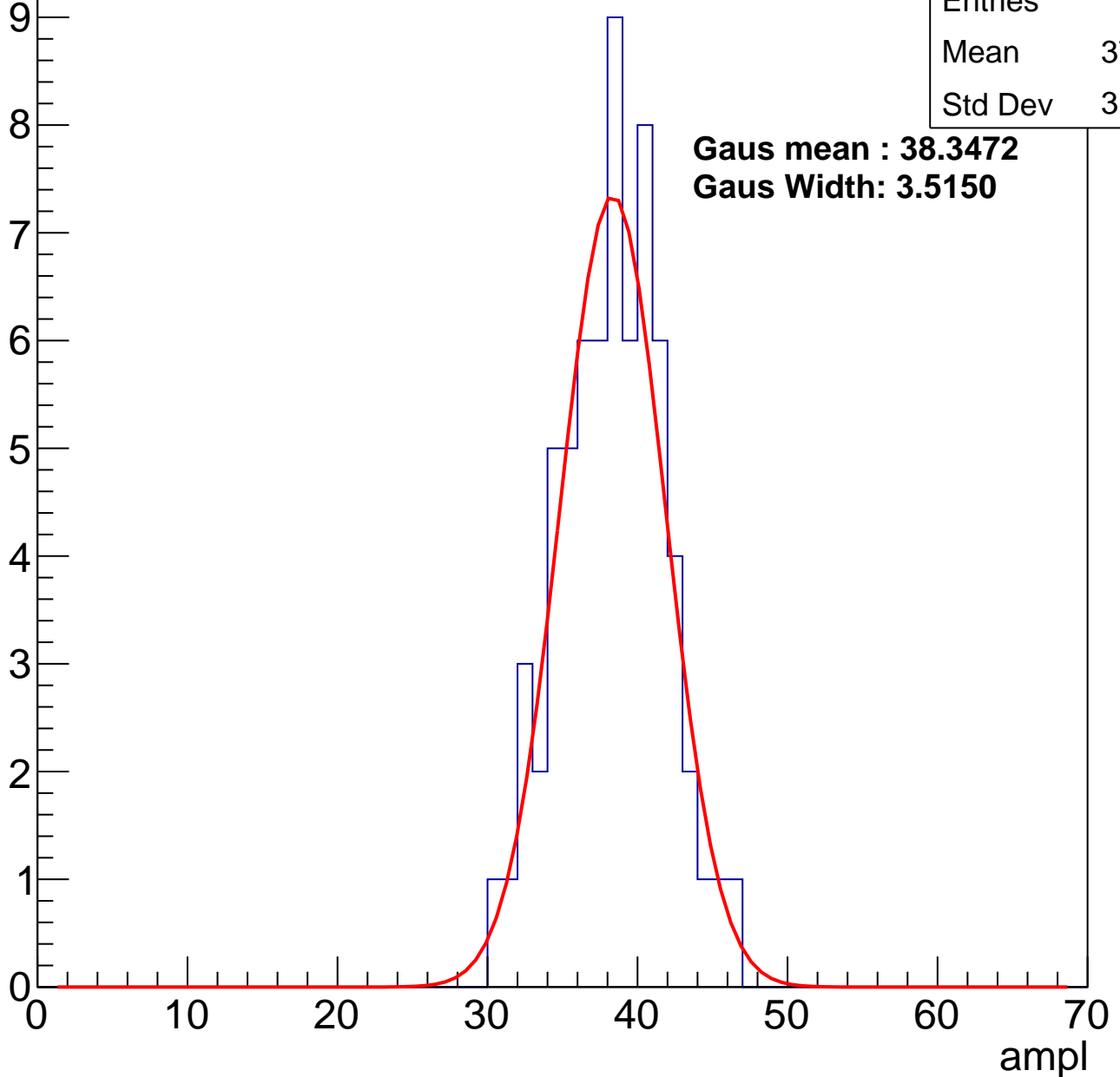
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.87
Std Dev	3.412

Gaus mean : 38.3472

Gaus Width: 3.5150



B1L003S, U13-ch14, adc2

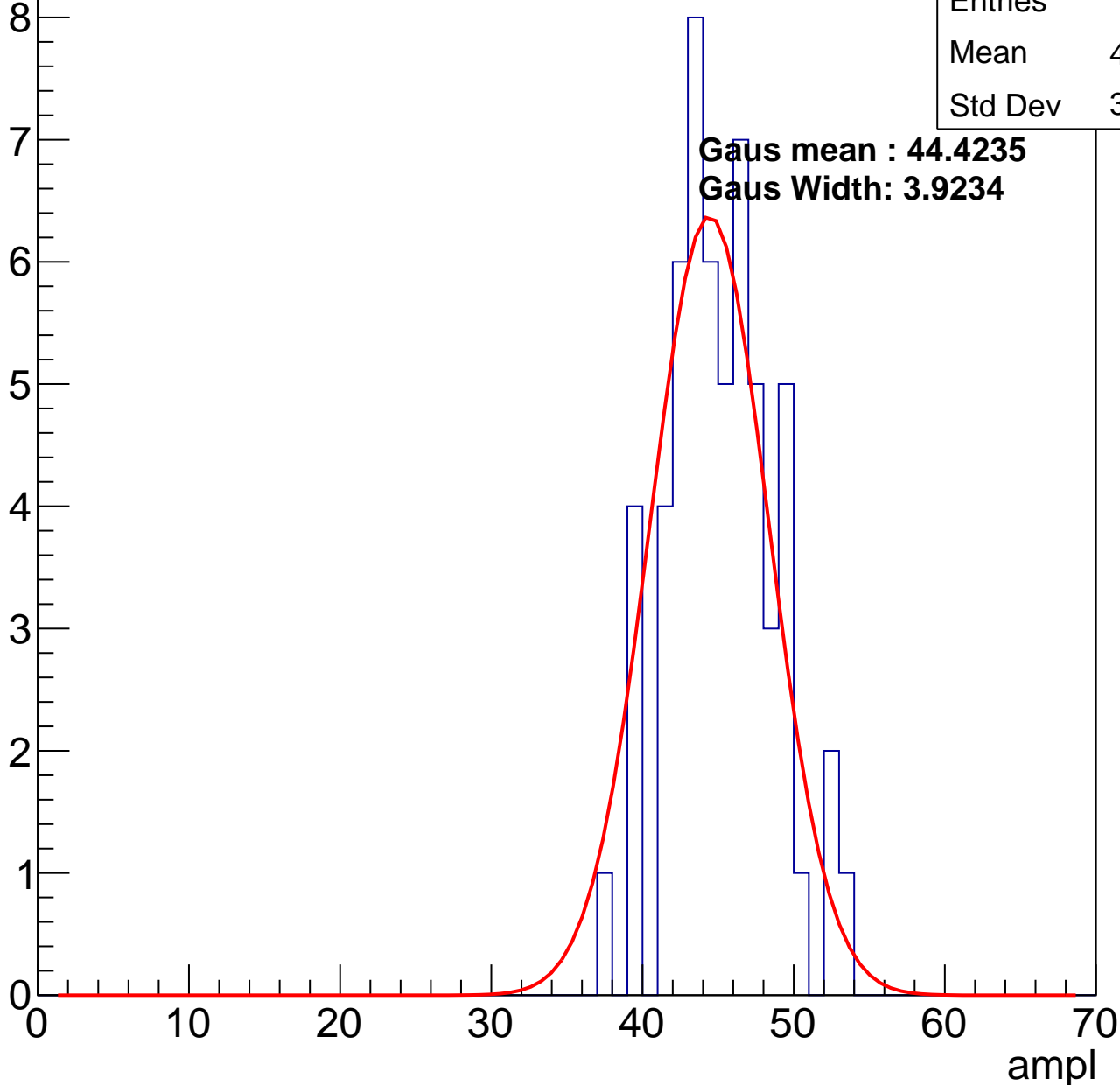
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.74
Std Dev	3.427

Gaus mean : 44.4235

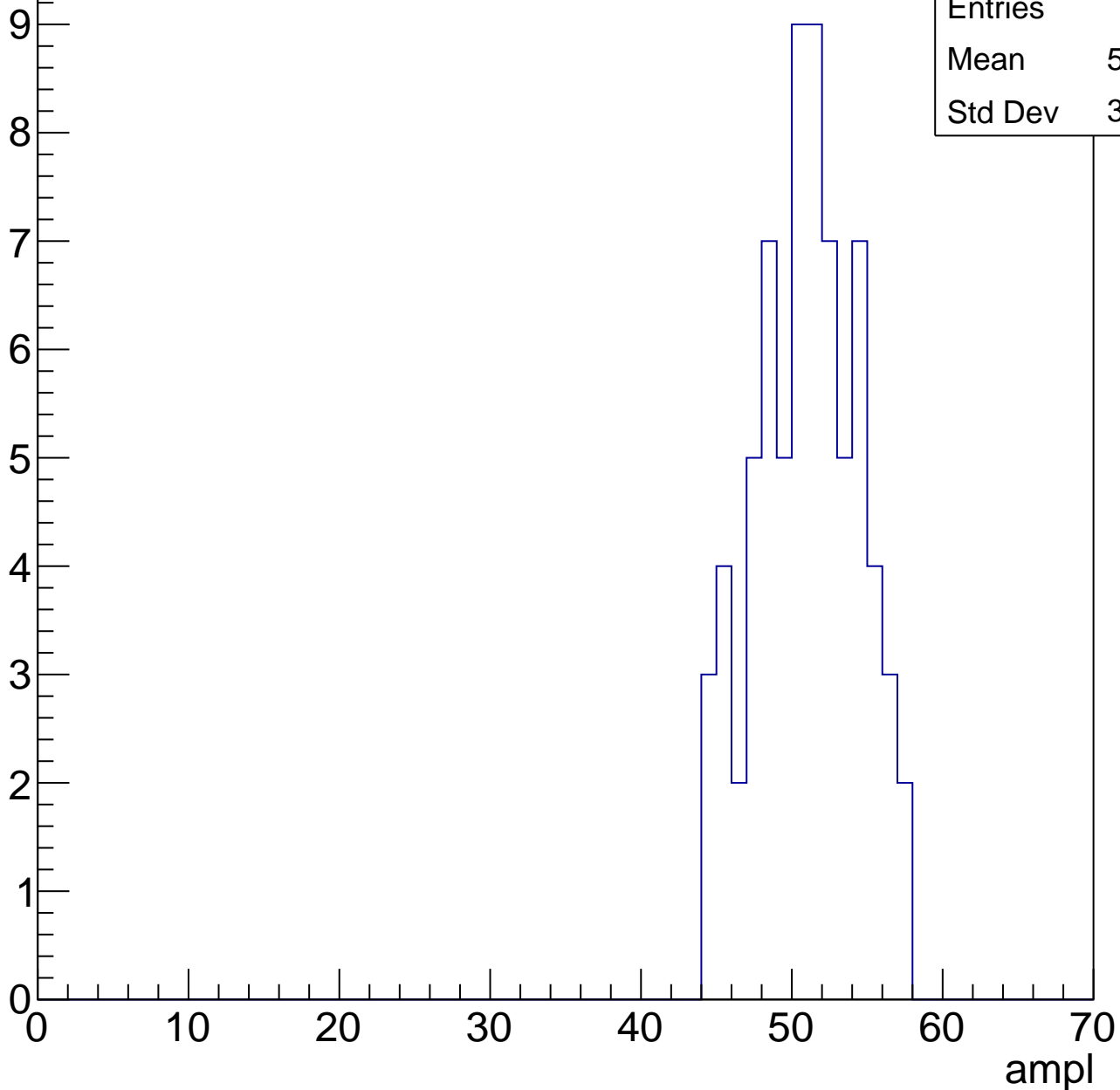
Gaus Width: 3.9234



B1L003S, U13-ch14, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	72
Mean	50.53
Std Dev	3.329

B1L003S, U13-ch14, adc4

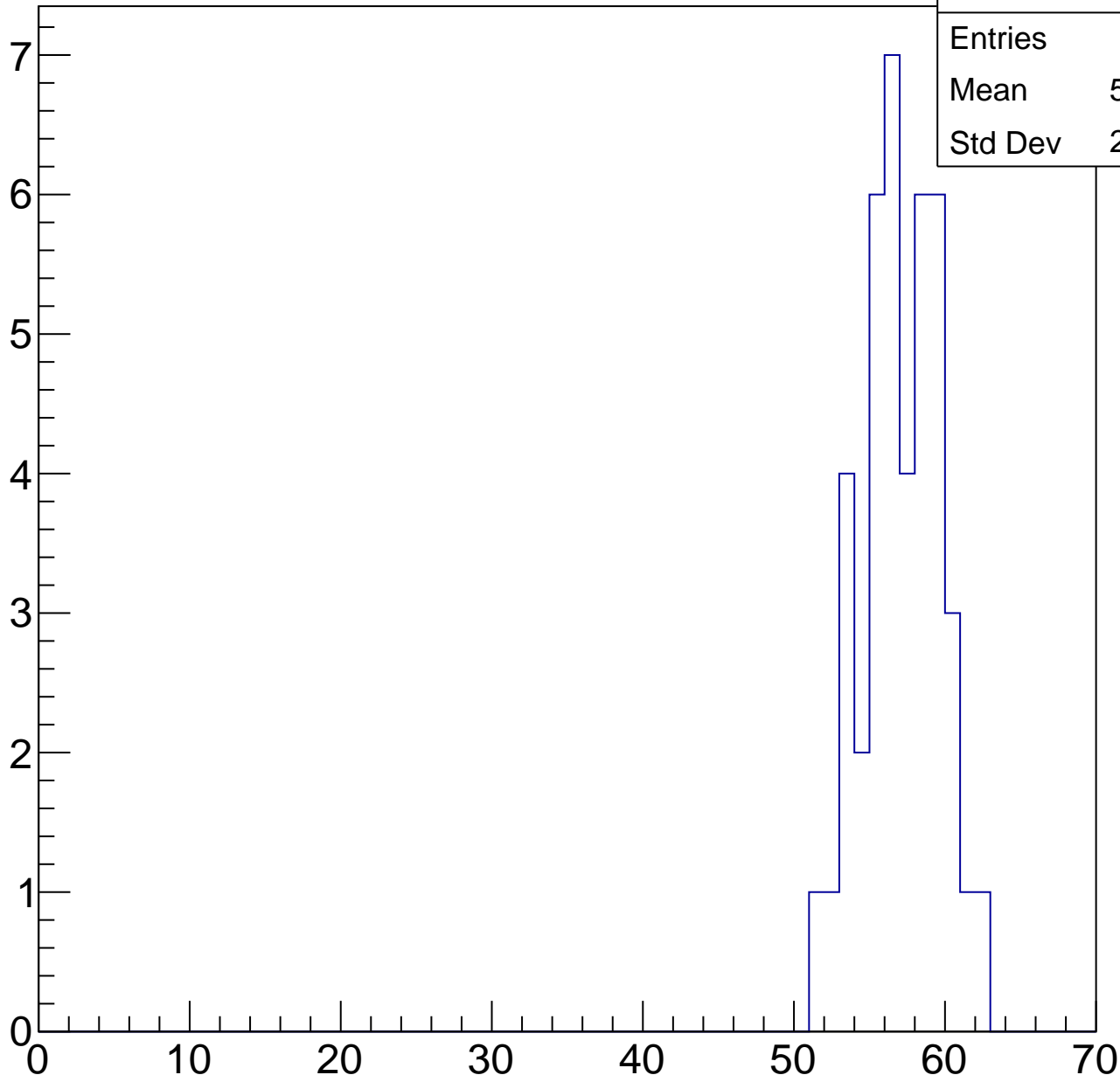
calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	42
Mean	56.62
Std Dev	2.516

ampl



B1L003S, U13-ch14, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	47
Mean	60.53
Std Dev	2.112

Entry

10

8

6

4

2

0

0

10

20

30

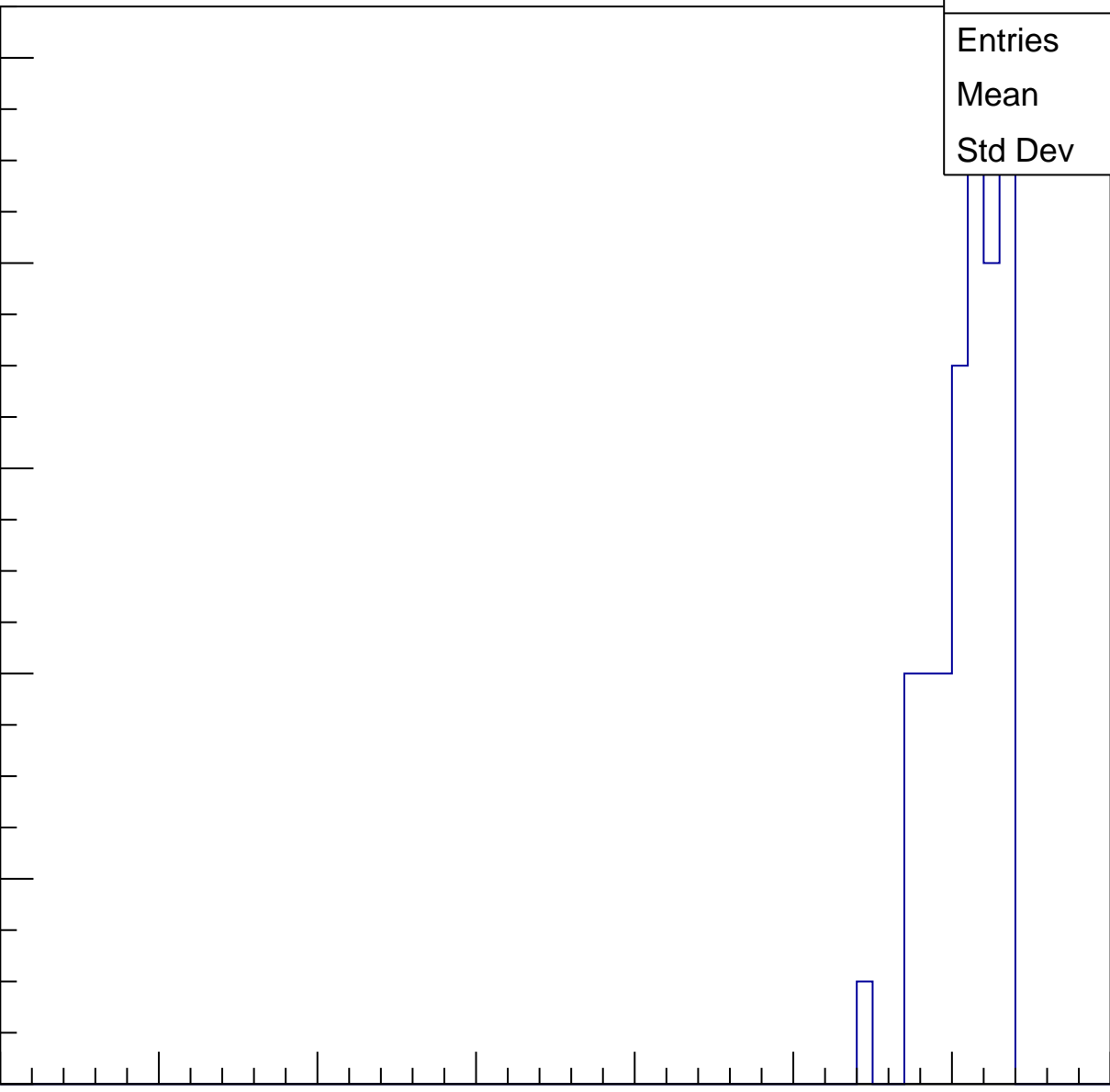
40

50

60

ampl

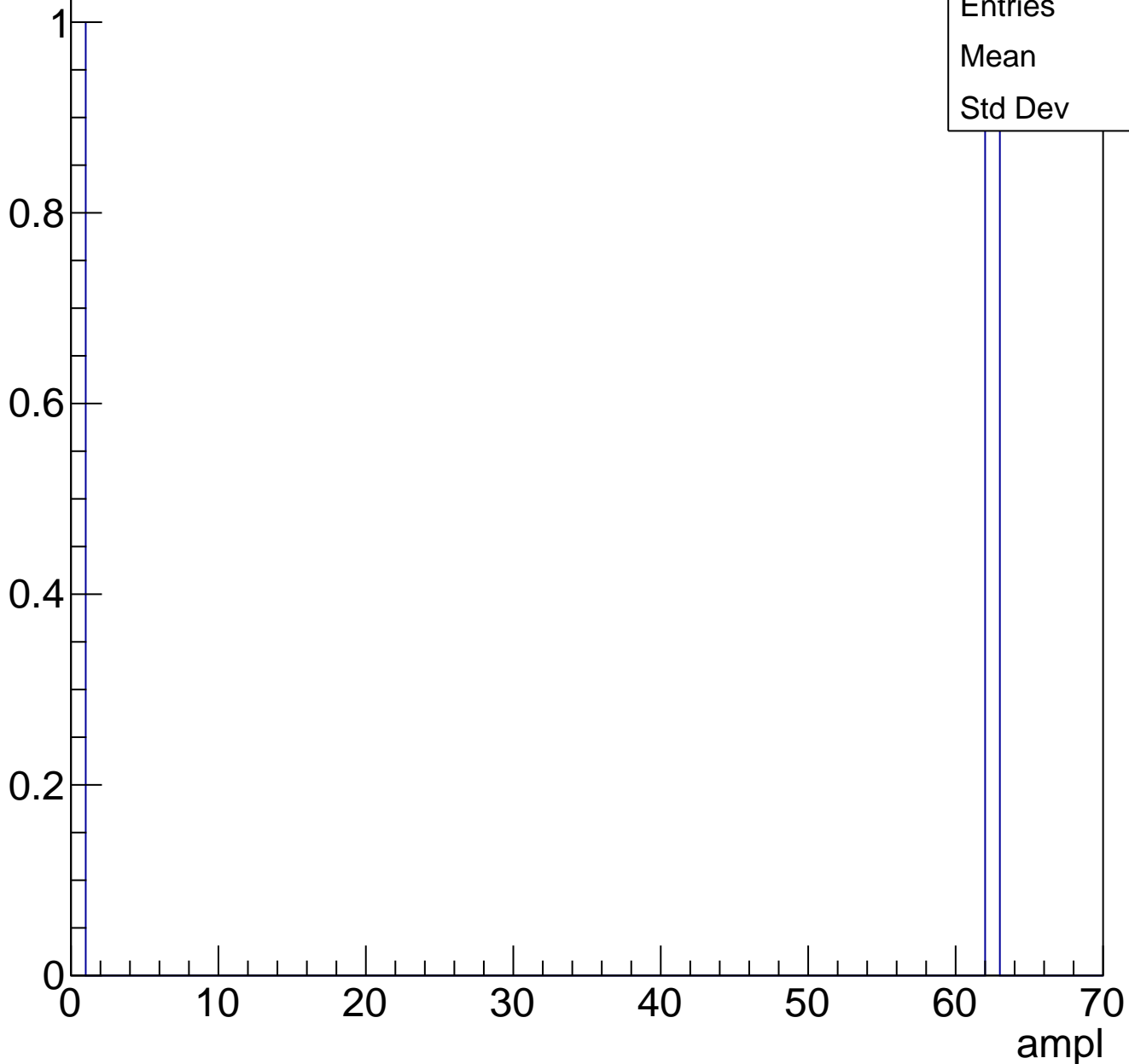
70



B1L003S, U13-ch14, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	31
Std Dev	31

B1L003S, U13-ch14, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch15, adc0

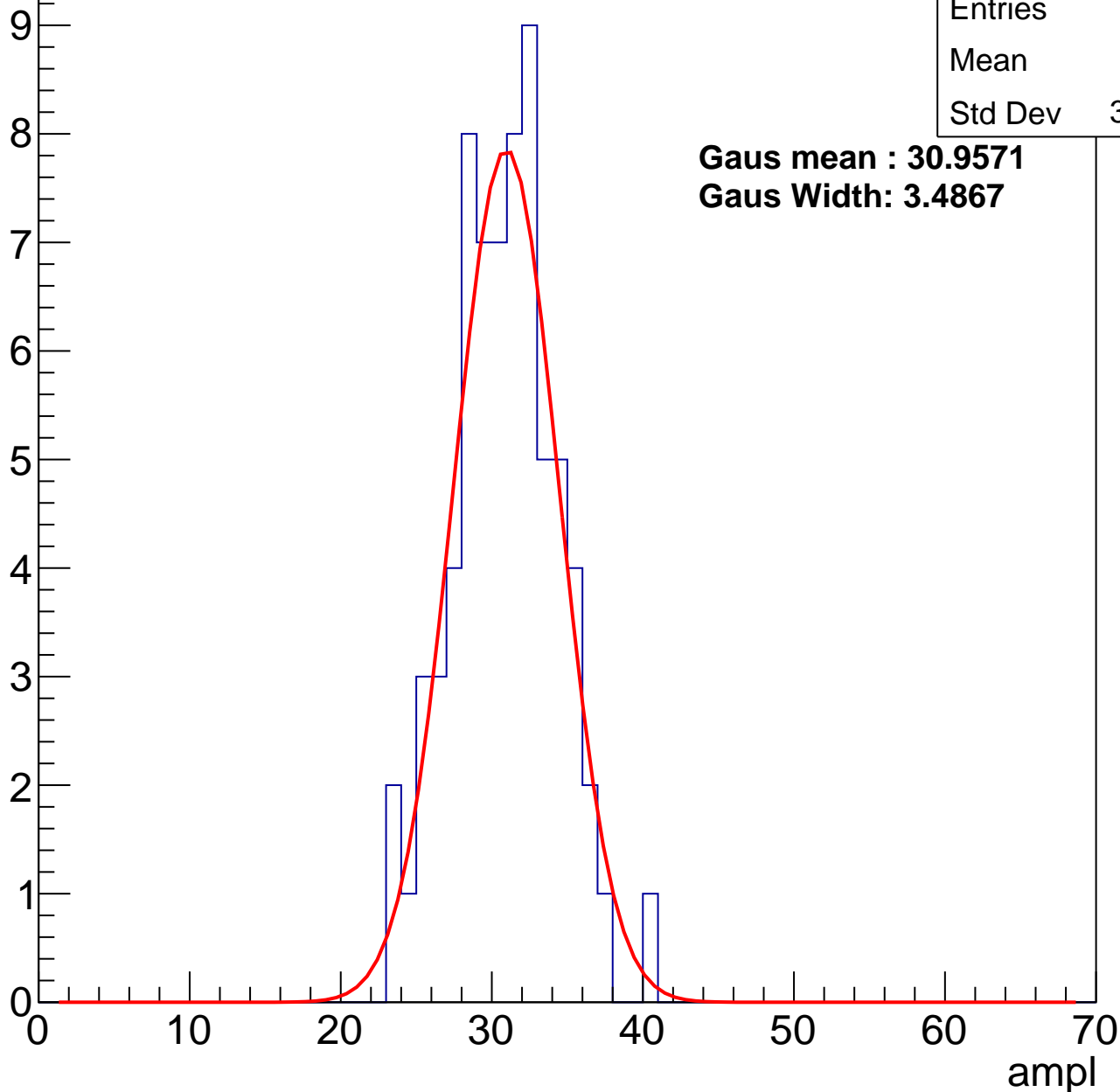
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.4
Std Dev	3.403

Gaus mean : 30.9571

Gaus Width: 3.4867



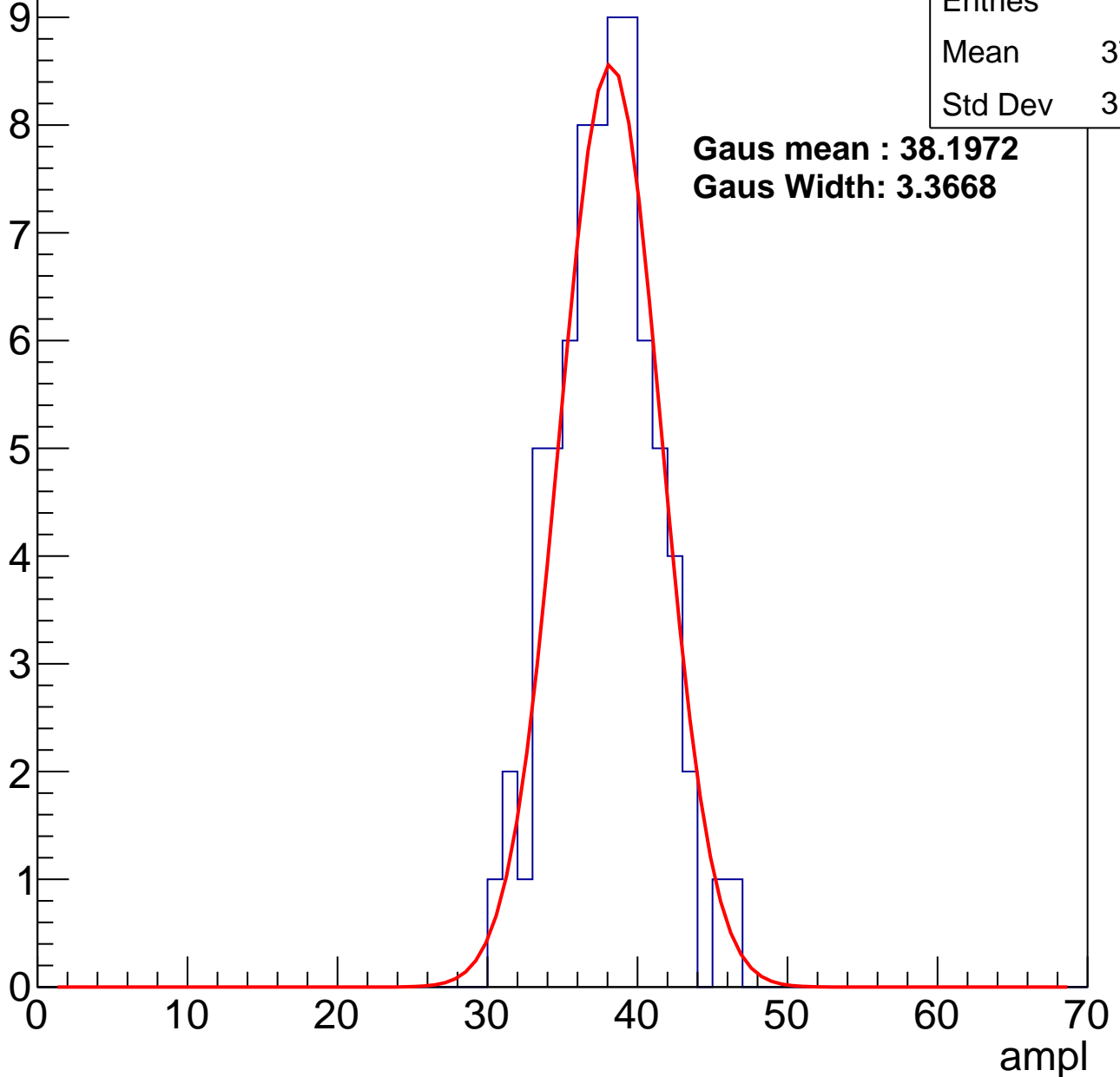
B1L003S, U13-ch15, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.48
Std Dev	3.273

Gaus mean : 38.1972
Gaus Width: 3.3668



B1L003S, U13-ch15, adc2

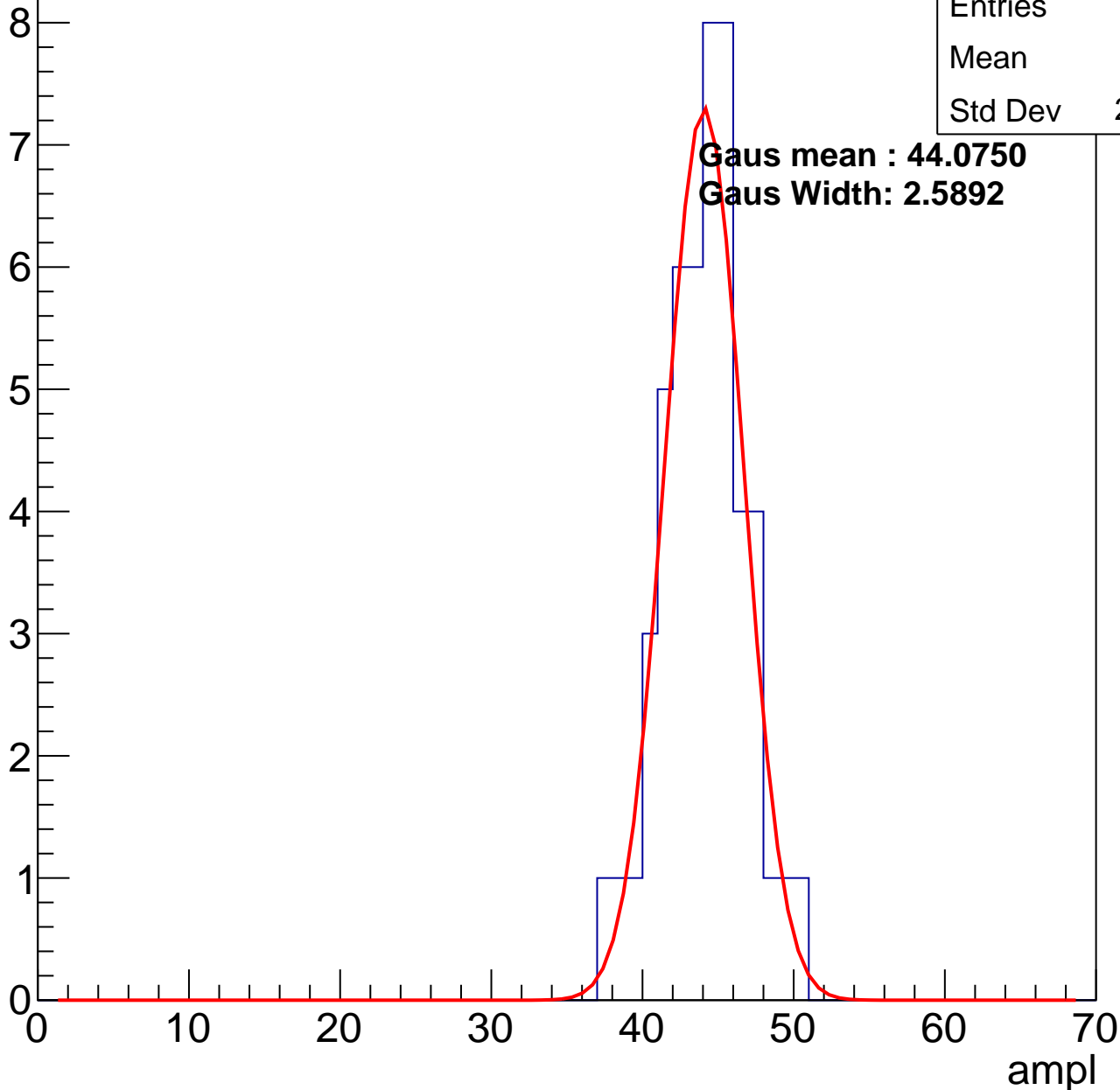
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.6
Std Dev	2.691

Gaus mean : 44.0750

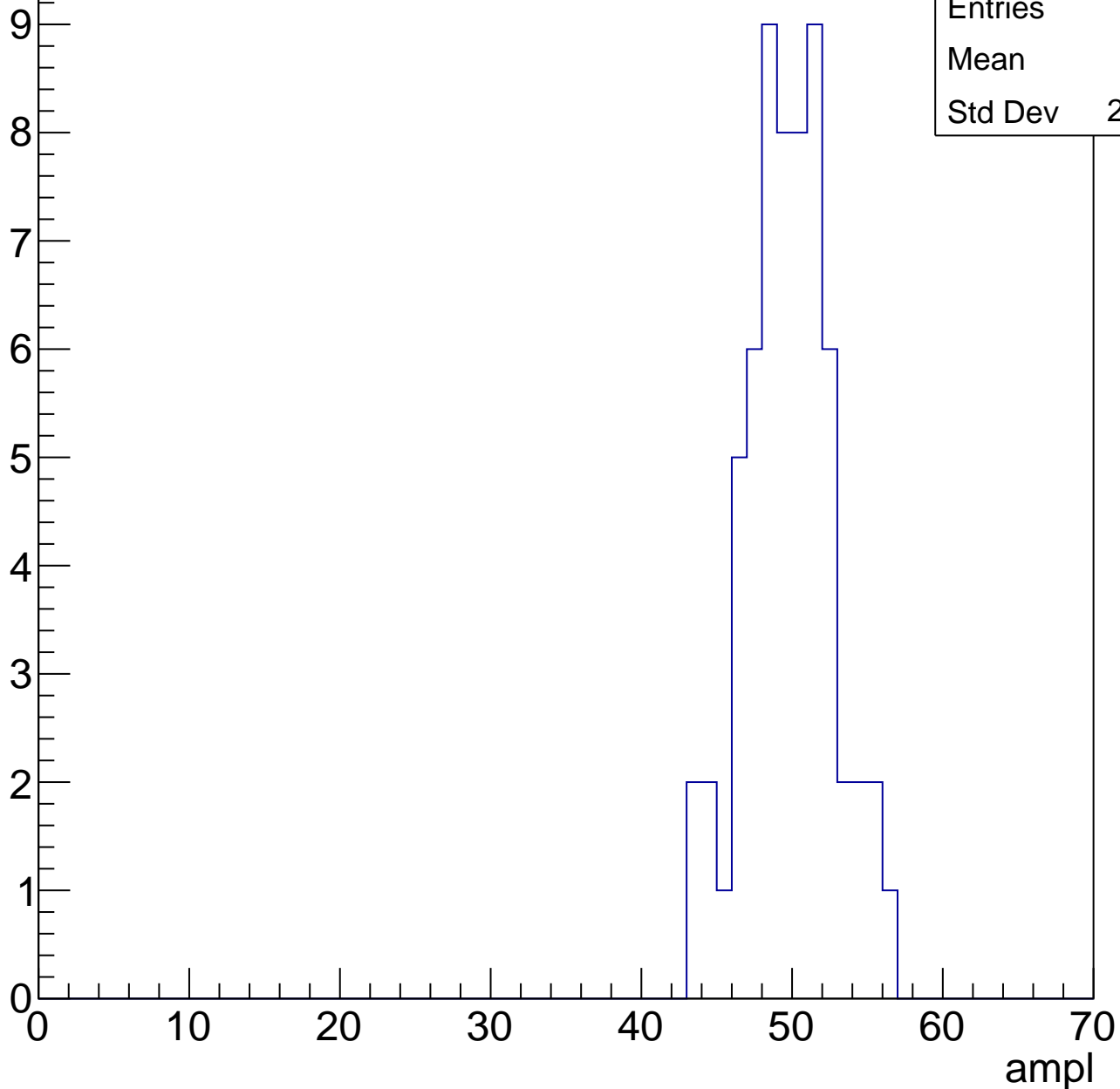
Gaus Width: 2.5892



B1L003S, U13-ch15, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	63
Mean	49.3
Std Dev	2.849

B1L003S, U13-ch15, adc4

calib_packv5_042523_0143.root, FC#13, port D2

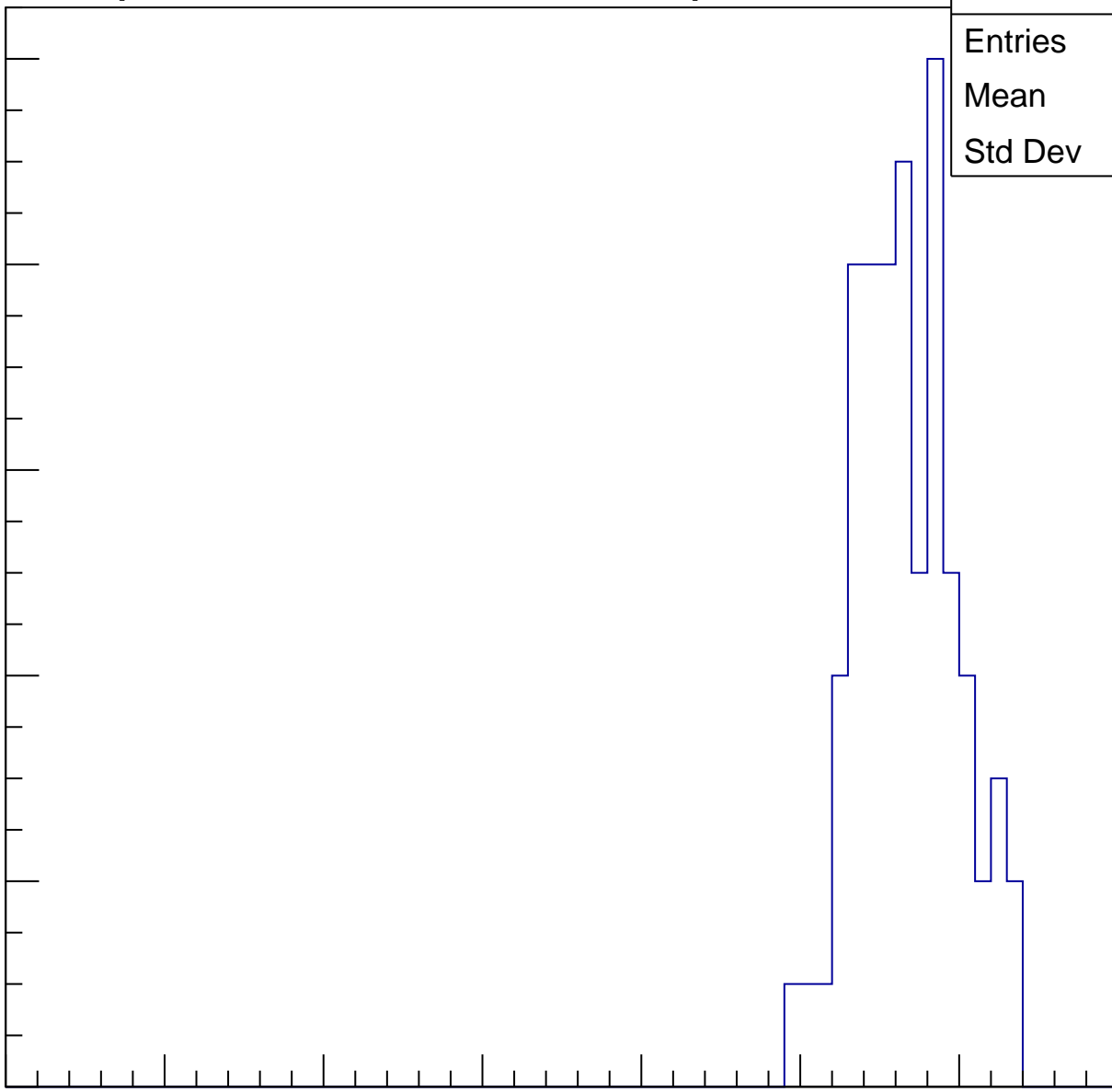
Entries	71
Mean	56.23
Std Dev	3.123

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

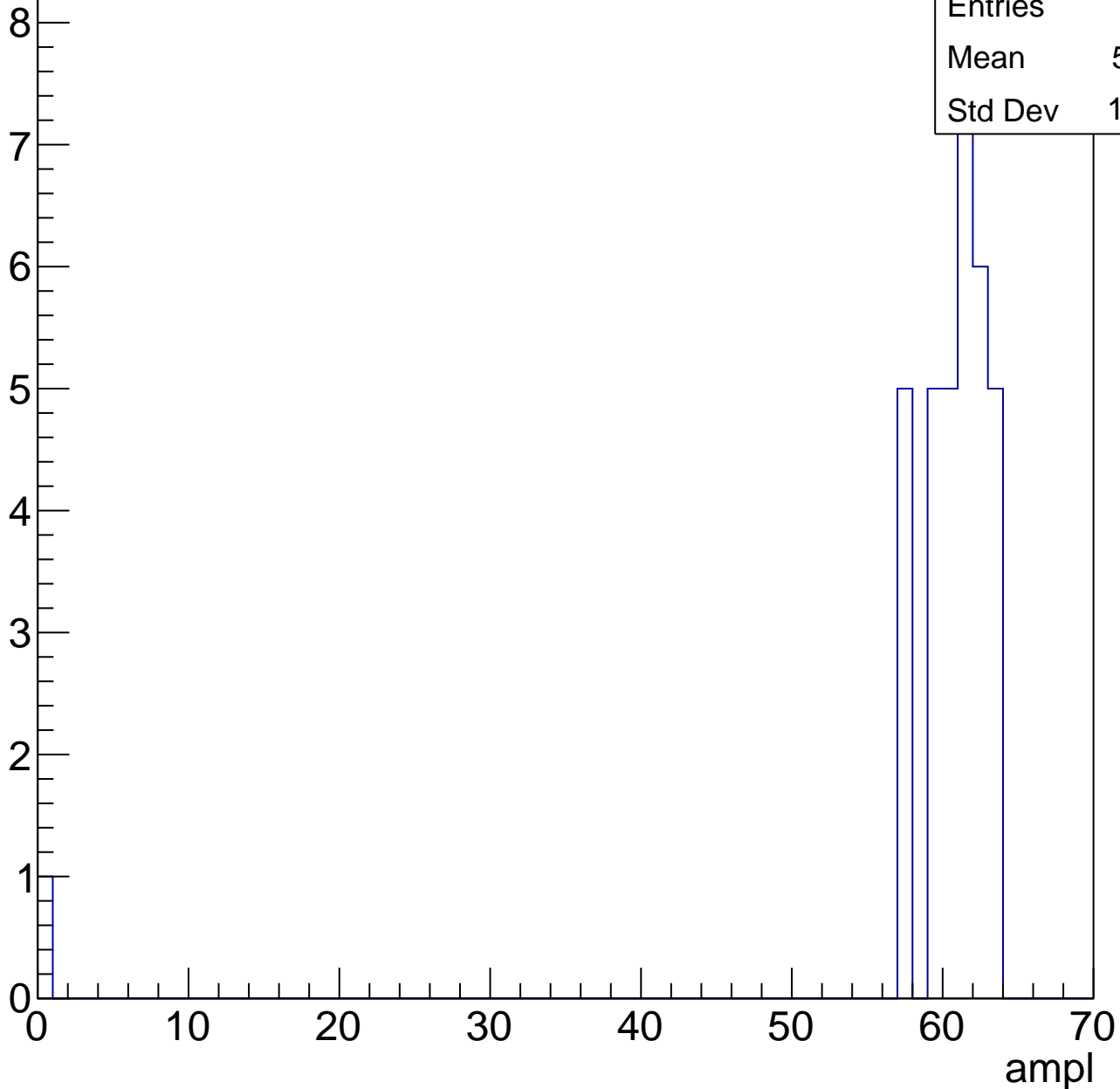


B1L003S, U13-ch15, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	58.71
Std Dev	10.24



B1L003S, U13-ch15, adc6

calib_packv5_042523_0143.root, FC#13, port D2

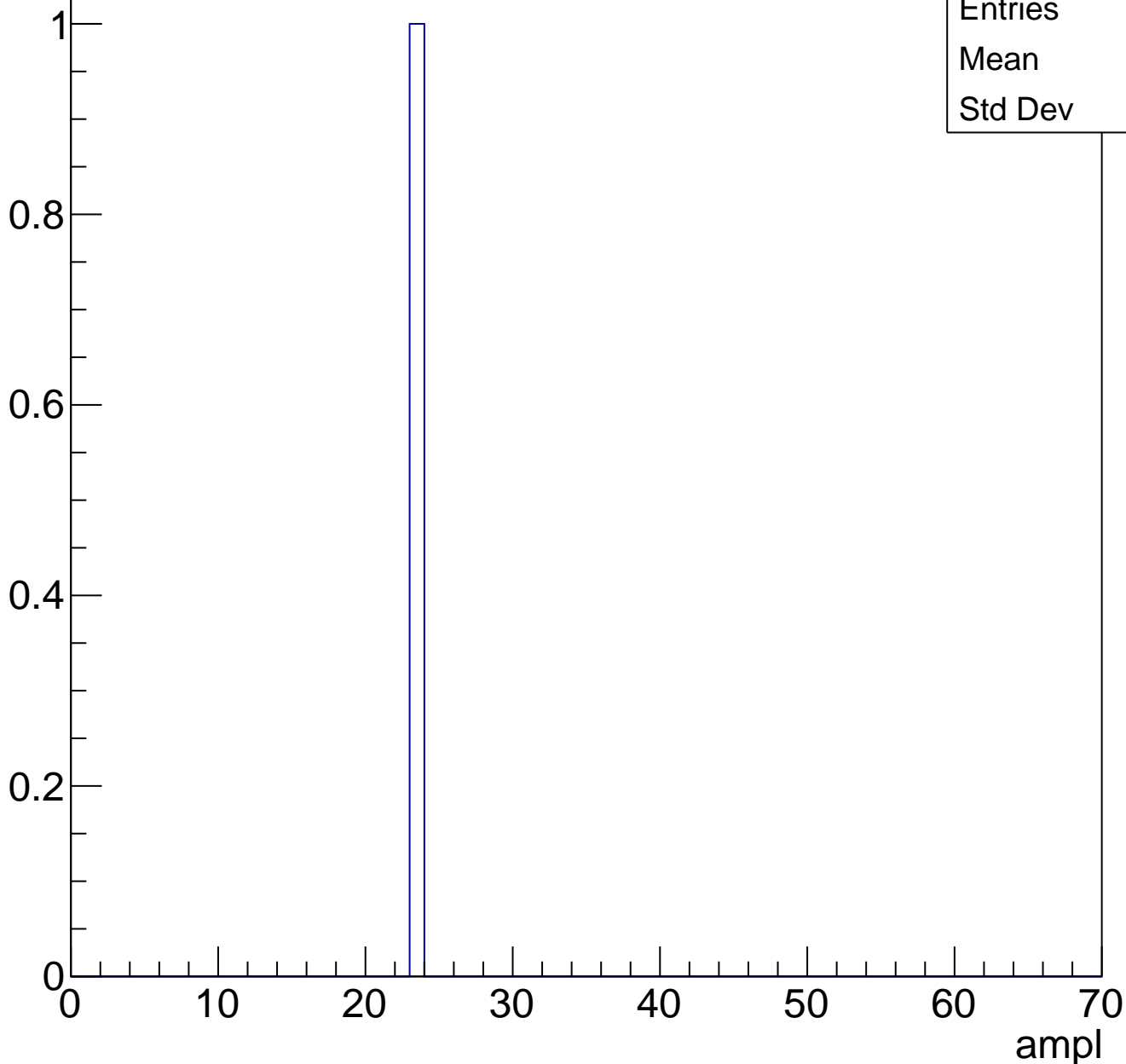
Entry



B1L003S, U13-ch15, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L003S, U13-ch16, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	29.66
Std Dev	4.631

Gaus mean : 30.6167

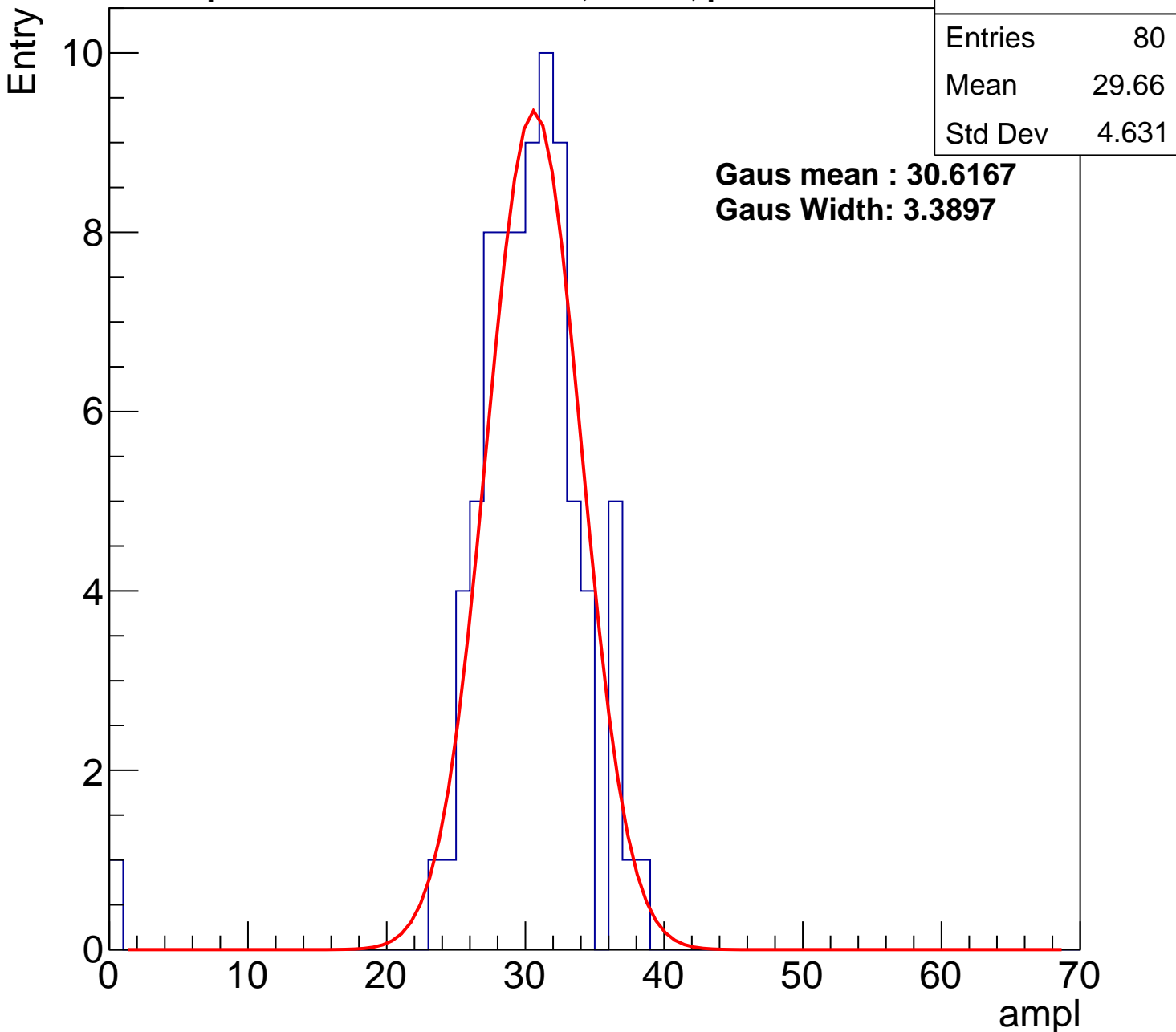
Gaus Width: 3.3897

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch16, adc1

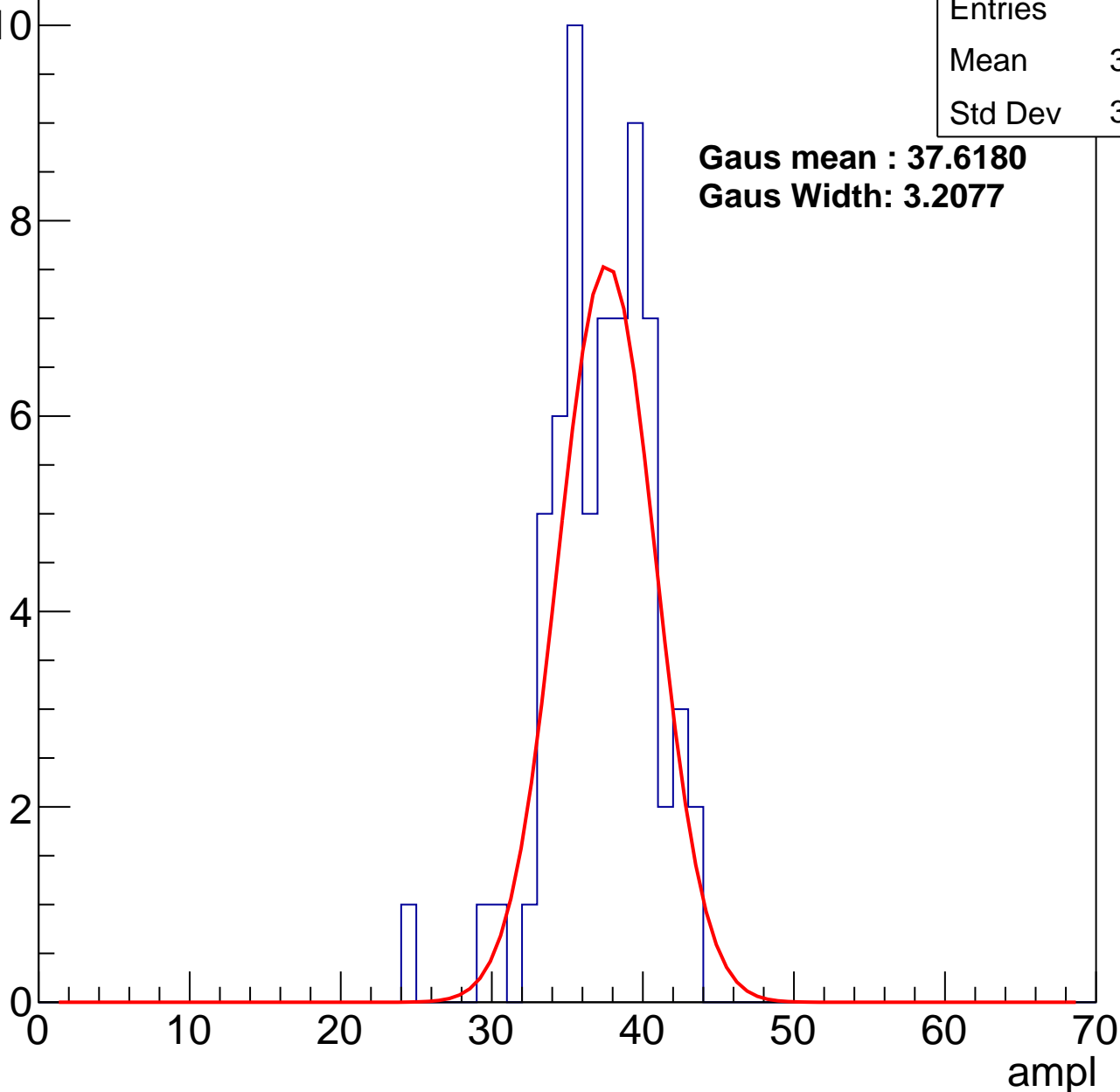
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.78
Std Dev	3.385

Gaus mean : 37.6180

Gaus Width: 3.2077



B1L003S, U13-ch16, adc2

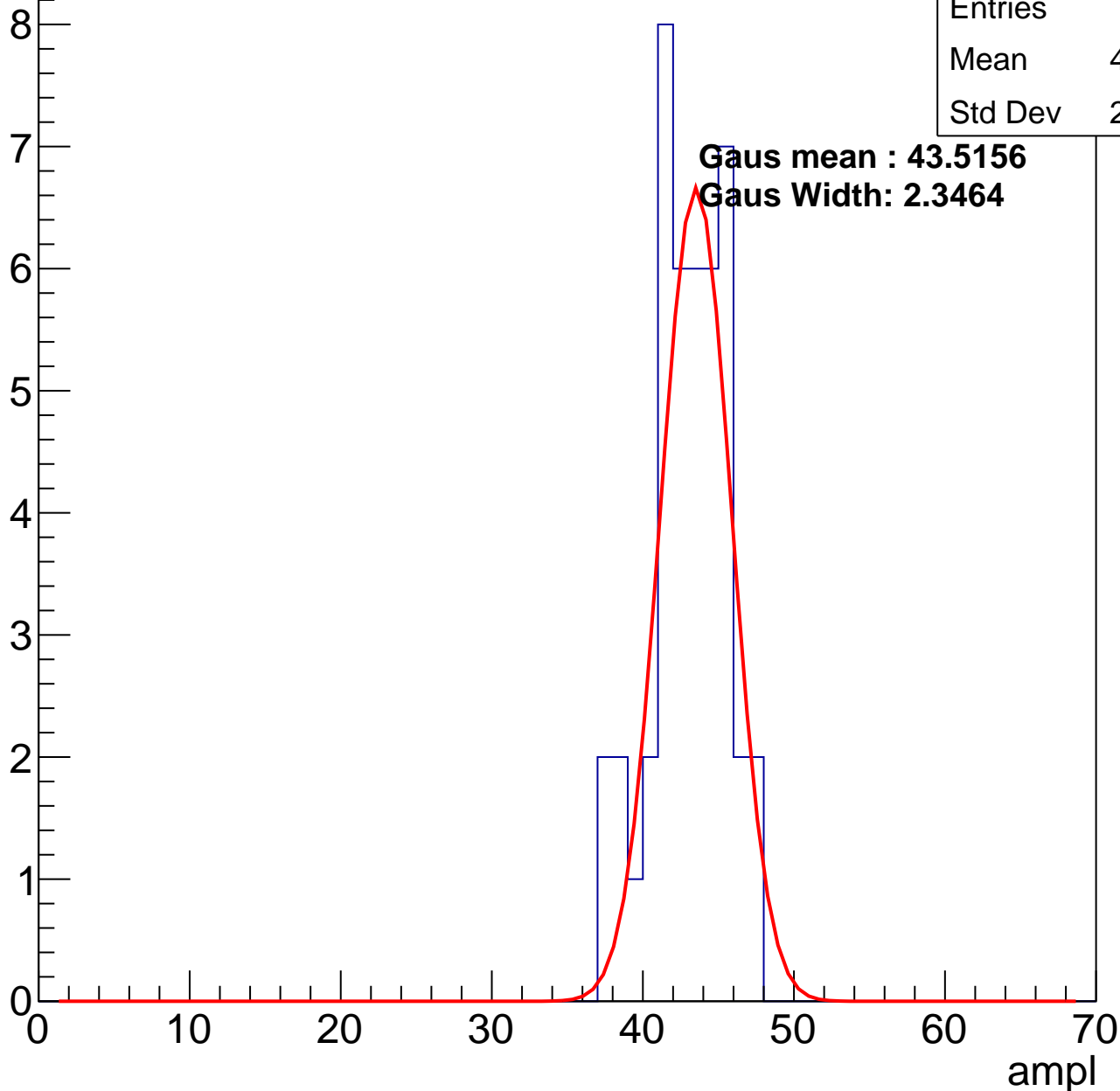
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	42.55
Std Dev	2.472

Gaus mean : 43.5156

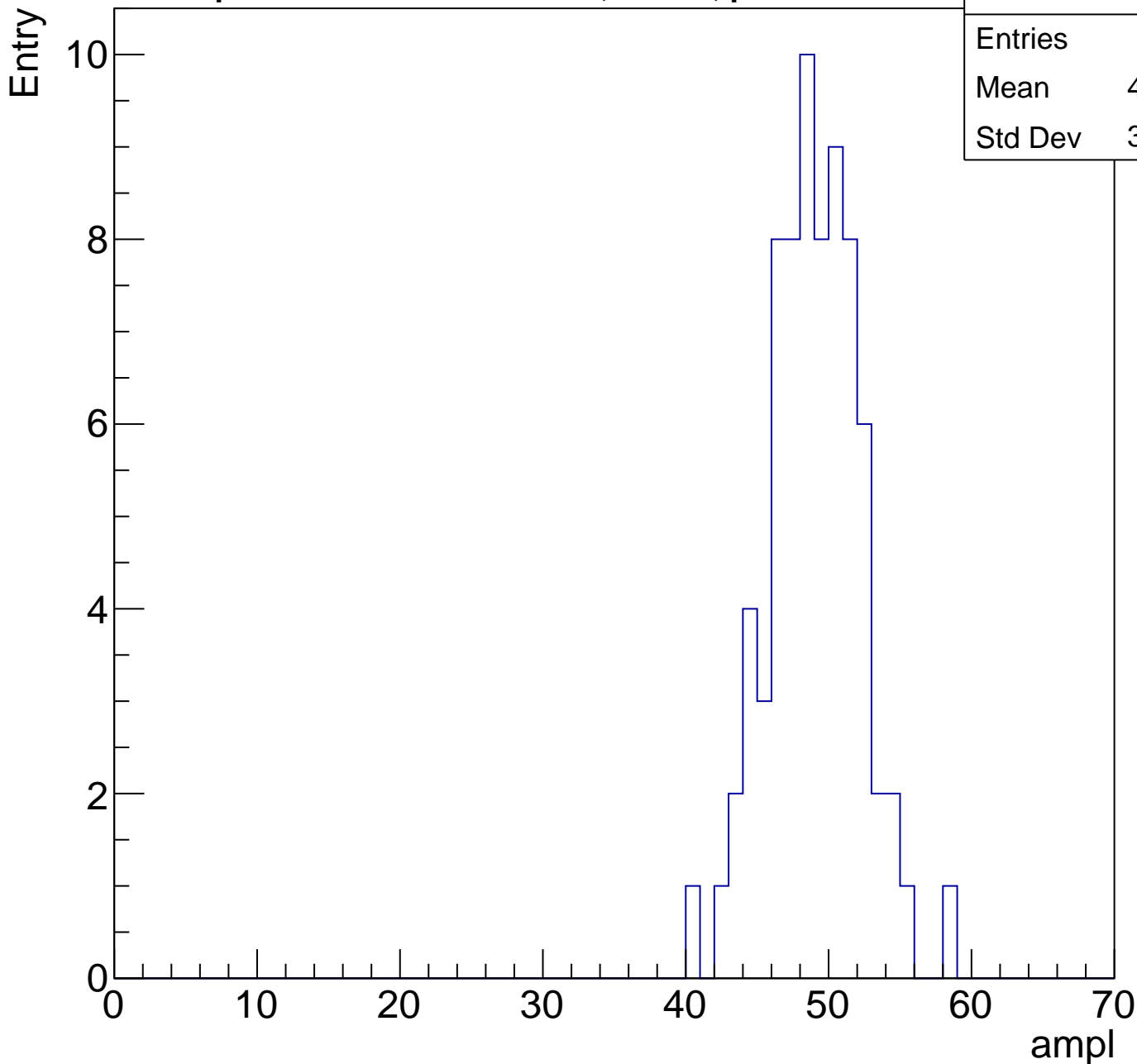
Gaus Width: 2.3464



B1L003S, U13-ch16, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	48.54
Std Dev	3.167



B1L003S, U13-ch16, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	55.88
Std Dev	2.986

Entry

10

8

6

4

2

0

0

10

20

30

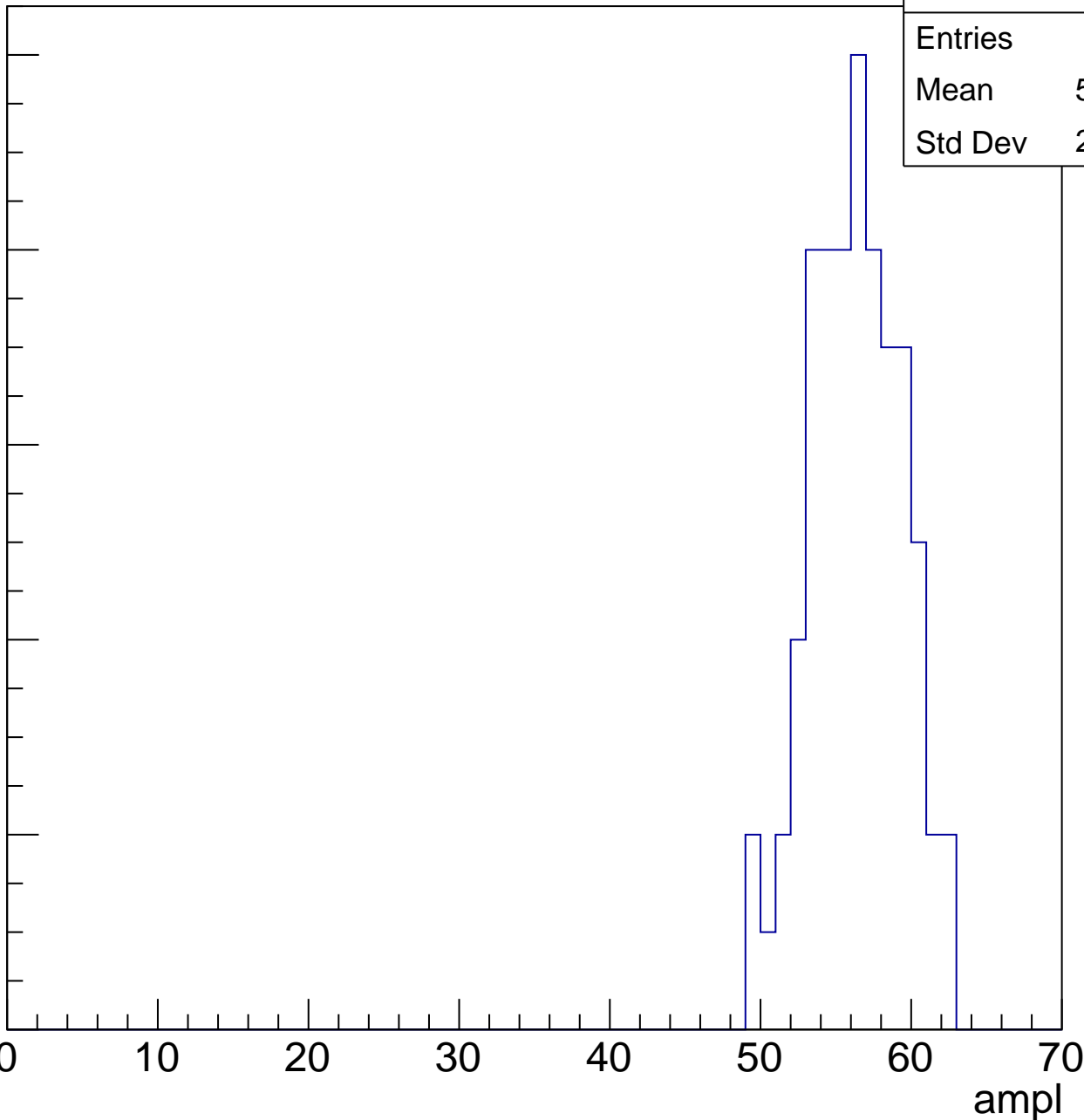
40

50

60

70

ampl

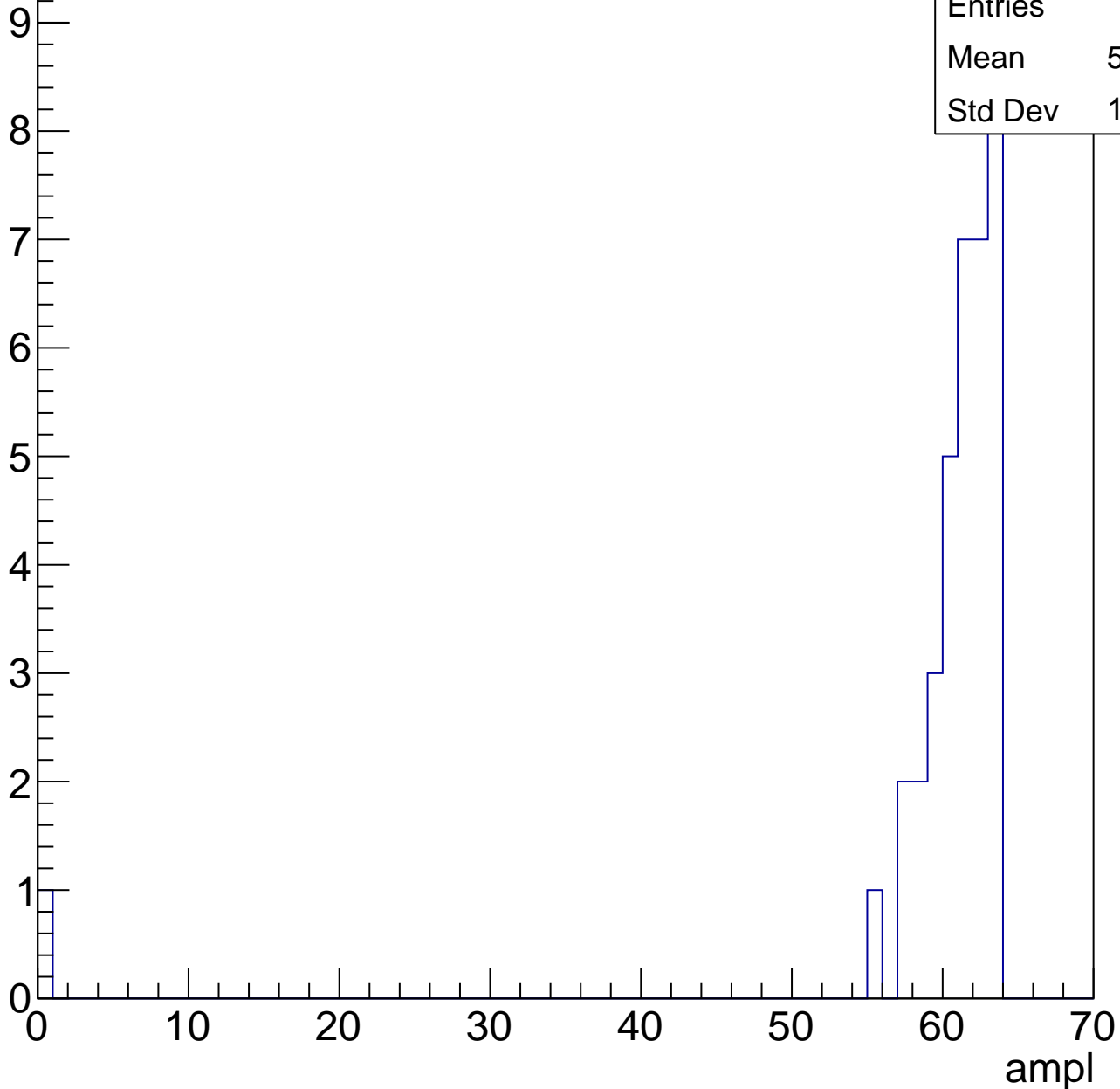


B1L003S, U13-ch16, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	59.19
Std Dev	10.06



B1L003S, U13-ch16, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch16, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10
Std Dev	10

B1L003S, U13-ch17, adc0

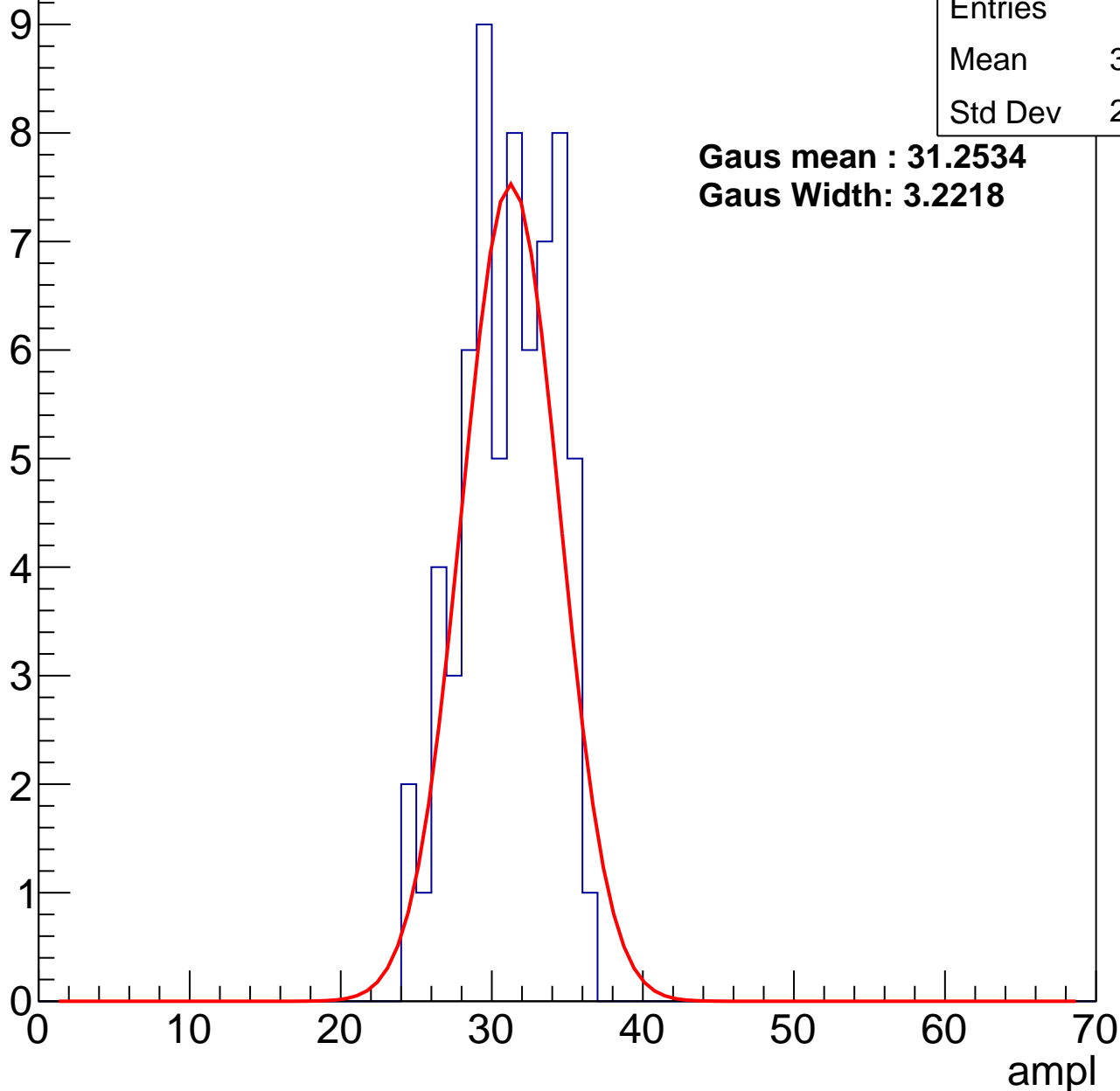
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.63
Std Dev	2.985

Gaus mean : 31.2534

Gaus Width: 3.2218



B1L003S, U13-ch17, adc1

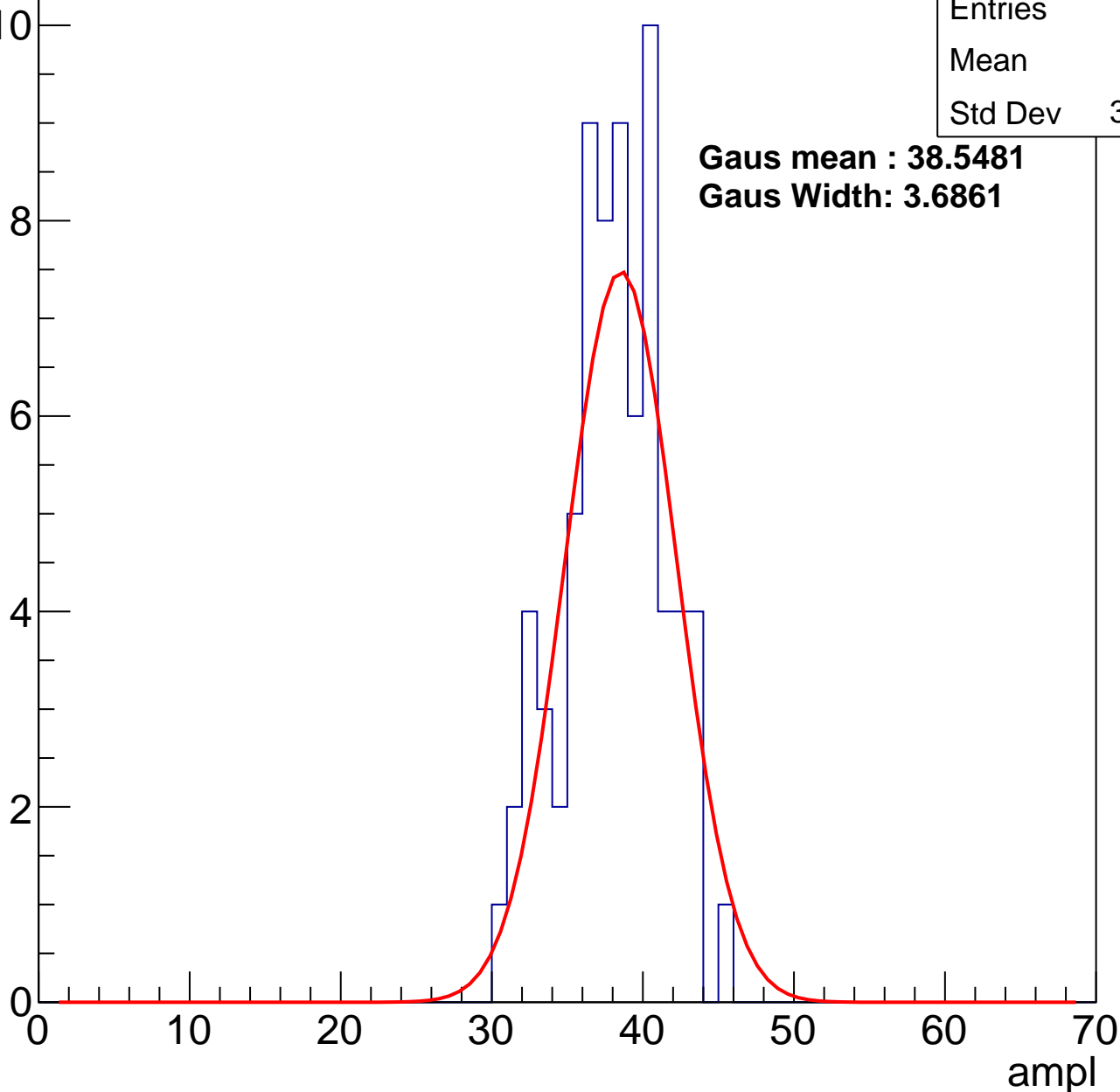
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.6
Std Dev	3.303

Gaus mean : 38.5481

Gaus Width: 3.6861



B1L003S, U13-ch17, adc2

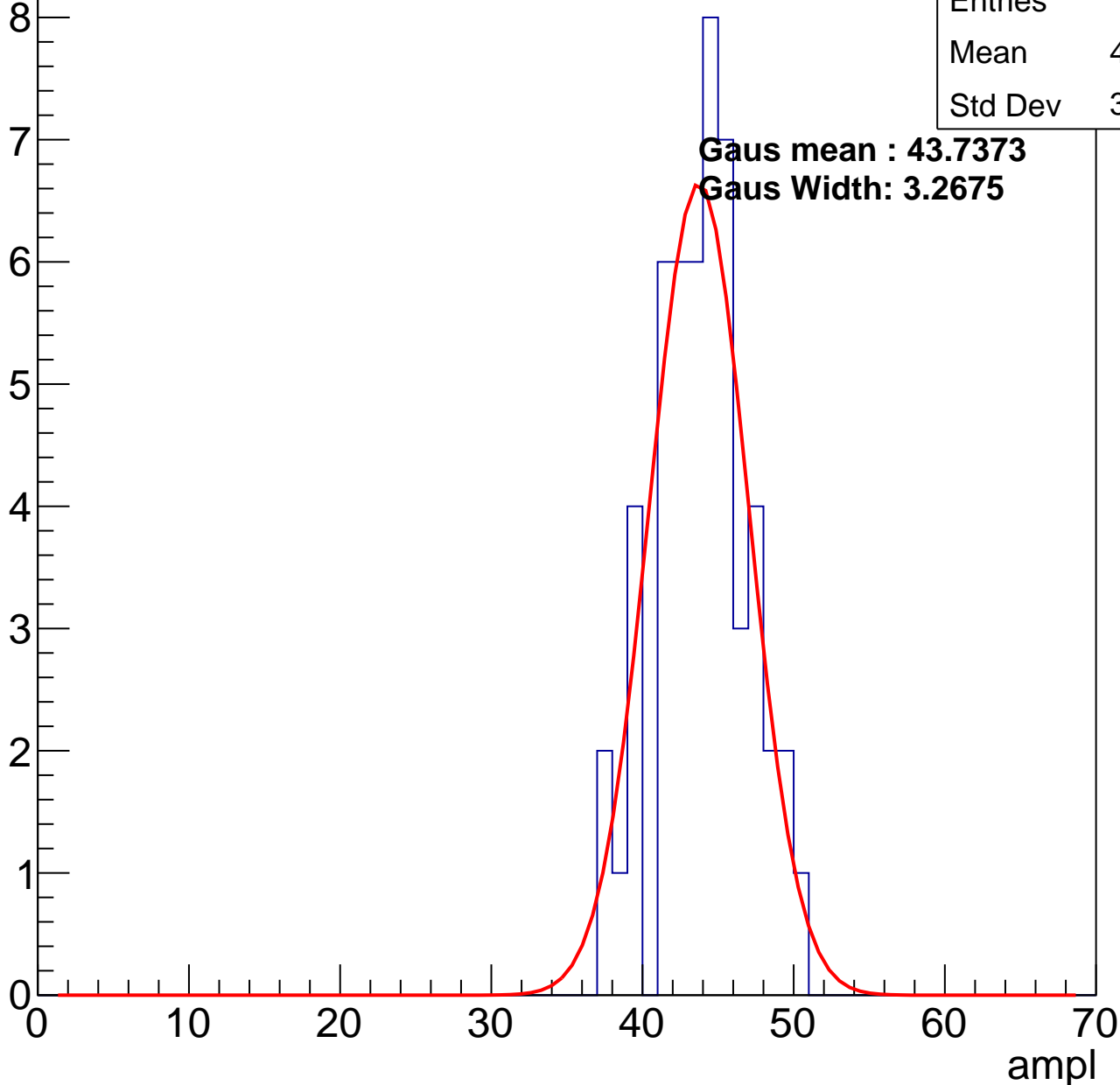
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.48
Std Dev	3.029

Gaus mean : 43.7373

Gaus Width: 3.2675

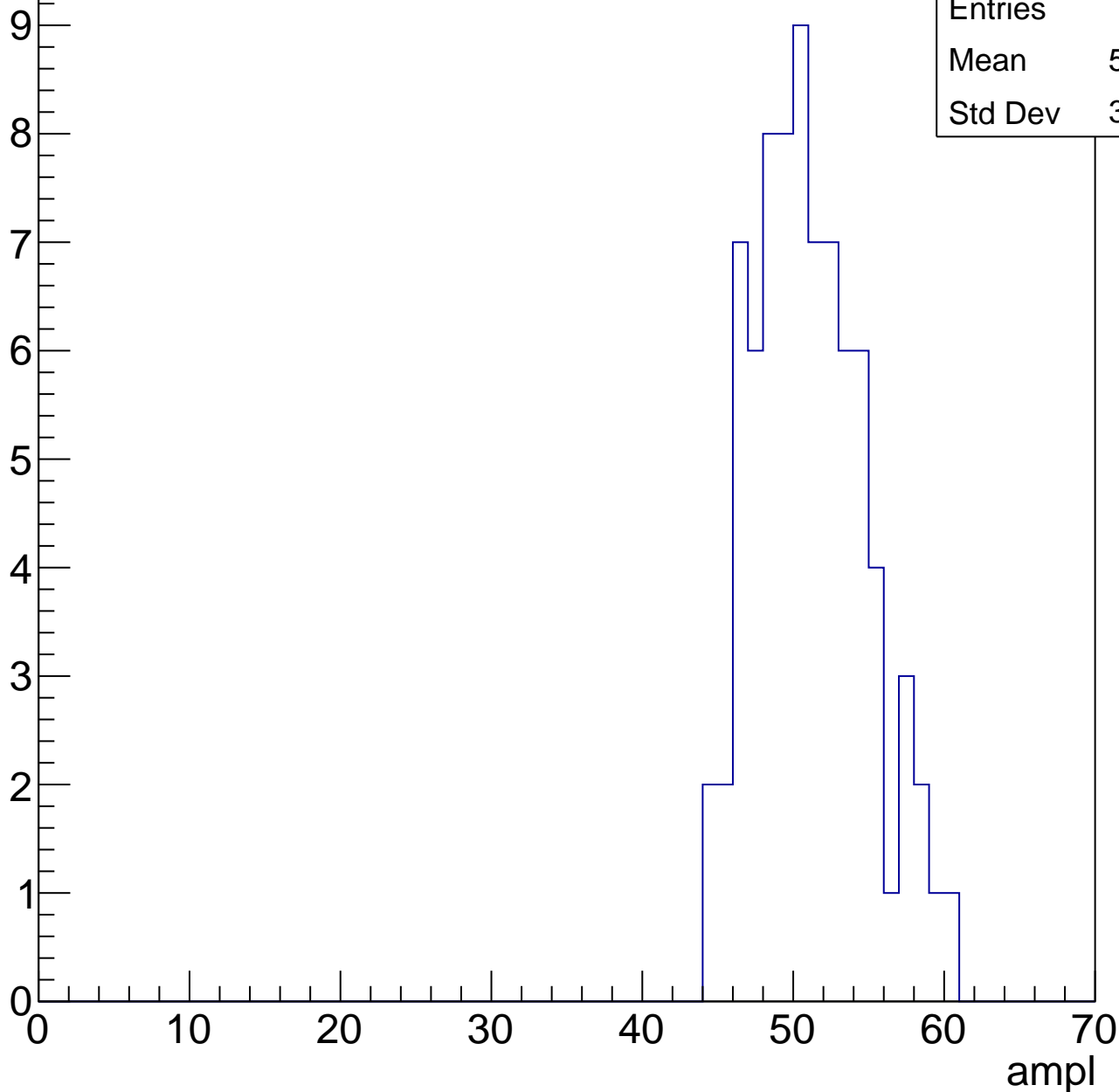


B1L003S, U13-ch17, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	50.66
Std Dev	3.657

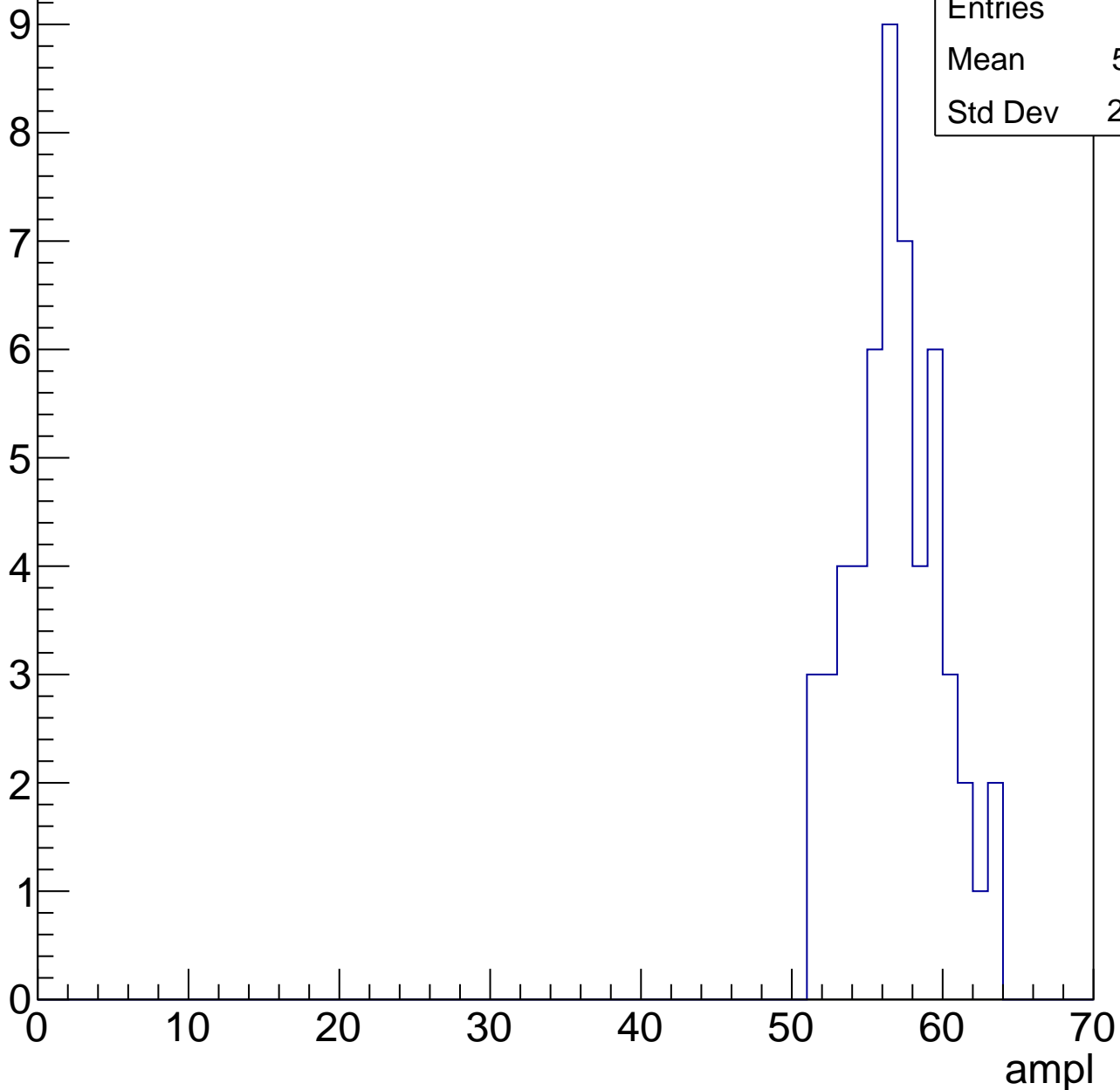


B1L003S, U13-ch17, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.41
Std Dev	2.985

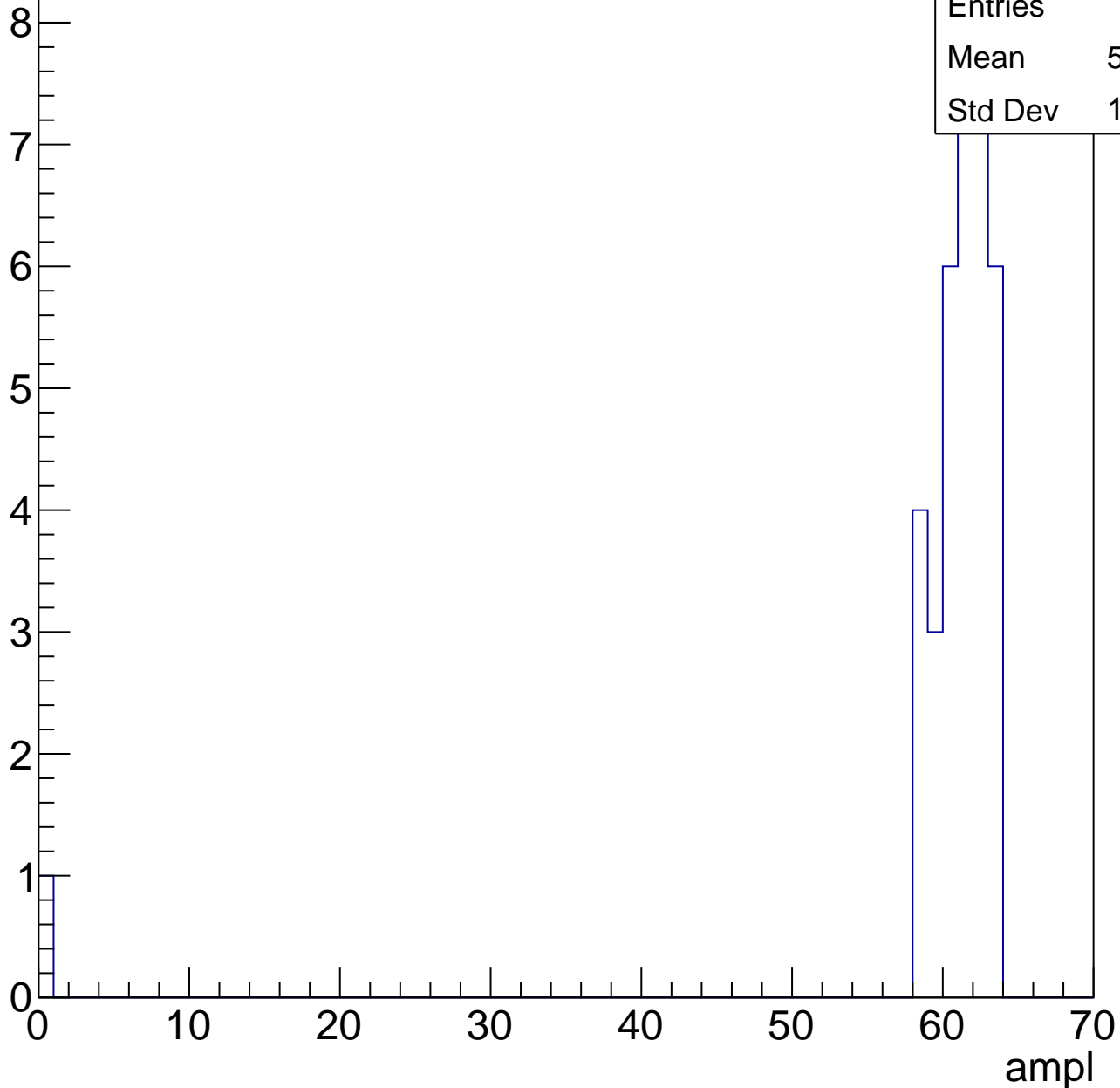


B1L003S, U13-ch17, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.19
Std Dev	10.12



B1L003S, U13-ch17, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch17, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch18, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	30.56
Std Dev	2.797

Gaus mean : 31.0361

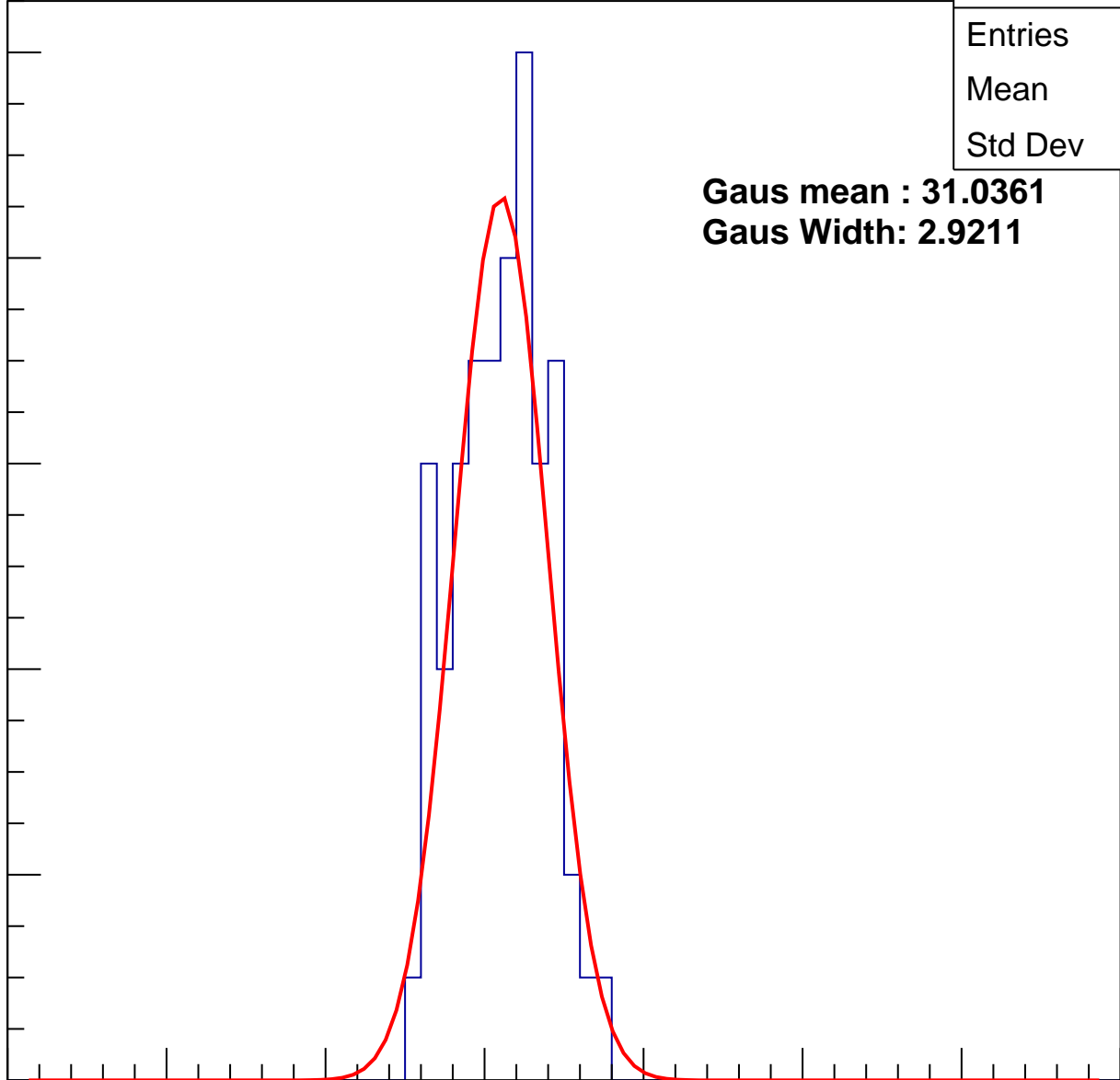
Gaus Width: 2.9211

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch18, adc1

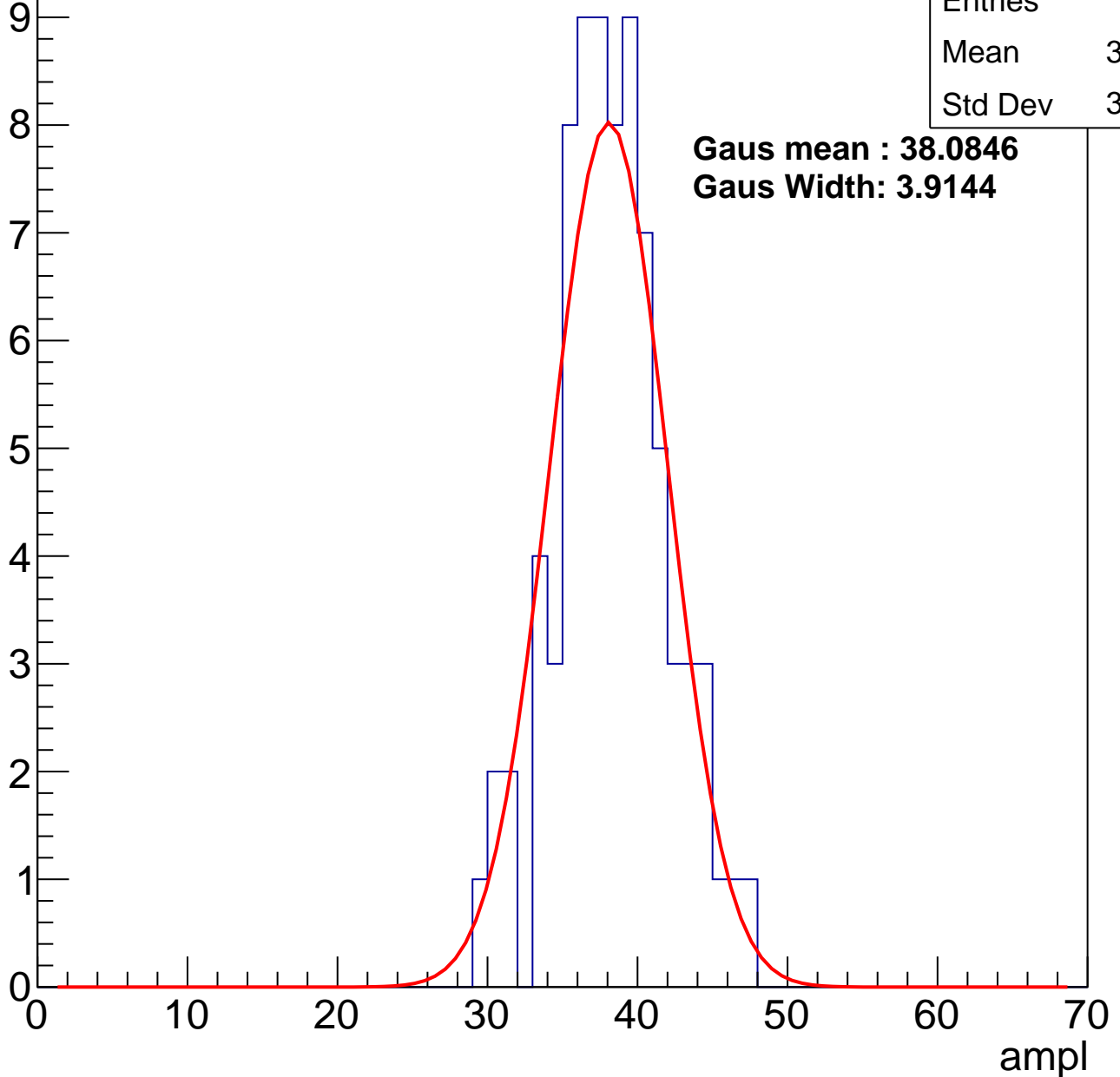
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	37.81
Std Dev	3.691

Gaus mean : 38.0846

Gaus Width: 3.9144



B1L003S, U13-ch18, adc2

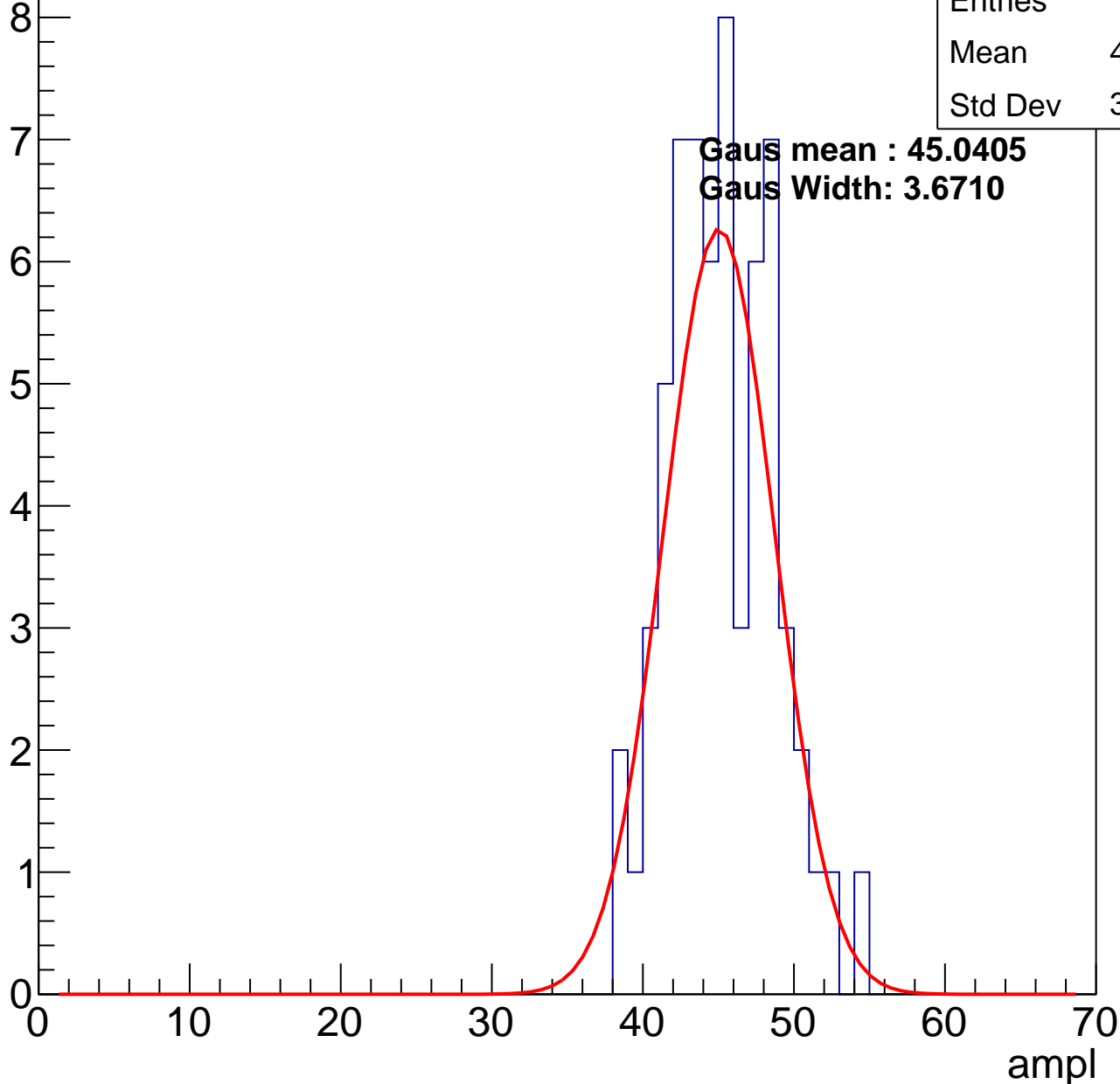
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	44.75
Std Dev	3.418

Gaus mean : 45.0405

Gaus Width: 3.6710

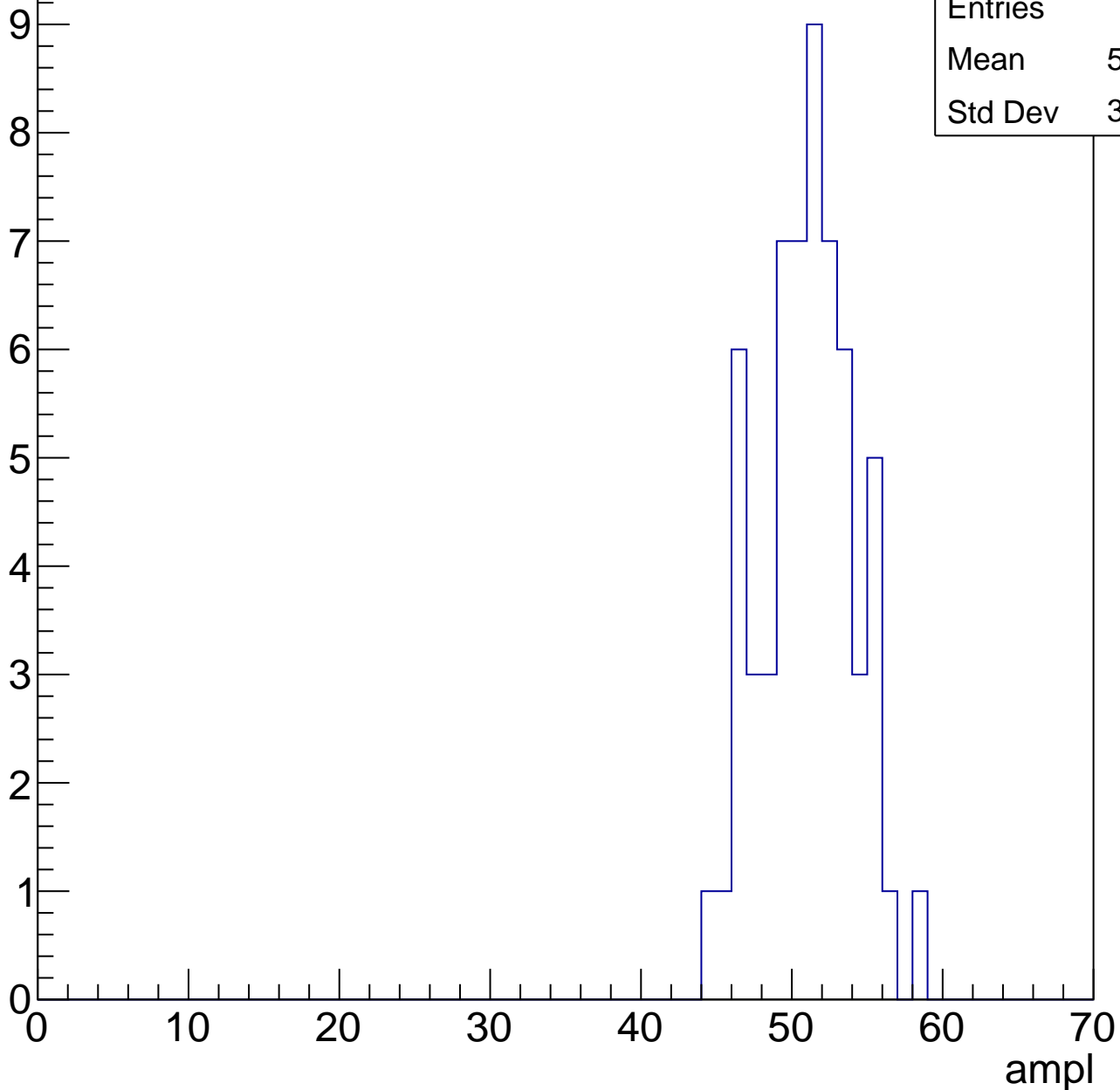


B1L003S, U13-ch18, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	50.58
Std Dev	3.018

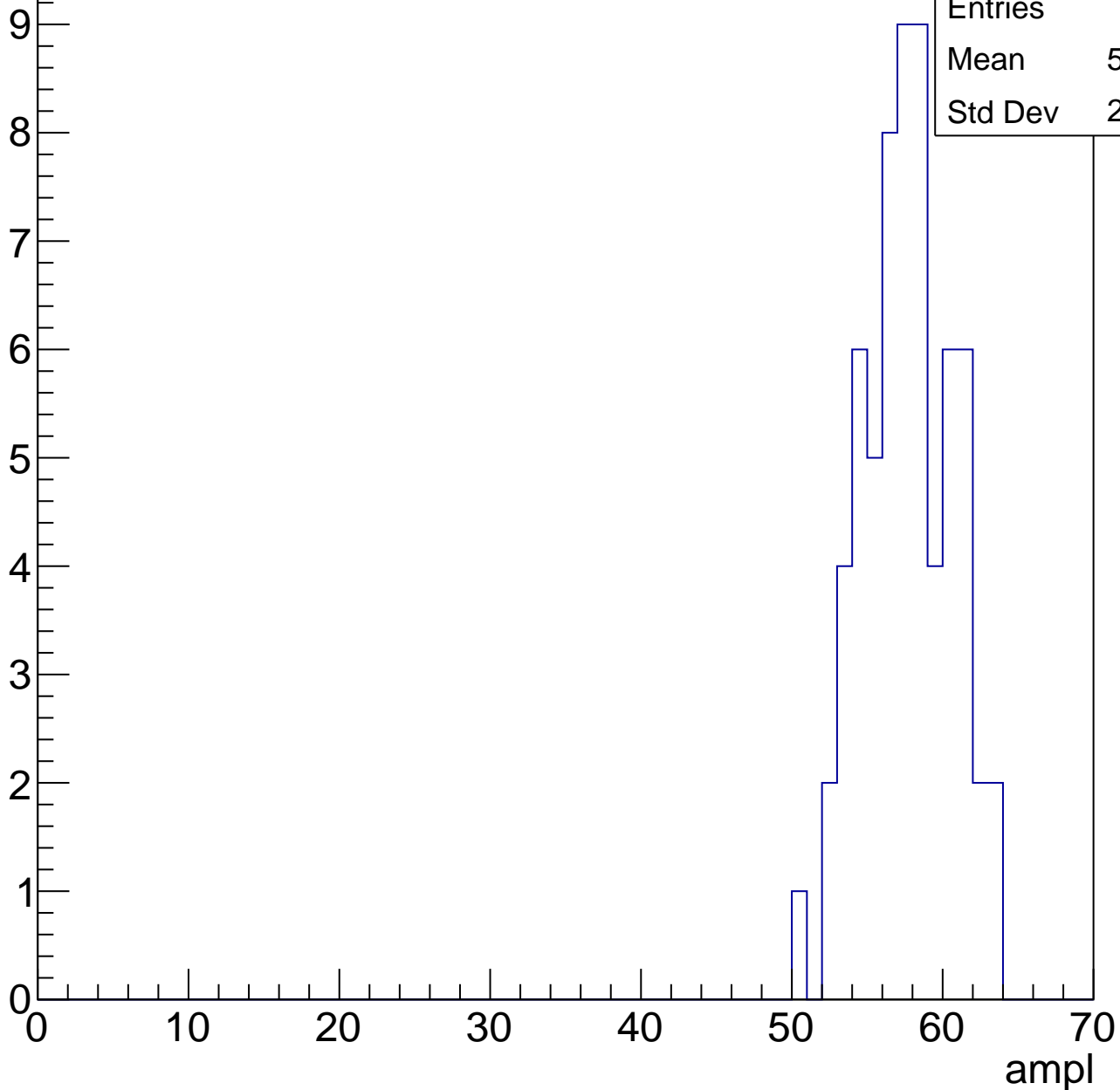


B1L003S, U13-ch18, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	57.19
Std Dev	2.904

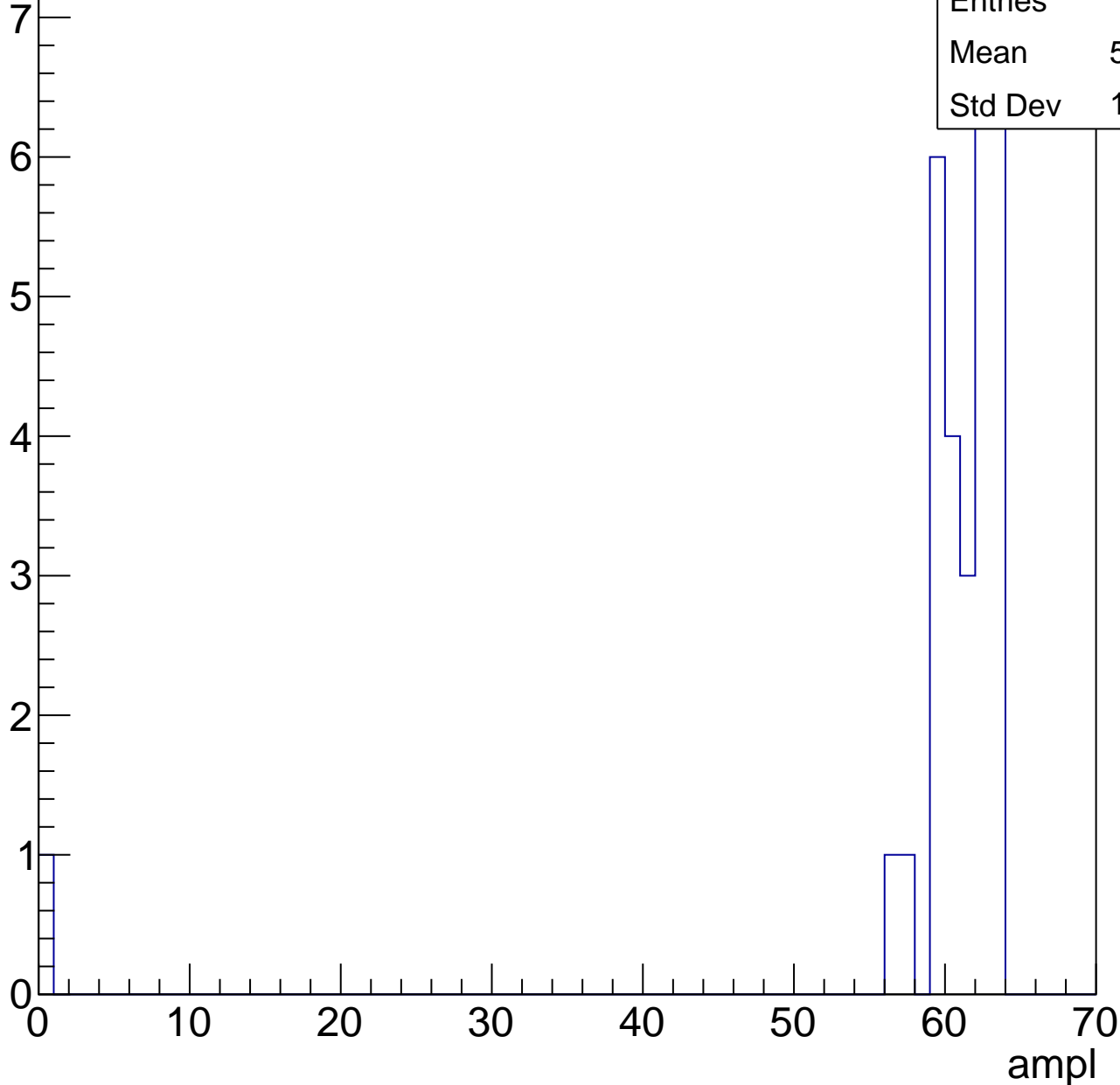


B1L003S, U13-ch18, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

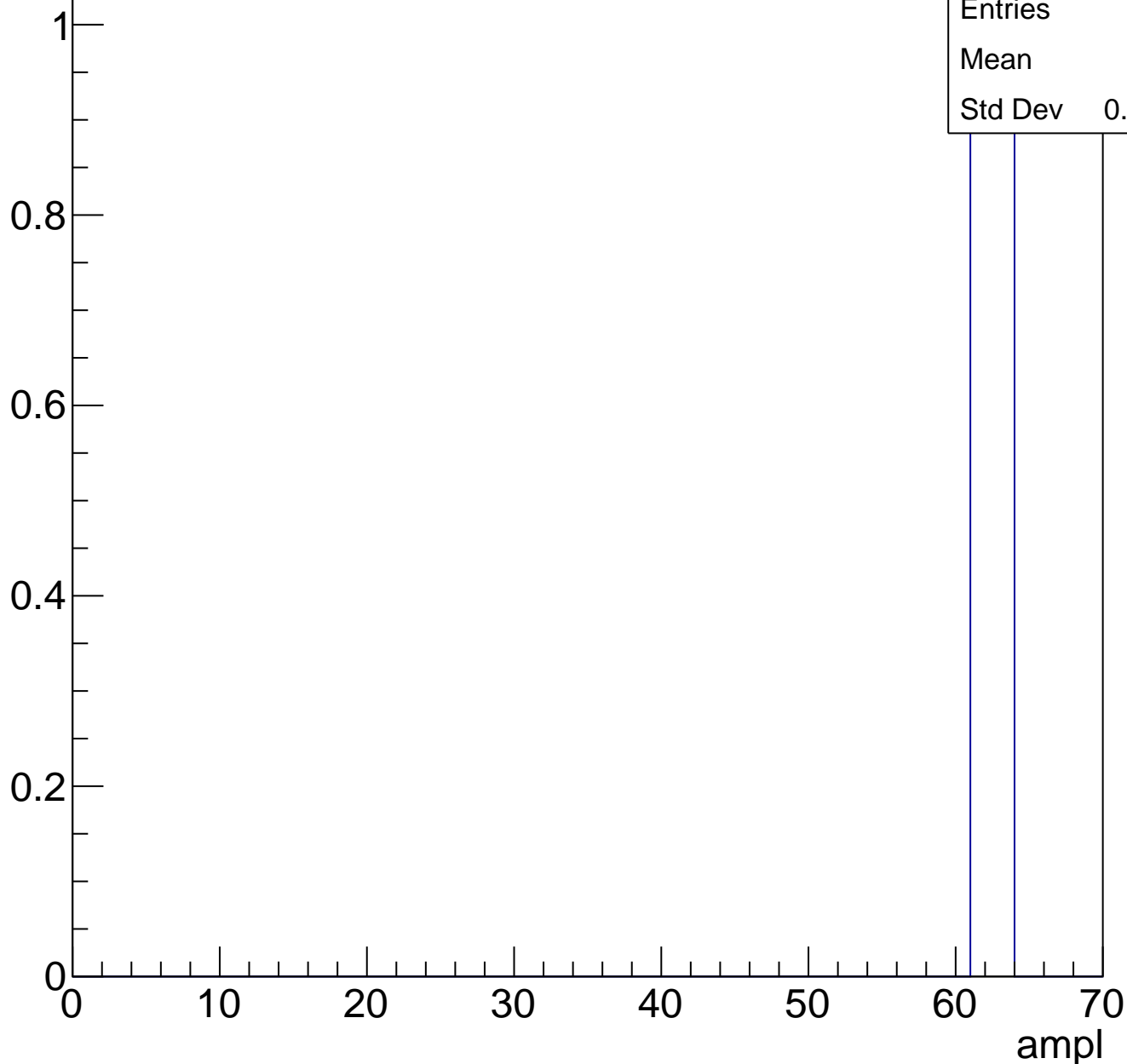
Entries	30
Mean	58.83
Std Dev	11.08



B1L003S, U13-ch18, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch18, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L003S, U13-ch19, adc0

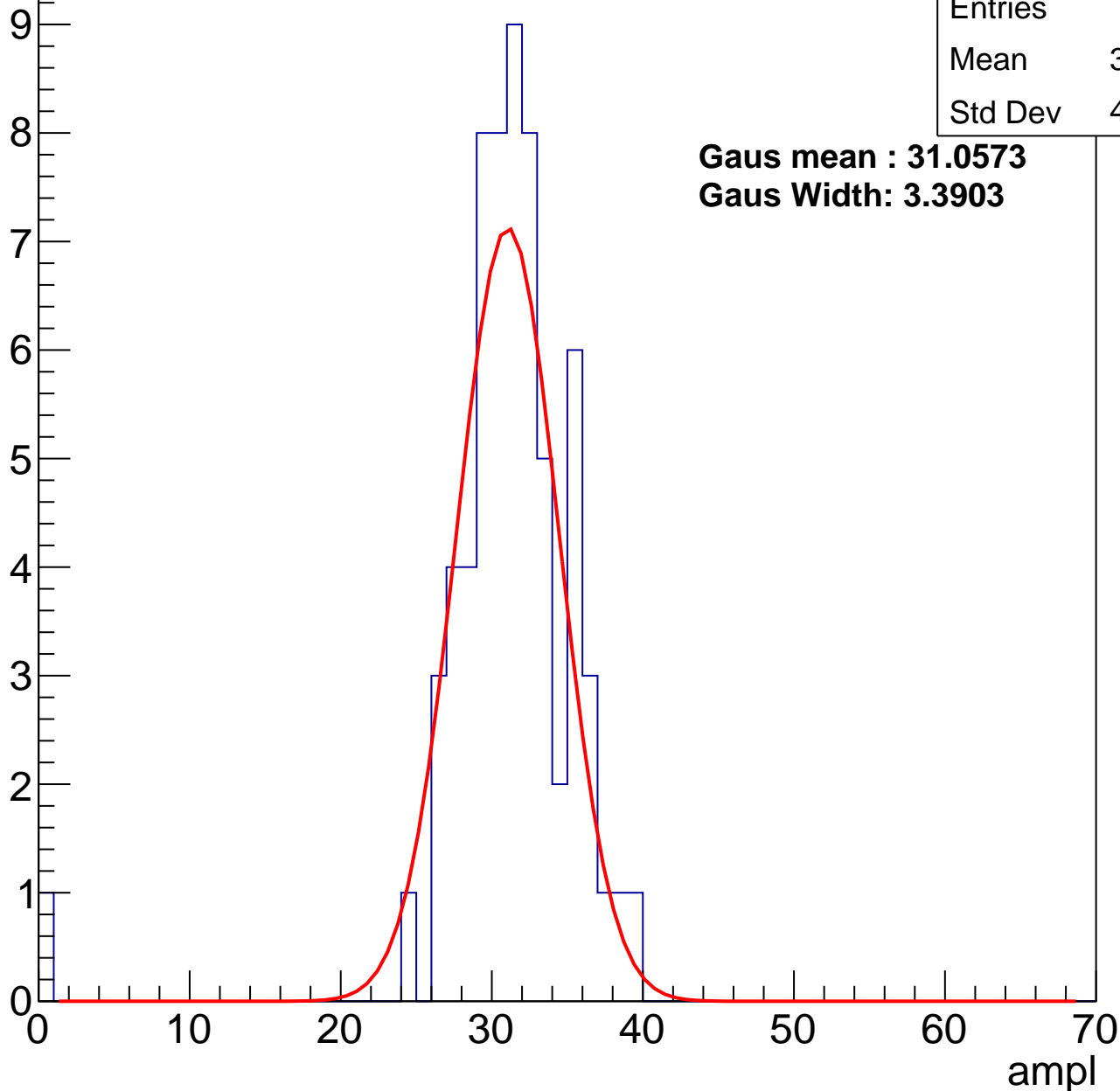
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.68
Std Dev	4.937

Gaus mean : 31.0573

Gaus Width: 3.3903



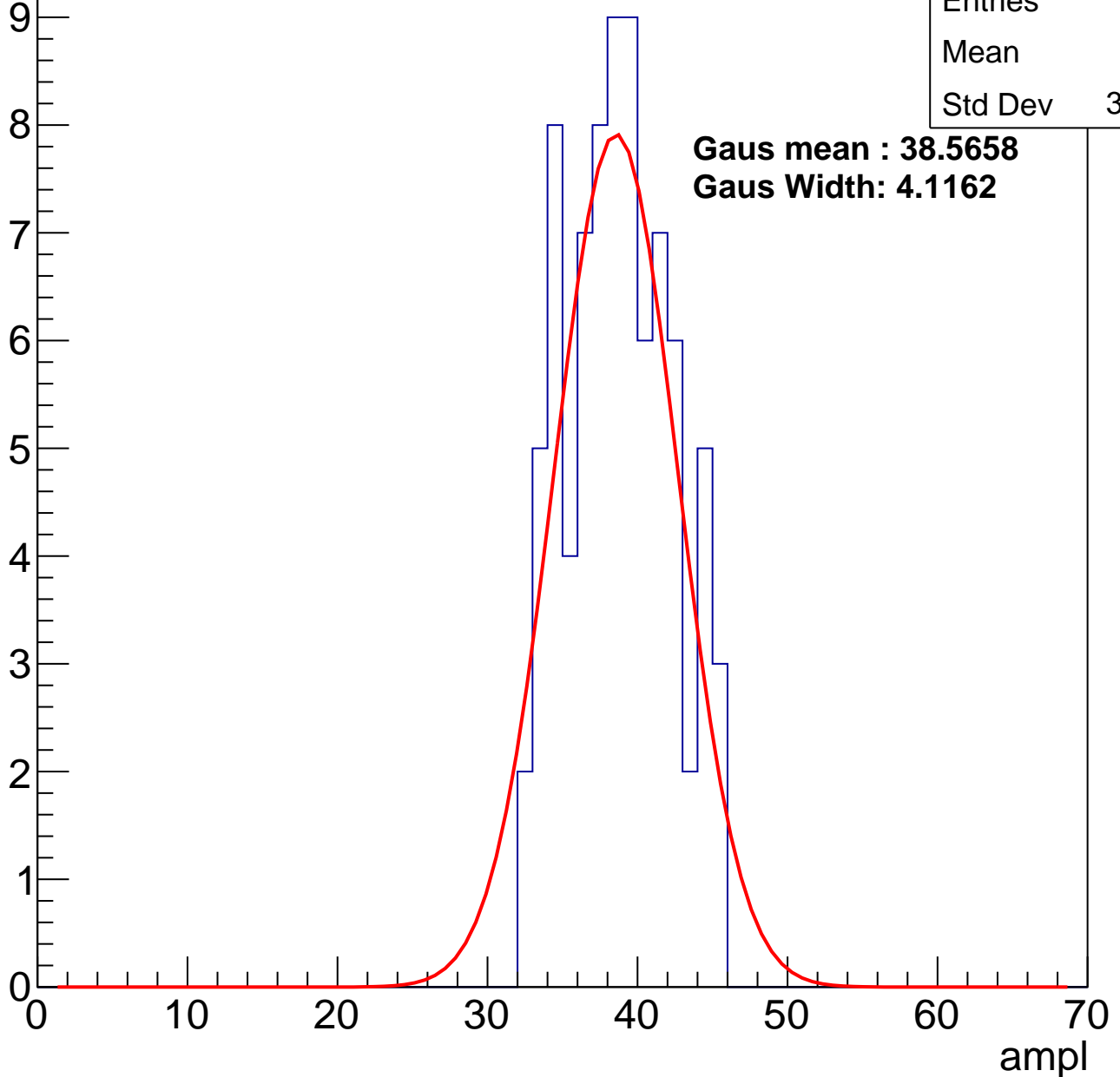
B1L003S, U13-ch19, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	38.3
Std Dev	3.441

Gaus mean : 38.5658
Gaus Width: 4.1162



B1L003S, U13-ch19, adc2

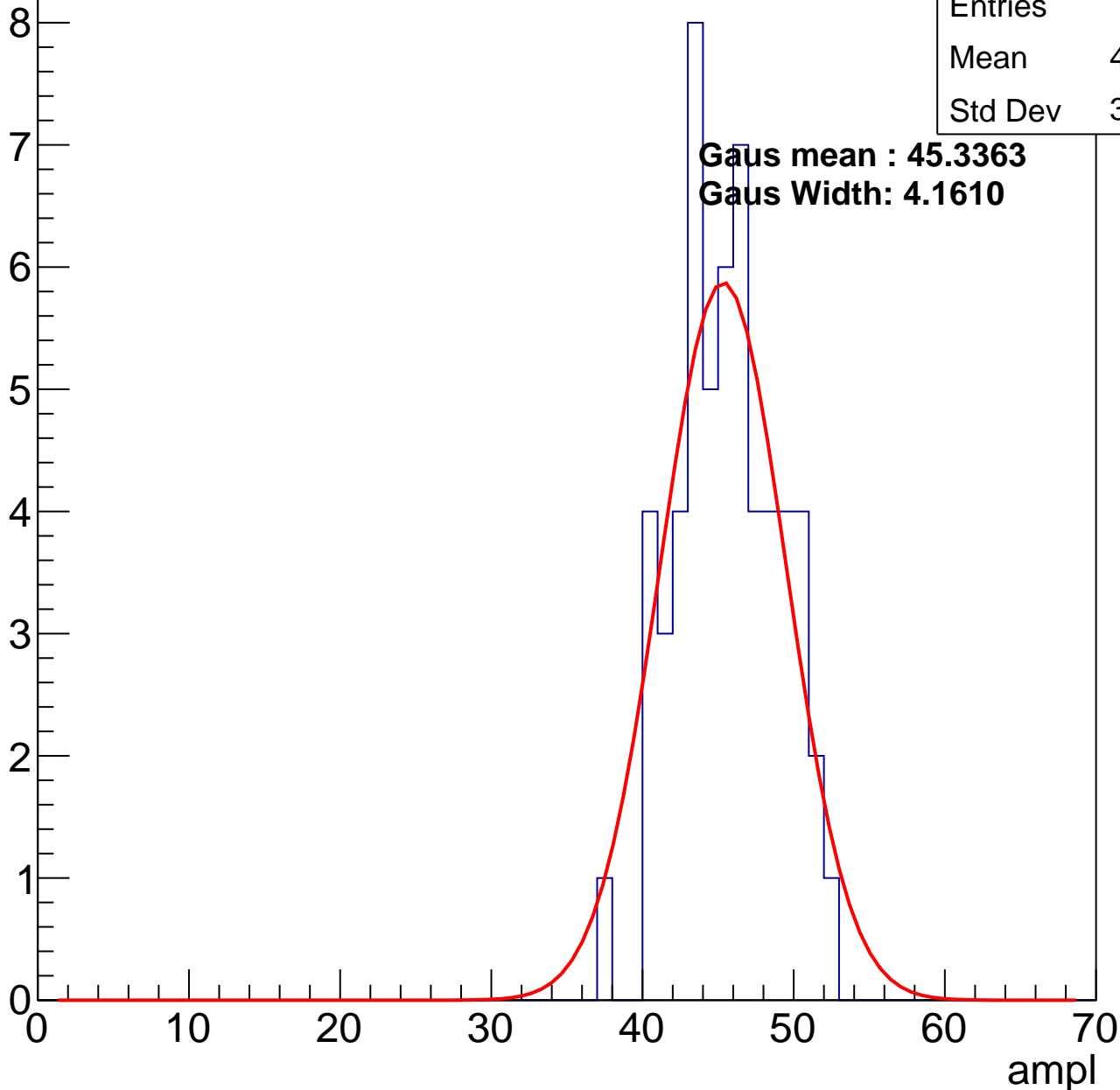
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	45.16
Std Dev	3.318

Gaus mean : 45.3363

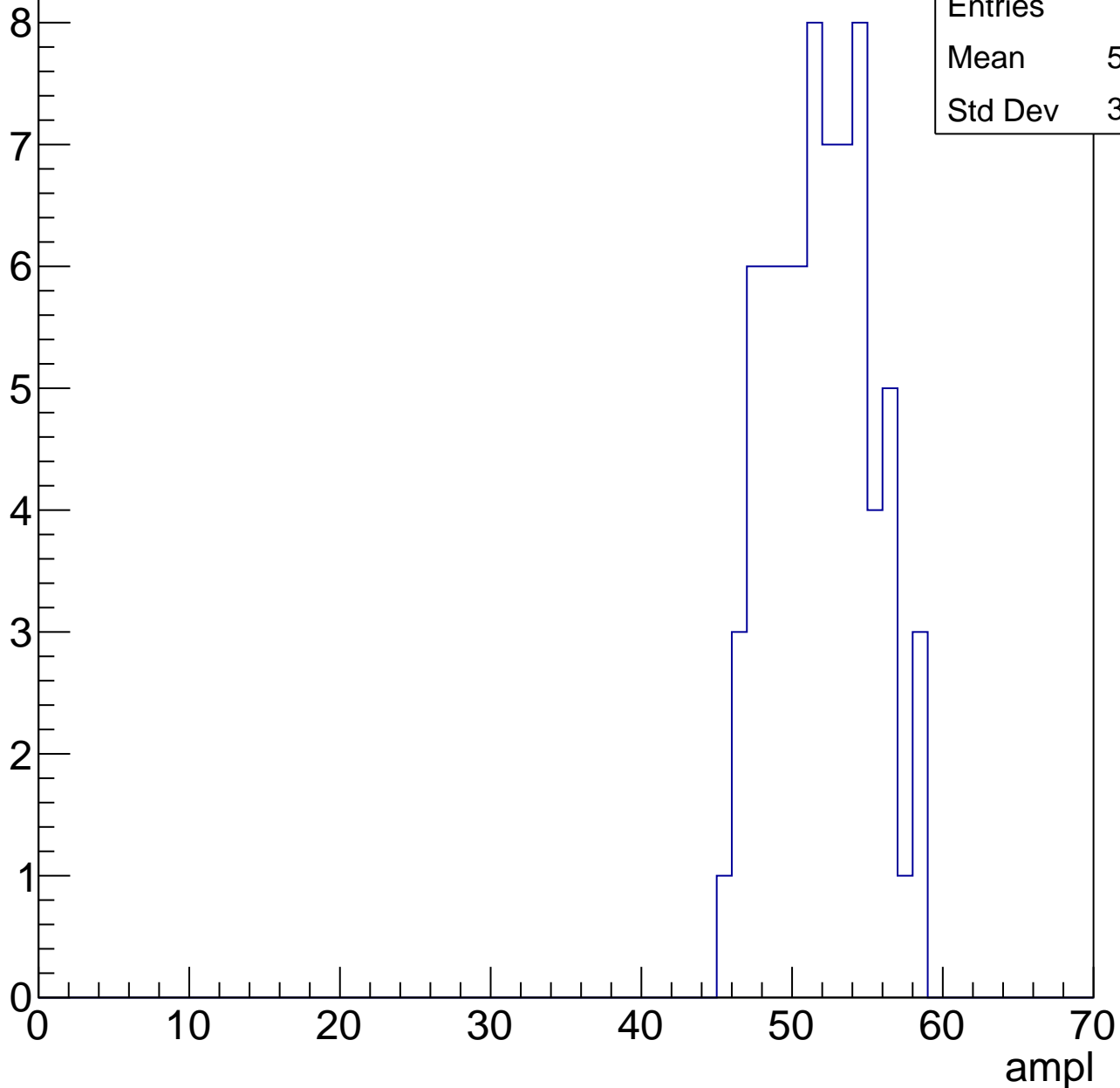
Gaus Width: 4.1610



B1L003S, U13-ch19, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

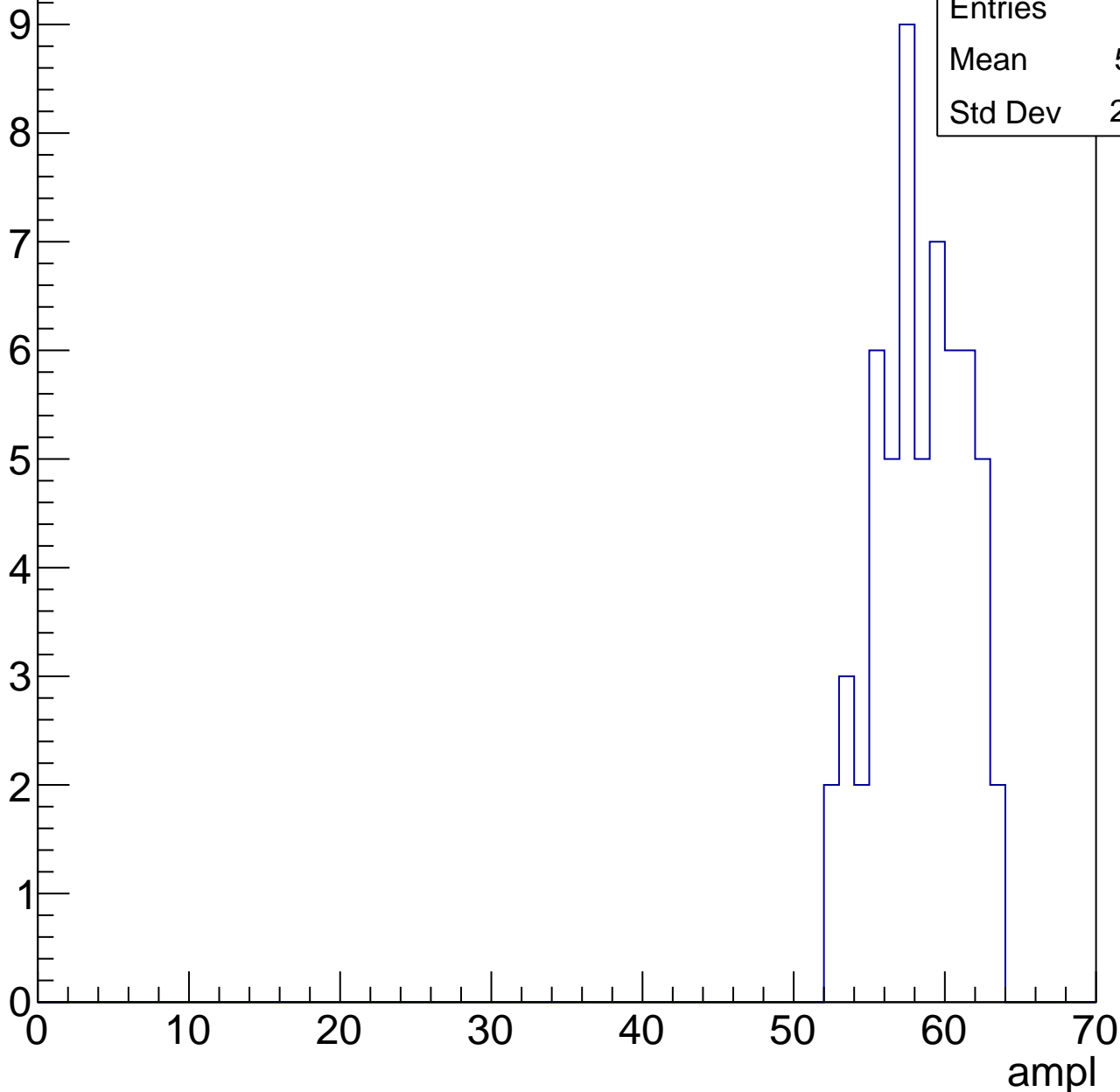


B1L003S, U13-ch19, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.91
Std Dev	2.866

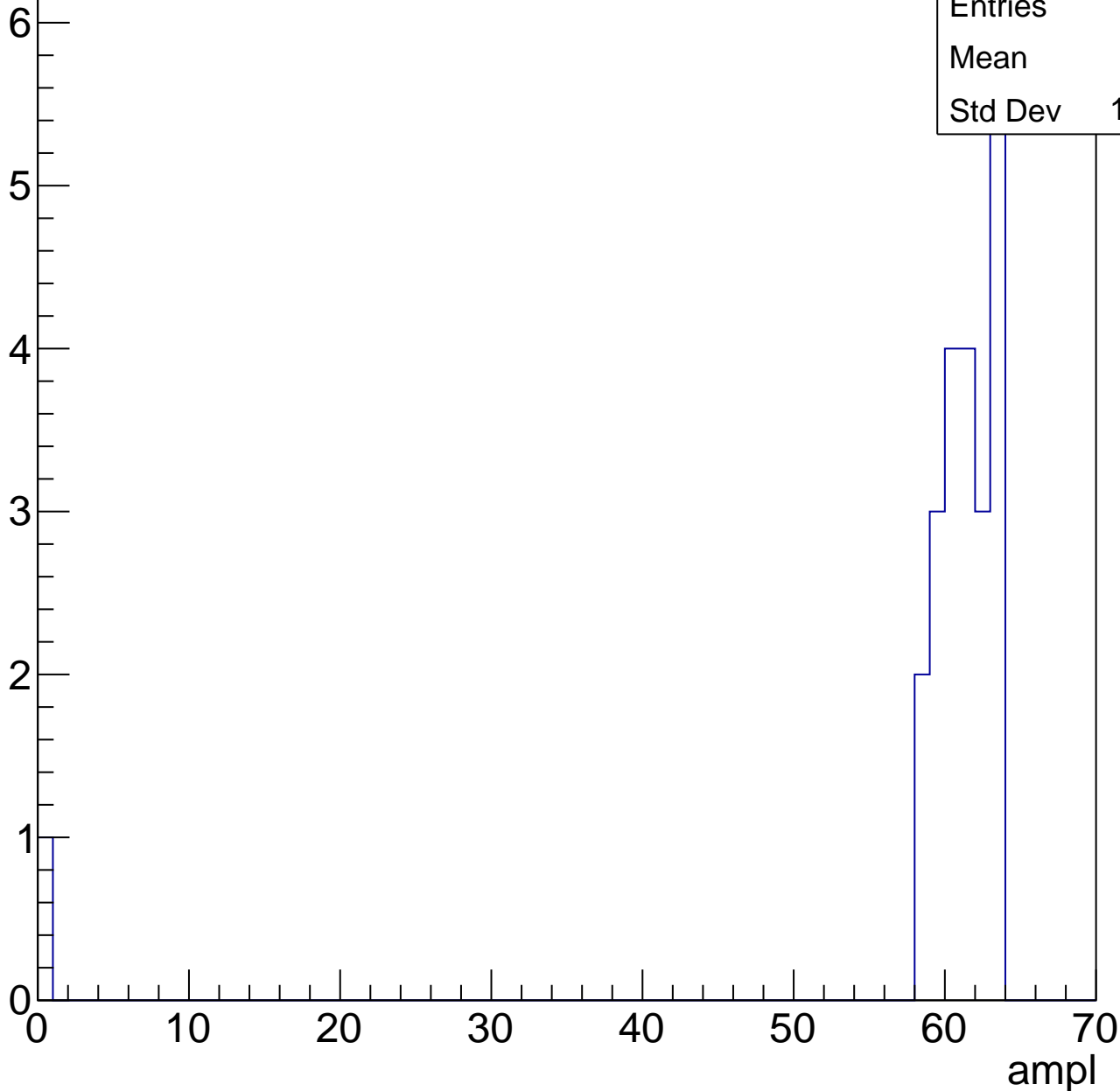


B1L003S, U13-ch19, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.3
Std Dev	12.54



B1L003S, U13-ch19, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch19, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L003S, U13-ch20, adc0

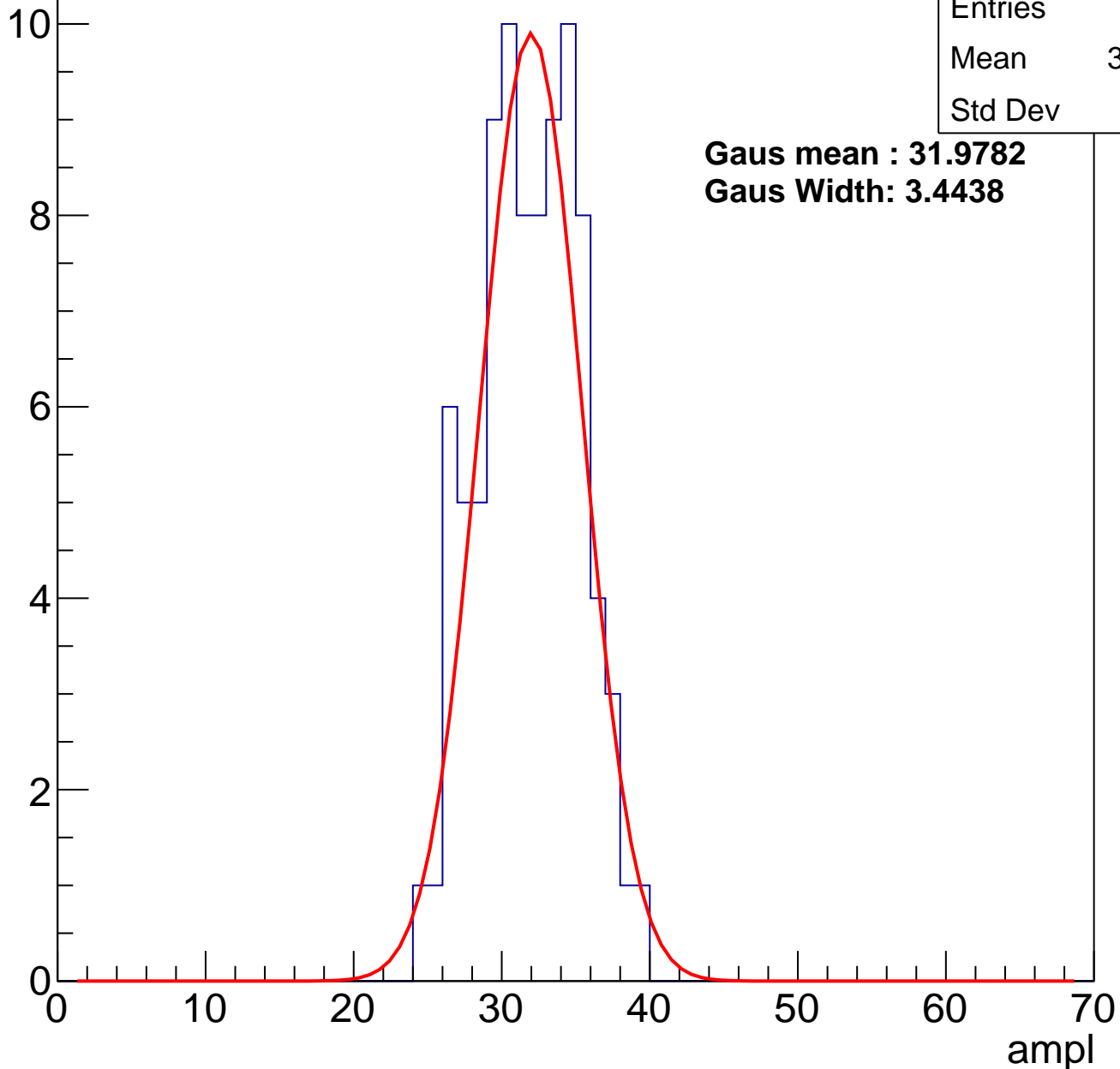
calib_packv5_042523_0143.root, FC#13, port D2

Entries	89
Mean	31.39
Std Dev	3.31

Gaus mean : 31.9782

Gaus Width: 3.4438

Entry



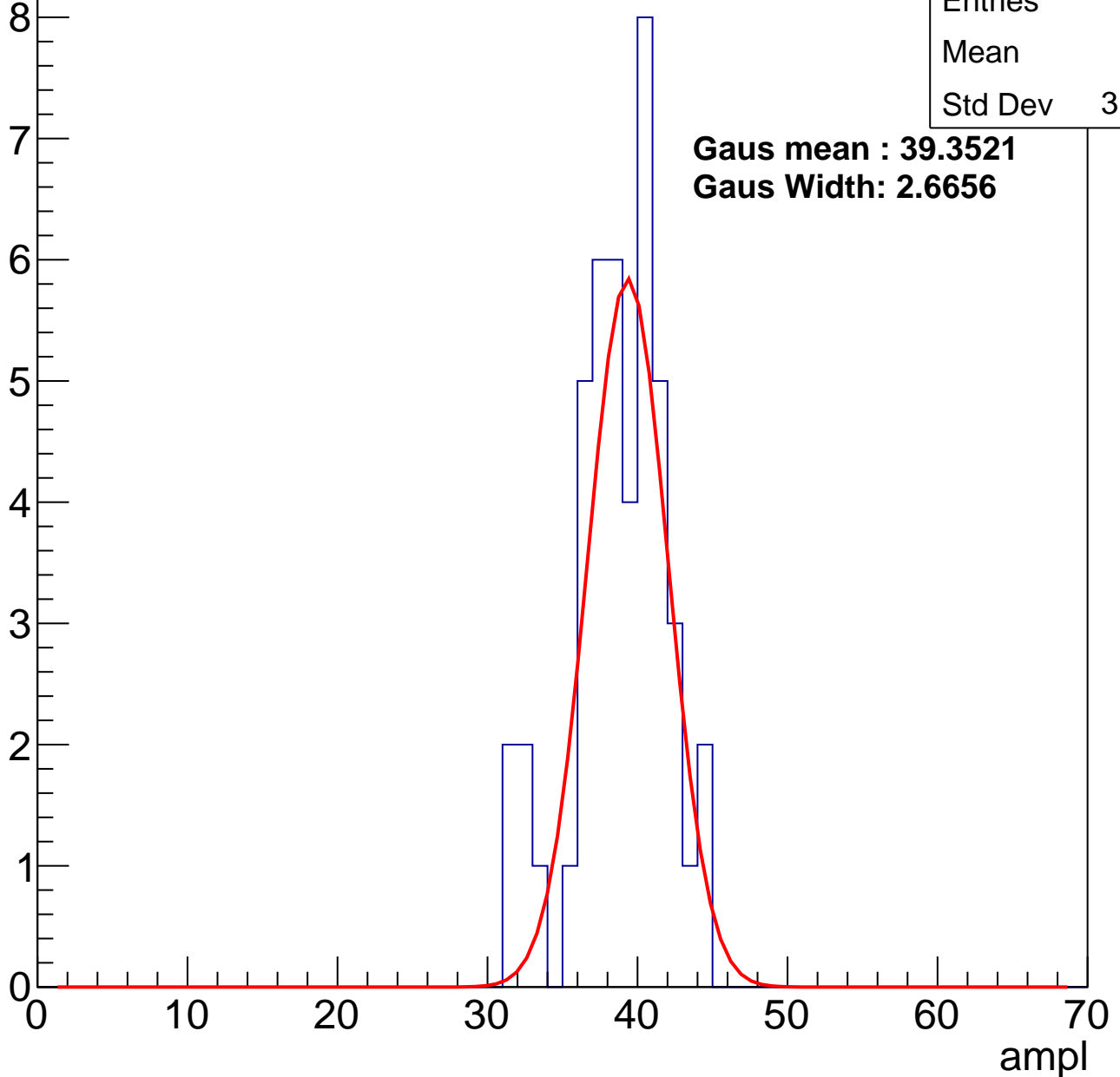
B1L003S, U13-ch20, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	38.3
Std Dev	3.134

Gaus mean : 39.3521
Gaus Width: 2.6656



B1L003S, U13-ch20, adc2

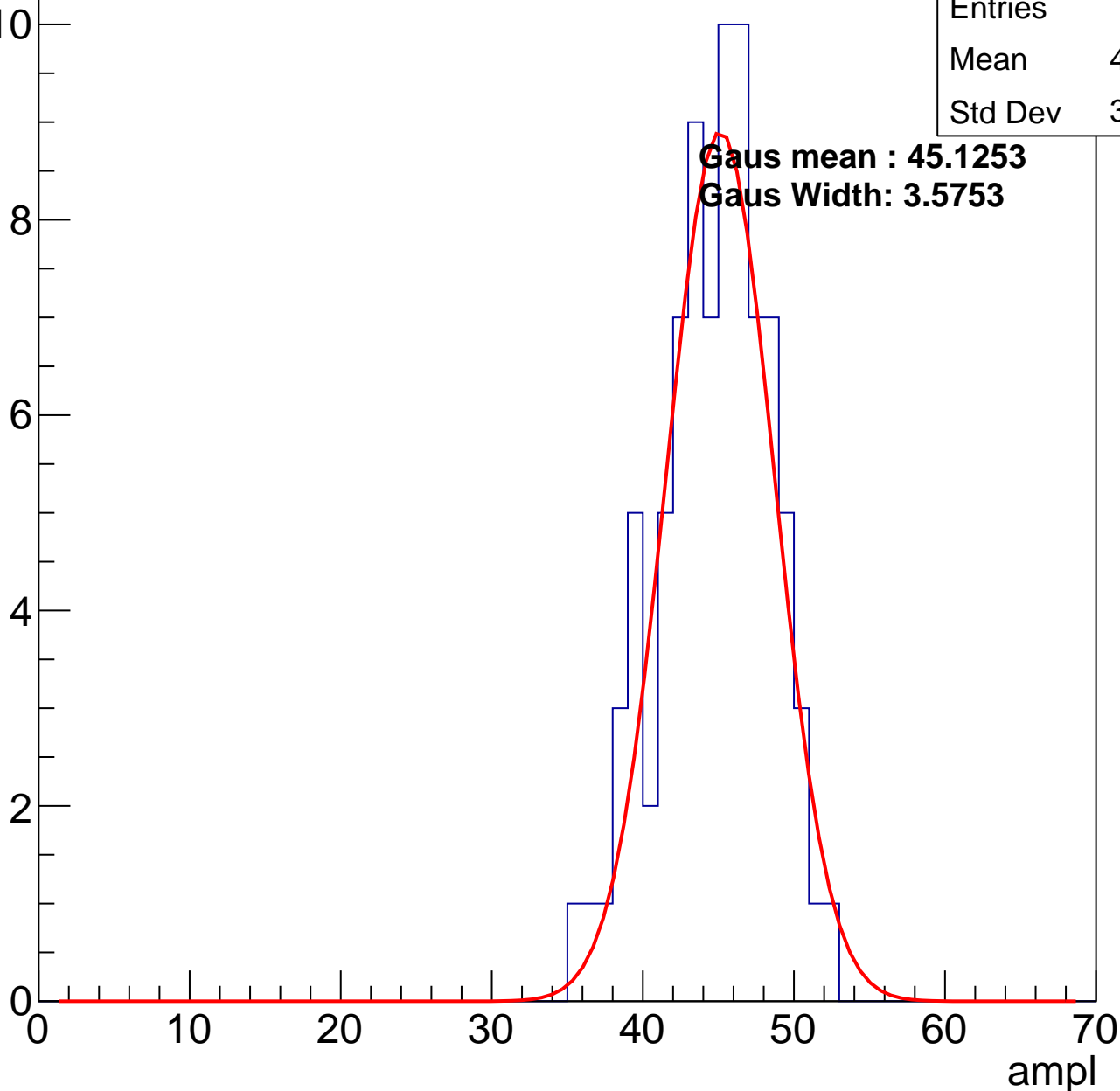
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	44.28
Std Dev	3.606

Gaus mean : 45.1253

Gaus Width: 3.5753

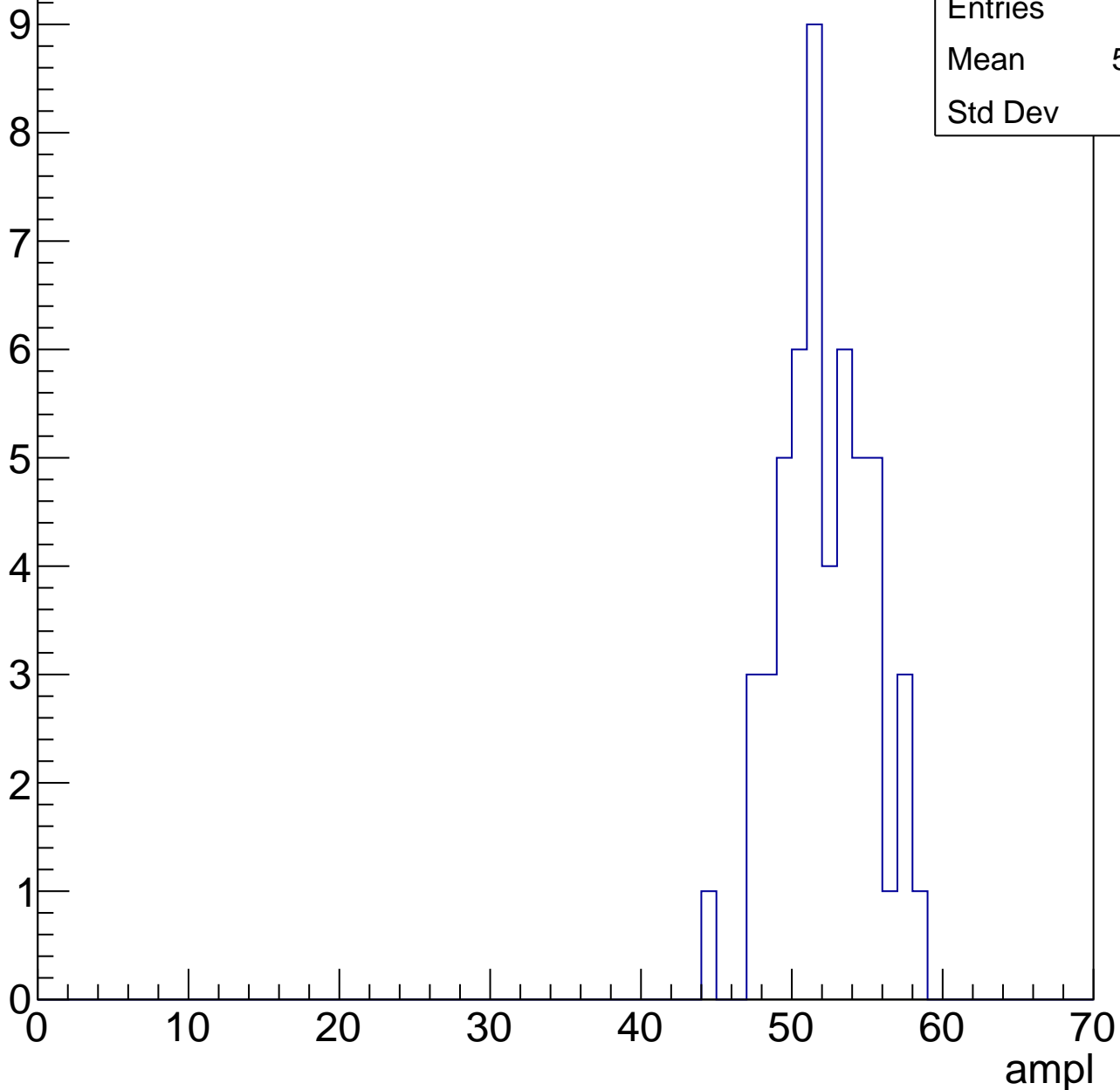


B1L003S, U13-ch20, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	51.71
Std Dev	2.97

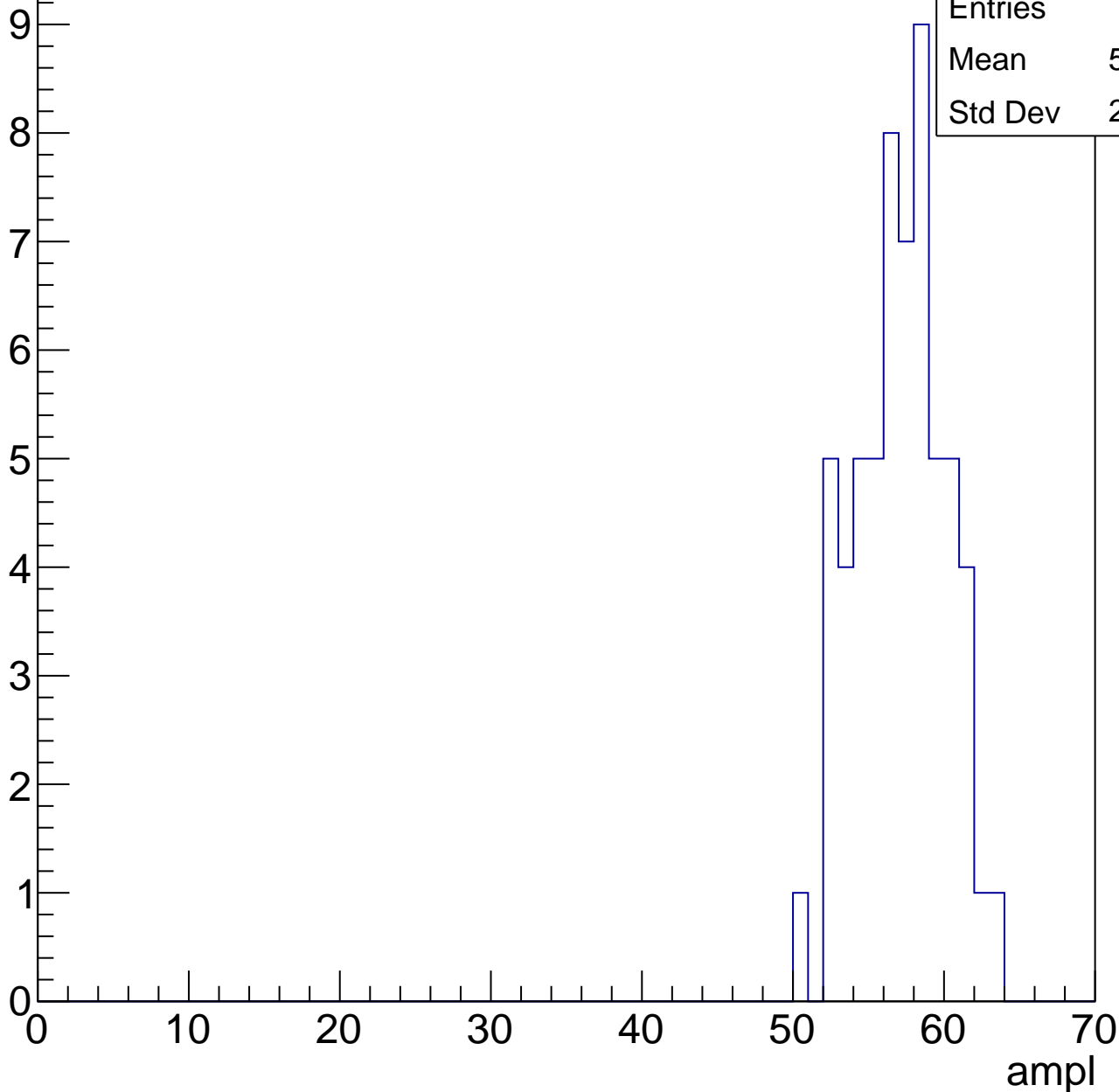


B1L003S, U13-ch20, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	56.67
Std Dev	2.896

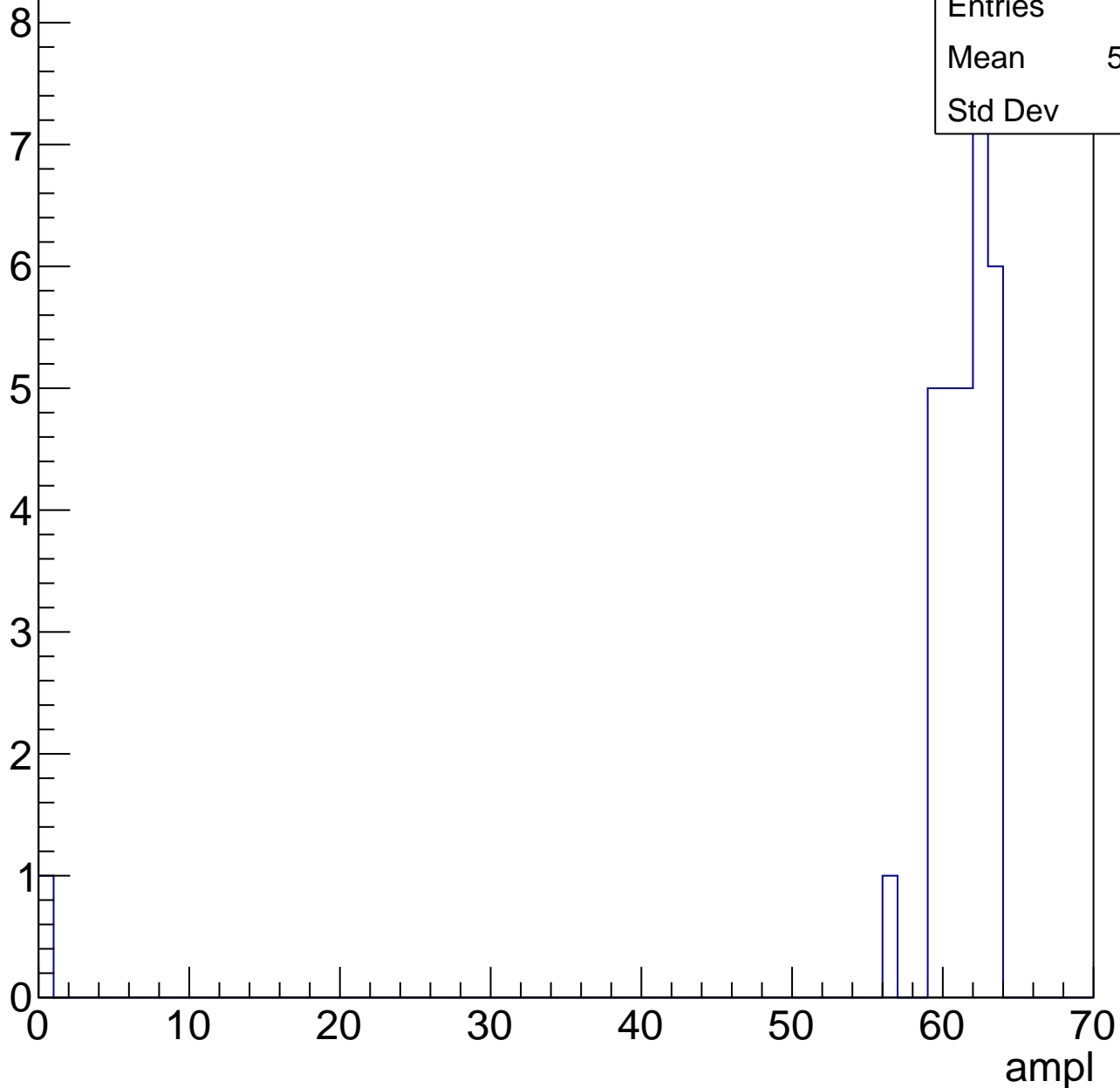


B1L003S, U13-ch20, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	31
Mean	59.03
Std Dev	10.9



B1L003S, U13-ch20, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch20, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch21, adc0

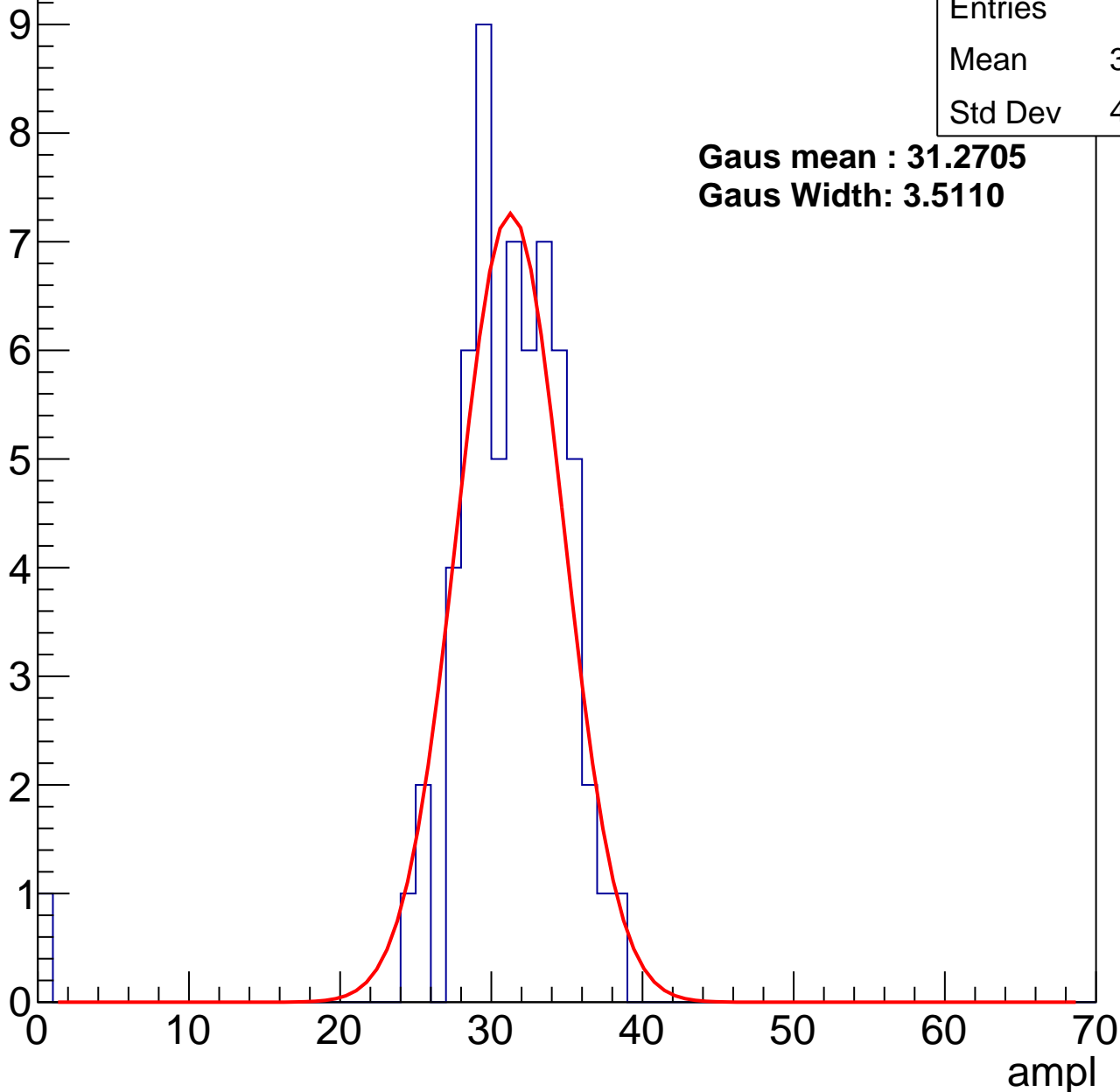
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.59
Std Dev	4.933

Gaus mean : 31.2705

Gaus Width: 3.5110



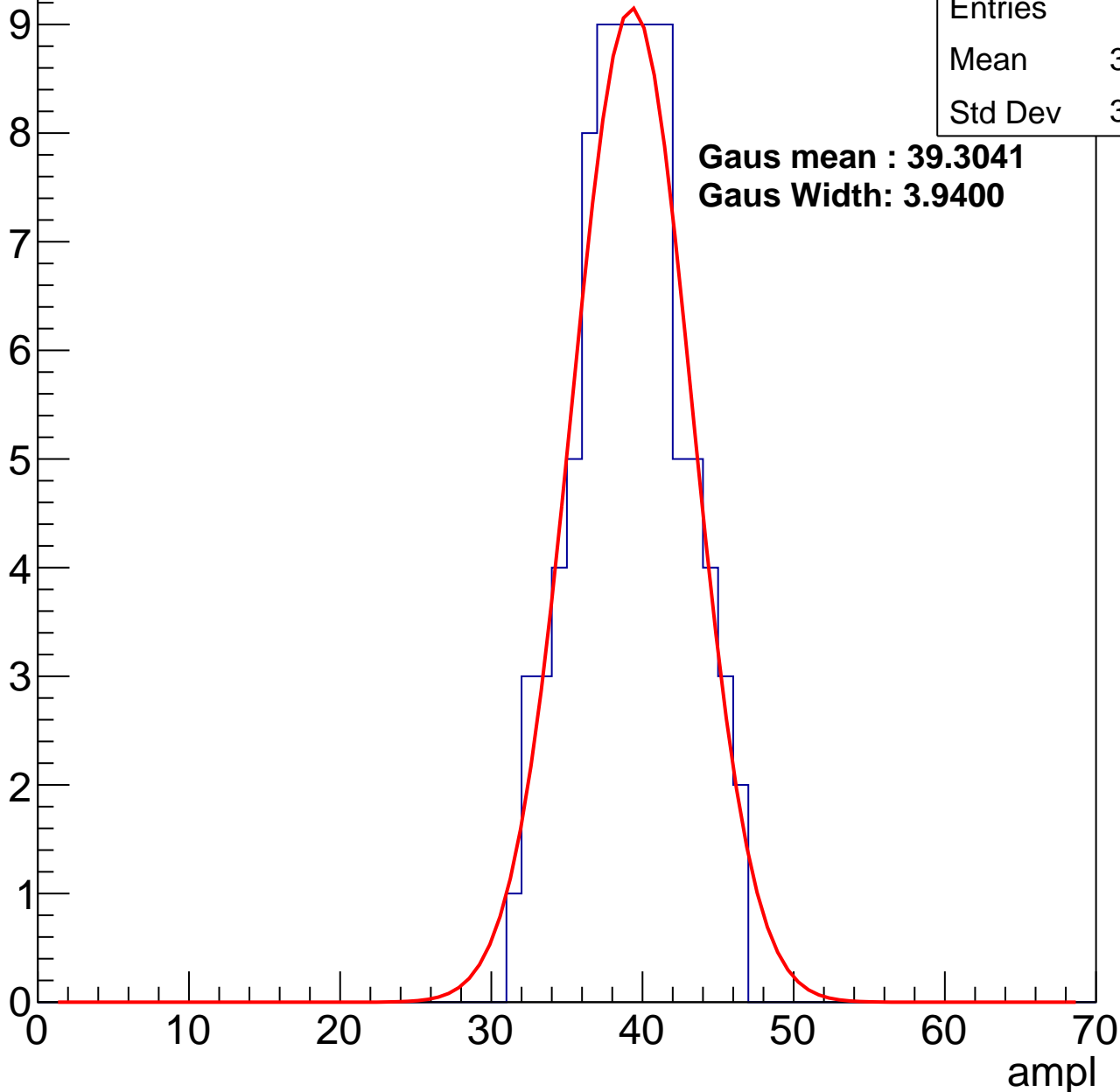
B1L003S, U13-ch21, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	88
Mean	38.73
Std Dev	3.512

Gaus mean : 39.3041
Gaus Width: 3.9400



B1L003S, U13-ch21, adc2

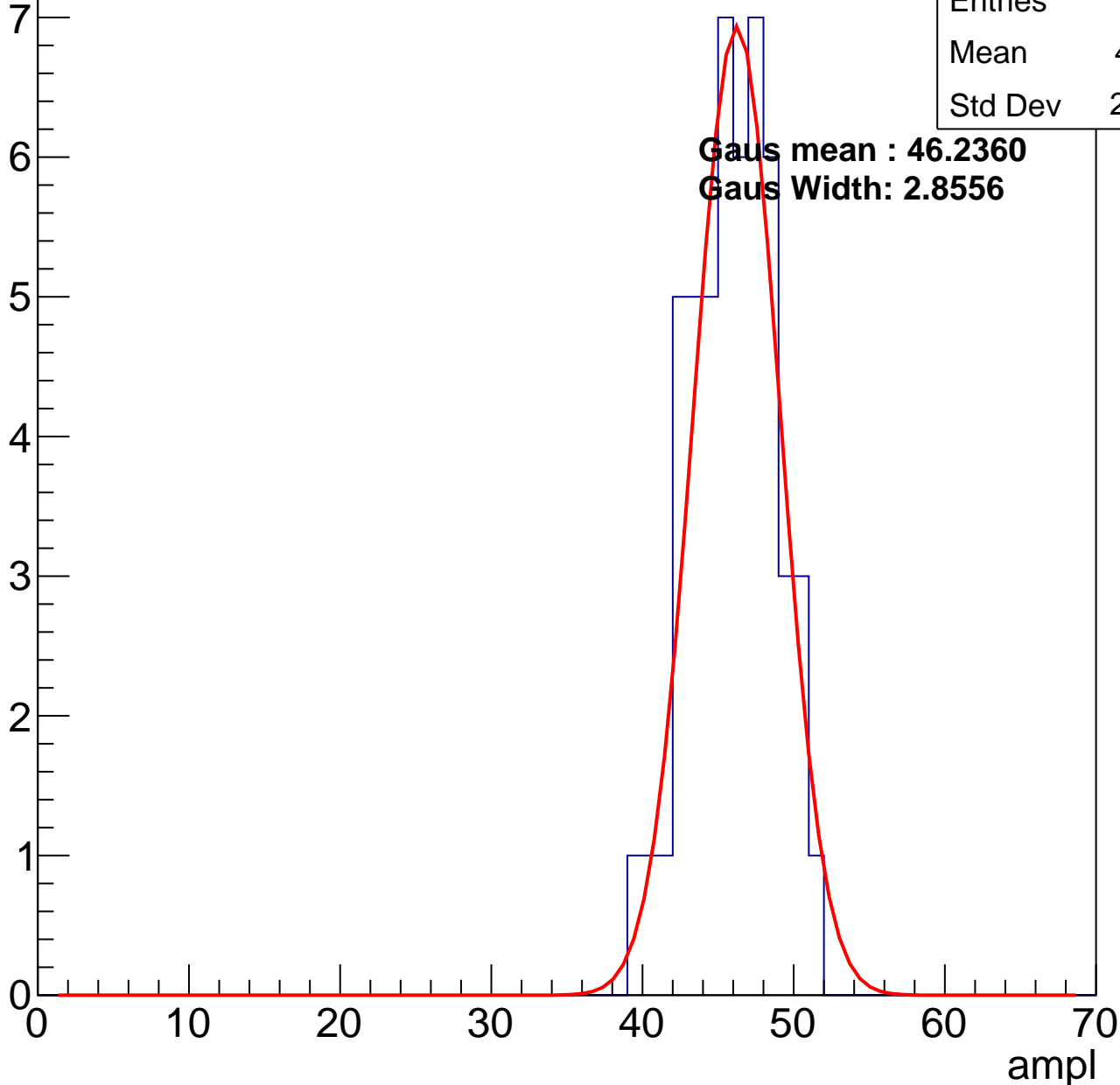
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	45.51
Std Dev	2.732

Gaus mean : 46.2360

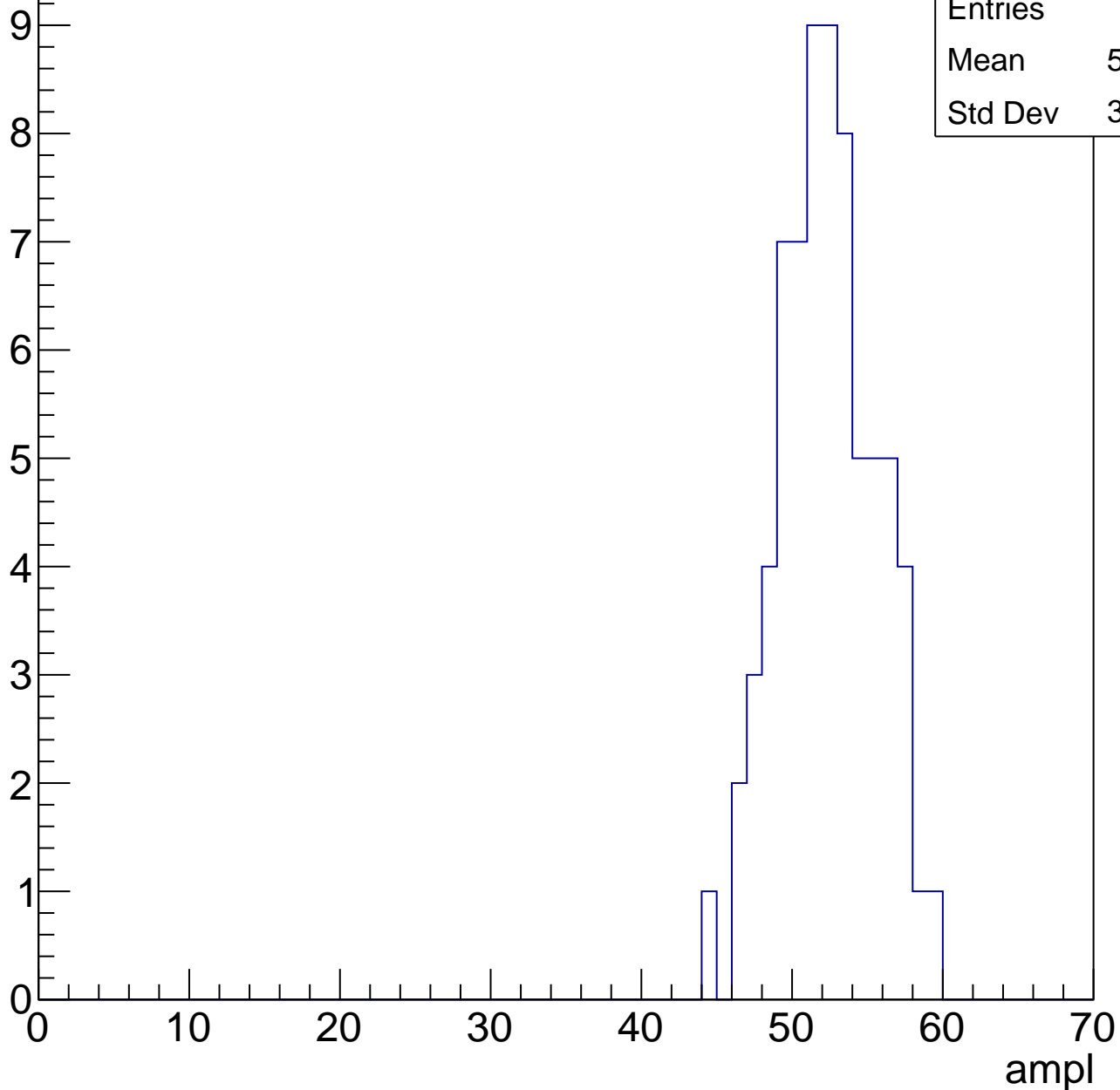
Gaus Width: 2.8556



B1L003S, U13-ch21, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

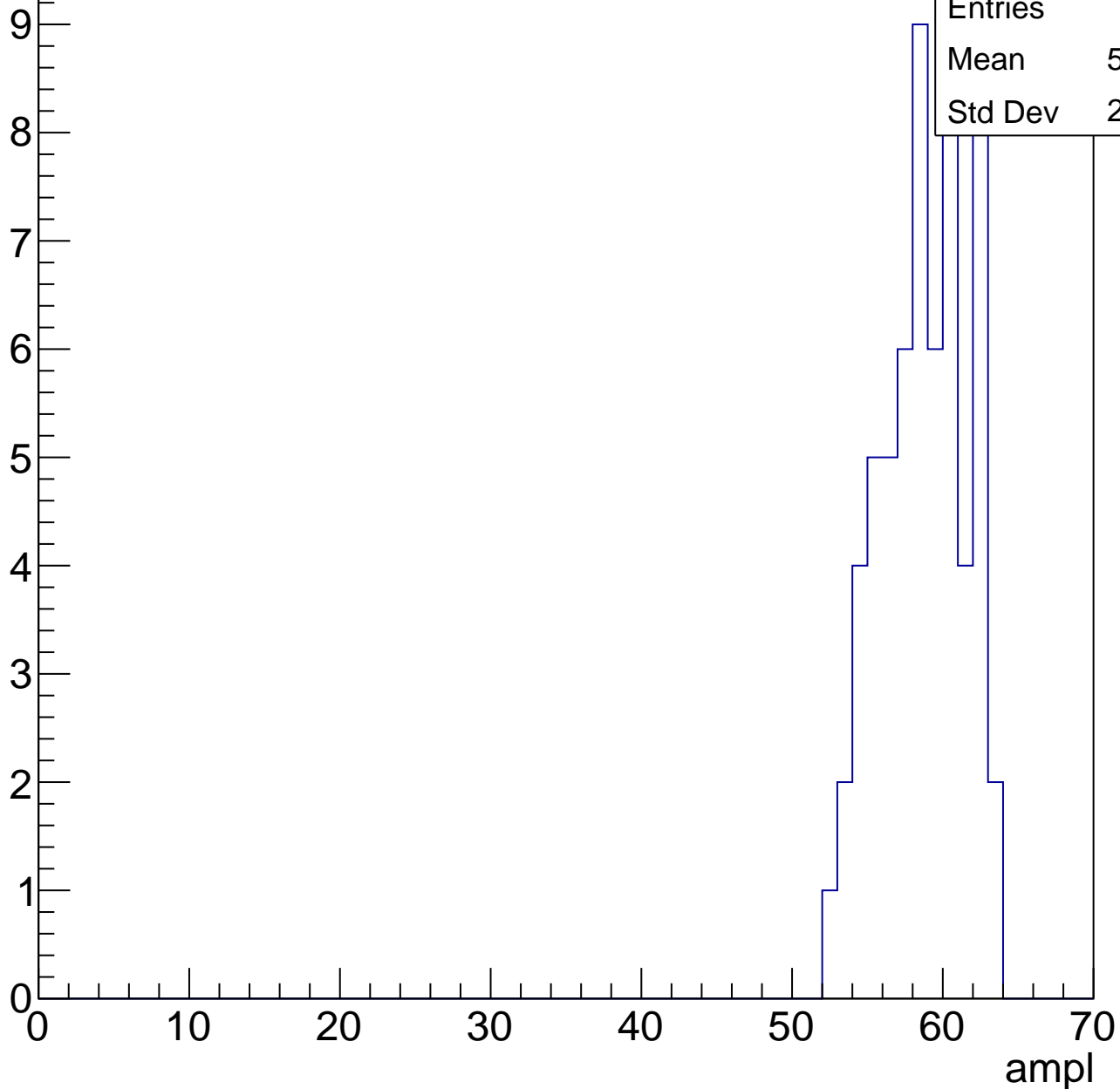


Entries	71
Mean	51.87
Std Dev	3.166

B1L003S, U13-ch21, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



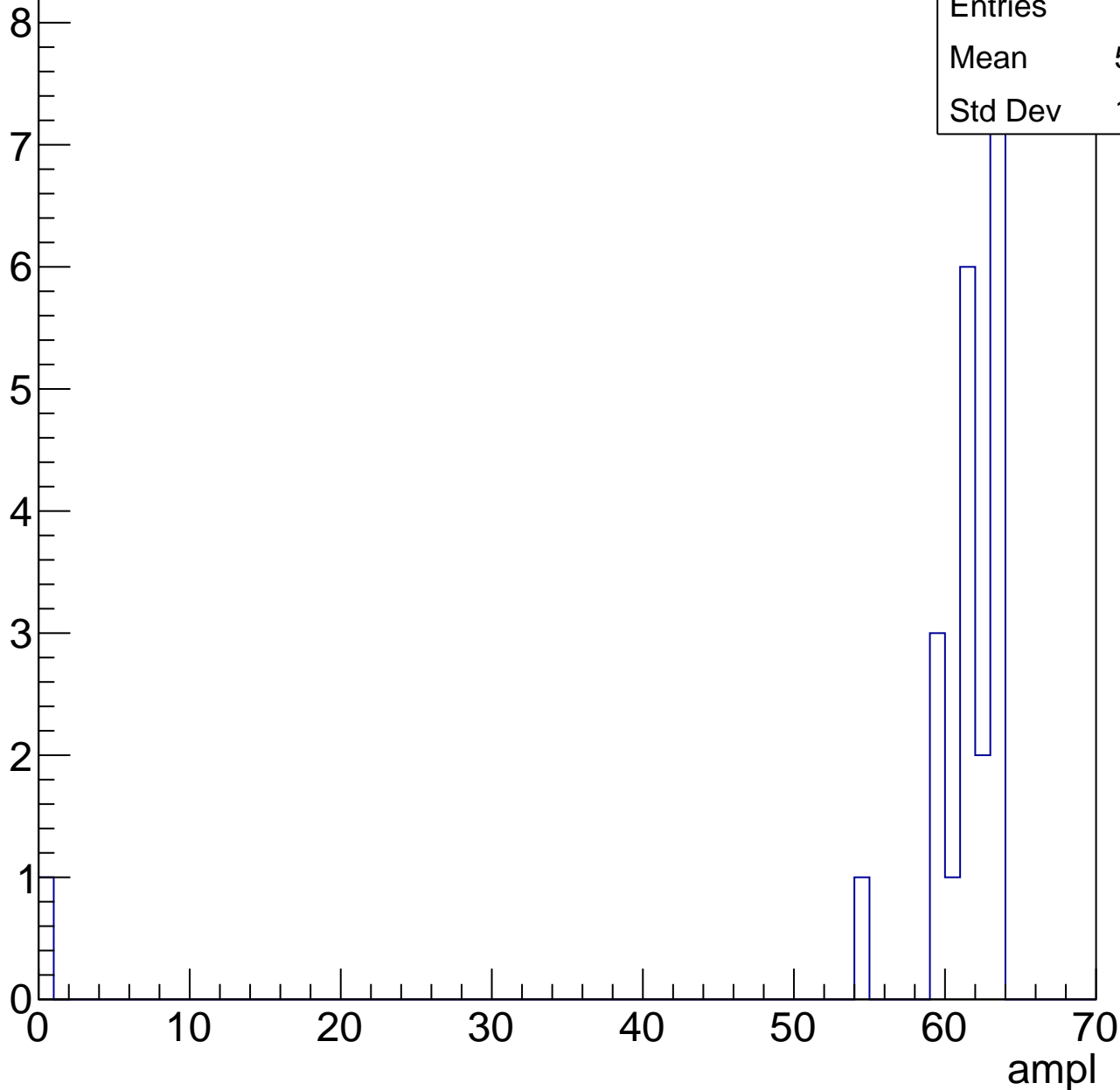
Entries	61
Mean	58.25
Std Dev	2.786

B1L003S, U13-ch21, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	22
Mean	58.41
Std Dev	12.91



B1L003S, U13-ch21, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch21, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L003S, U13-ch22, adc0

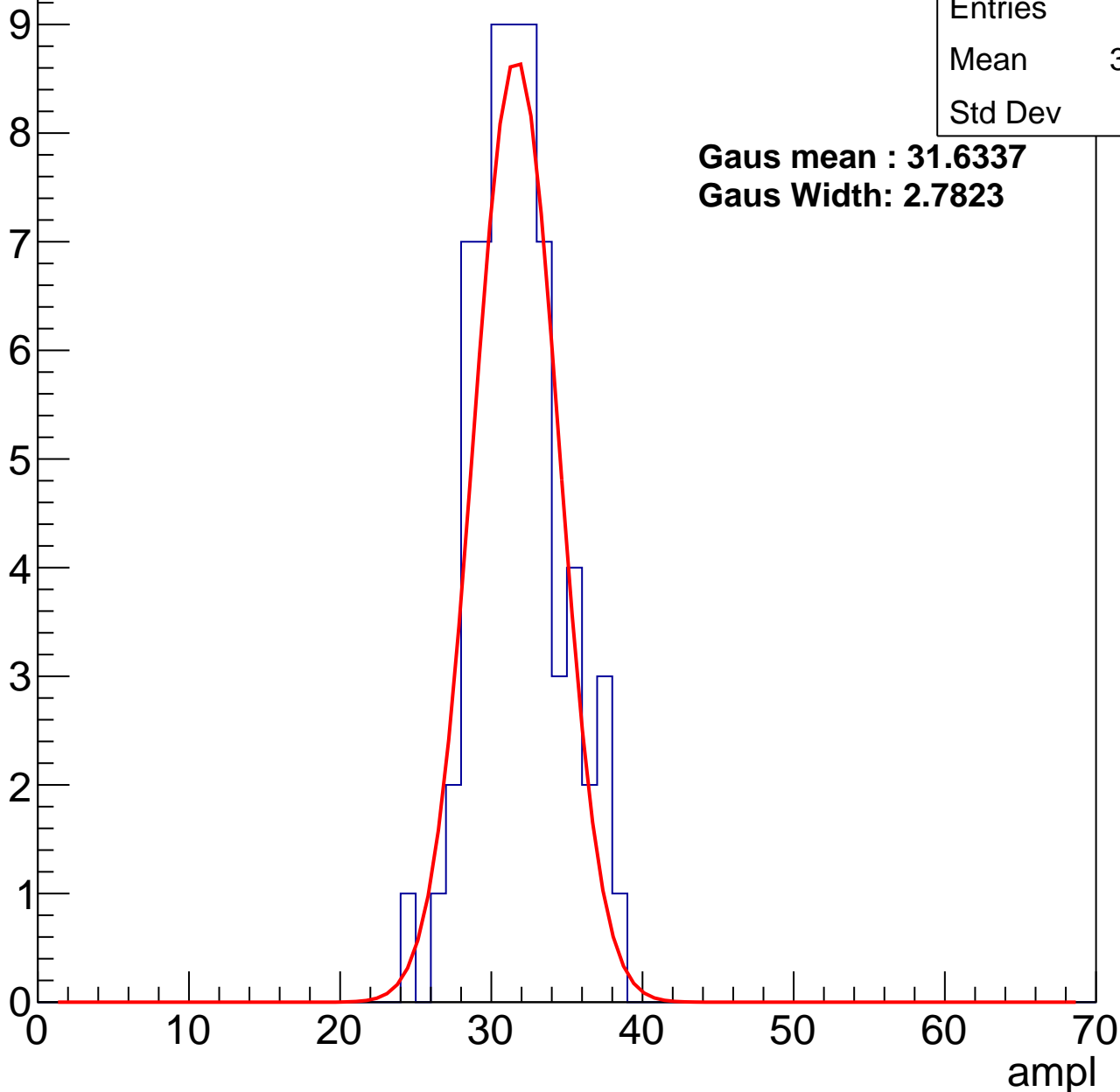
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	31.29
Std Dev	2.87

Gaus mean : 31.6337

Gaus Width: 2.7823



B1L003S, U13-ch22, adc1

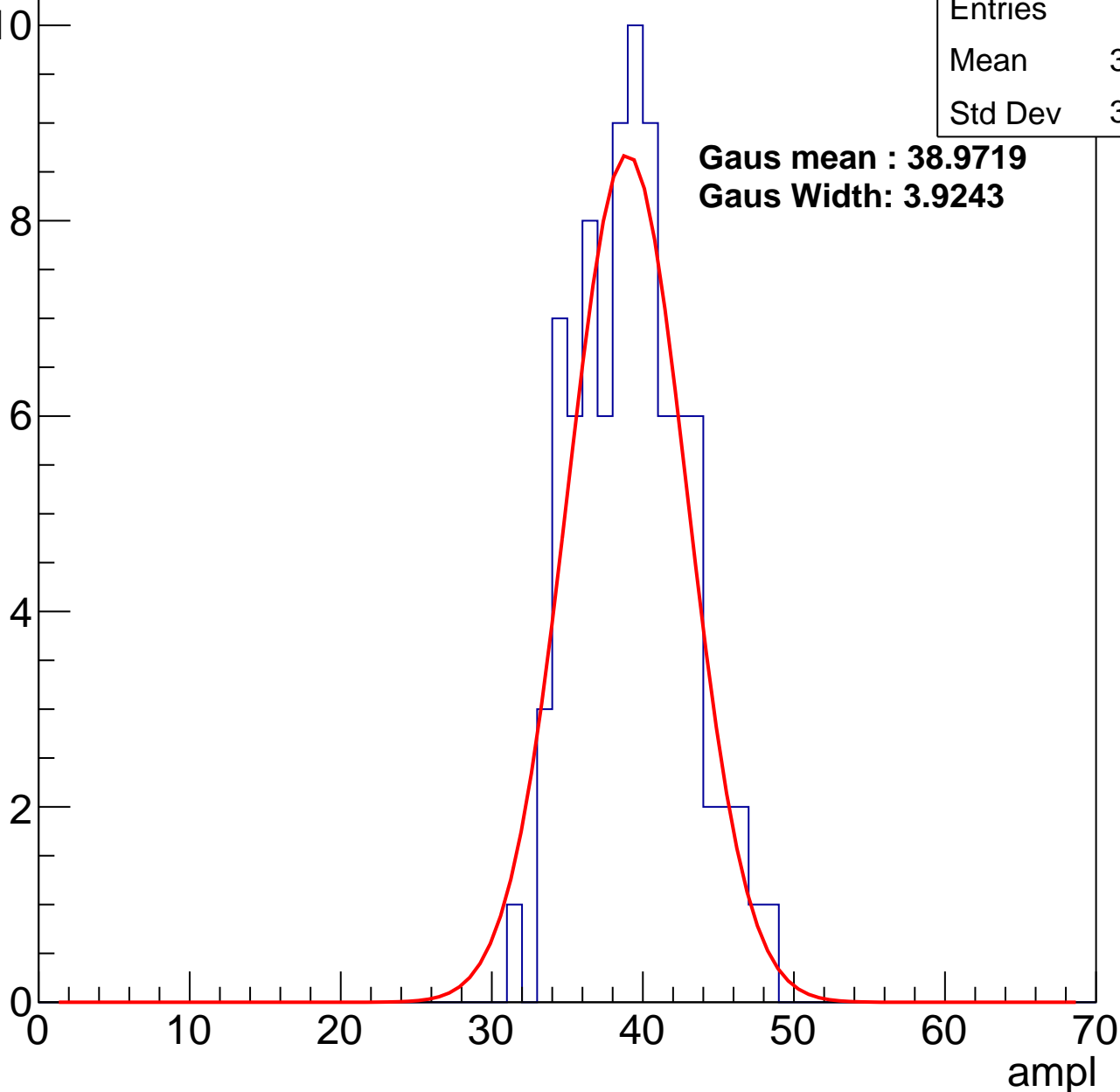
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	38.84
Std Dev	3.587

Gaus mean : 38.9719

Gaus Width: 3.9243



B1L003S, U13-ch22, adc2

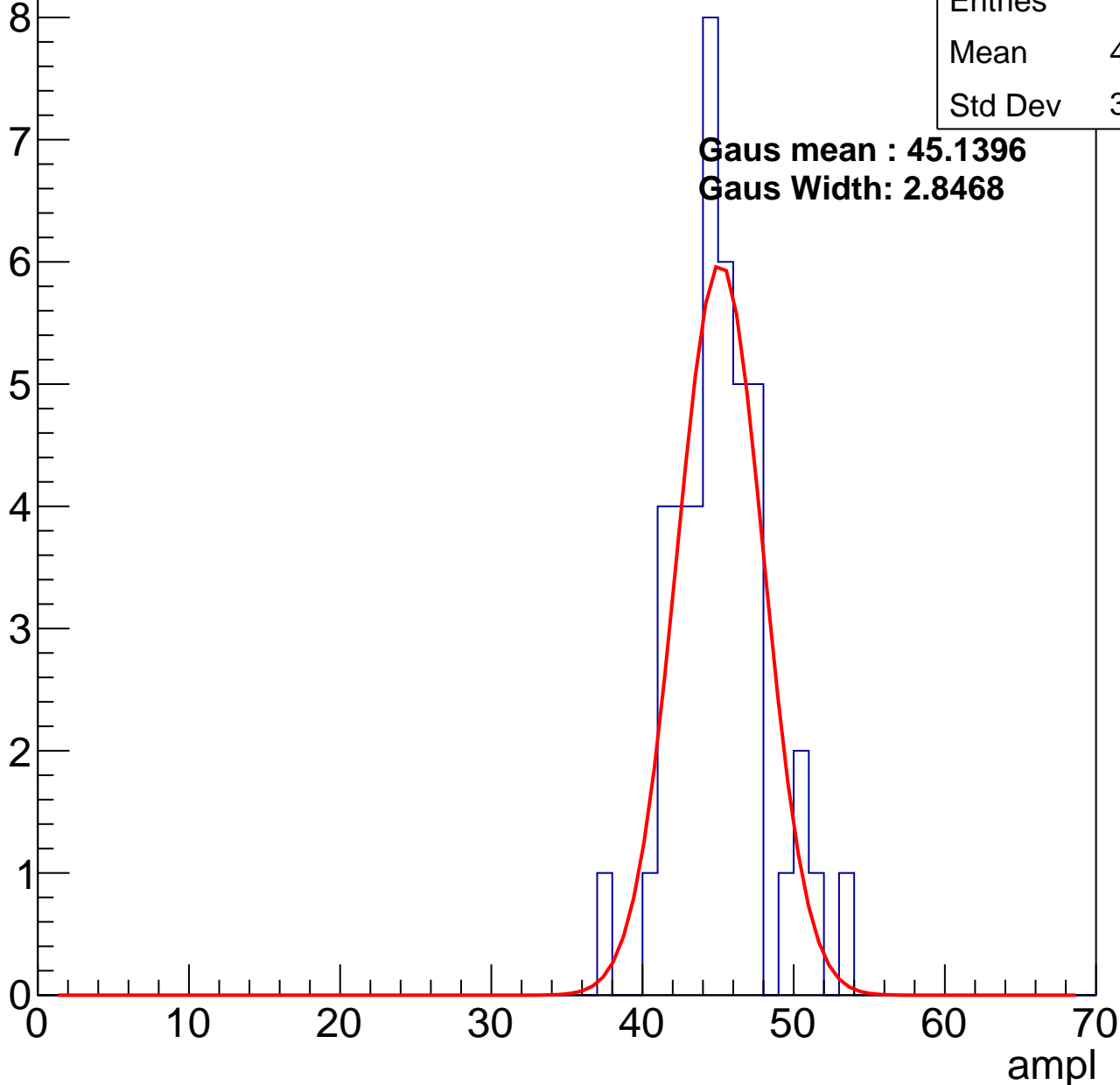
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	44.67
Std Dev	3.048

Gaus mean : 45.1396

Gaus Width: 2.8468



B1L003S, U13-ch22, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	96
Mean	52.07
Std Dev	3.621

Entry

10

8

6

4

2

0

0

10

20

30

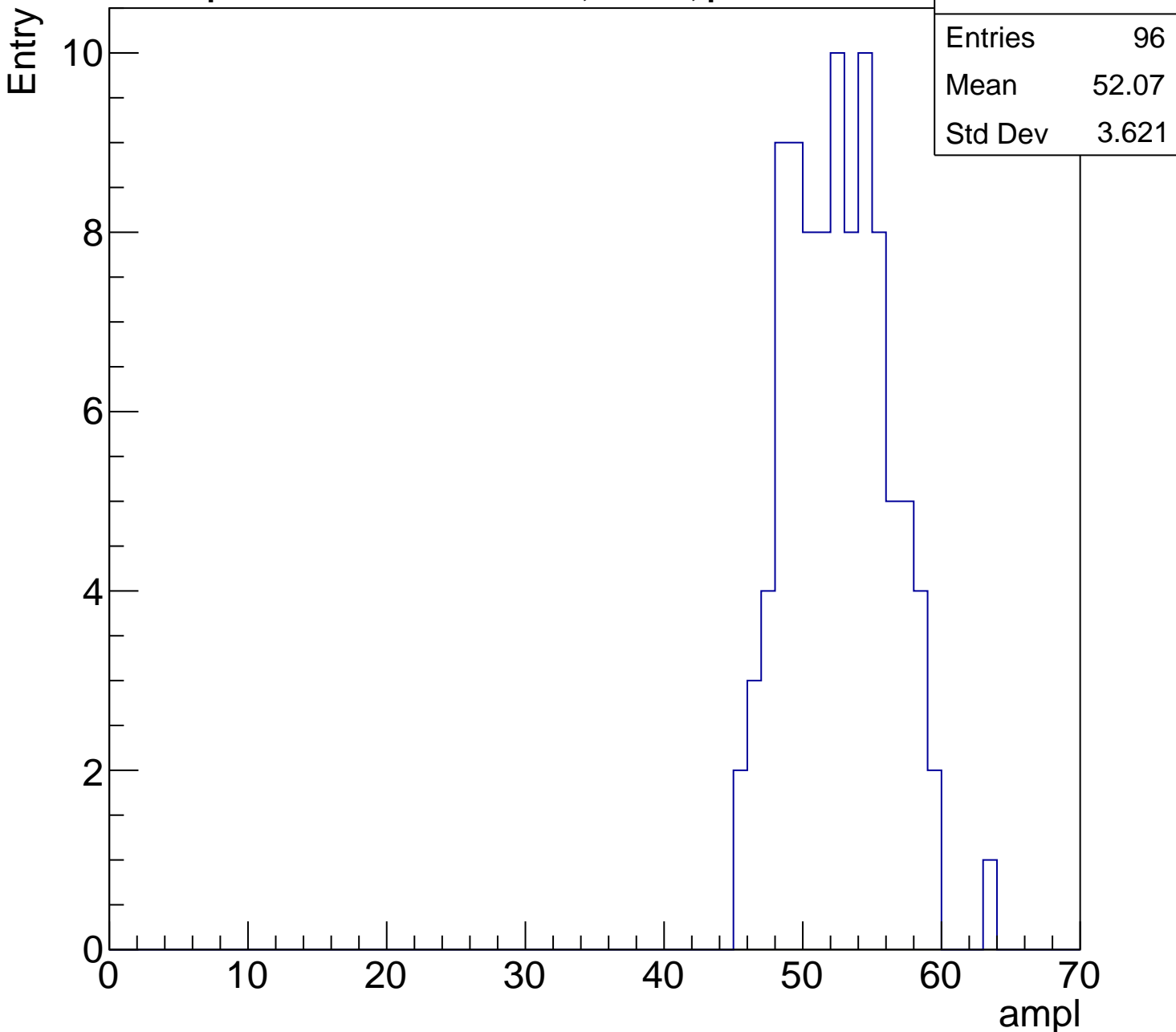
40

50

60

ampl

70

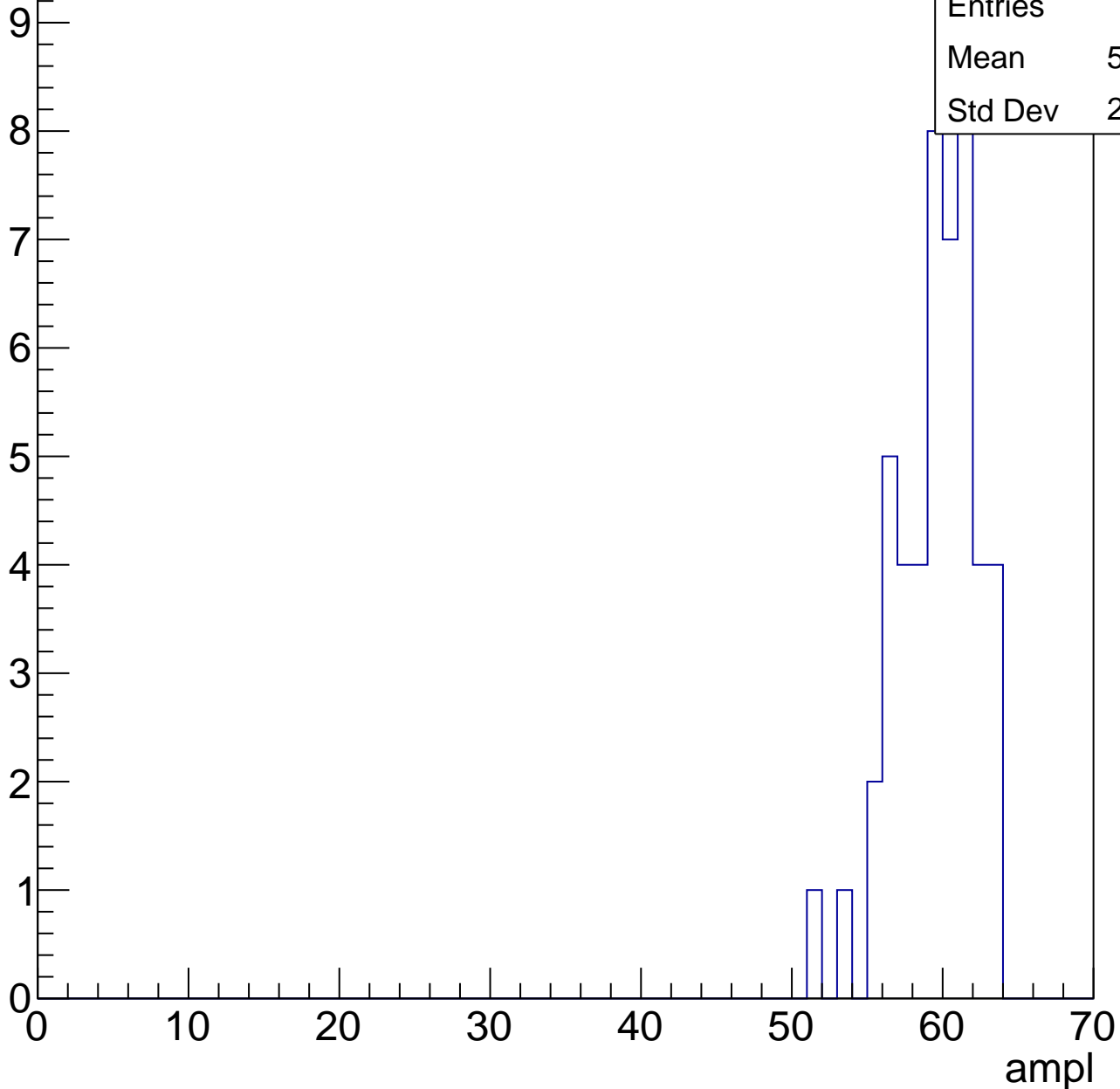


B1L003S, U13-ch22, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.08
Std Dev	2.633

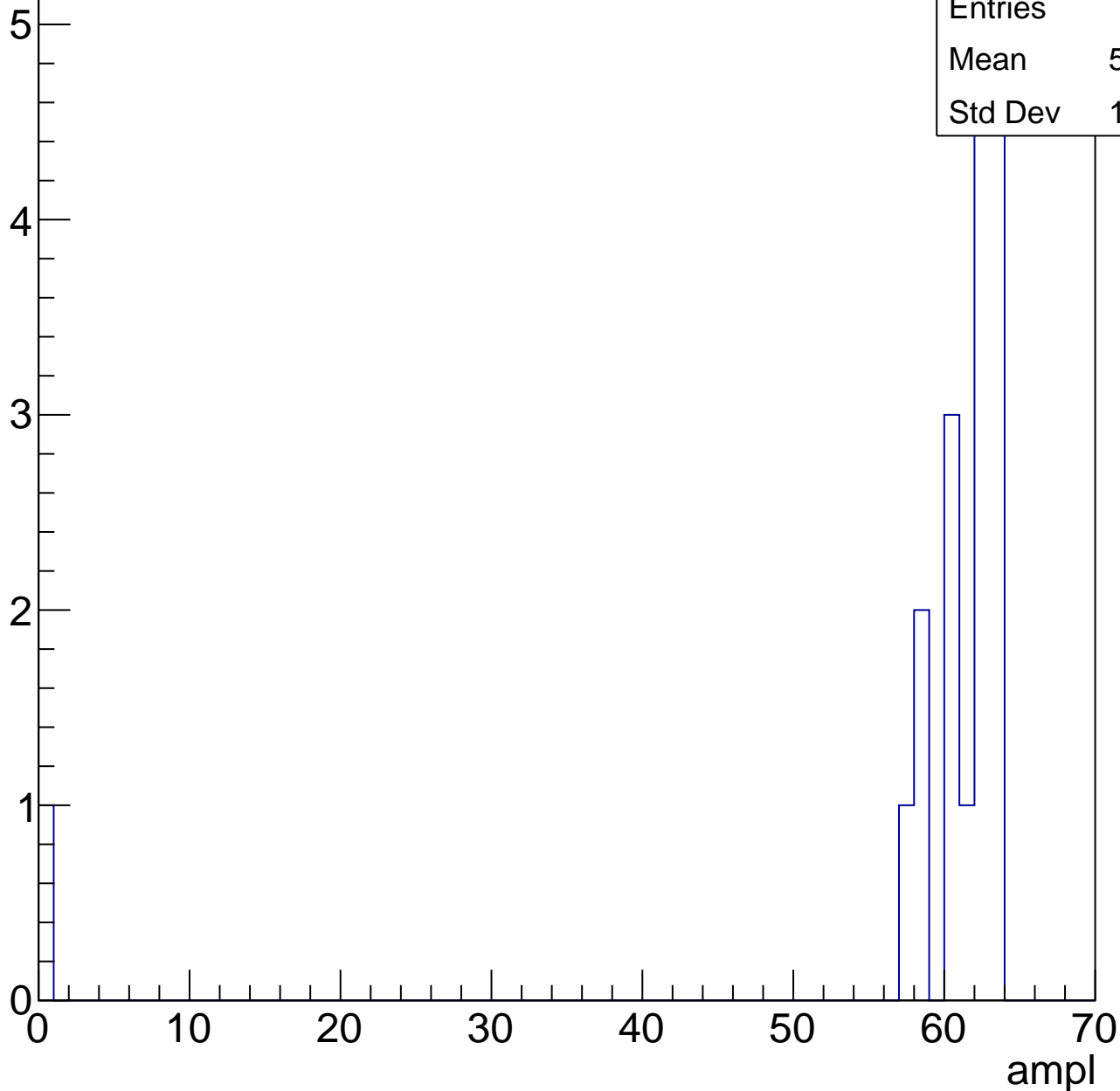


B1L003S, U13-ch22, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	57.72
Std Dev	14.12



B1L003S, U13-ch22, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch22, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L003S, U13-ch23, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.86
Std Dev	3.311

Gaus mean : 30.2033

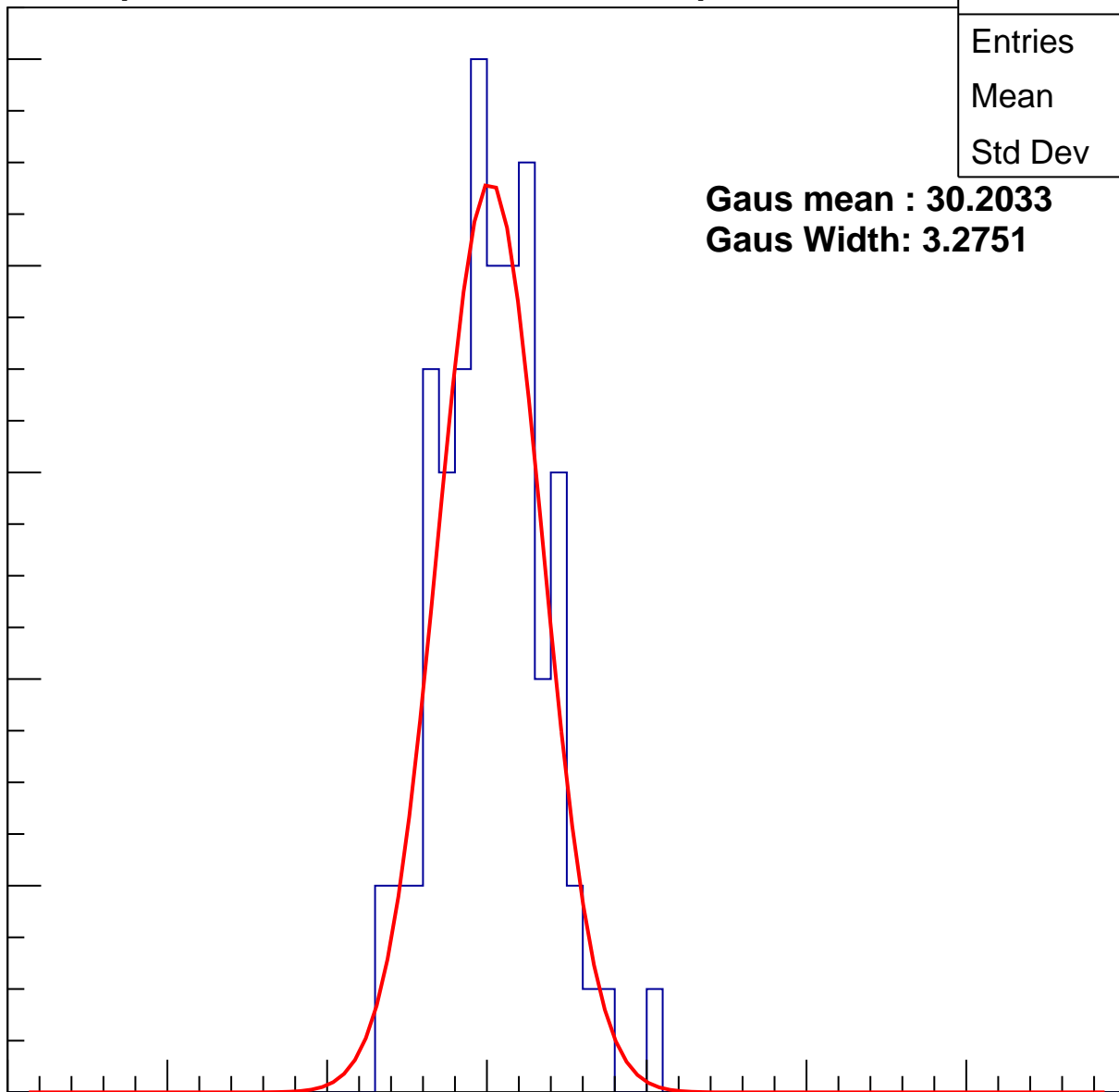
Gaus Width: 3.2751

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



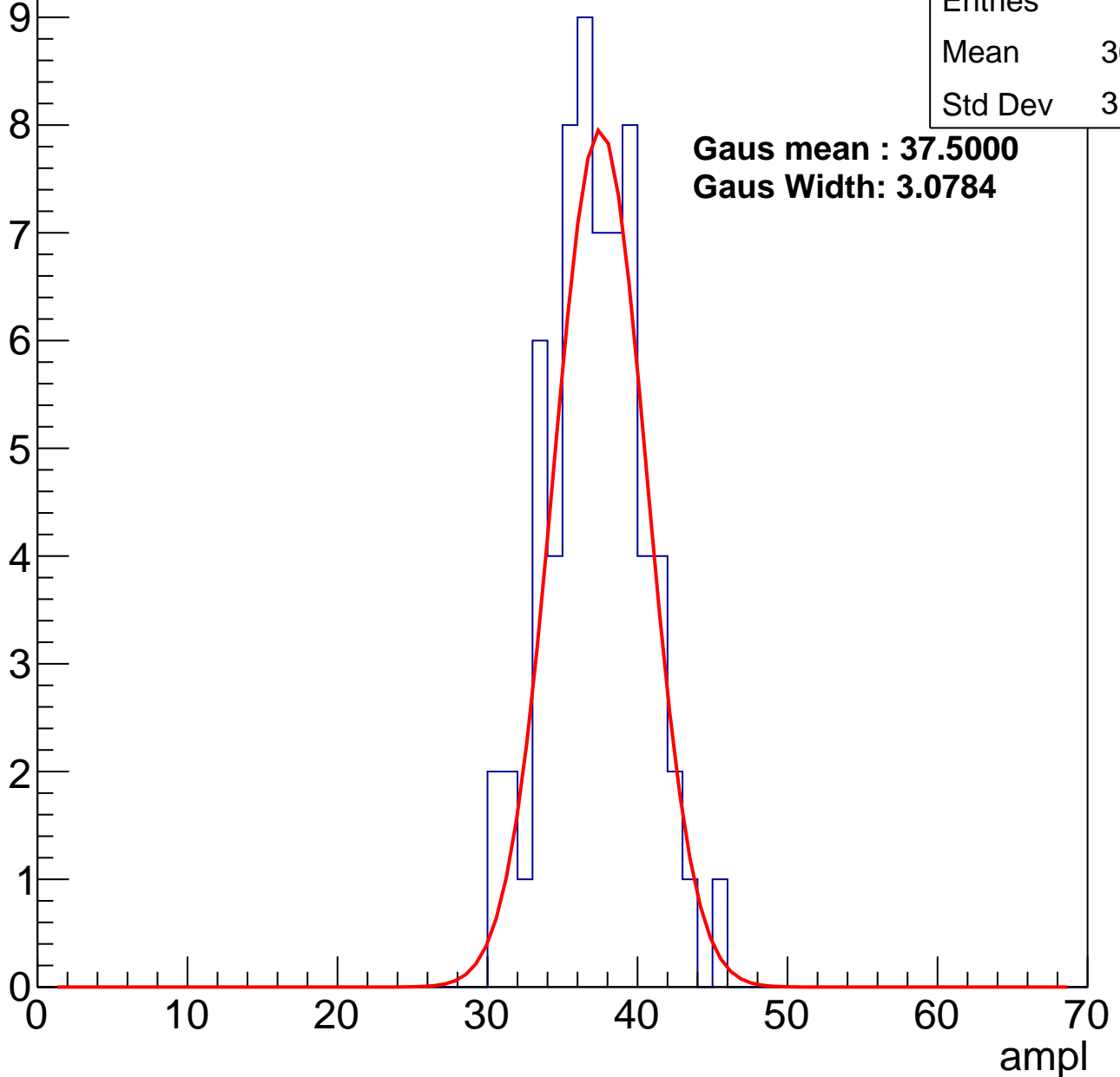
B1L003S, U13-ch23, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.74
Std Dev	3.145

Gaus mean : 37.5000
Gaus Width: 3.0784



B1L003S, U13-ch23, adc2

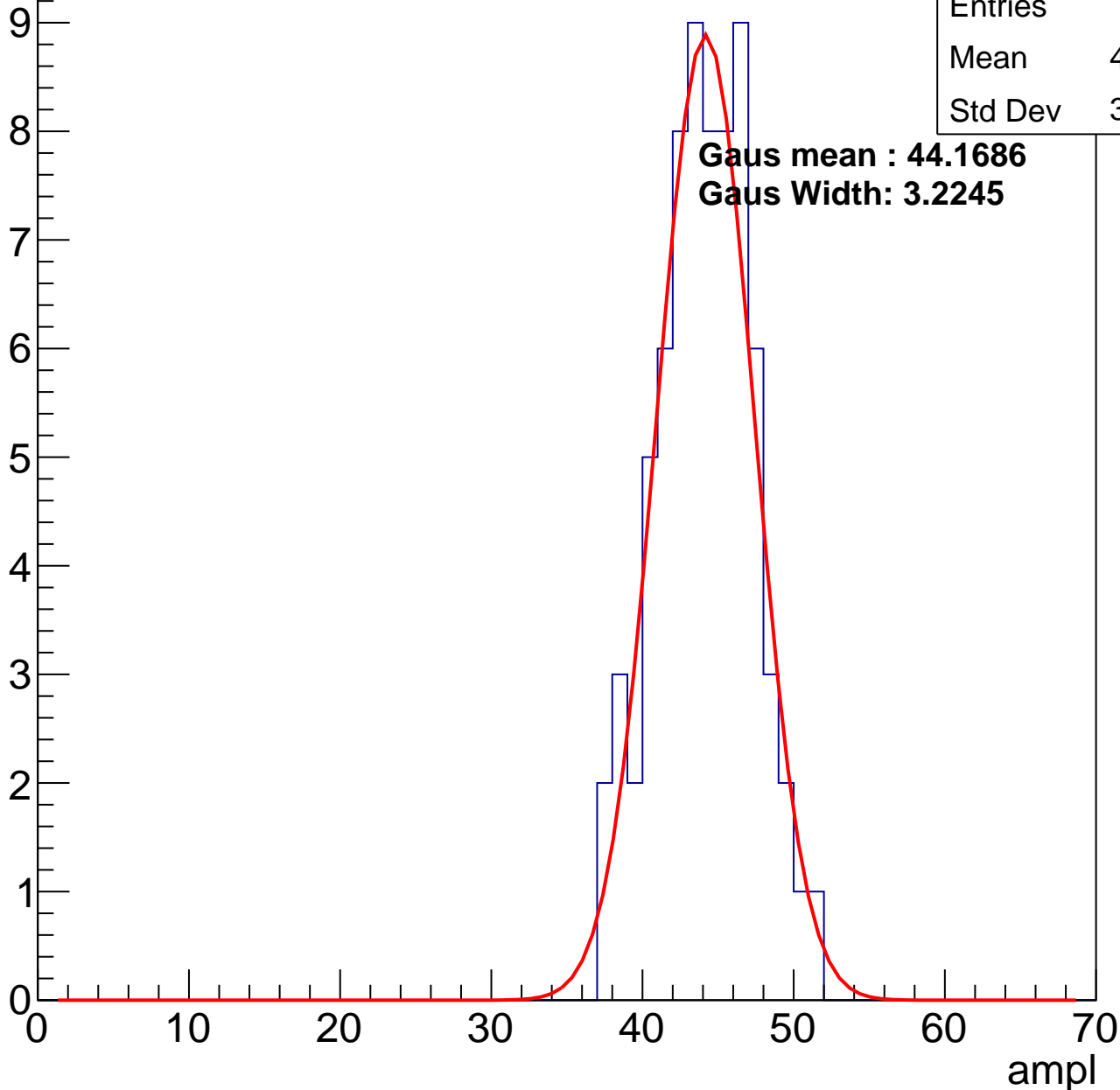
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.64
Std Dev	3.103

Gaus mean : 44.1686

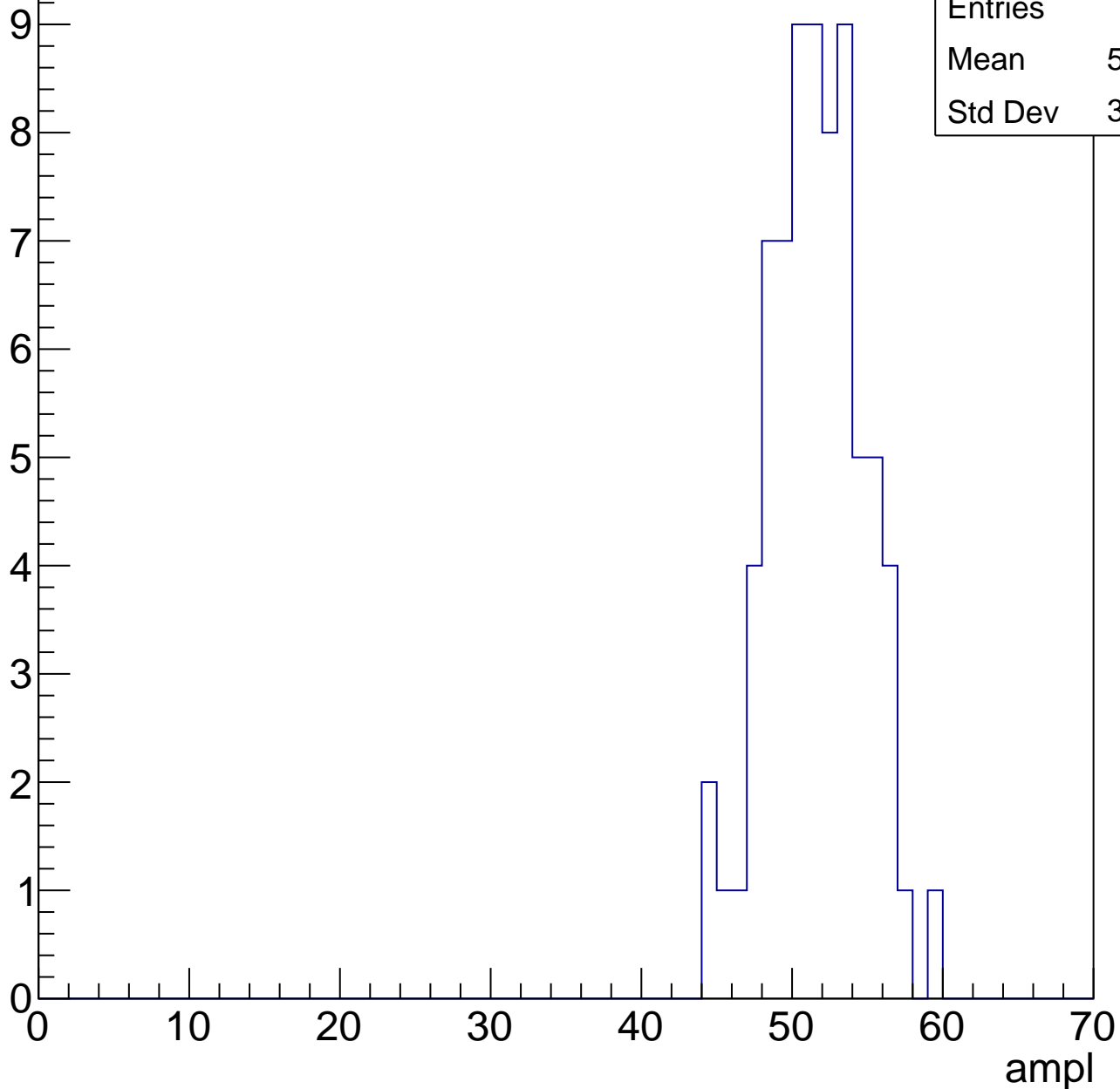
Gaus Width: 3.2245



B1L003S, U13-ch23, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



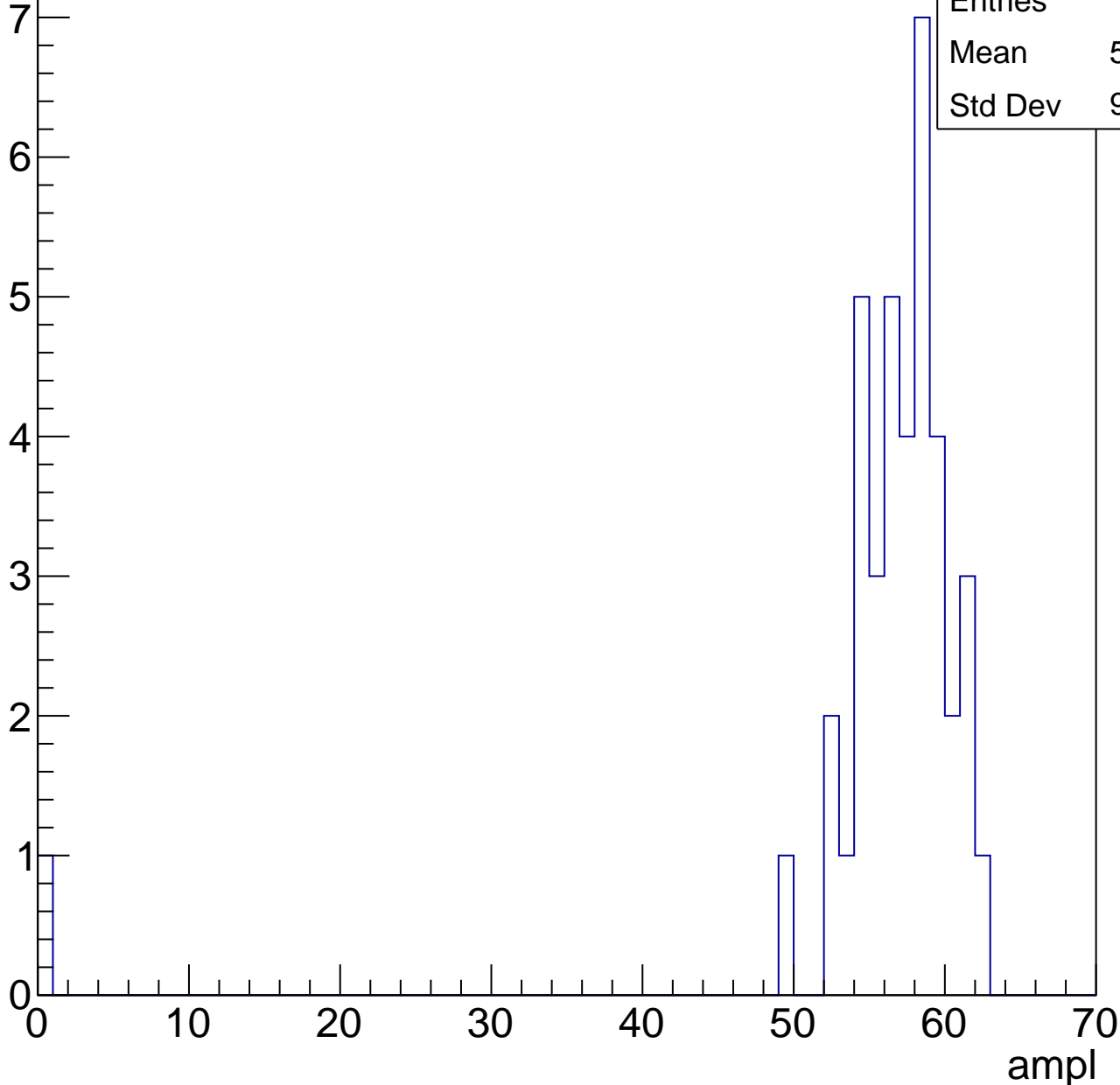
Entries	73
Mean	51.14
Std Dev	3.076

B1L003S, U13-ch23, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

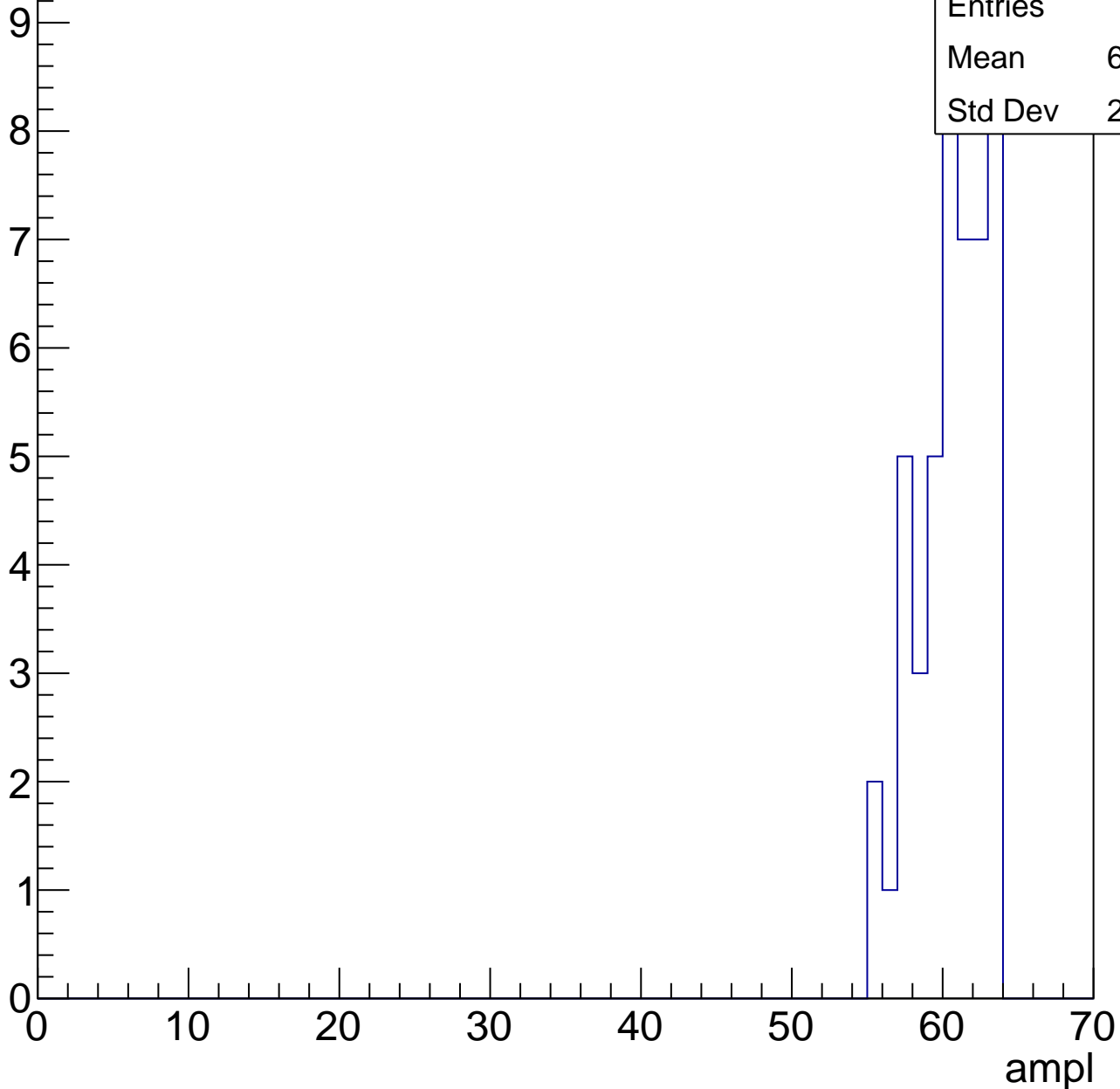
Entries	39
Mean	55.28
Std Dev	9.392



B1L003S, U13-ch23, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch23, adc6

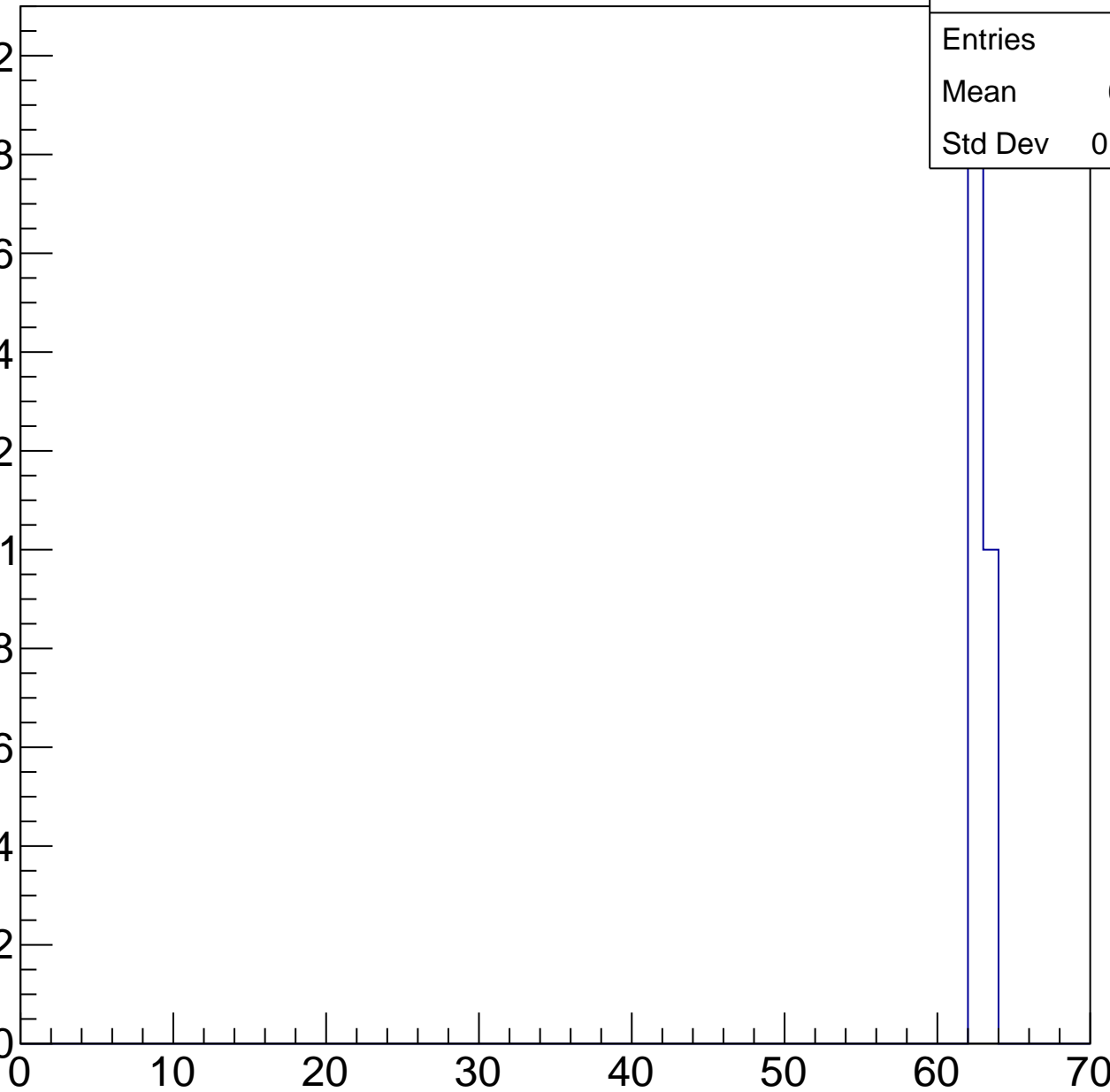
calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L003S, U13-ch23, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch24, adc0

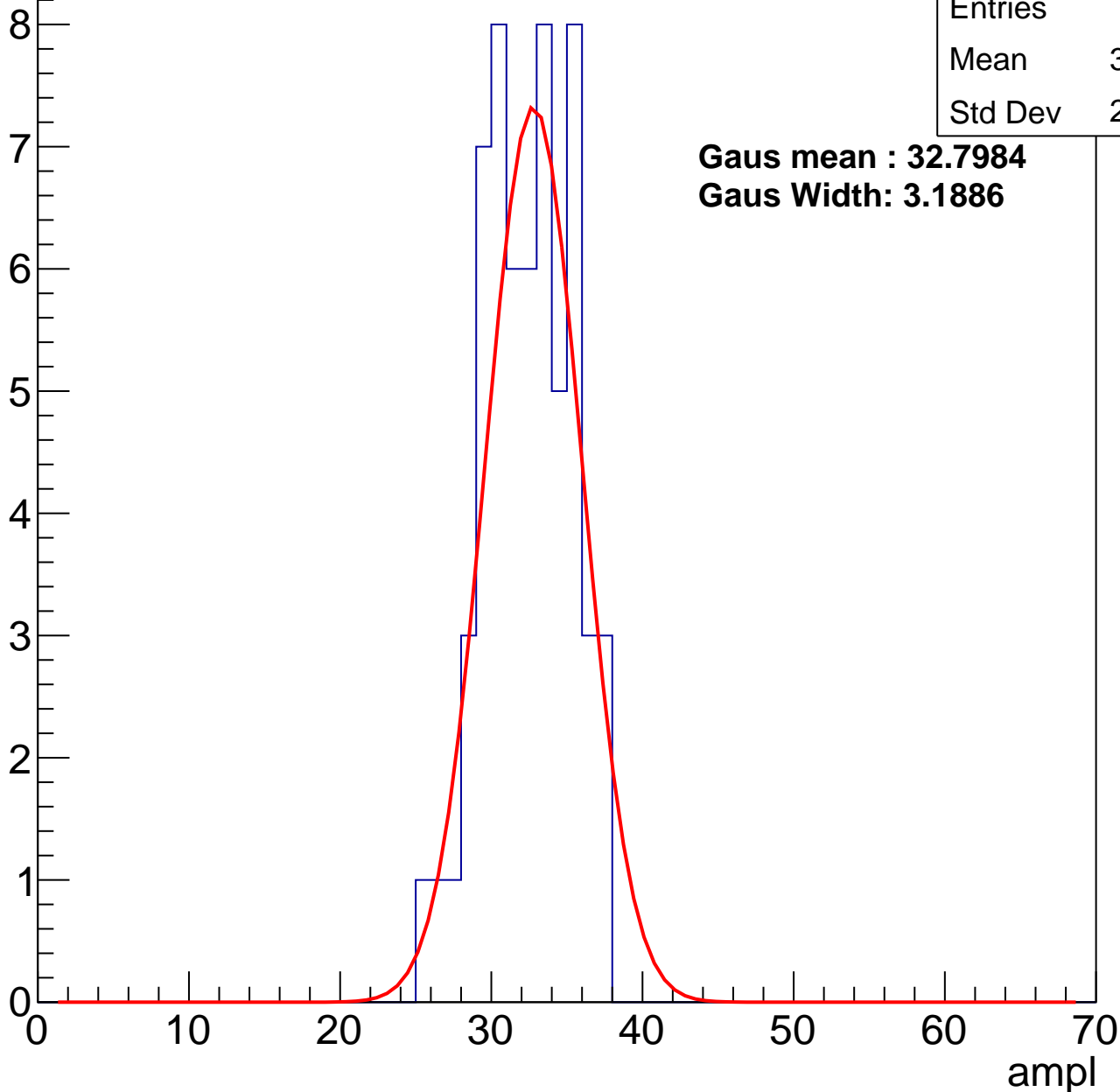
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	31.93
Std Dev	2.834

Gaus mean : 32.7984

Gaus Width: 3.1886

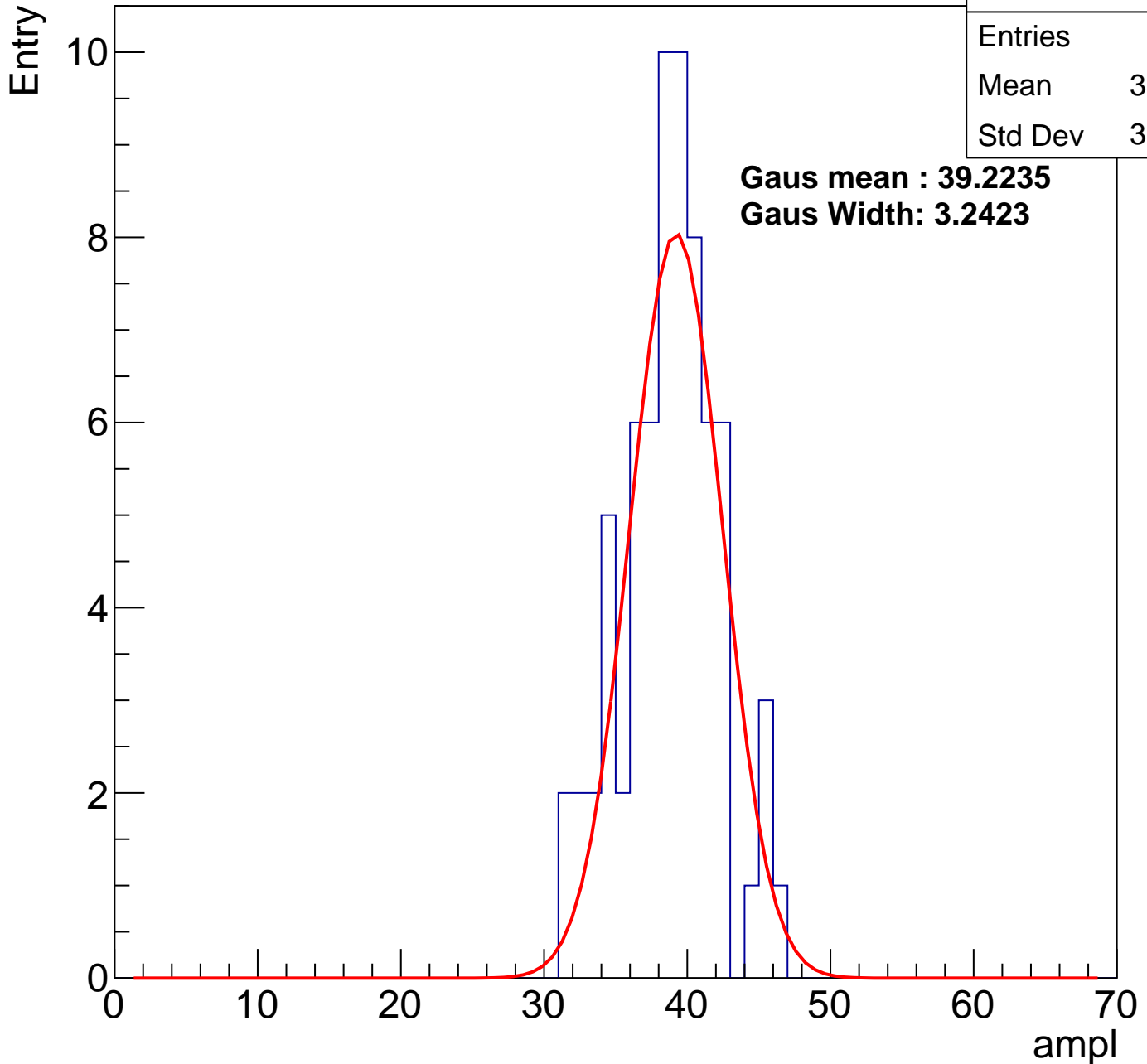


B1L003S, U13-ch24, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	38.33
Std Dev	3.337

Gaus mean : 39.2235
Gaus Width: 3.2423



B1L003S, U13-ch24, adc2

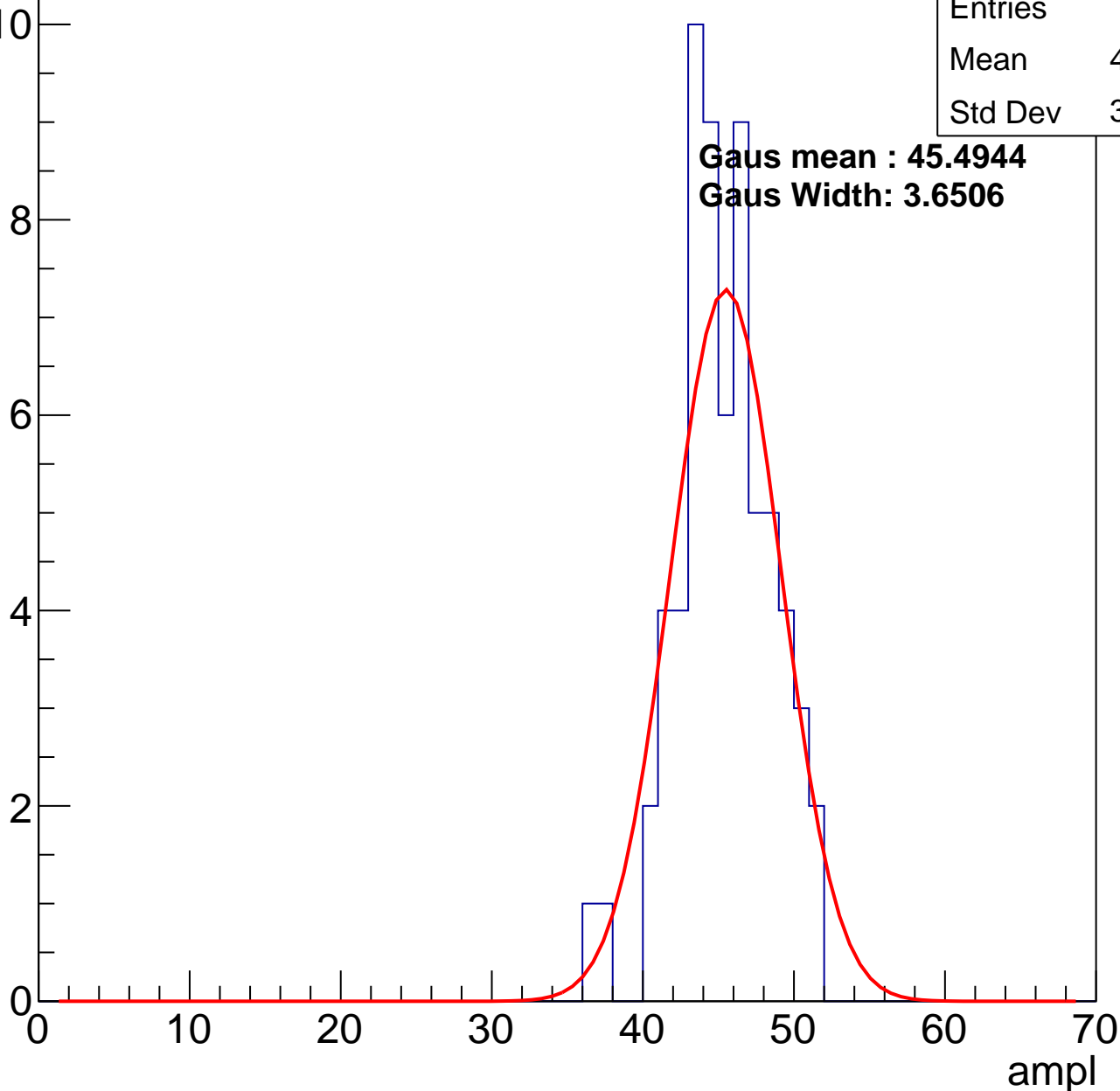
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	44.89
Std Dev	3.109

Gaus mean : 45.4944

Gaus Width: 3.6506



B1L003S, U13-ch24, adc3

calib_packv5_042523_0143.root, FC#13, port D2

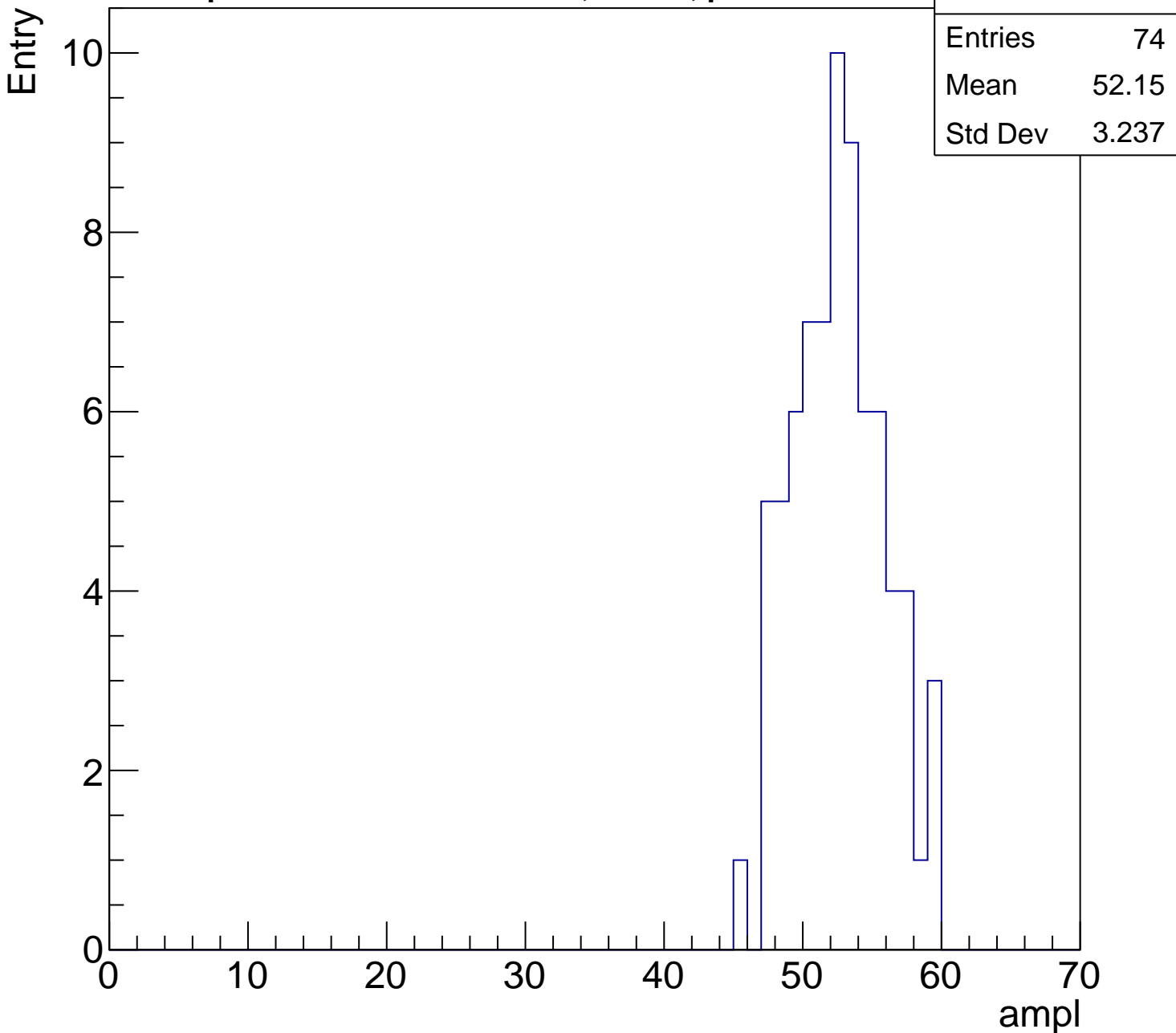
Entries	74
Mean	52.15
Std Dev	3.237

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

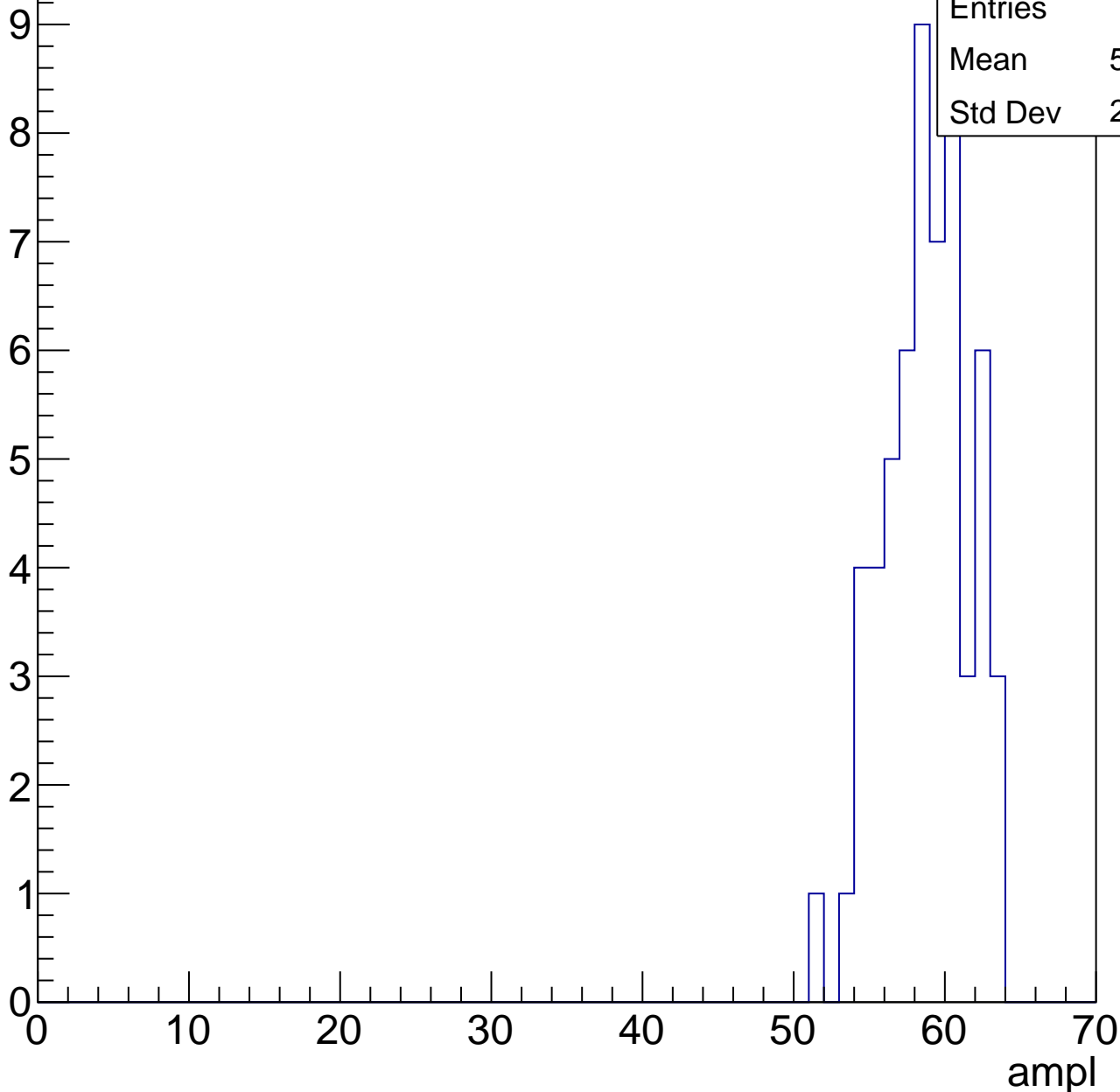


B1L003S, U13-ch24, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.26
Std Dev	2.756

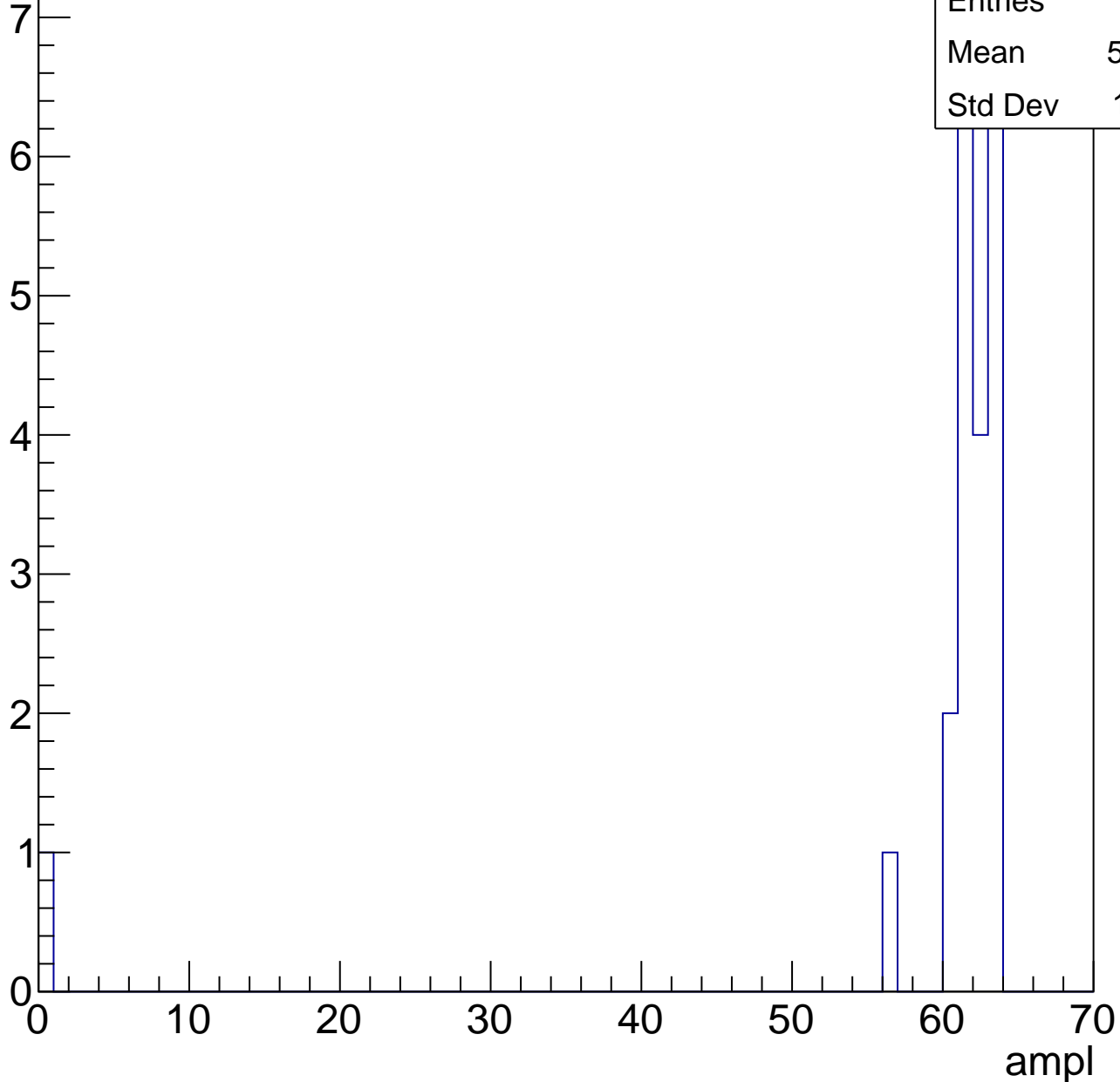


B1L003S, U13-ch24, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	22
Mean	58.73
Std Dev	12.91



B1L003S, U13-ch24, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch24, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch25, adc0

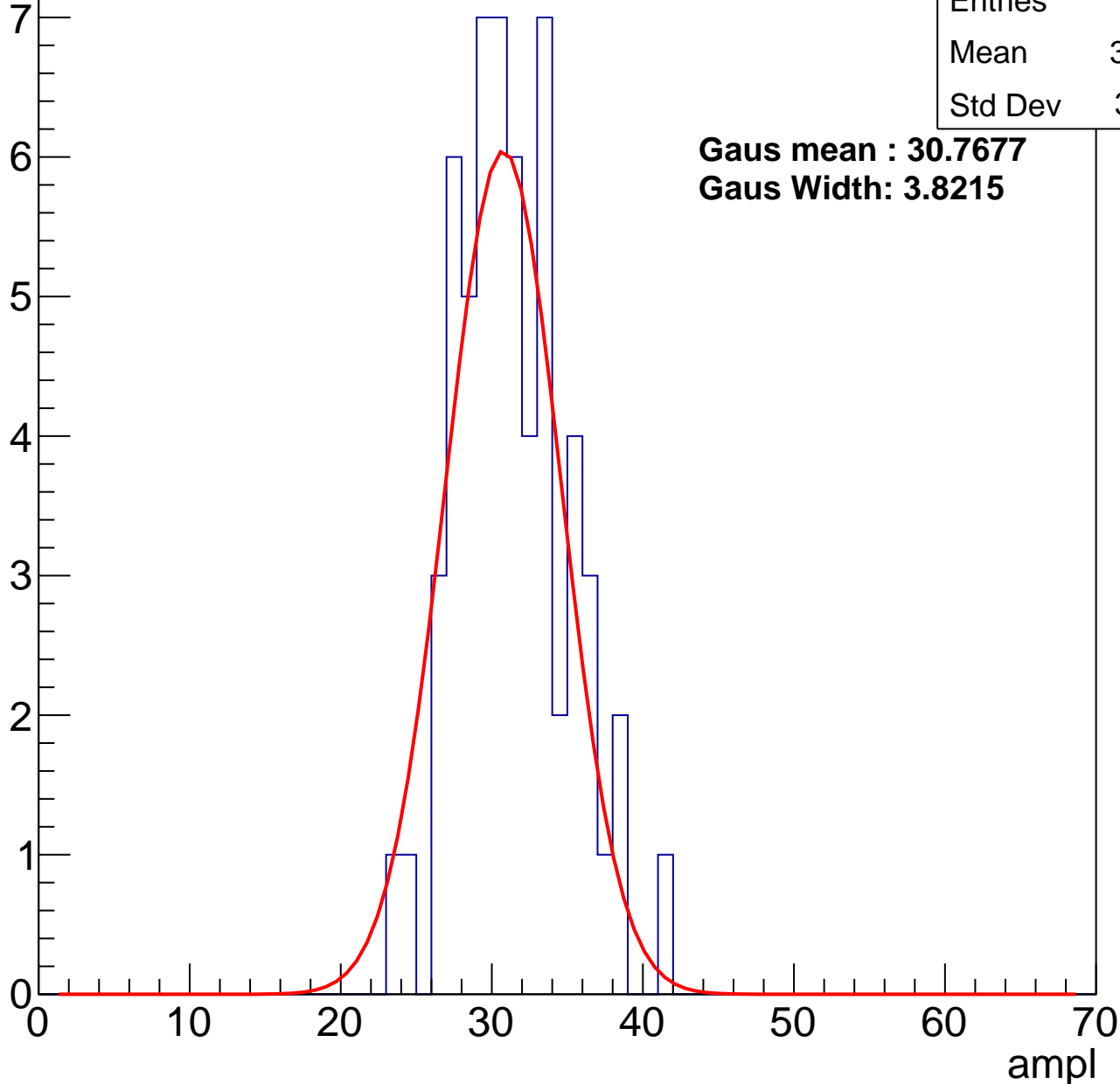
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	30.92
Std Dev	3.621

Gaus mean : 30.7677

Gaus Width: 3.8215



B1L003S, U13-ch25, adc1

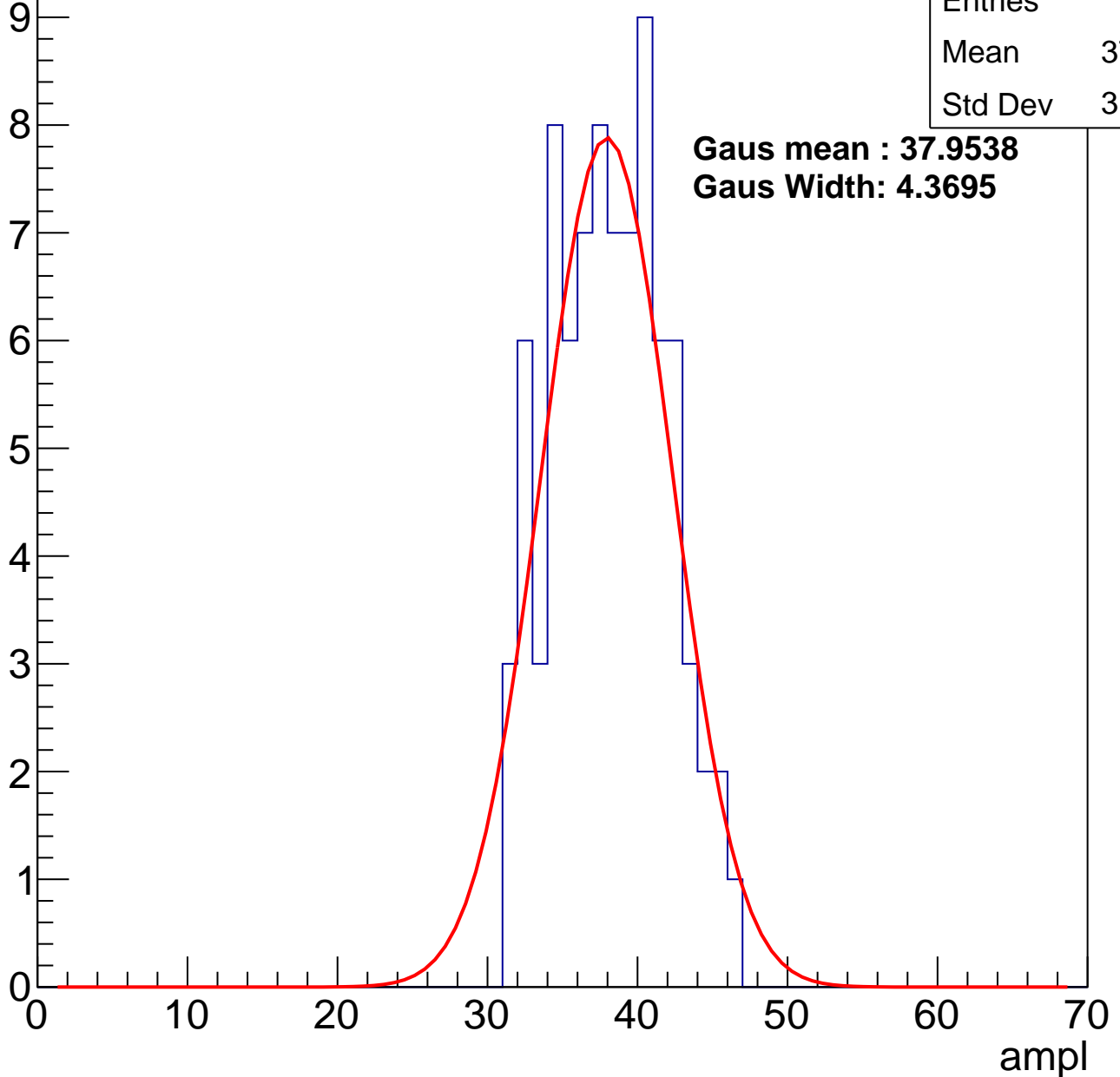
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	37.67
Std Dev	3.698

Gaus mean : 37.9538

Gaus Width: 4.3695



B1L003S, U13-ch25, adc2

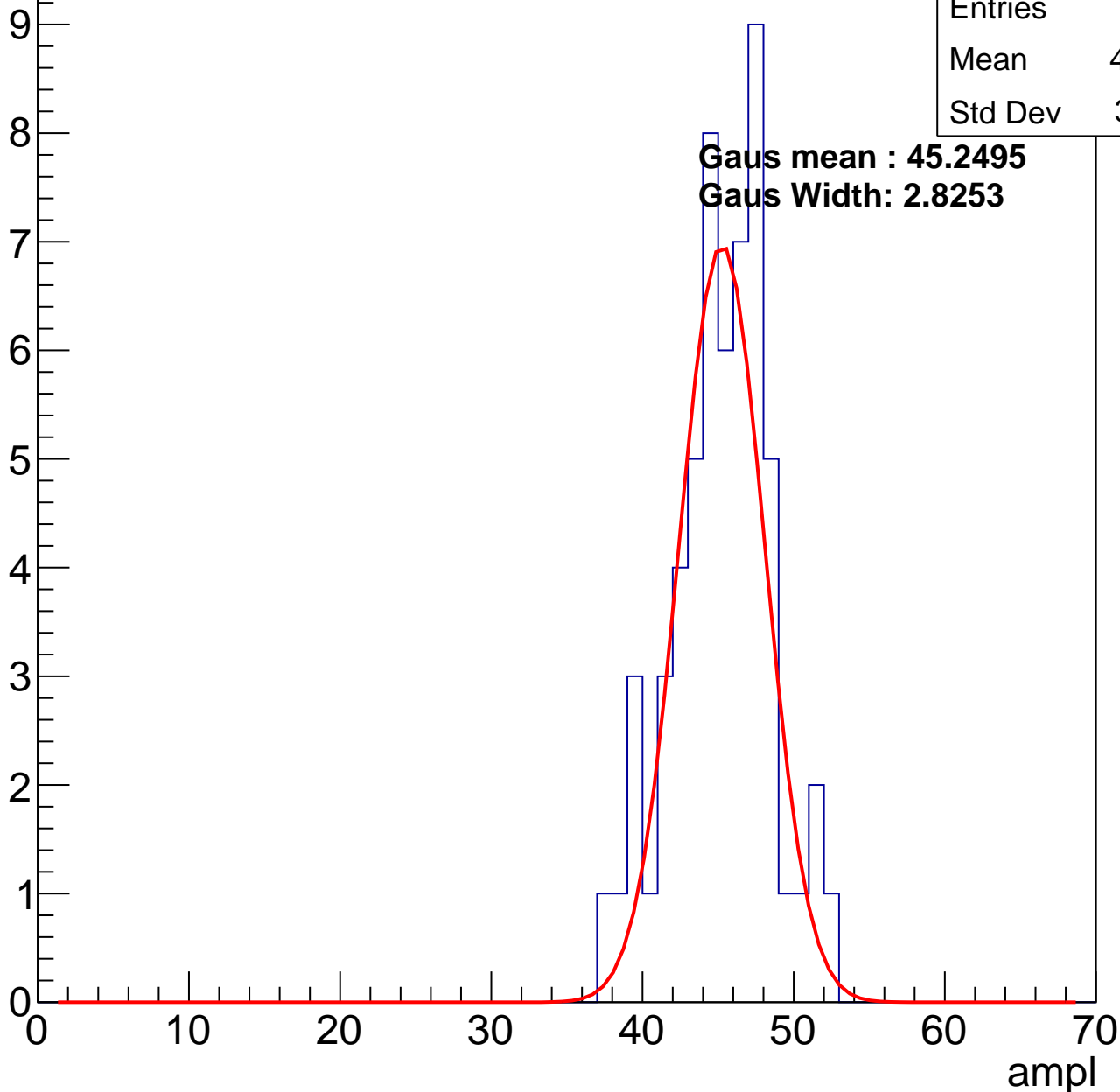
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.79
Std Dev	3.231

Gaus mean : 45.2495

Gaus Width: 2.8253

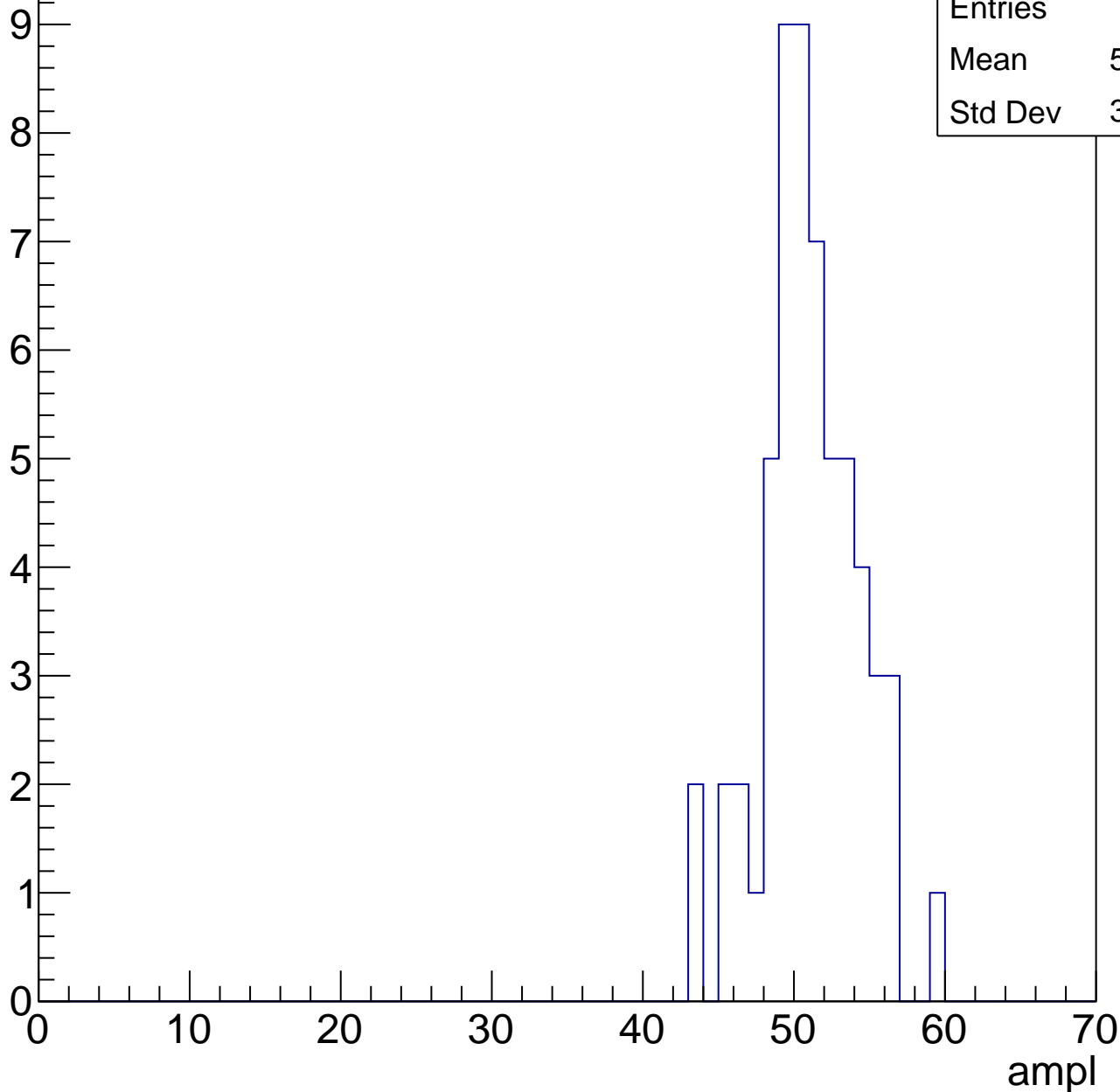


B1L003S, U13-ch25, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	50.62
Std Dev	3.205

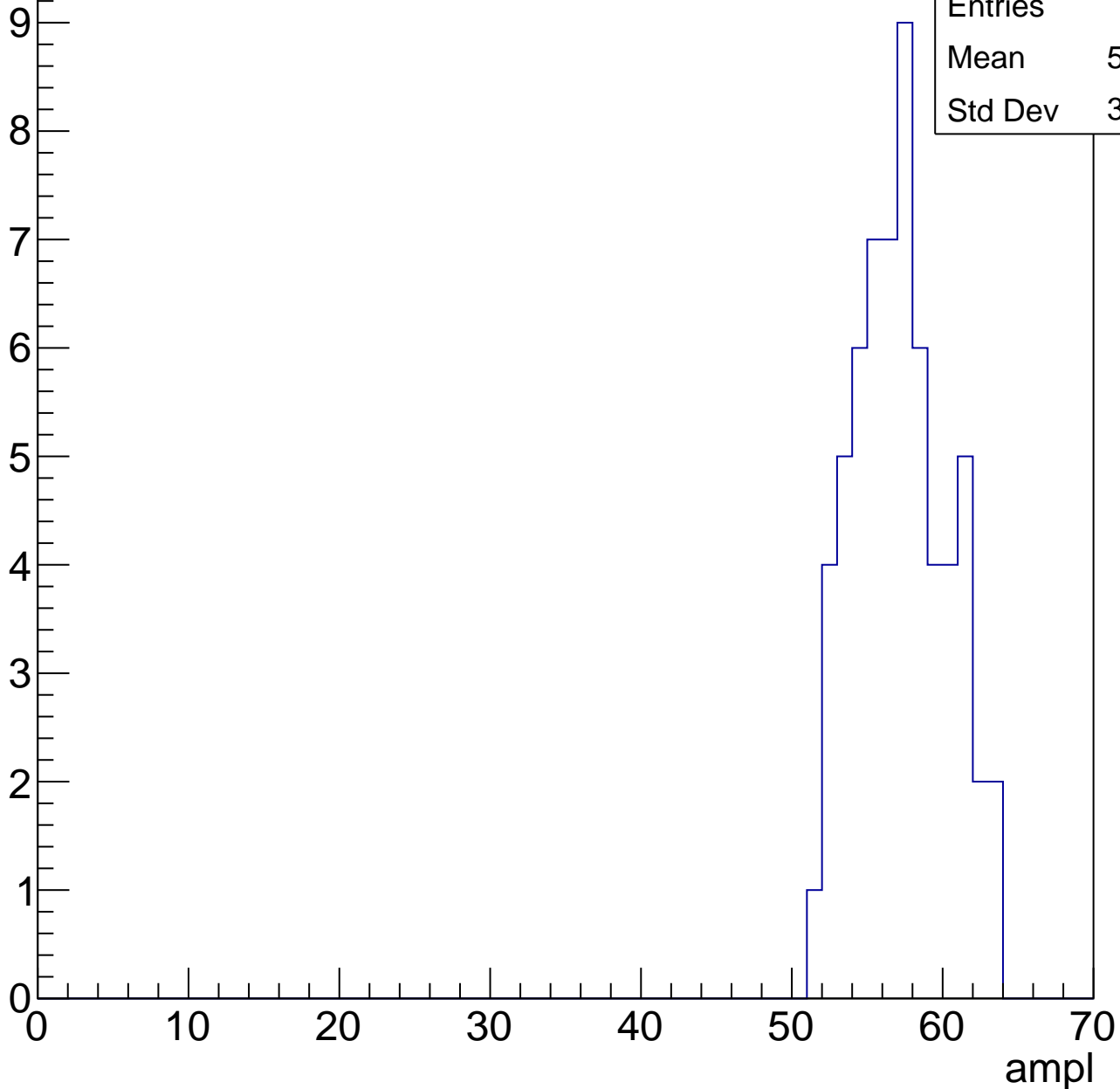


B1L003S, U13-ch25, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	56.73
Std Dev	3.006

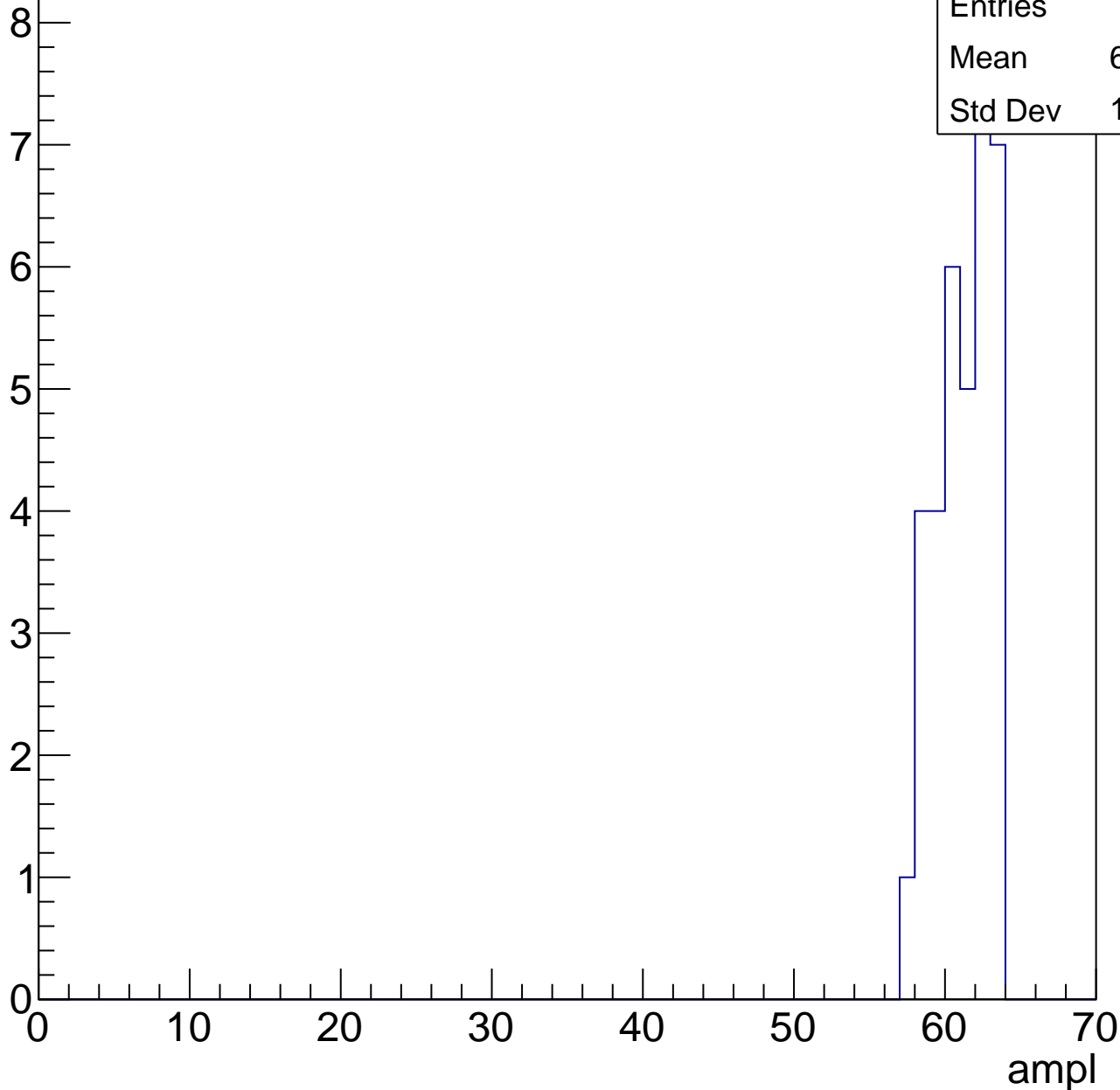


B1L003S, U13-ch25, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

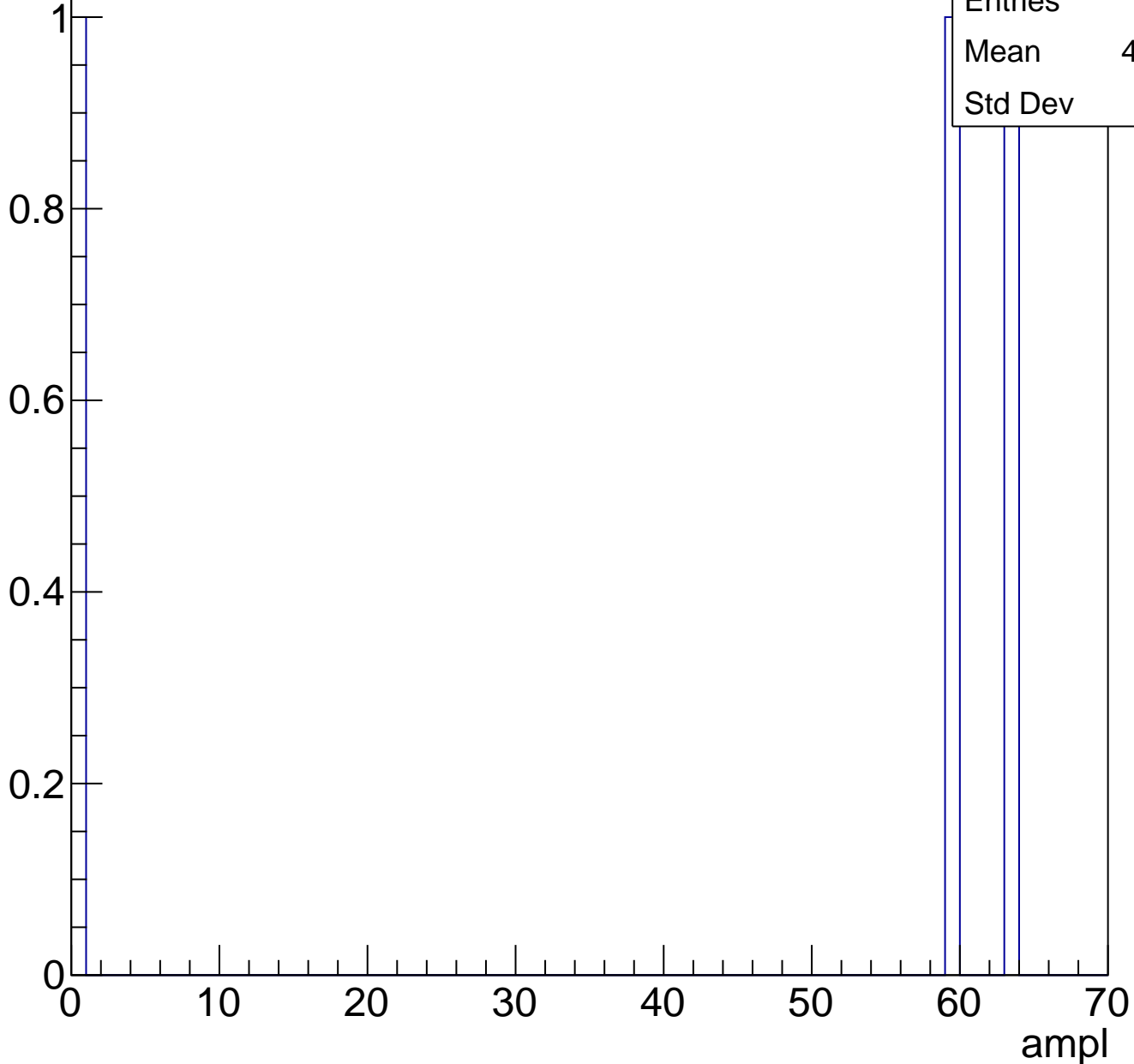
Entries	35
Mean	60.77
Std Dev	1.758



B1L003S, U13-ch25, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch25, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch26, adc0

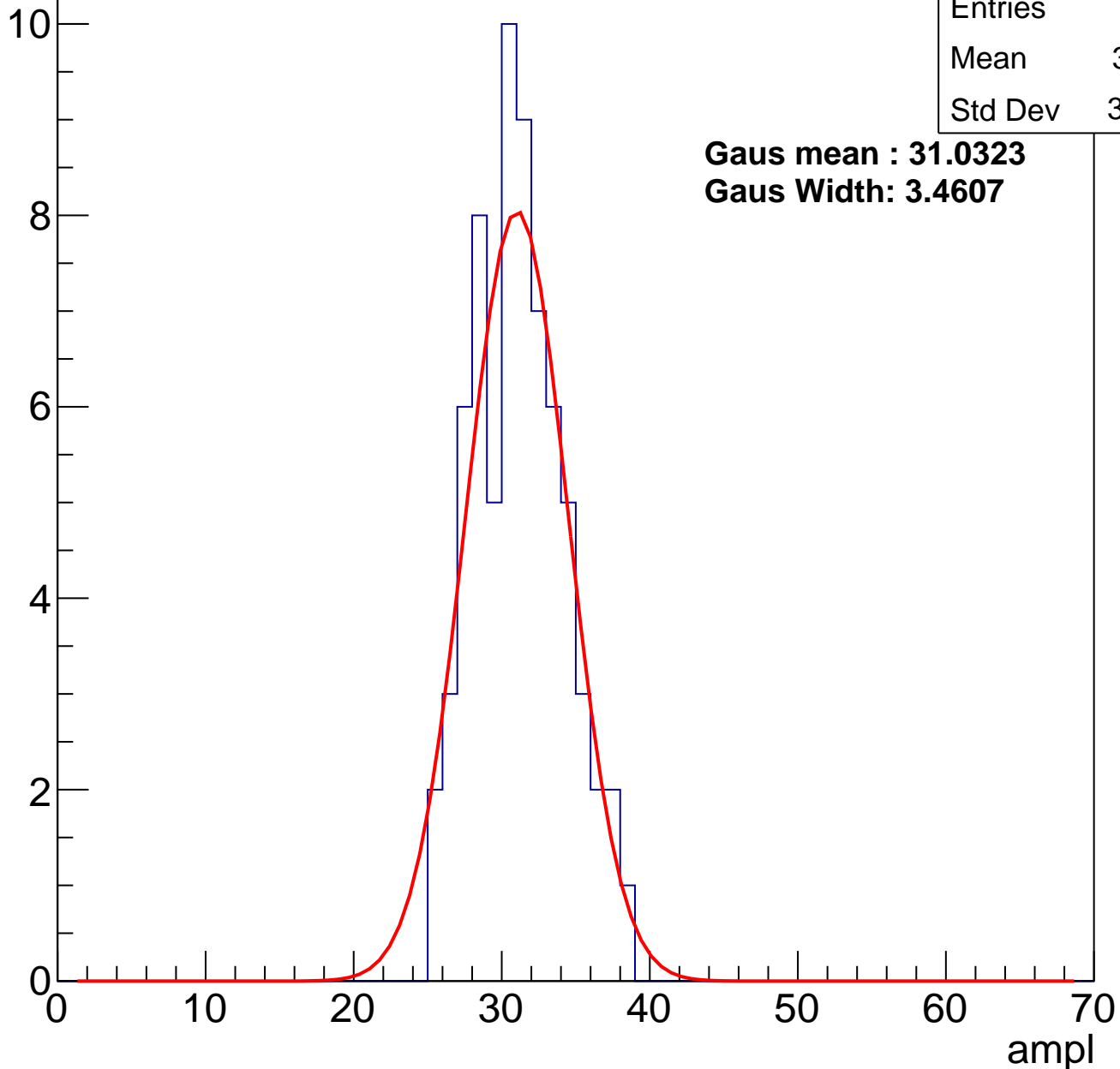
calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	30.71
Std Dev	3.032

Gaus mean : 31.0323

Gaus Width: 3.4607

Entry



B1L003S, U13-ch26, adc1

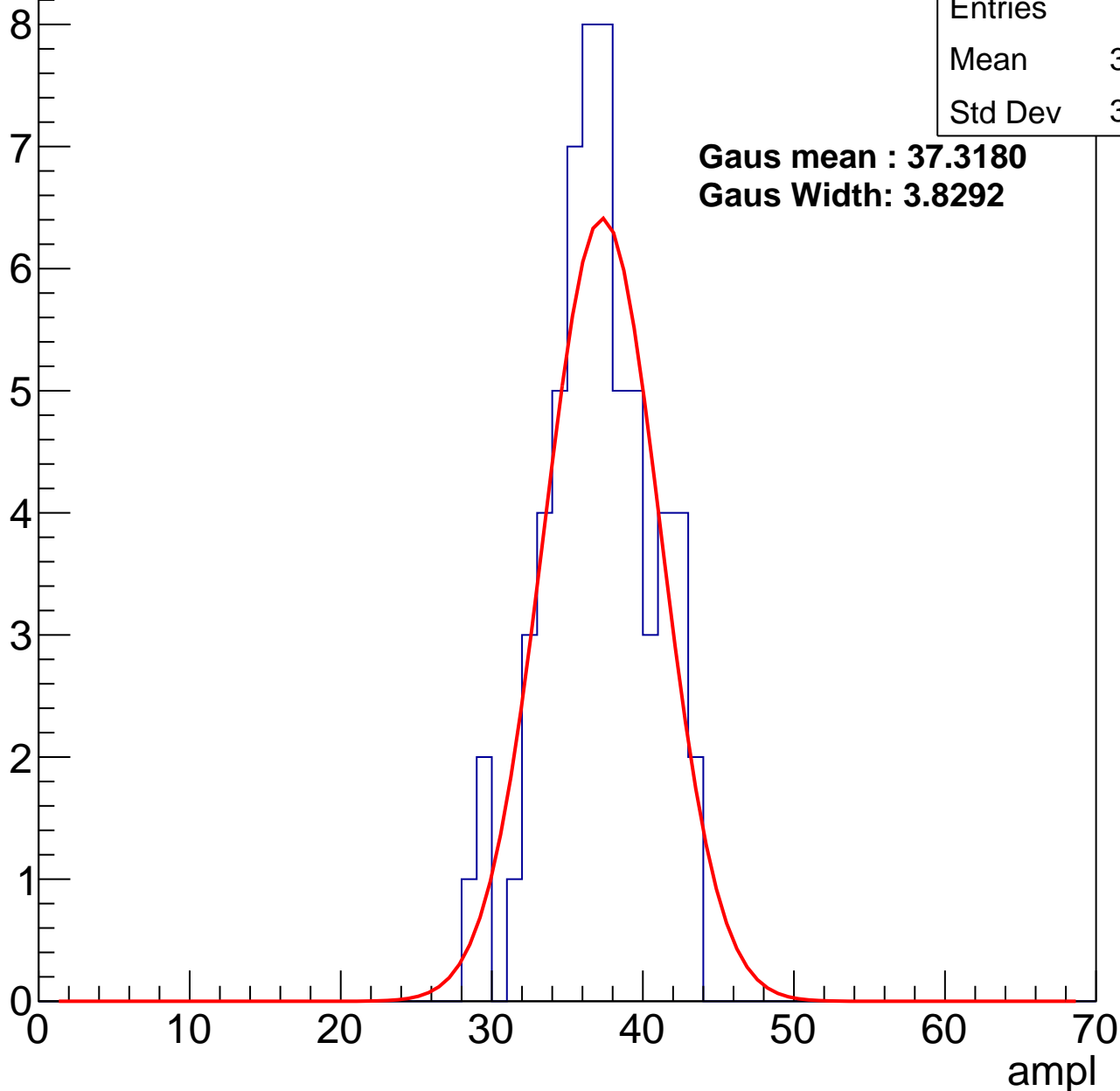
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.56
Std Dev	3.458

Gaus mean : 37.3180

Gaus Width: 3.8292



B1L003S, U13-ch26, adc2

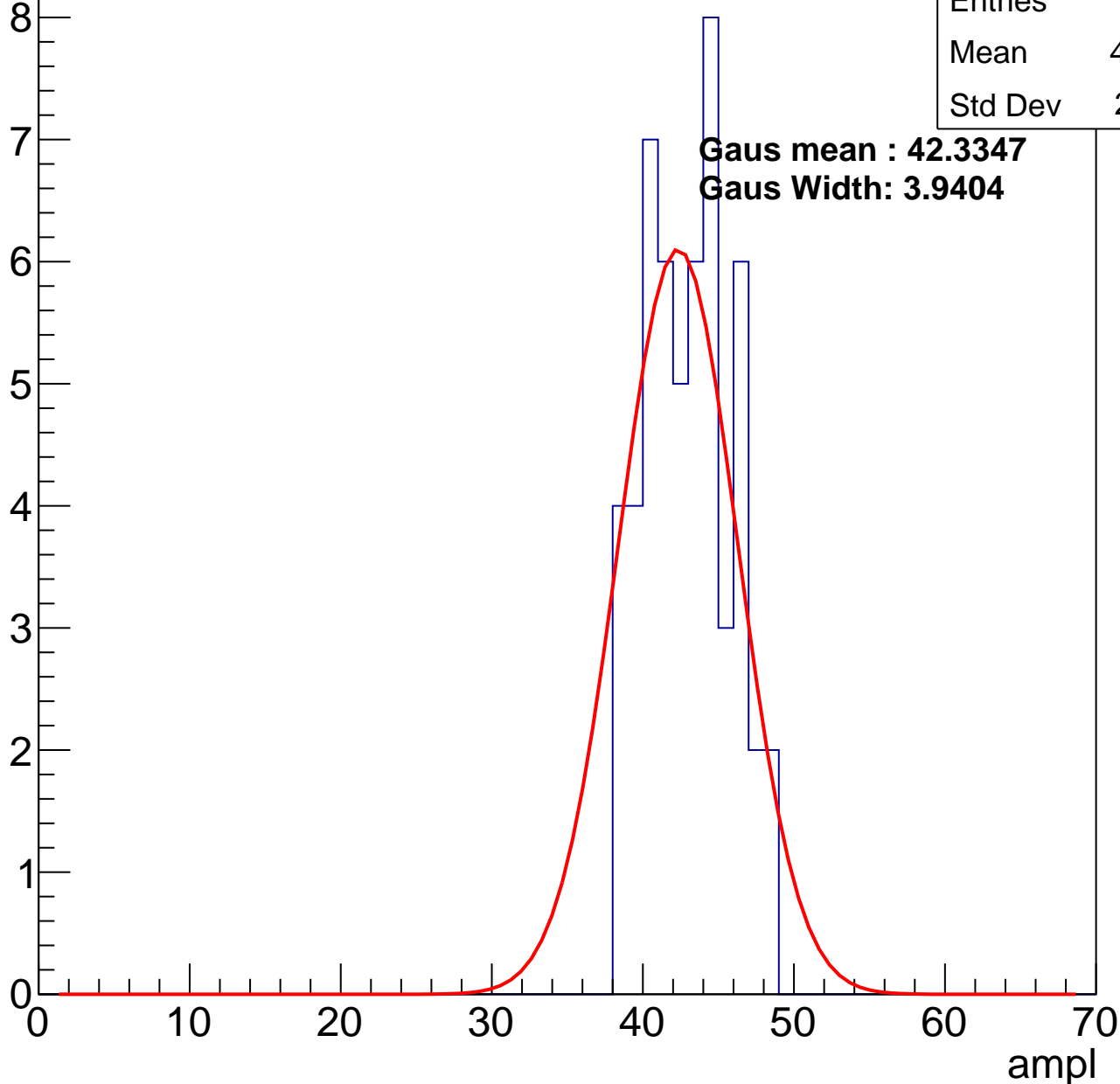
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.55
Std Dev	2.751

Gaus mean : 42.3347

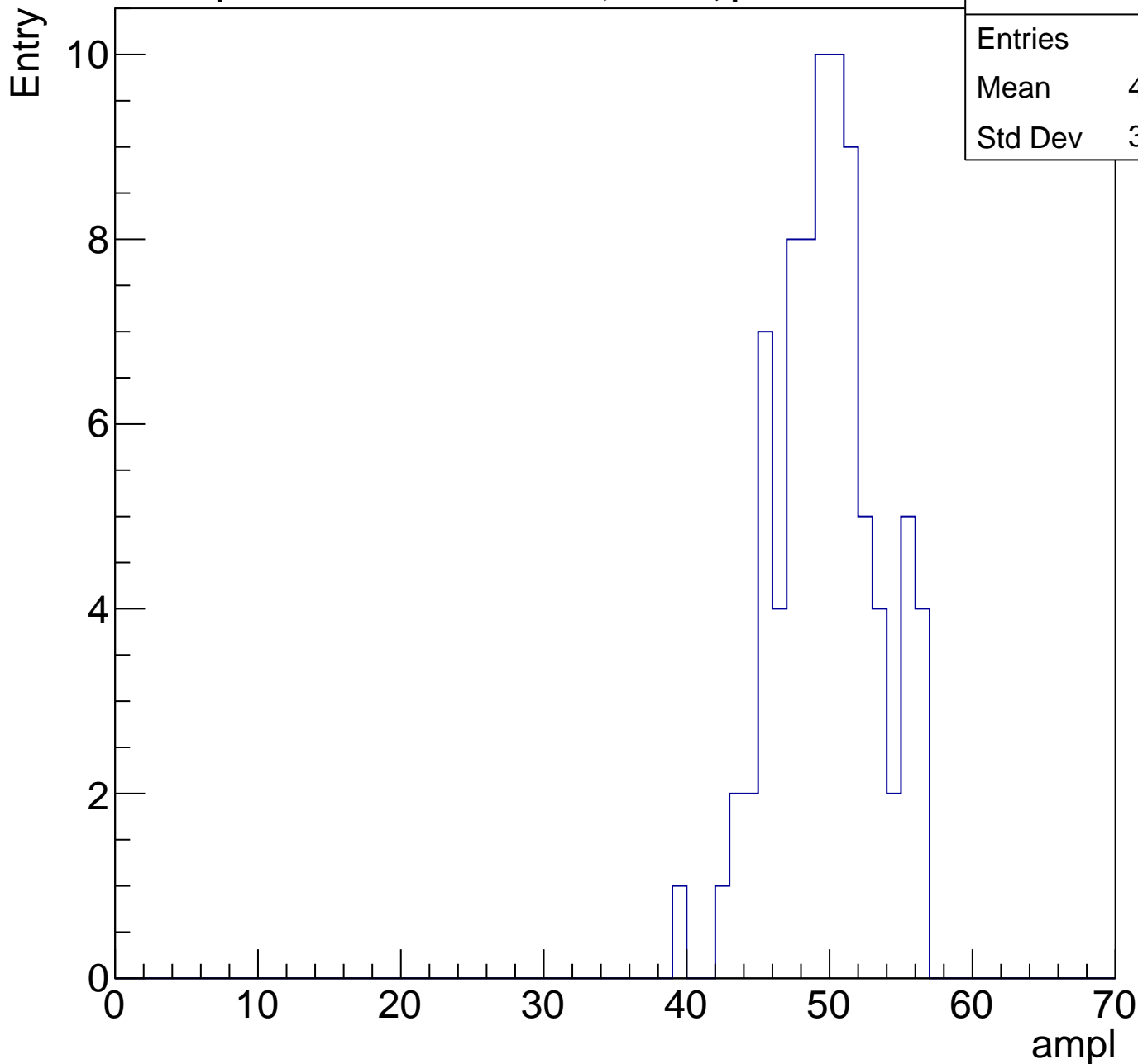
Gaus Width: 3.9404



B1L003S, U13-ch26, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	49.29
Std Dev	3.549

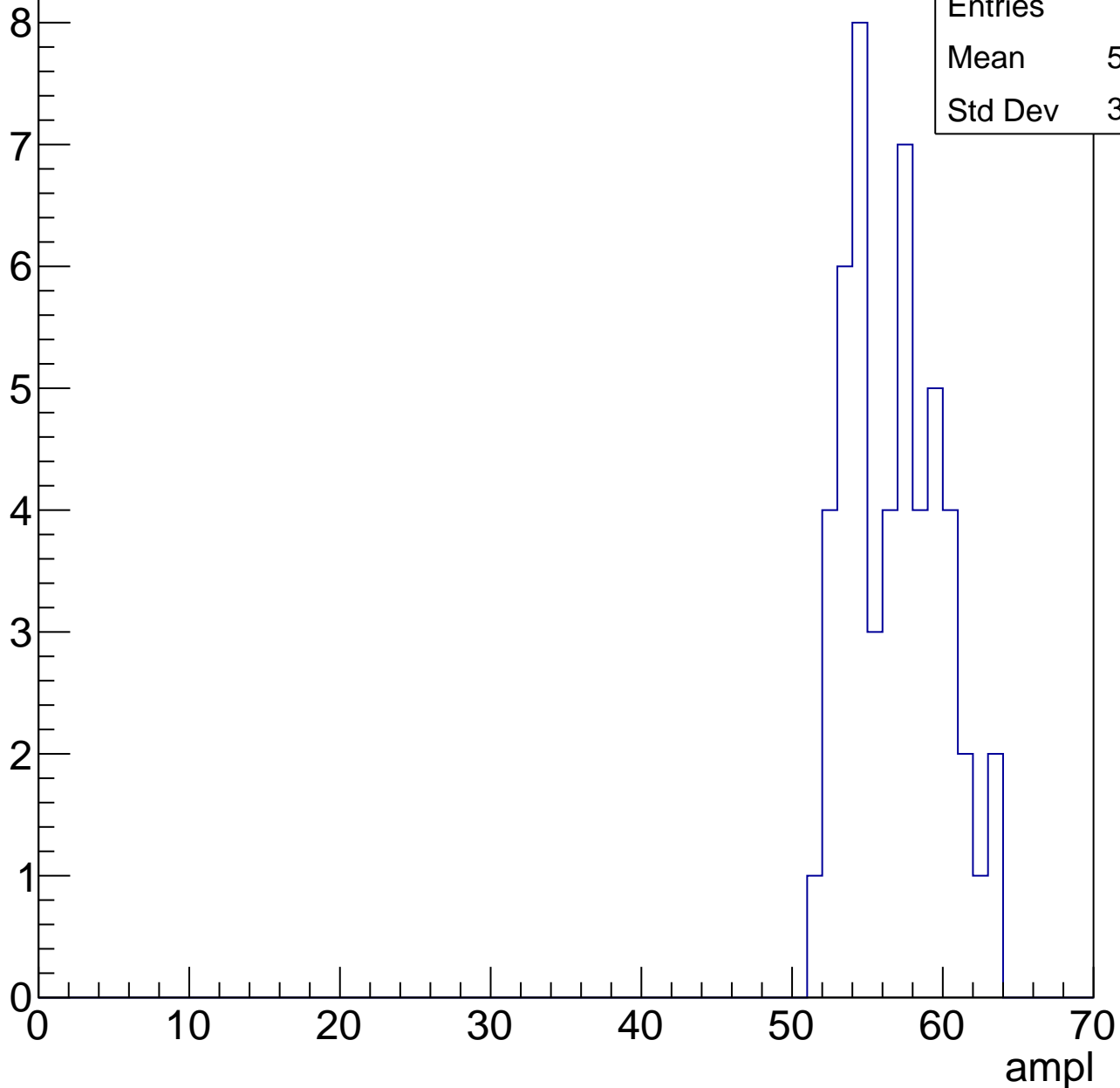


B1L003S, U13-ch26, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.35
Std Dev	3.092

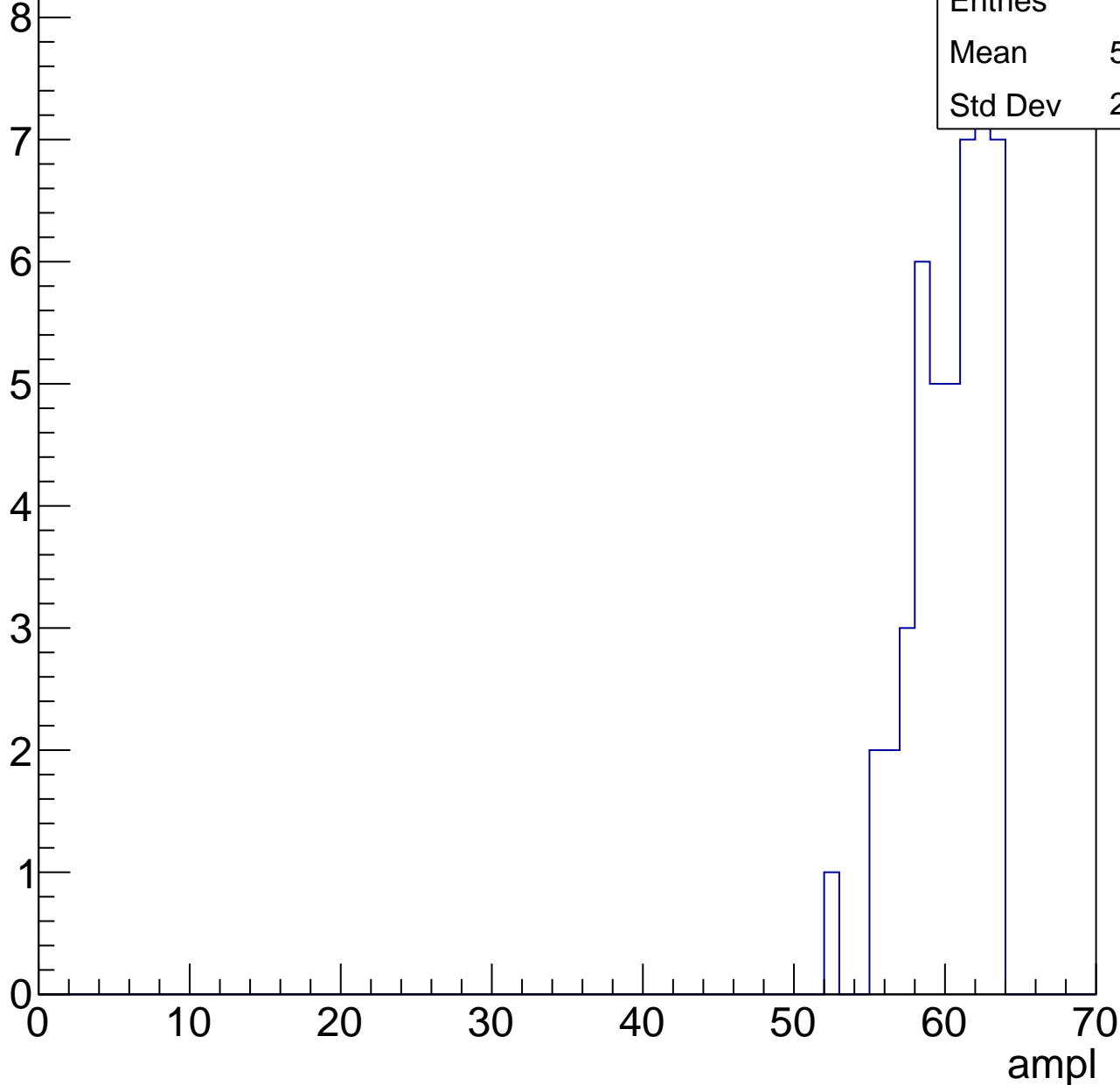


B1L003S, U13-ch26, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

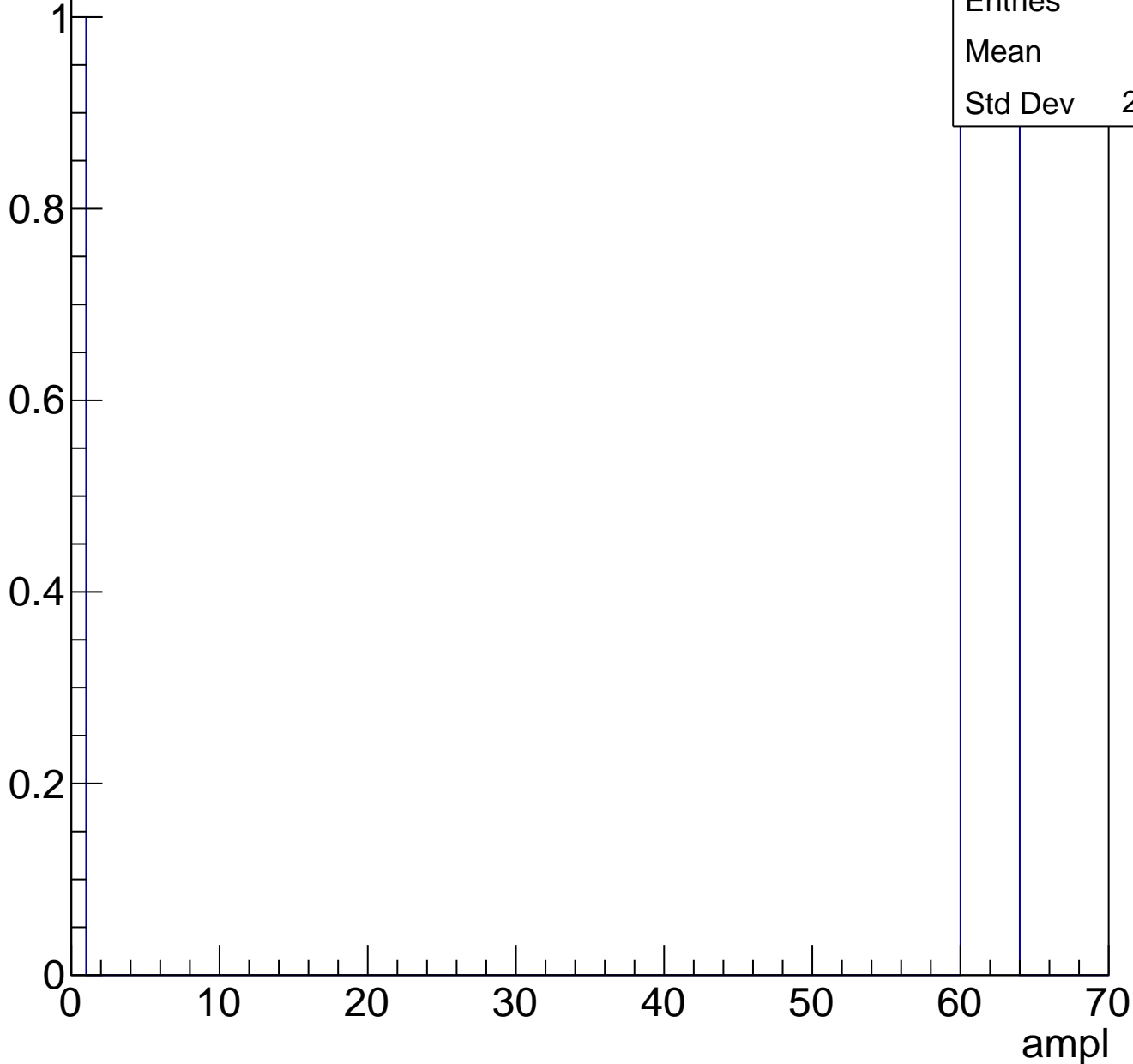
Entries	46
Mean	59.83
Std Dev	2.565



B1L003S, U13-ch26, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch26, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch27, adc0

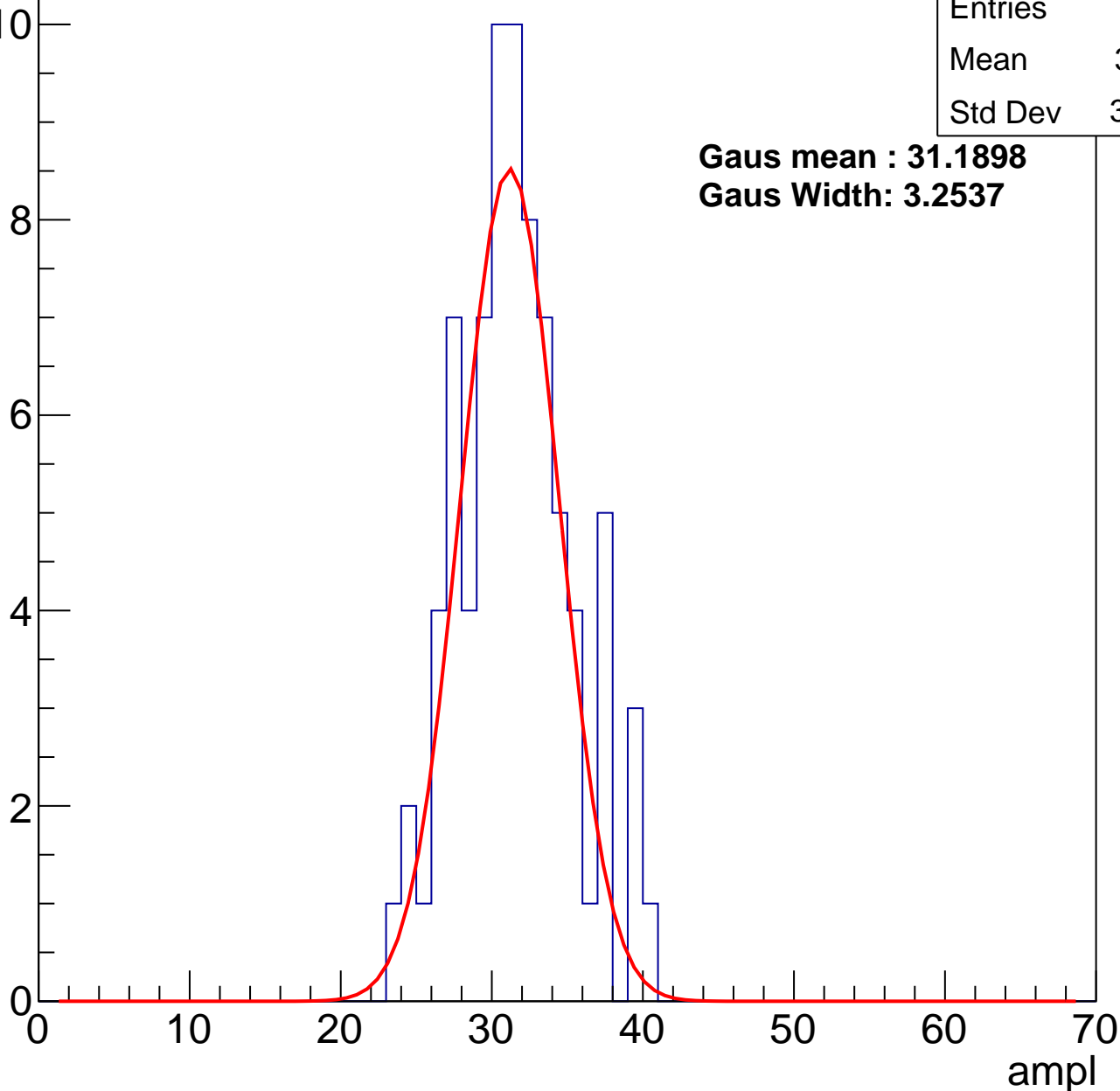
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	31.11
Std Dev	3.718

Gaus mean : 31.1898

Gaus Width: 3.2537



B1L003S, U13-ch27, adc1

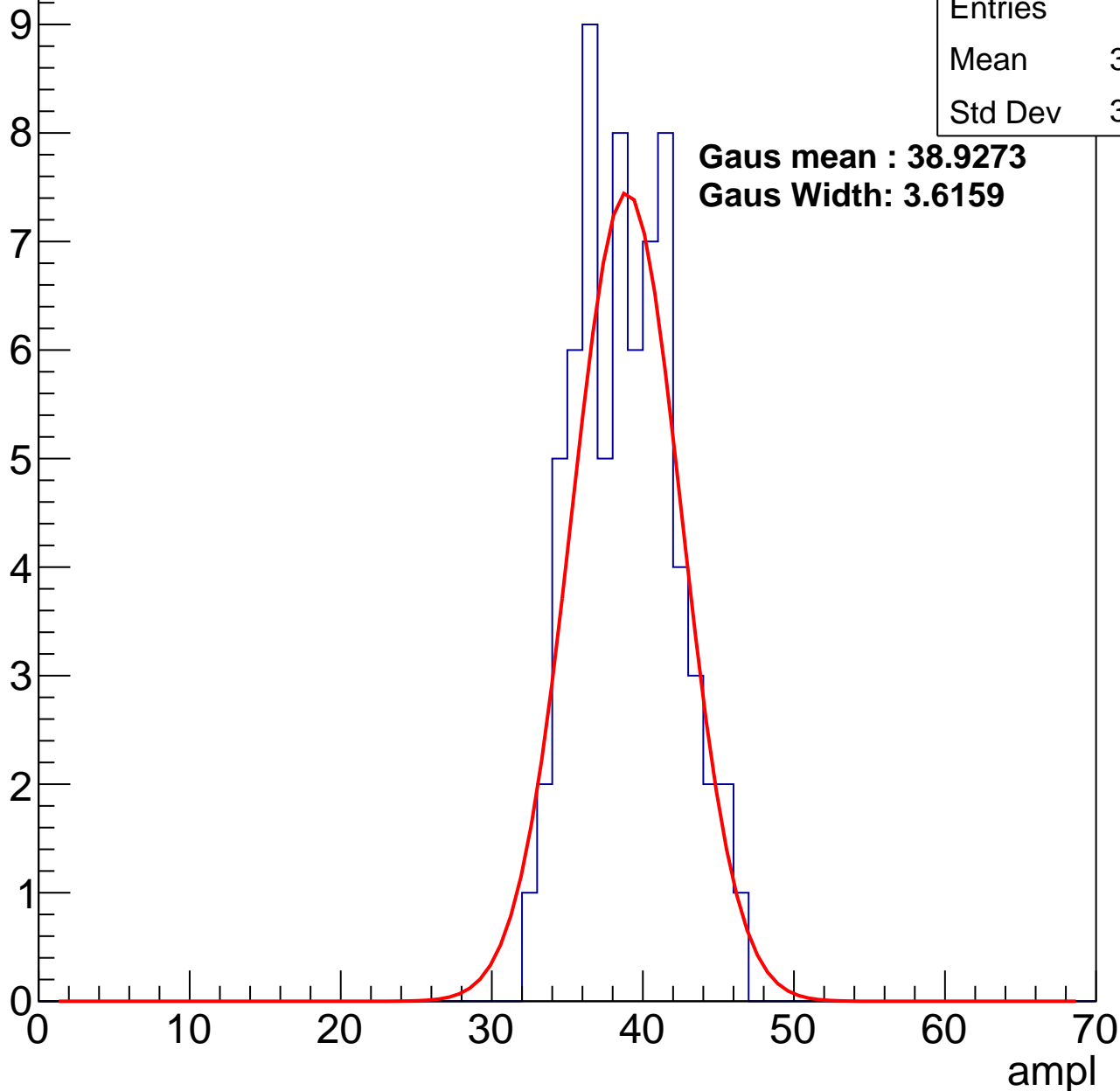
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	38.46
Std Dev	3.242

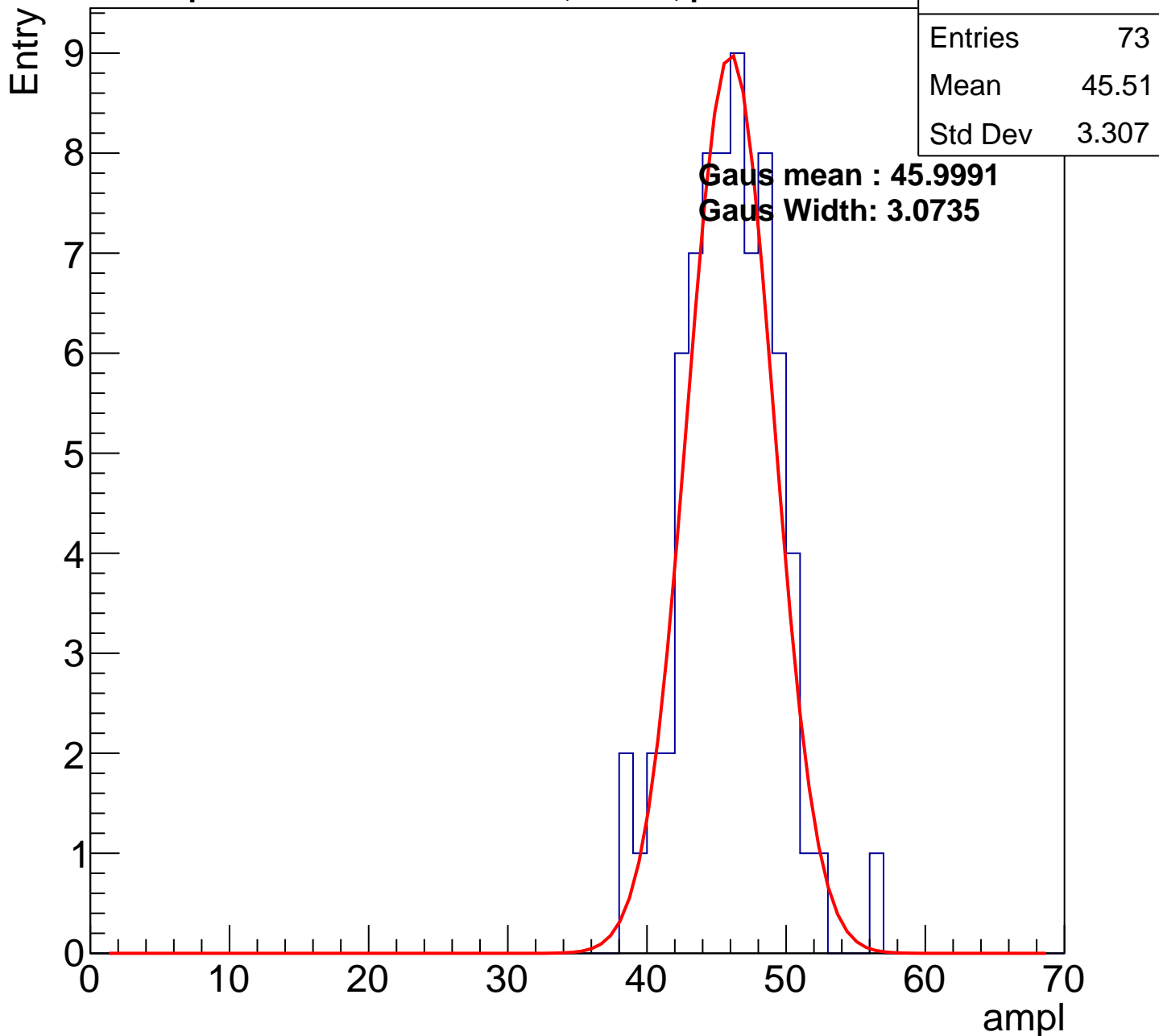
Gaus mean : 38.9273

Gaus Width: 3.6159



B1L003S, U13-ch27, adc2

calib_packv5_042523_0143.root, FC#13, port D2

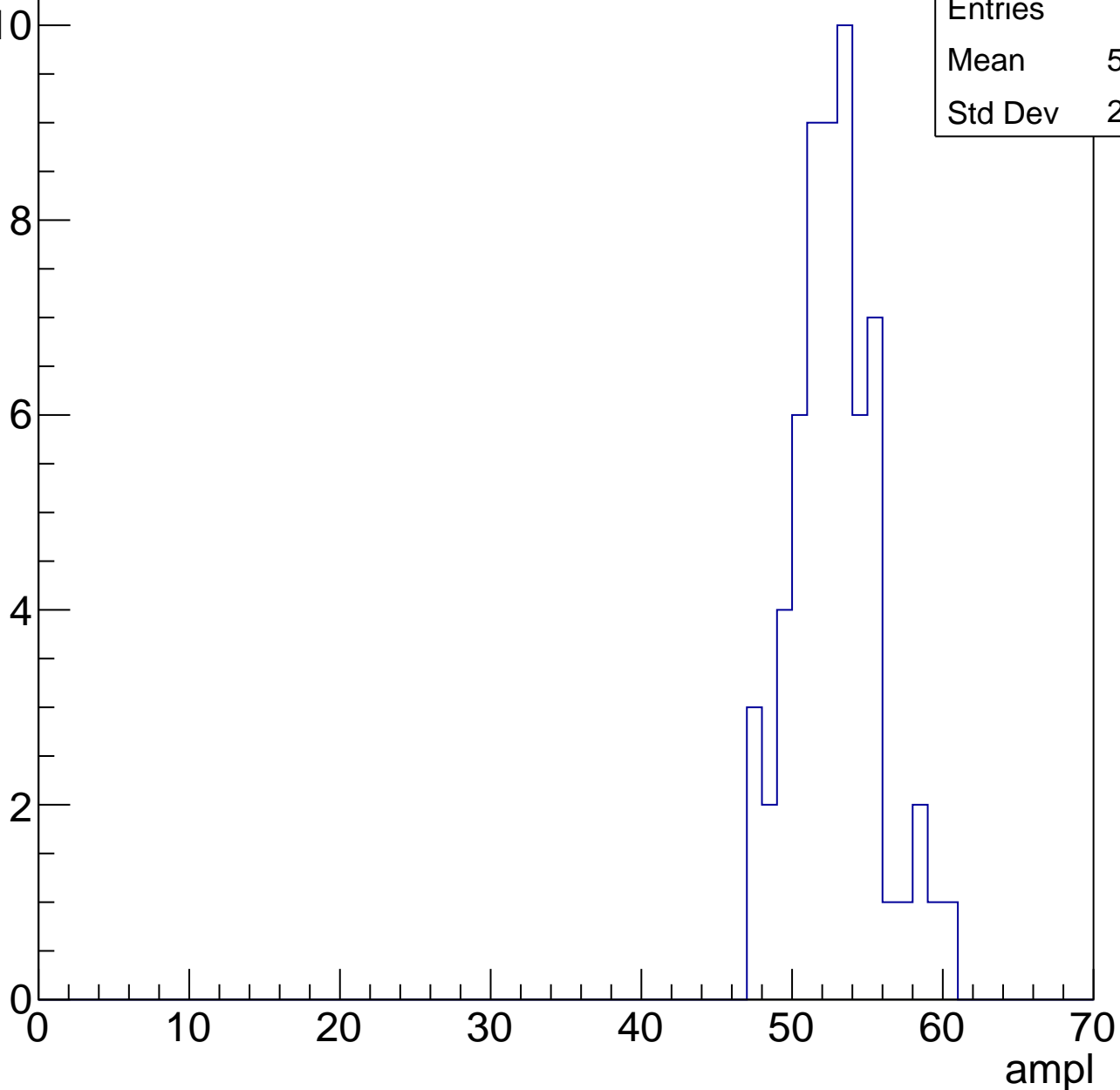


B1L003S, U13-ch27, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	52.37
Std Dev	2.813

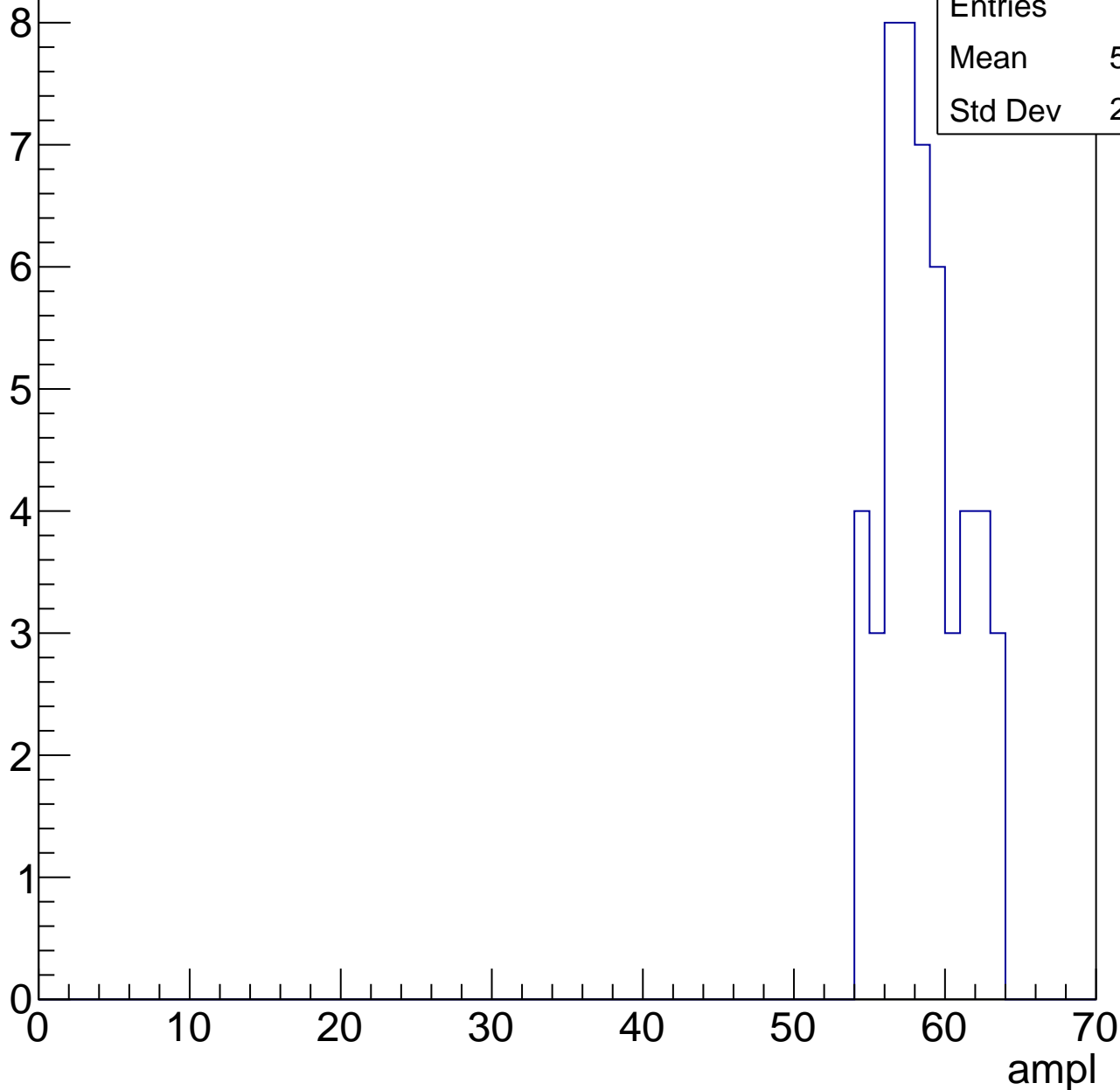


B1L003S, U13-ch27, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	58.12
Std Dev	2.543

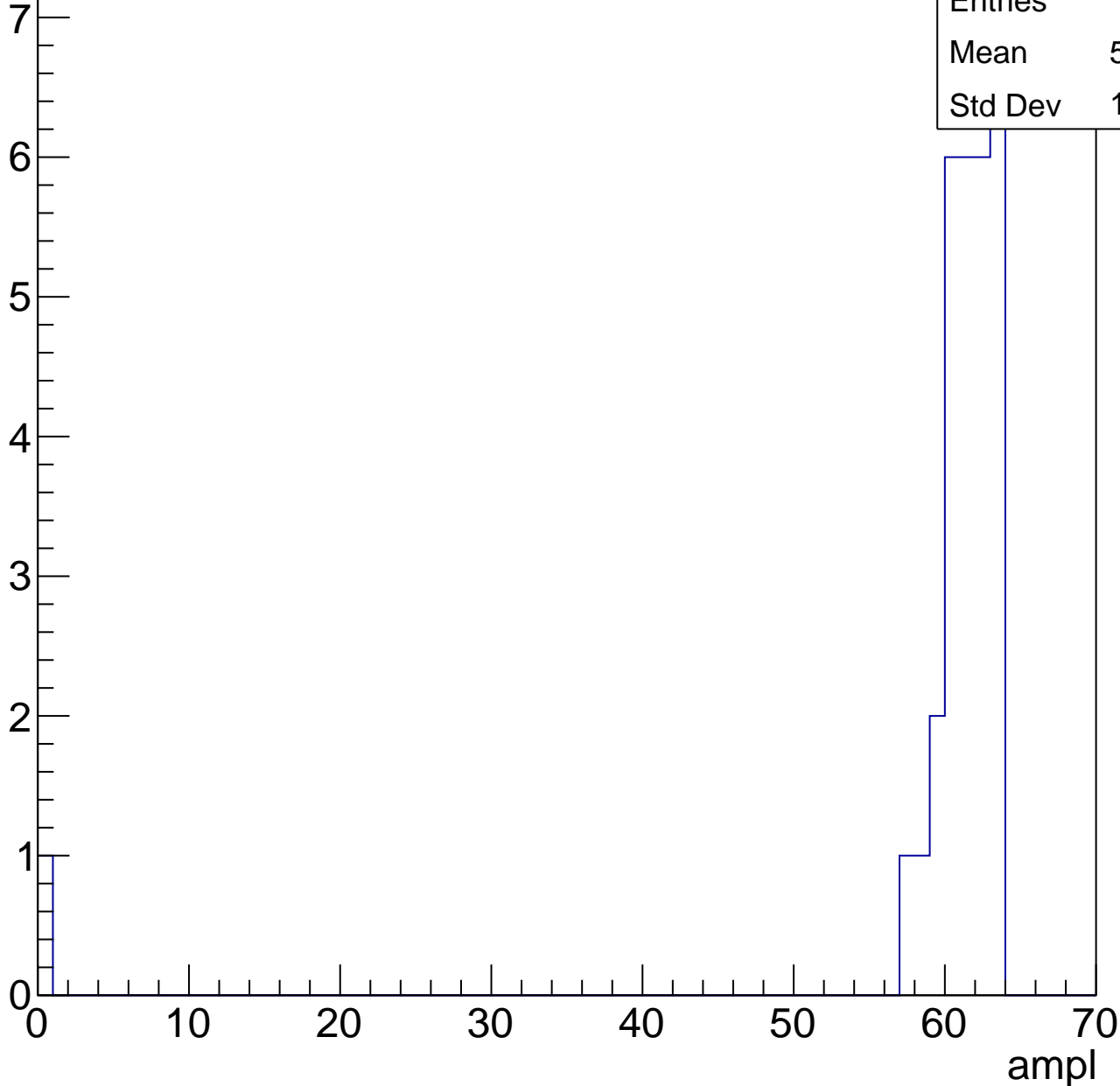


B1L003S, U13-ch27, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

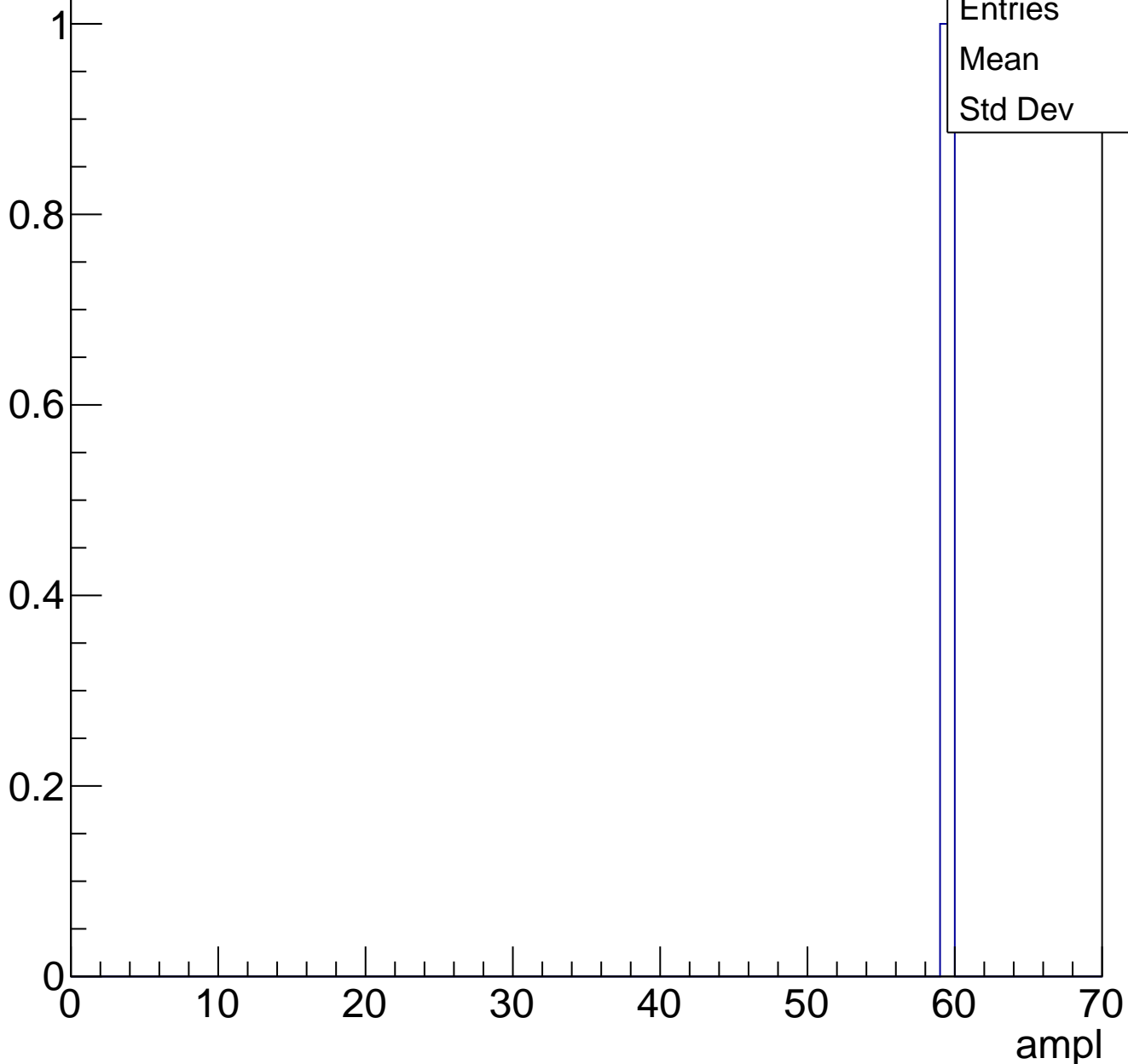
Entries	30
Mean	59.07
Std Dev	11.08



B1L003S, U13-ch27, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	59
Std Dev	0

B1L003S, U13-ch27, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch28, adc0

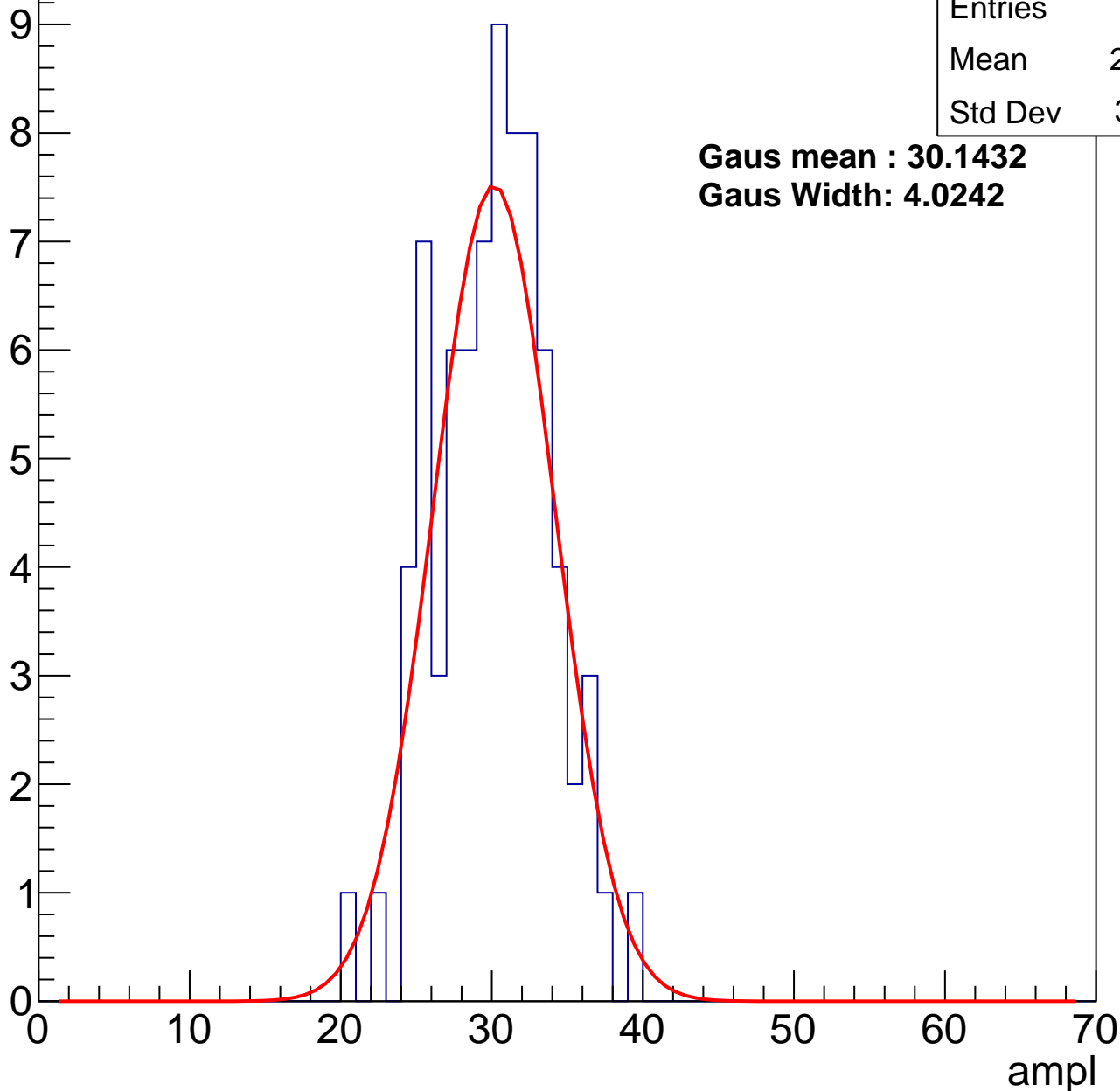
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	29.69
Std Dev	3.701

Gaus mean : 30.1432

Gaus Width: 4.0242

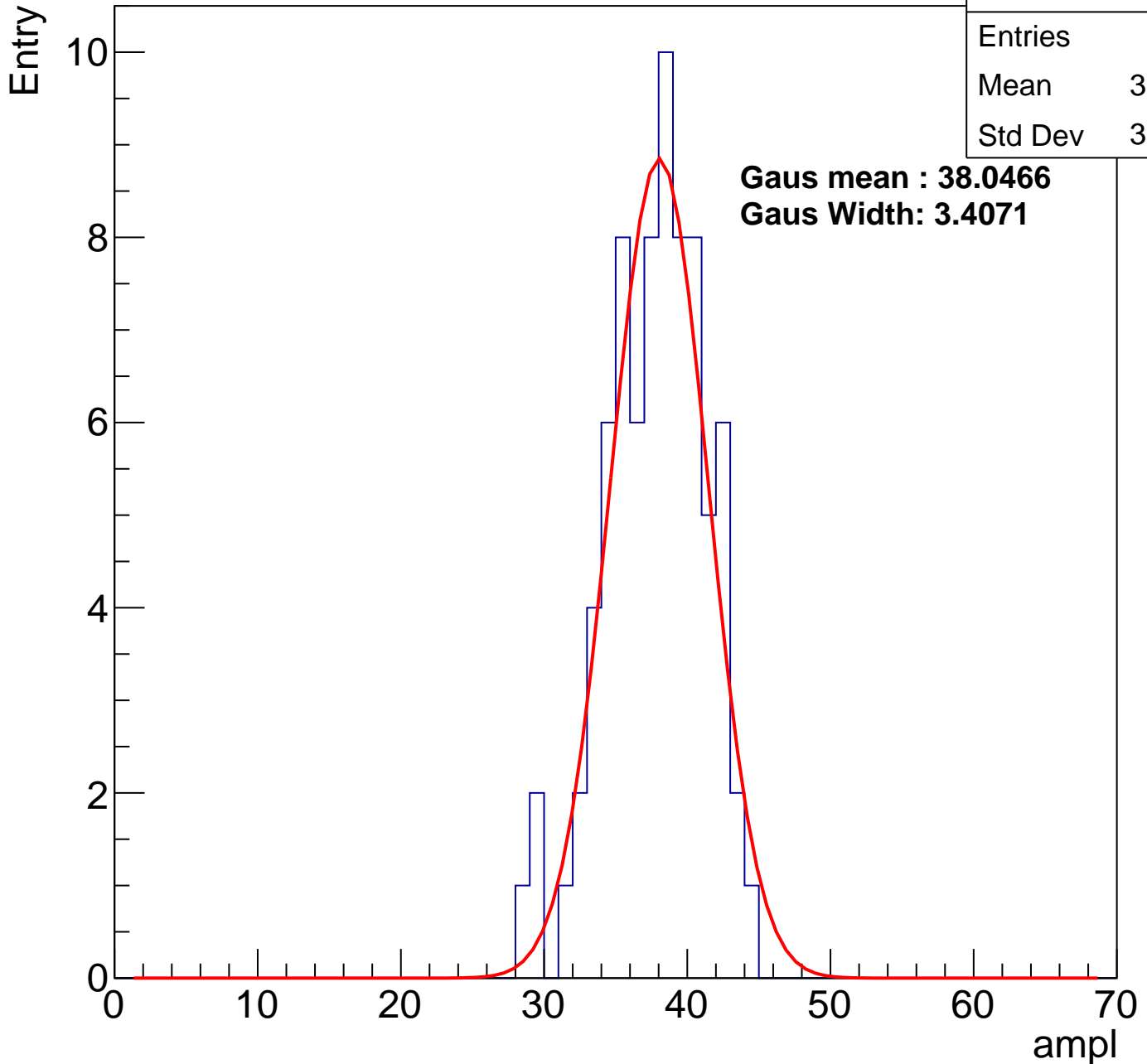


B1L003S, U13-ch28, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	37.28
Std Dev	3.415

Gaus mean : 38.0466
Gaus Width: 3.4071



B1L003S, U13-ch28, adc2

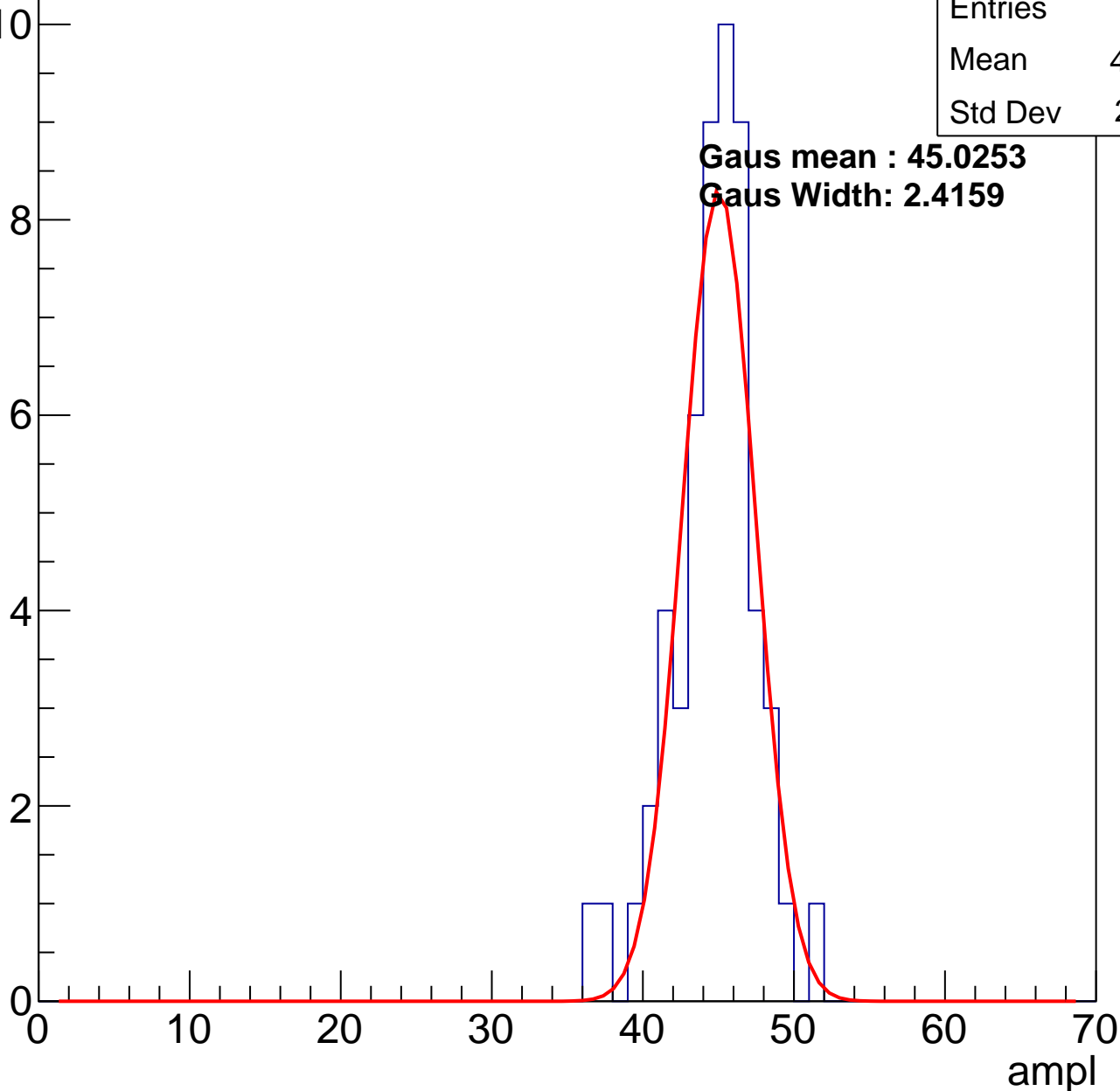
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	44.22
Std Dev	2.781

Gaus mean : 45.0253

Gaus Width: 2.4159

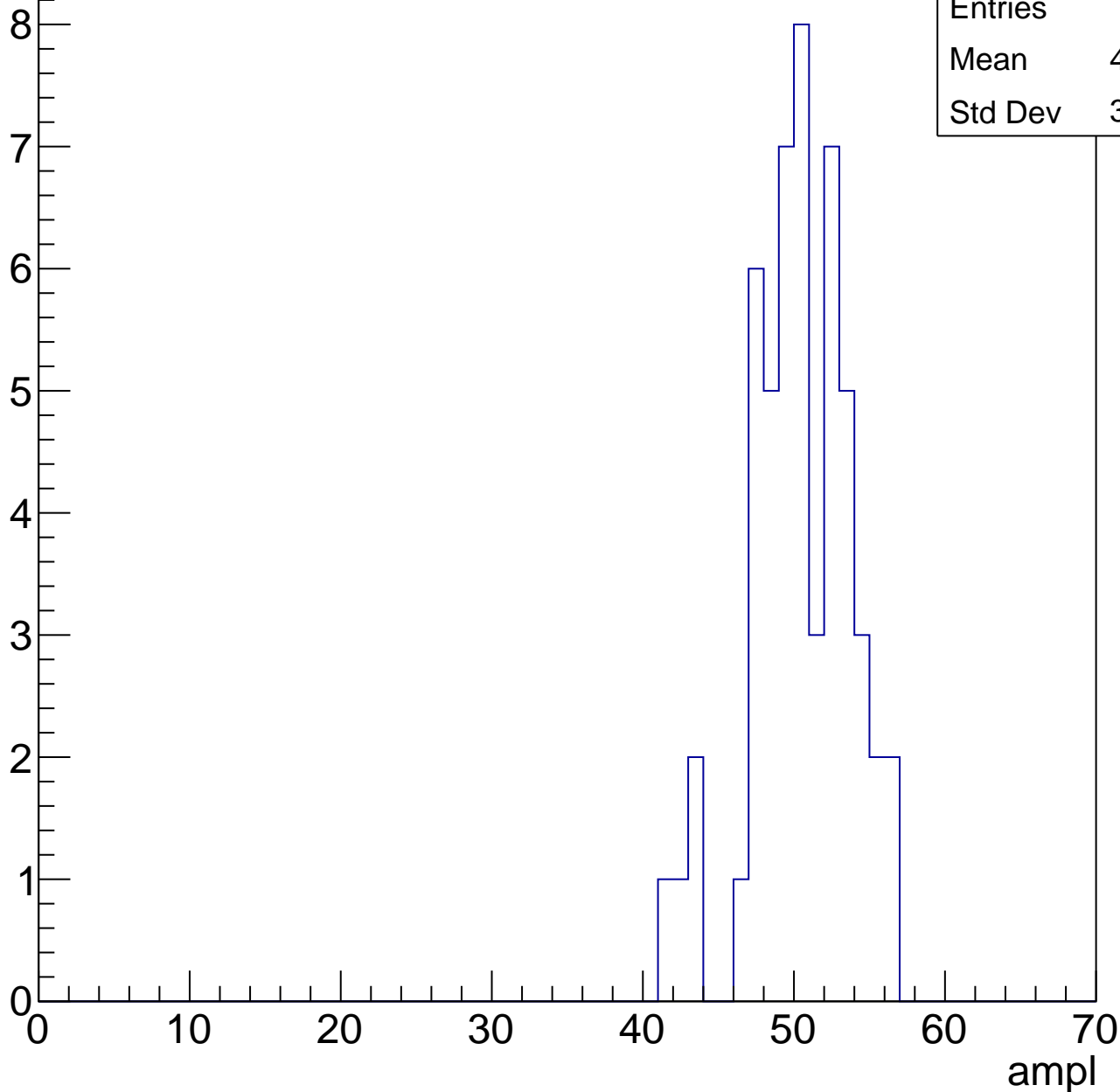


B1L003S, U13-ch28, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

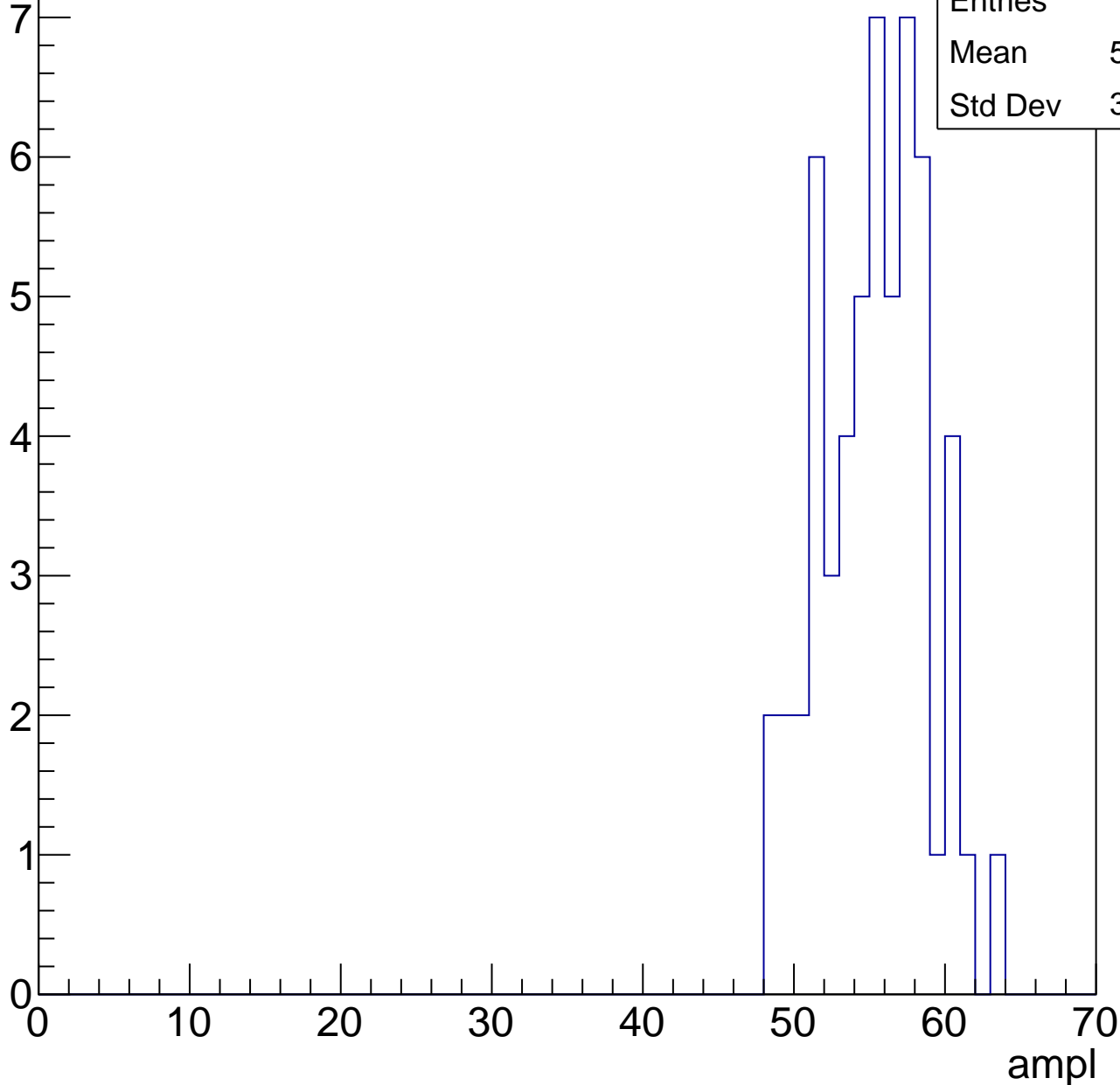
Entries	53
Mean	49.92
Std Dev	3.324



B1L003S, U13-ch28, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

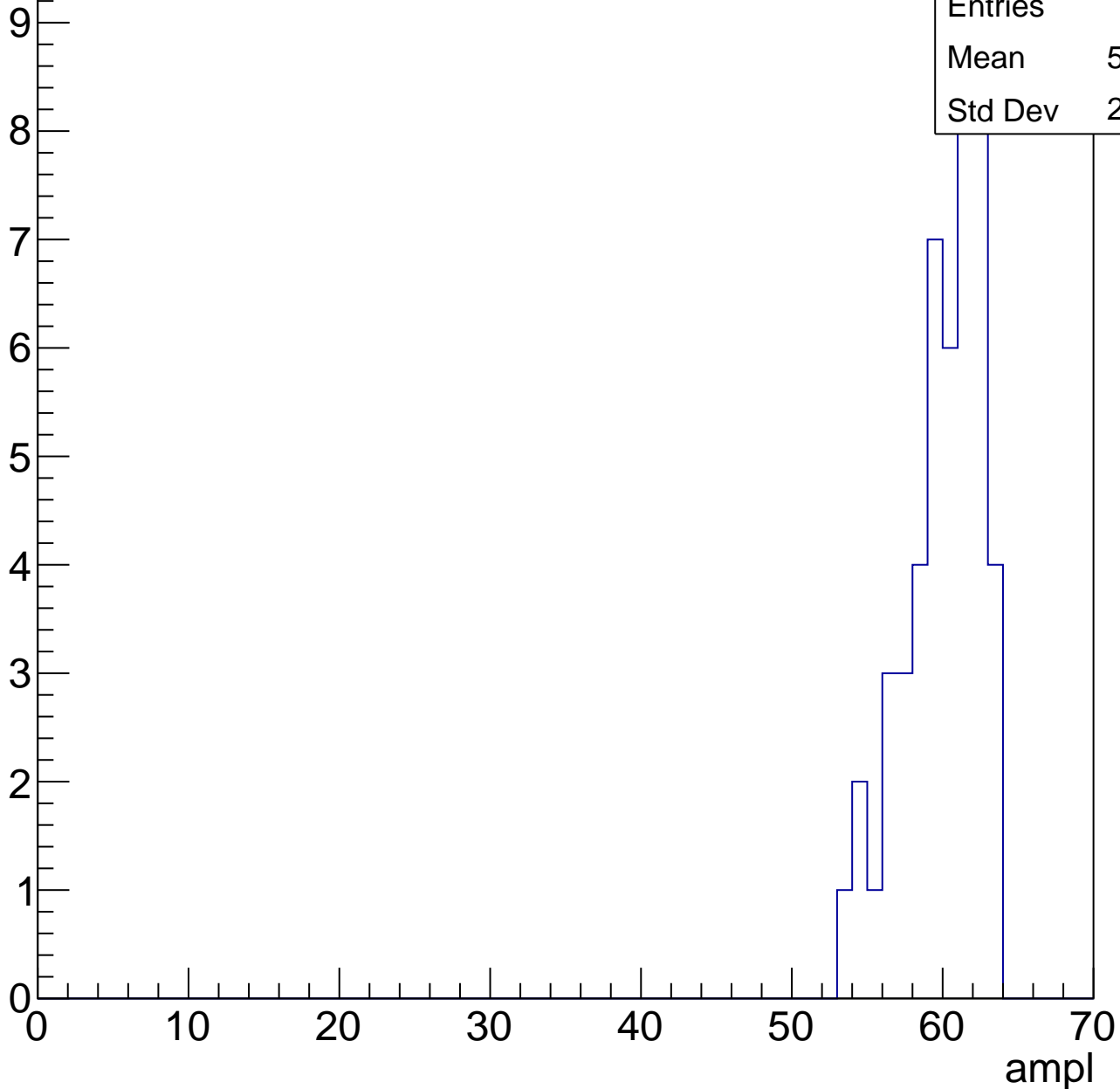


B1L003S, U13-ch28, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.57
Std Dev	2.539

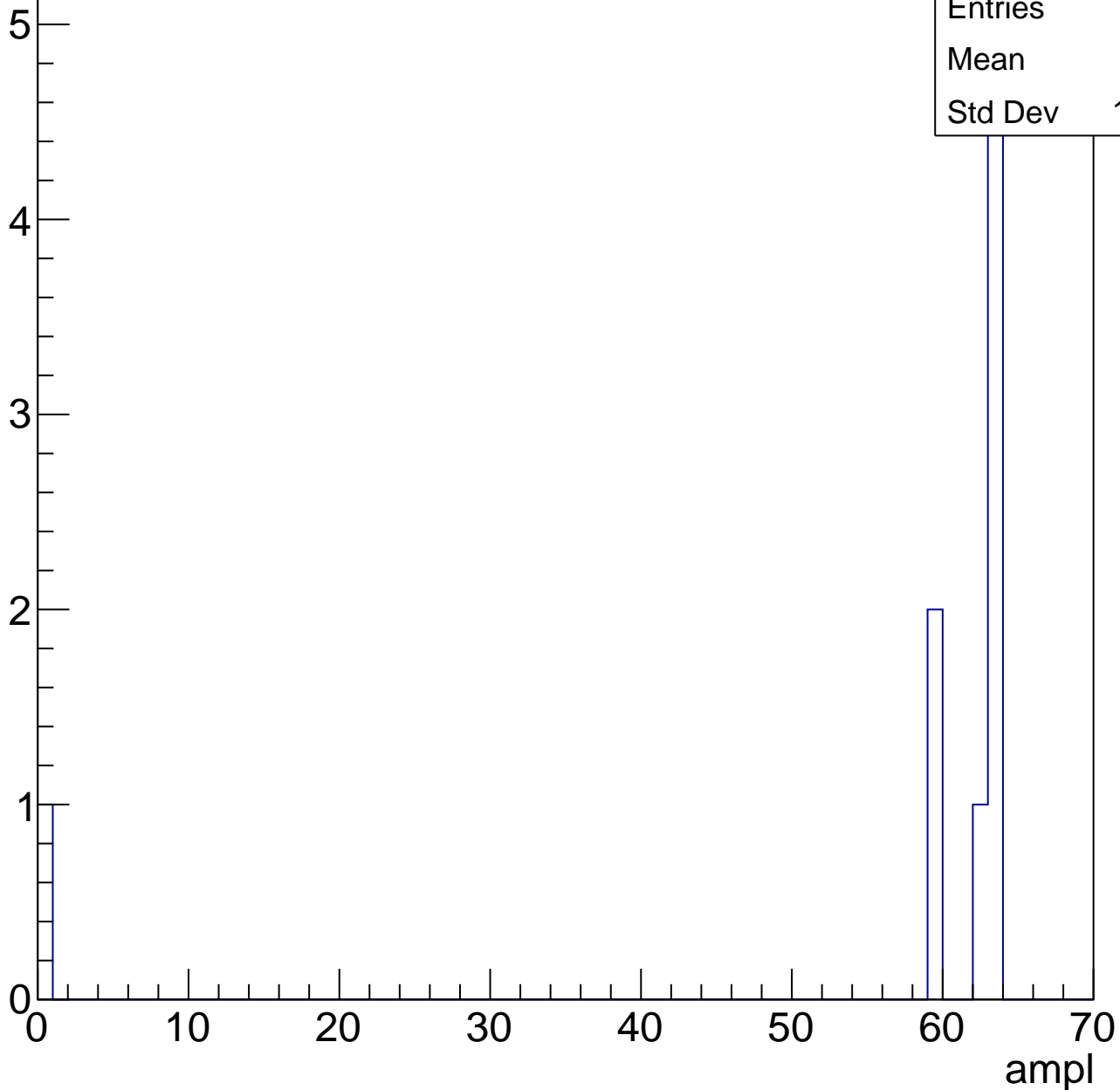


B1L003S, U13-ch28, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	55
Std Dev	19.51



B1L003S, U13-ch28, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch29, adc0

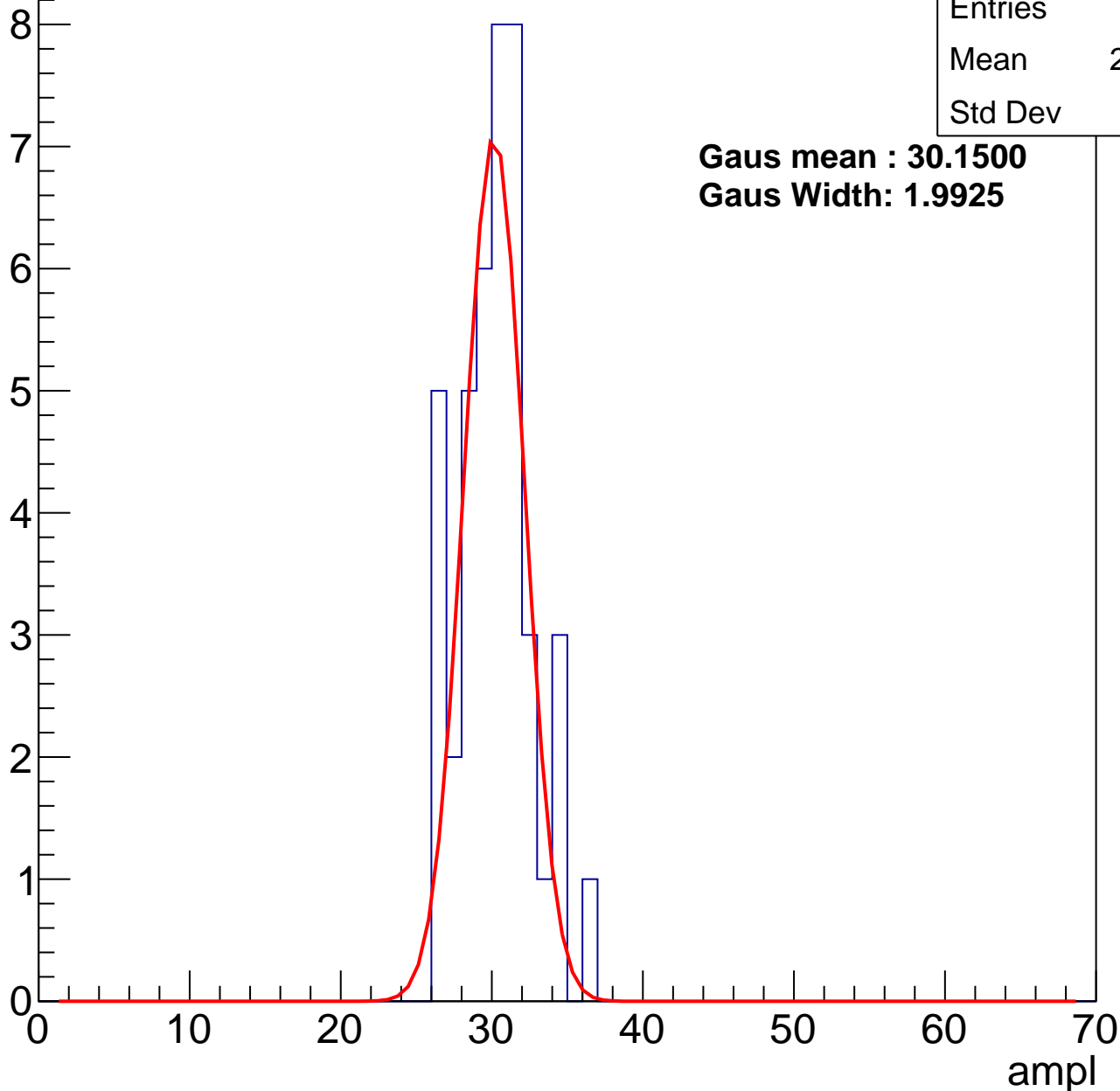
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	29.83
Std Dev	2.37

Gaus mean : 30.1500

Gaus Width: 1.9925



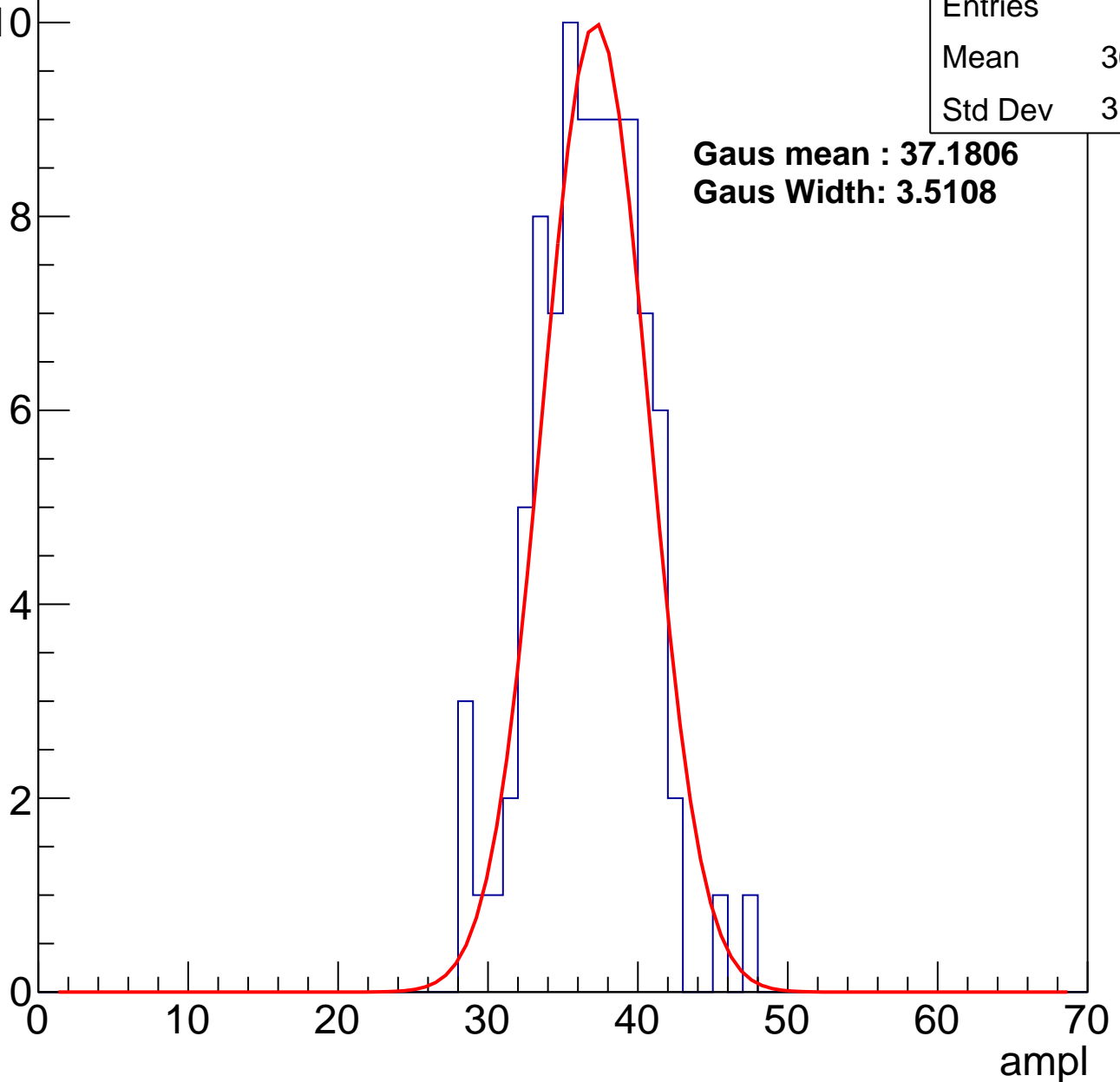
B1L003S, U13-ch29, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	90
Mean	36.32
Std Dev	3.605

Gaus mean : 37.1806
Gaus Width: 3.5108



B1L003S, U13-ch29, adc2

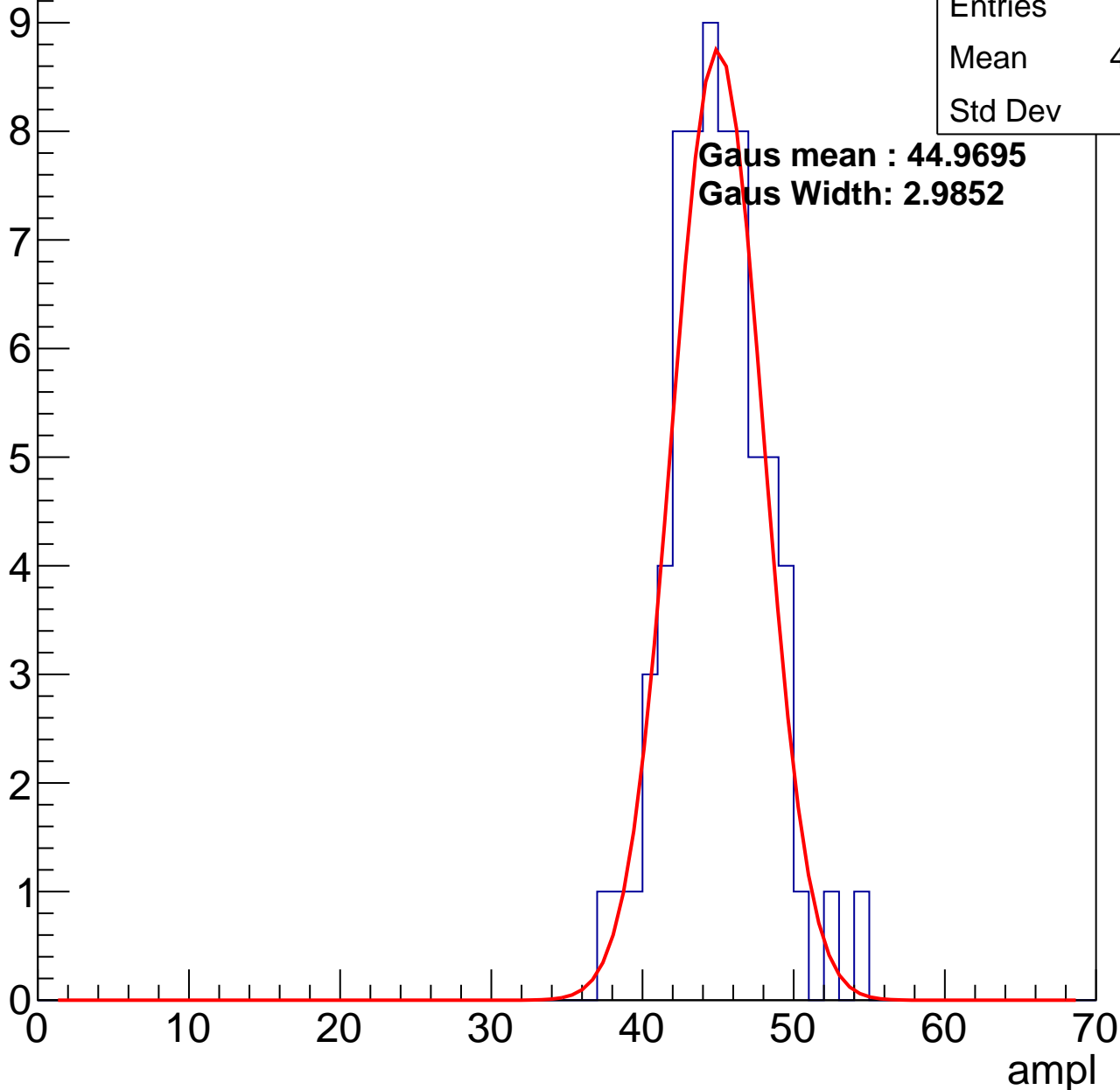
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	44.54
Std Dev	3.16

Gaus mean : 44.9695

Gaus Width: 2.9852

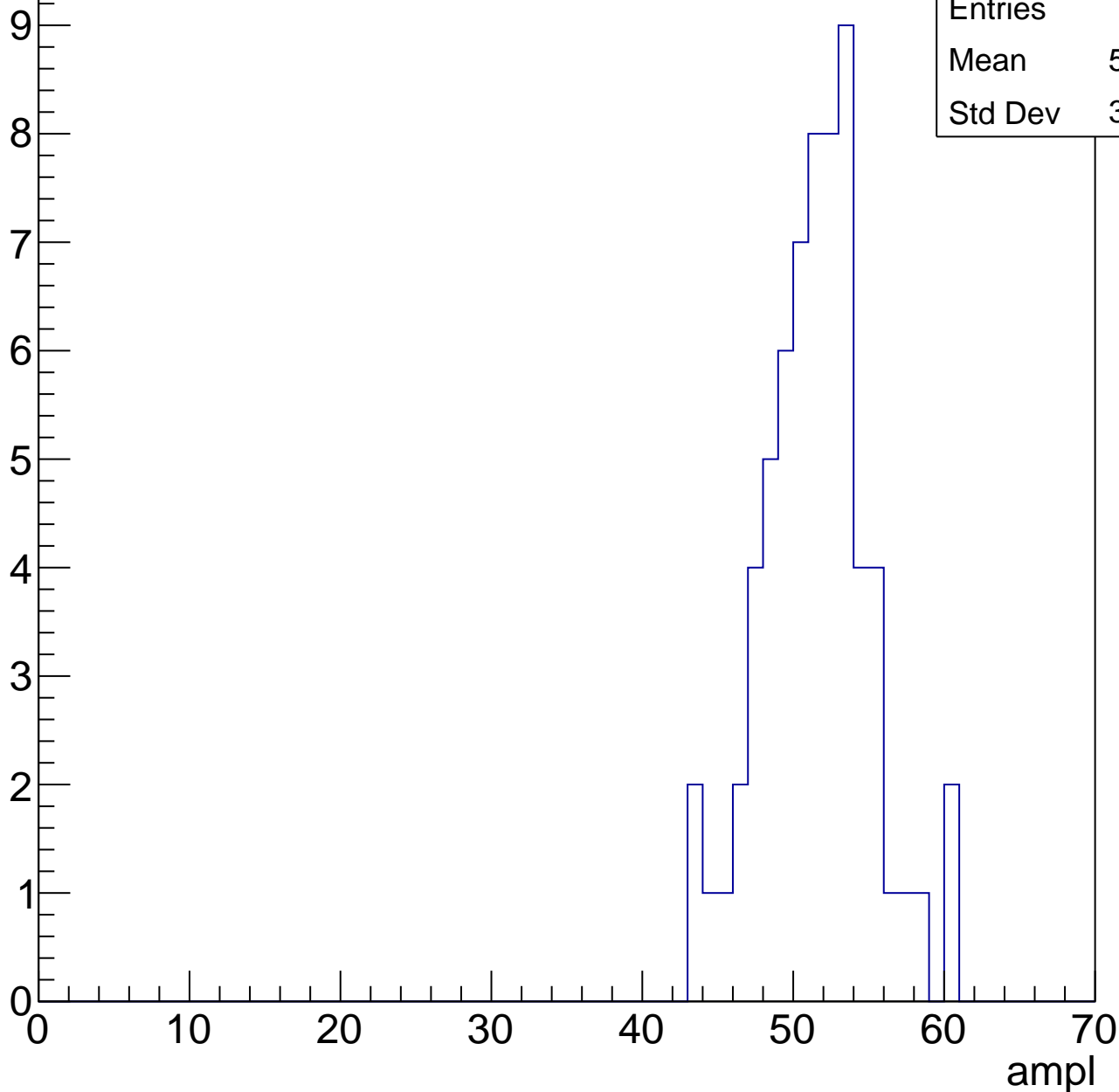


B1L003S, U13-ch29, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	51.02
Std Dev	3.523

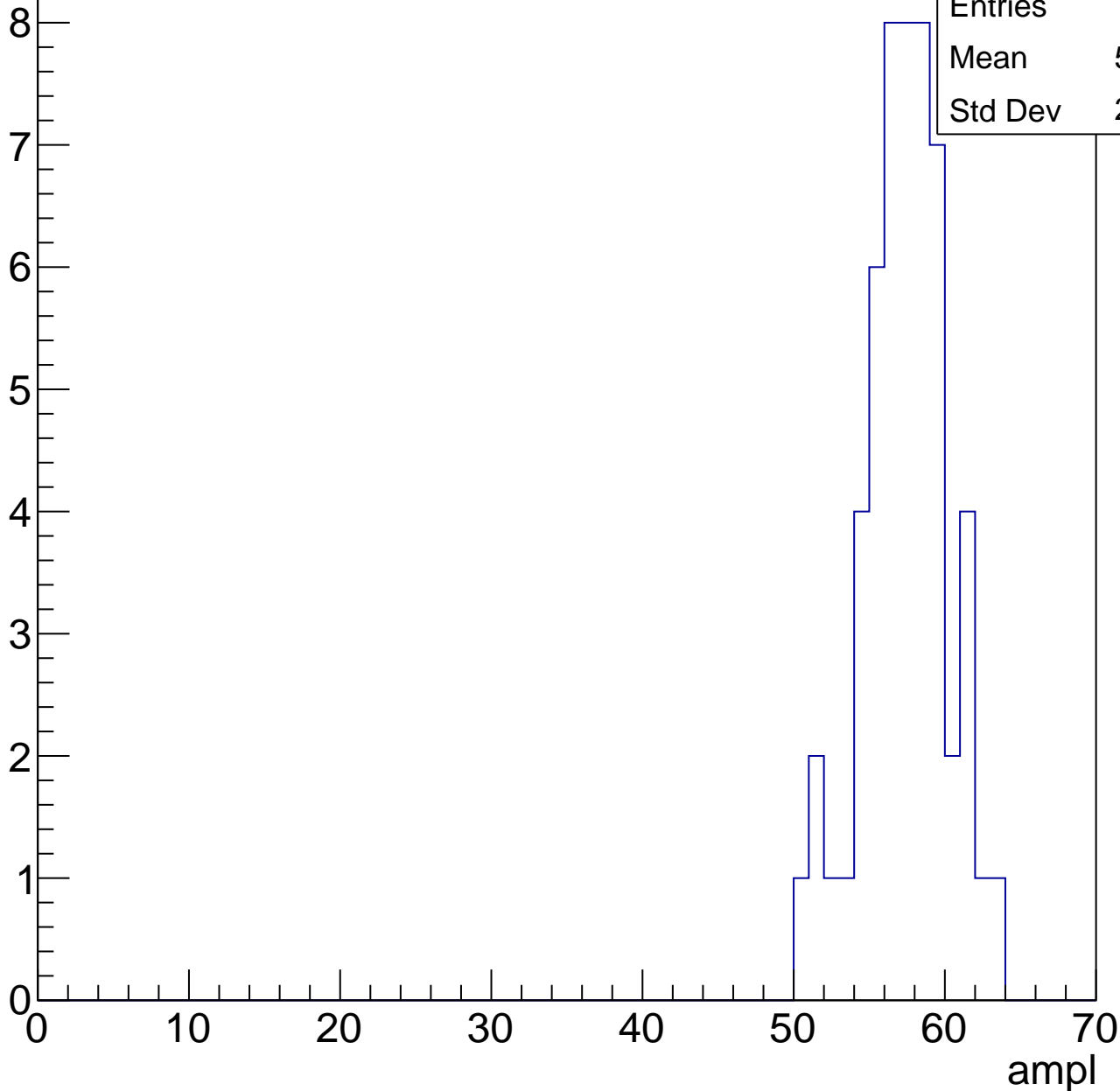


B1L003S, U13-ch29, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.91
Std Dev	2.751

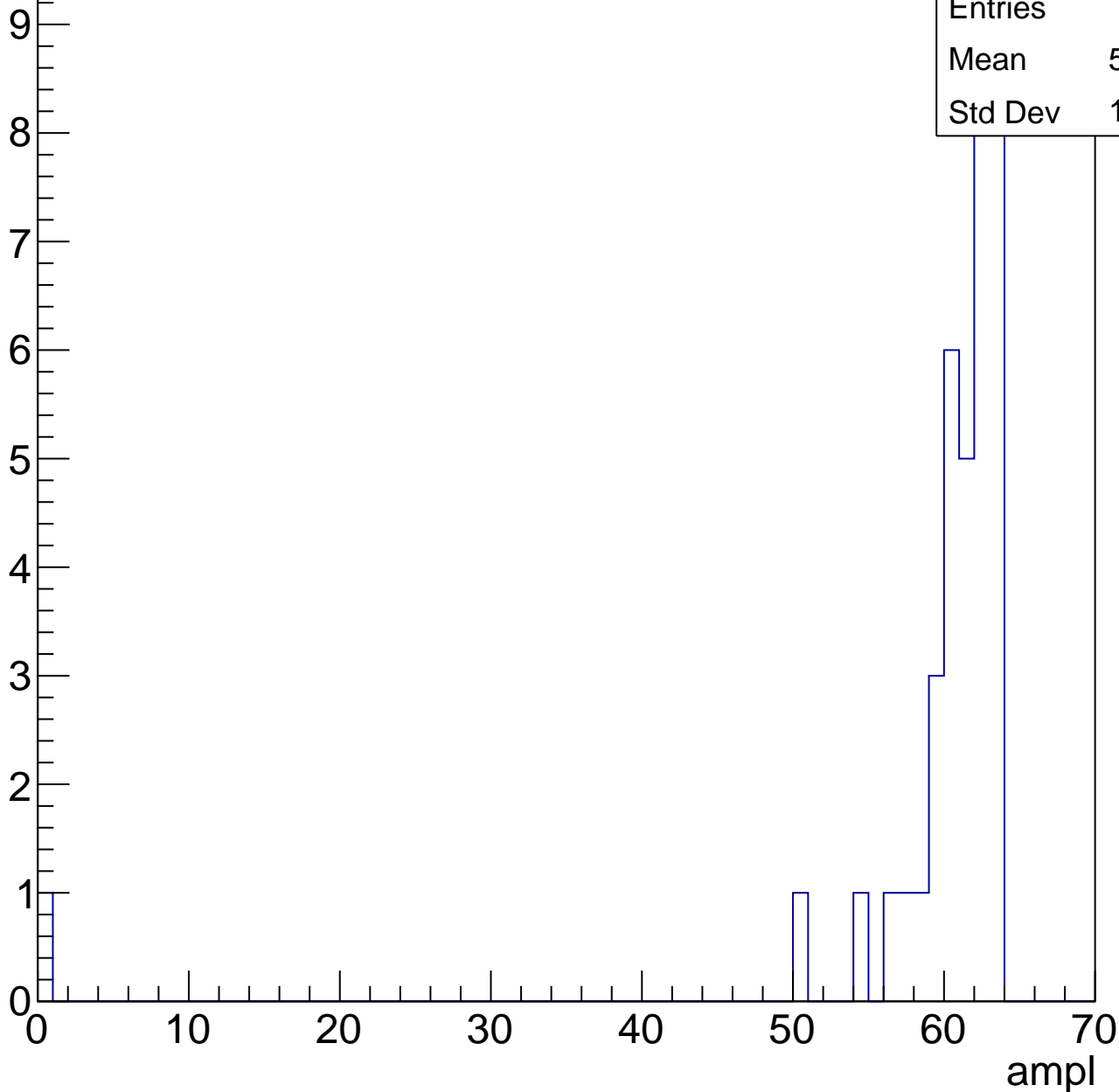


B1L003S, U13-ch29, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

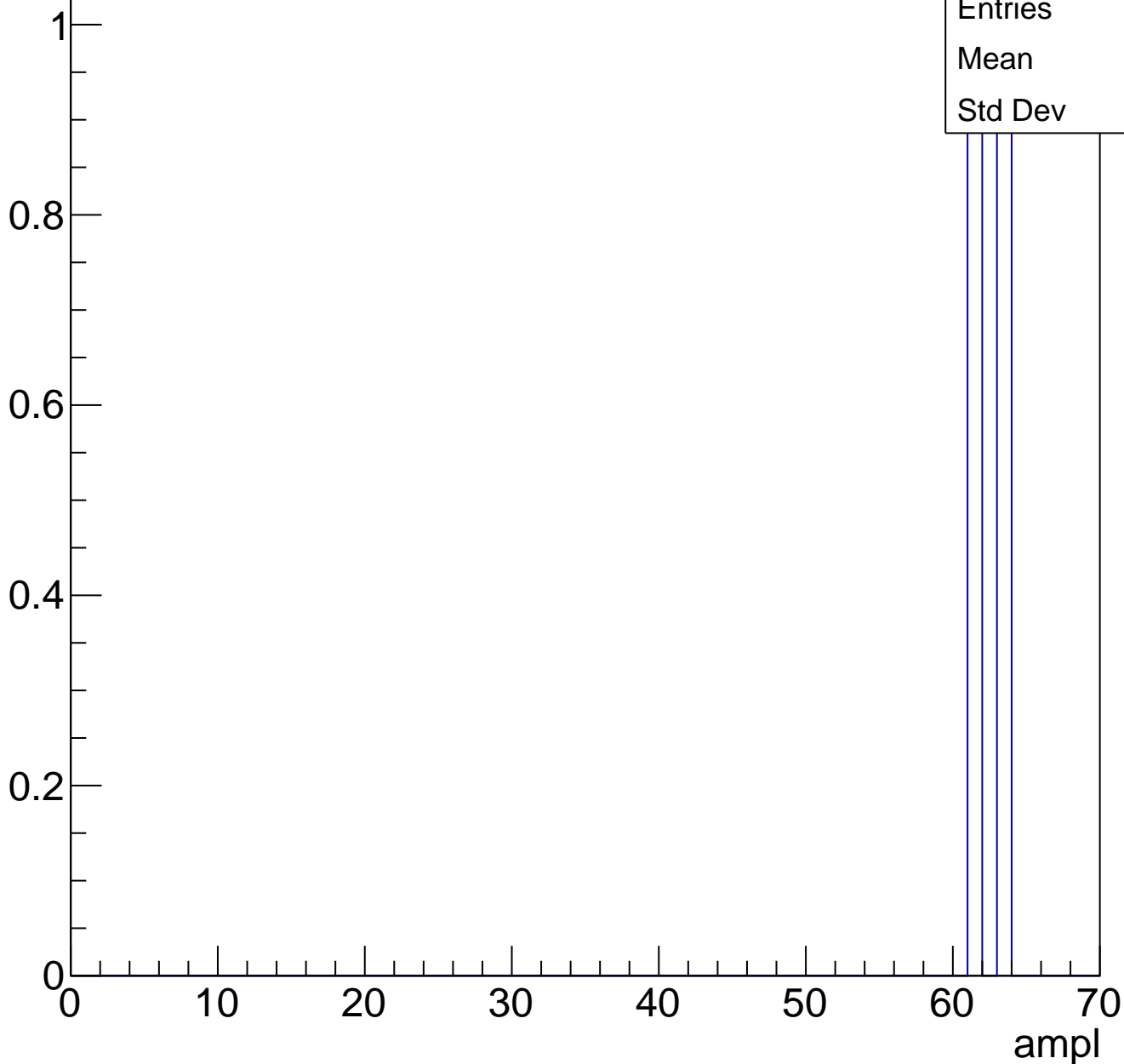
Entries	37
Mean	58.89
Std Dev	10.18



B1L003S, U13-ch29, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch29, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

0 10 20 30 40 50 60 70

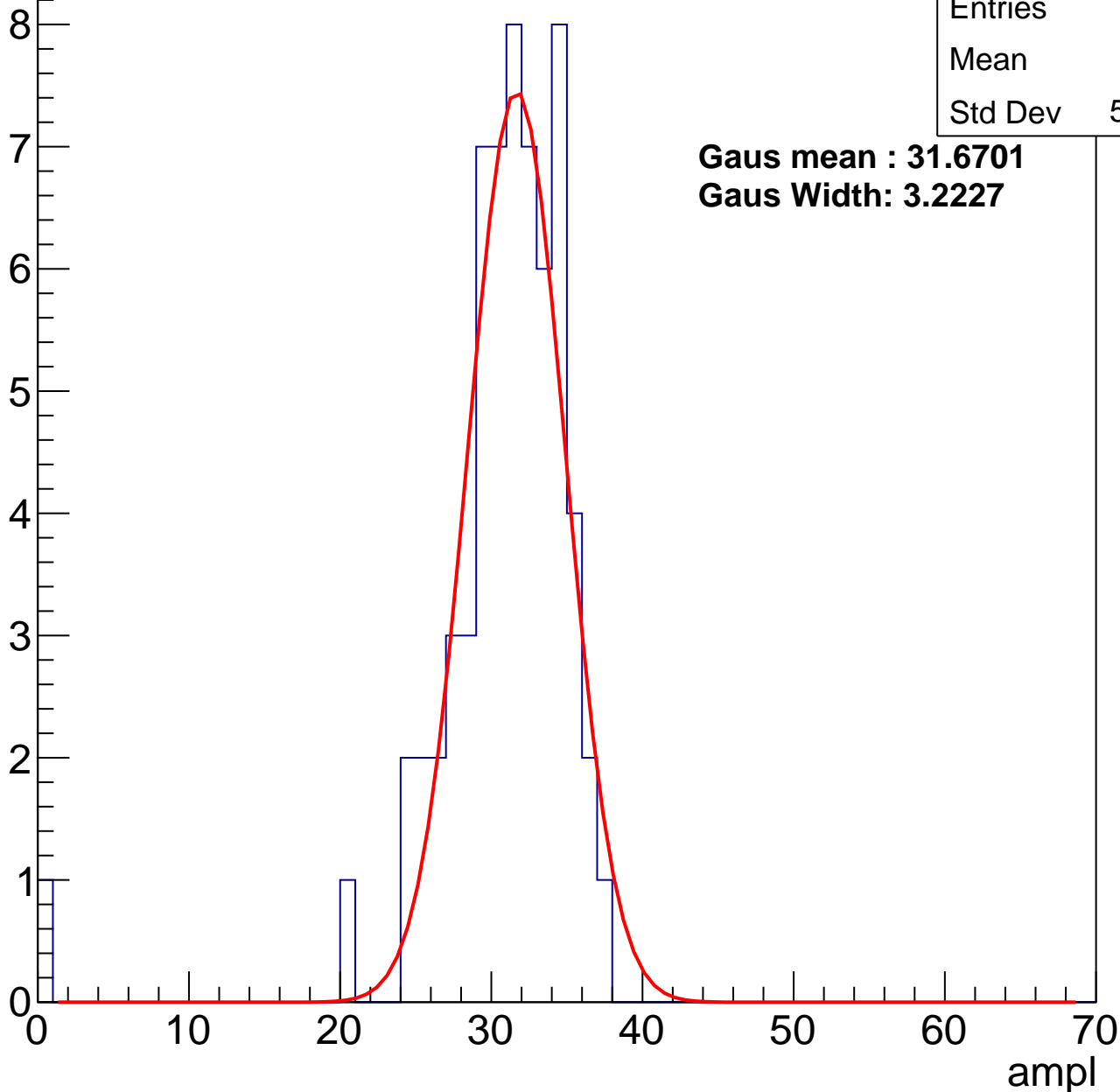
B1L003S, U13-ch30, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.3
Std Dev	5.052

Gaus mean : 31.6701
Gaus Width: 3.2227



B1L003S, U13-ch30, adc1

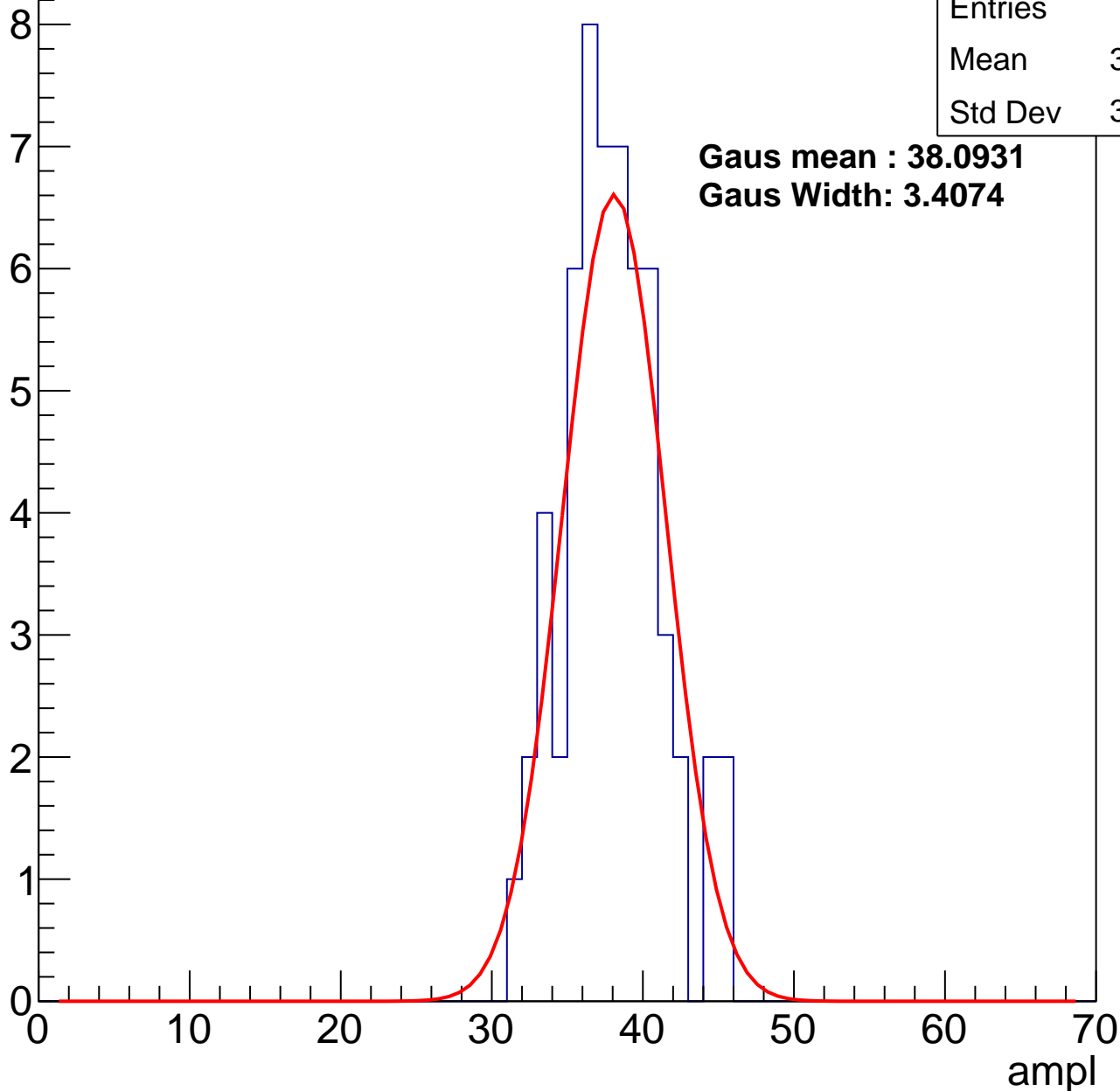
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	37.53
Std Dev	3.196

Gaus mean : 38.0931

Gaus Width: 3.4074



B1L003S, U13-ch30, adc2

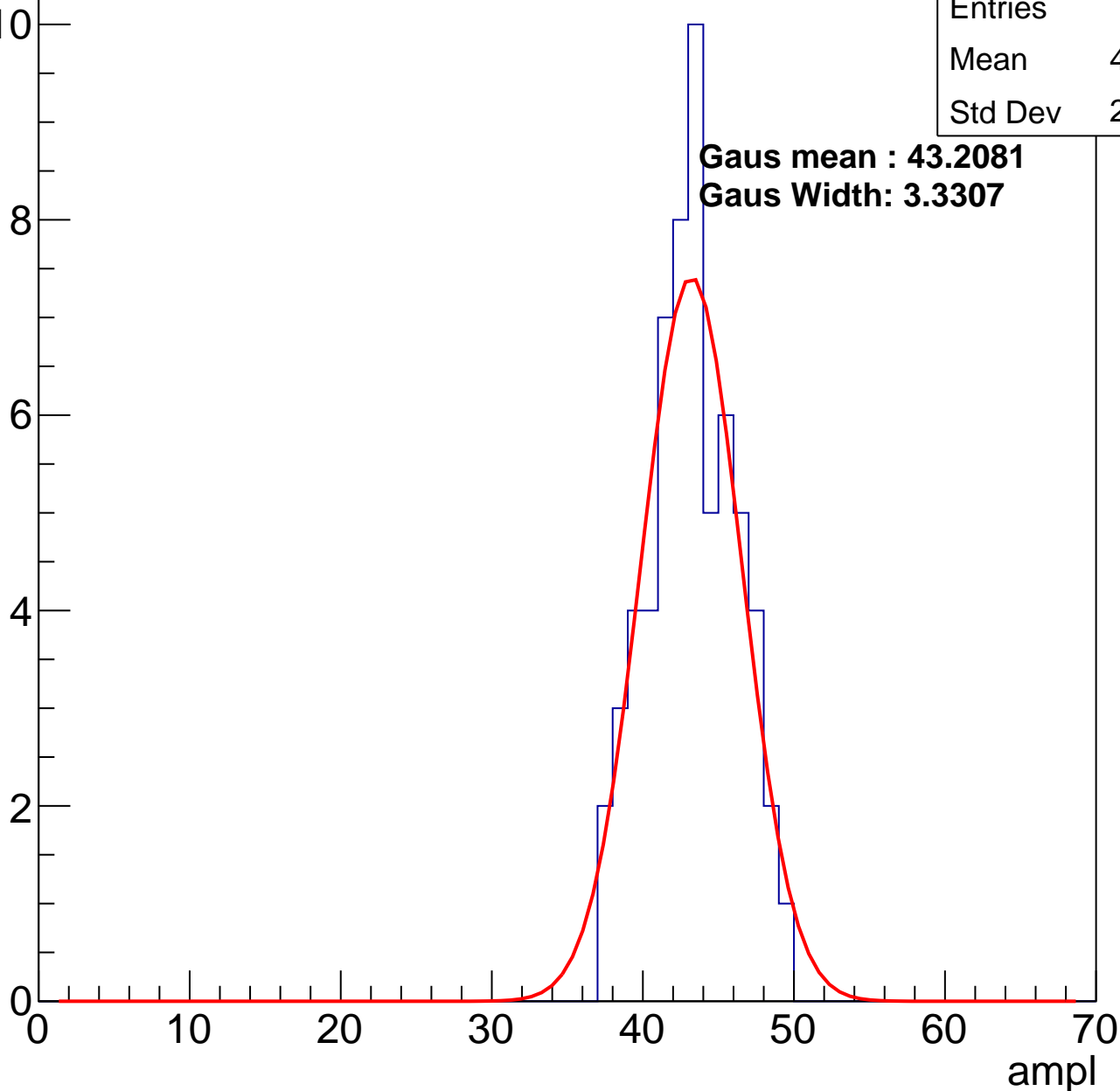
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.79
Std Dev	2.875

Gaus mean : 43.2081

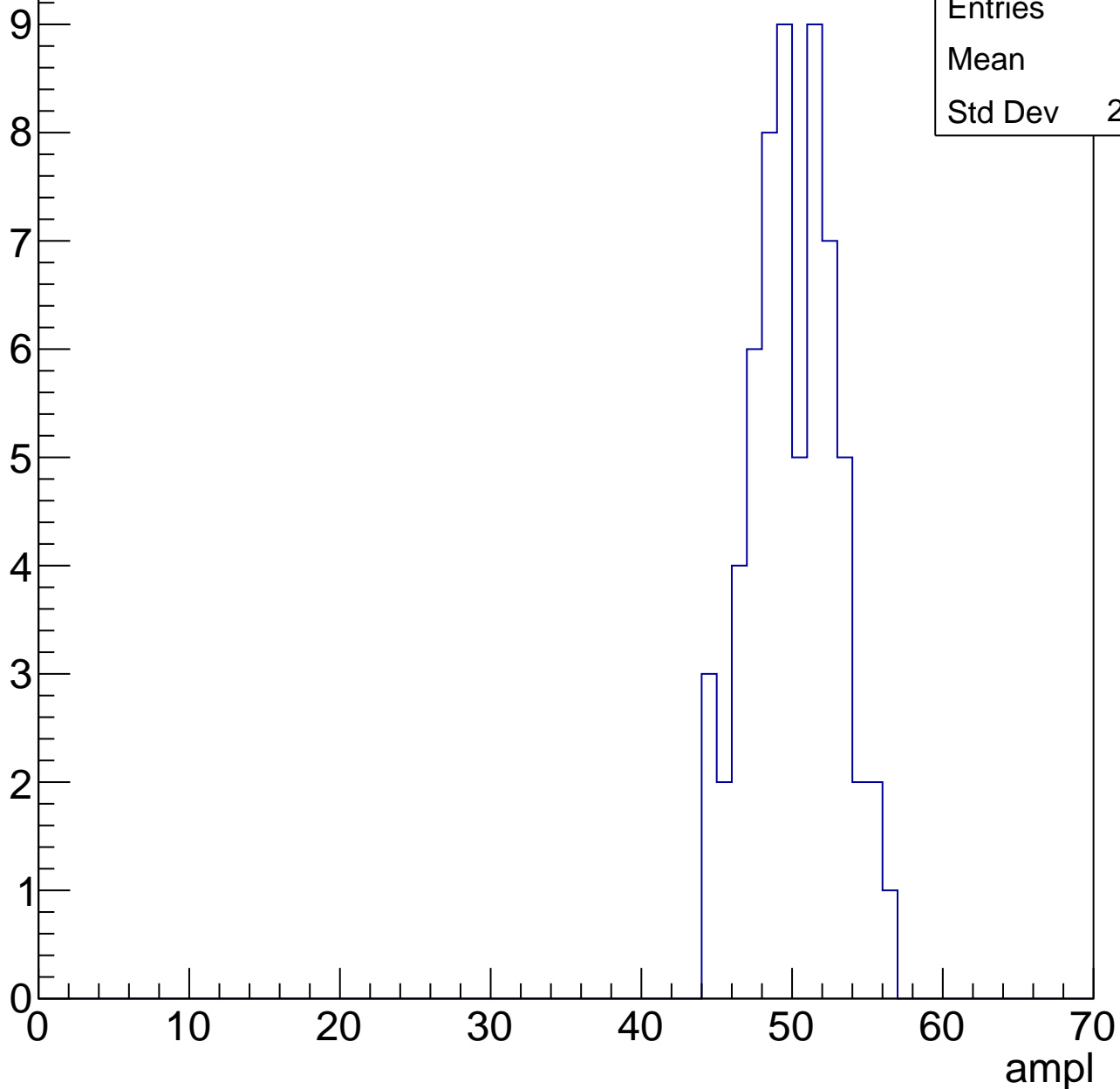
Gaus Width: 3.3307



B1L003S, U13-ch30, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	63
Mean	49.6
Std Dev	2.837

B1L003S, U13-ch30, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	55.46
Std Dev	7.155

Entry

10

8

6

4

2

0

0

10

20

30

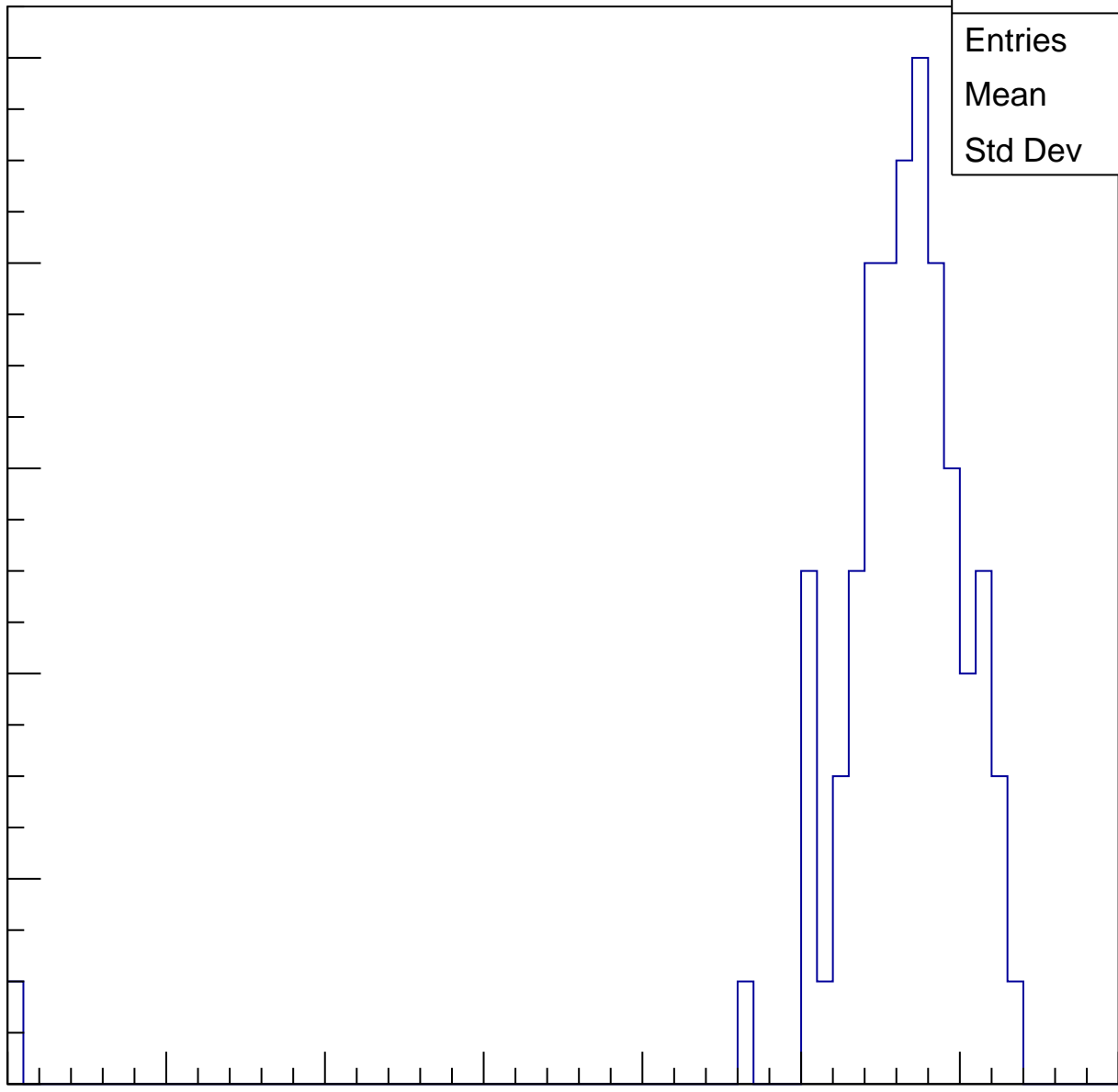
40

50

60

70

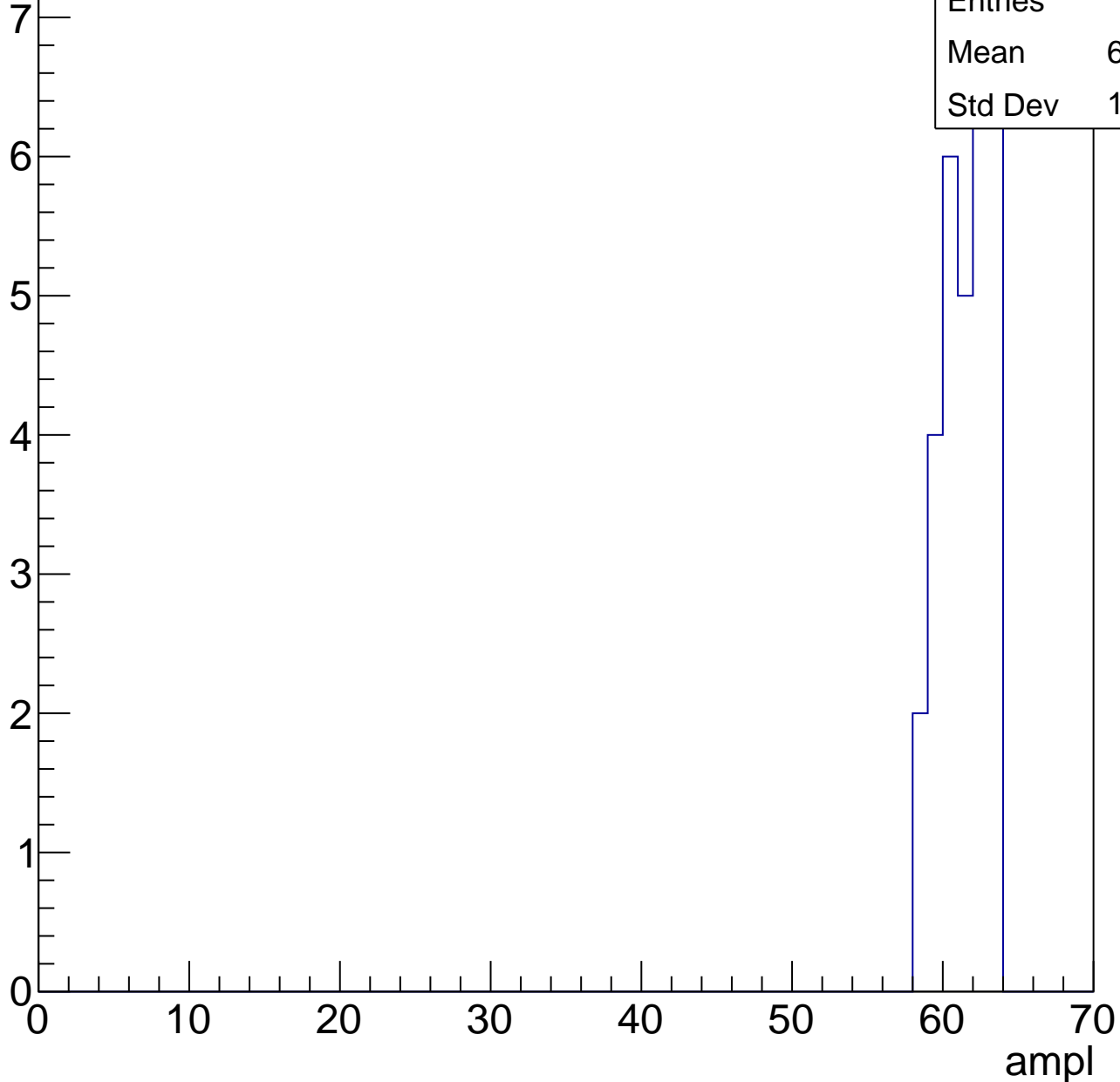
ampl



B1L003S, U13-ch30, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch30, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch30, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch31, adc0

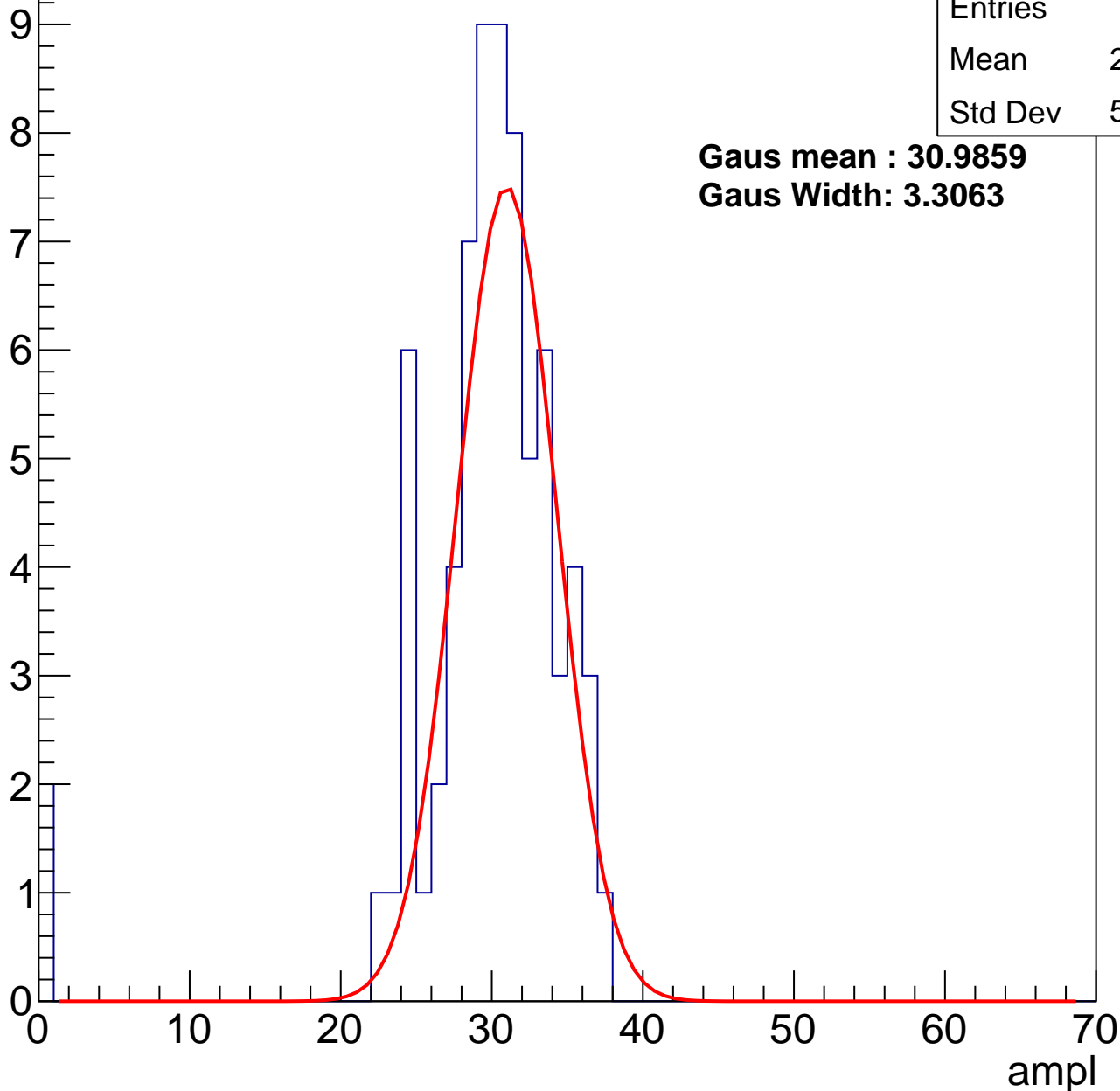
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.08
Std Dev	5.992

Gaus mean : 30.9859

Gaus Width: 3.3063



B1L003S, U13-ch31, adc1

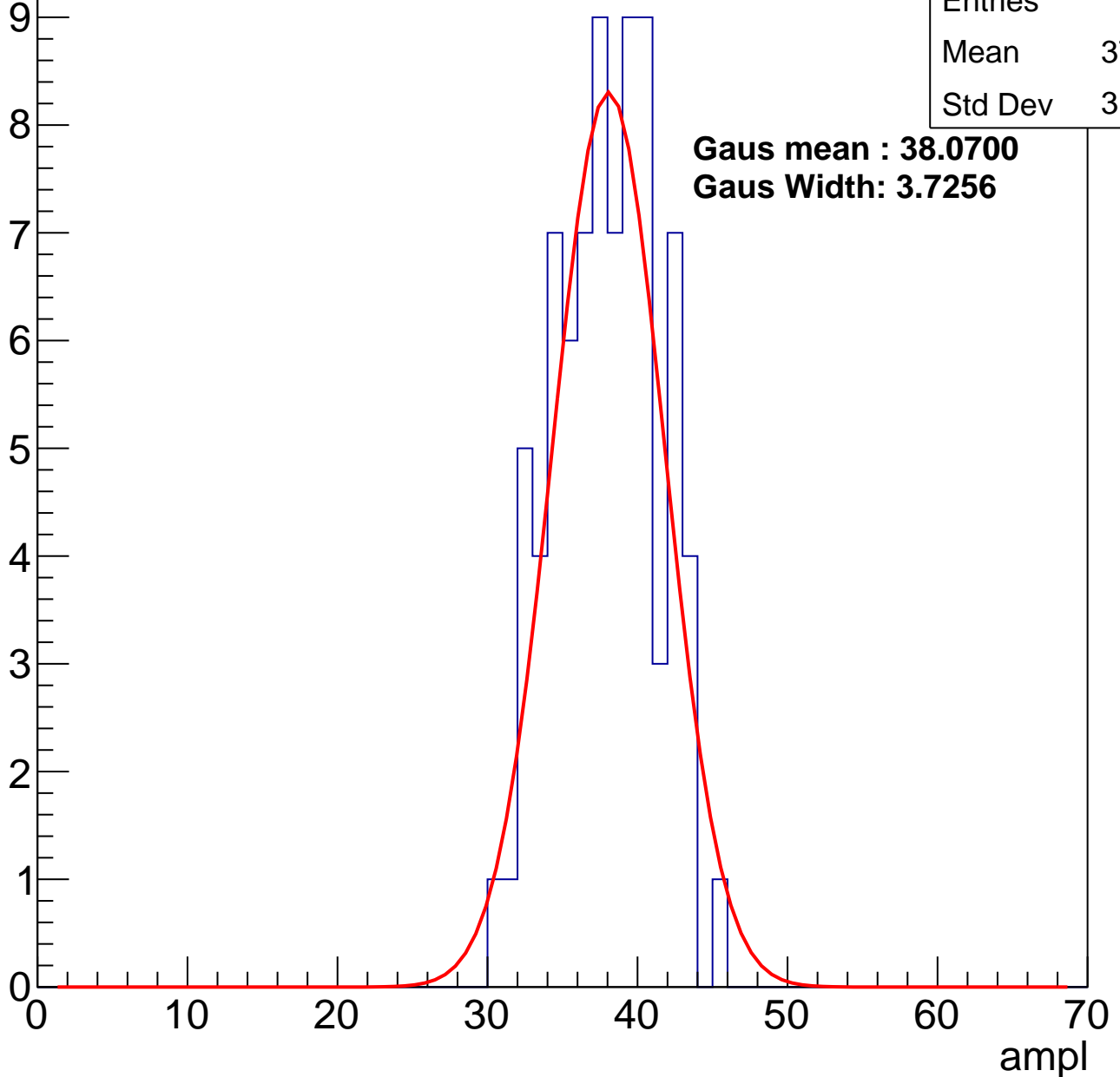
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	37.46
Std Dev	3.365

Gaus mean : 38.0700

Gaus Width: 3.7256



B1L003S, U13-ch31, adc2

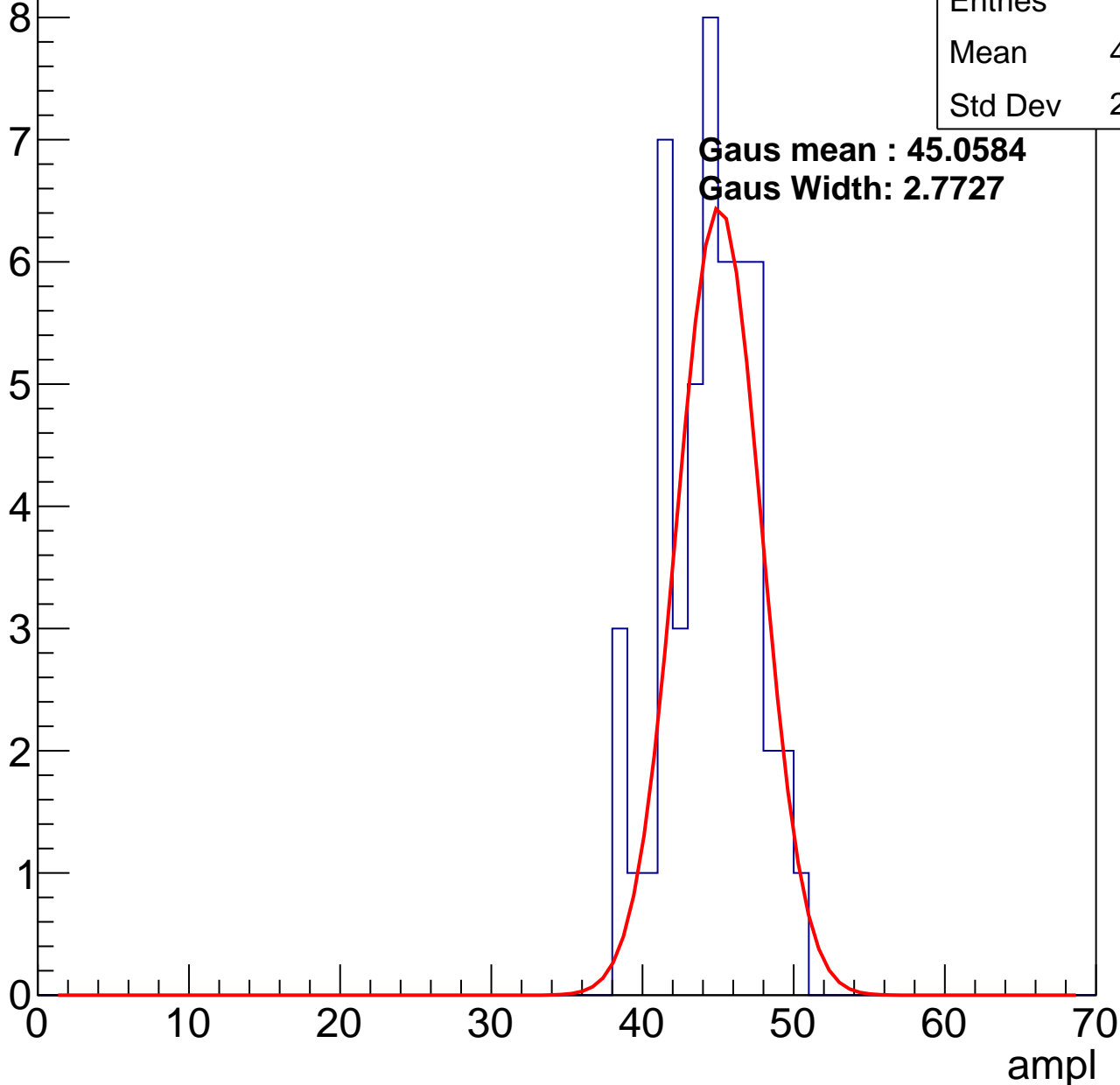
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	44.02
Std Dev	2.907

Gaus mean : 45.0584

Gaus Width: 2.7727

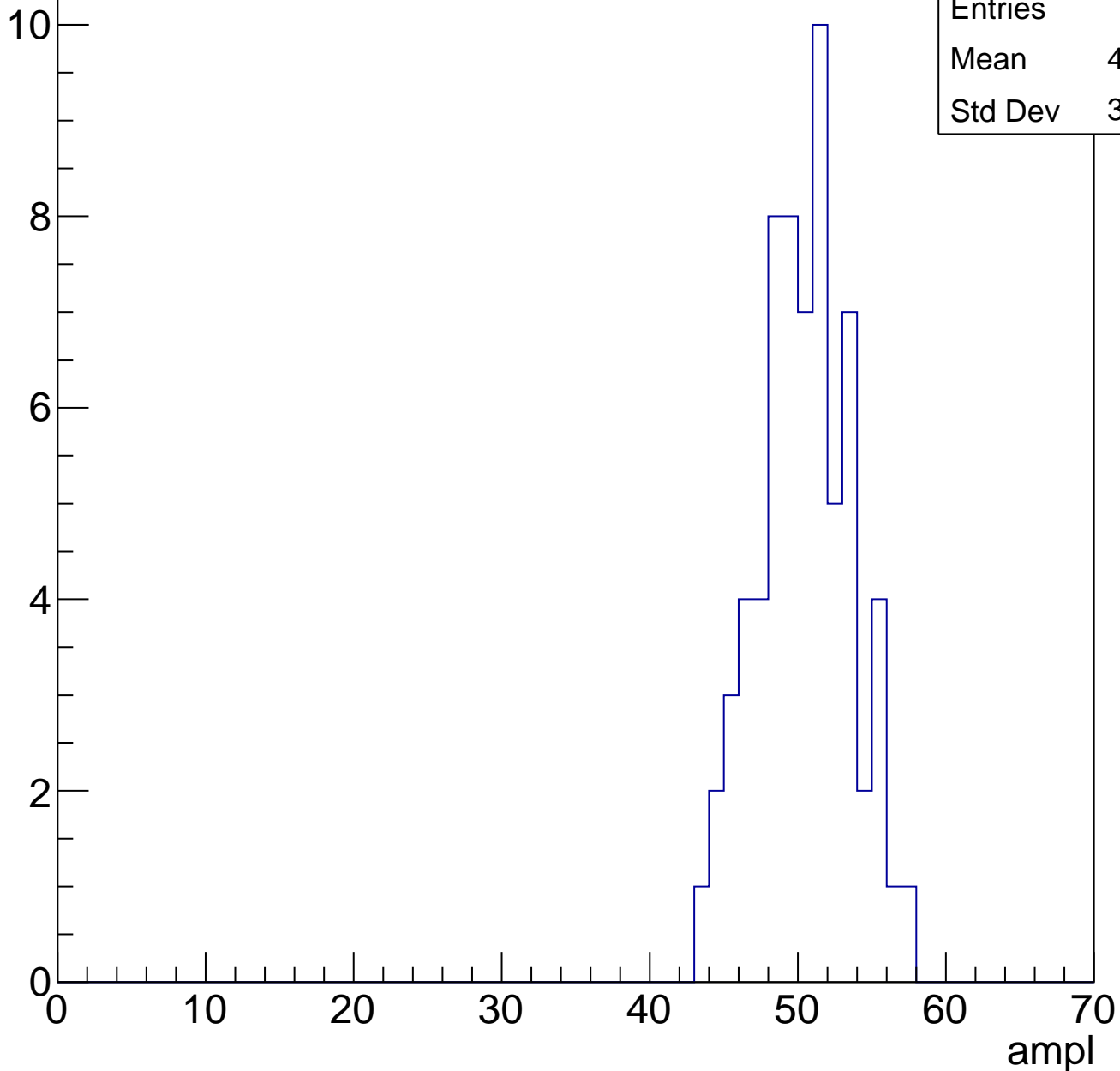


B1L003S, U13-ch31, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	49.94
Std Dev	3.105

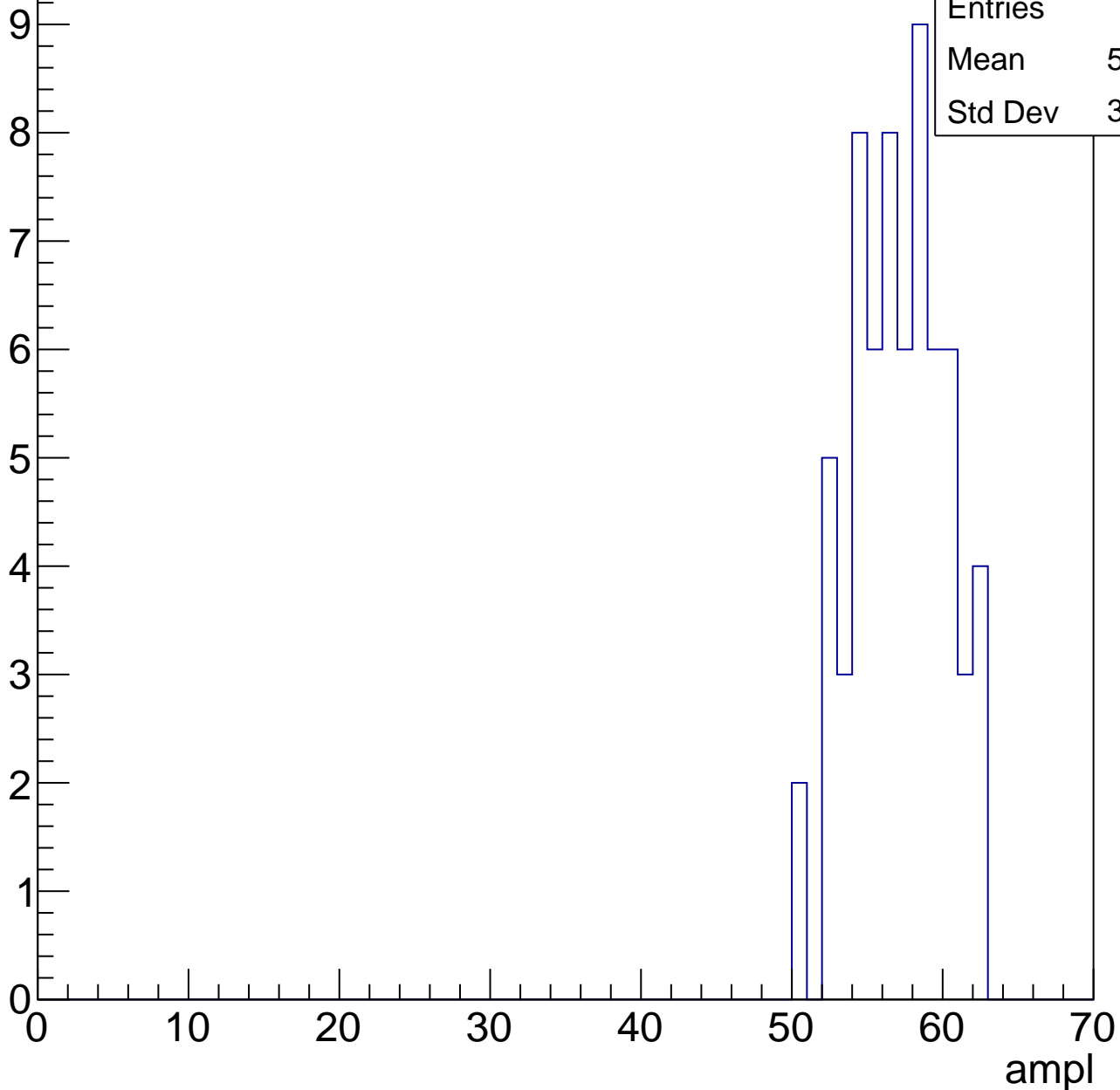


B1L003S, U13-ch31, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	56.64
Std Dev	3.018

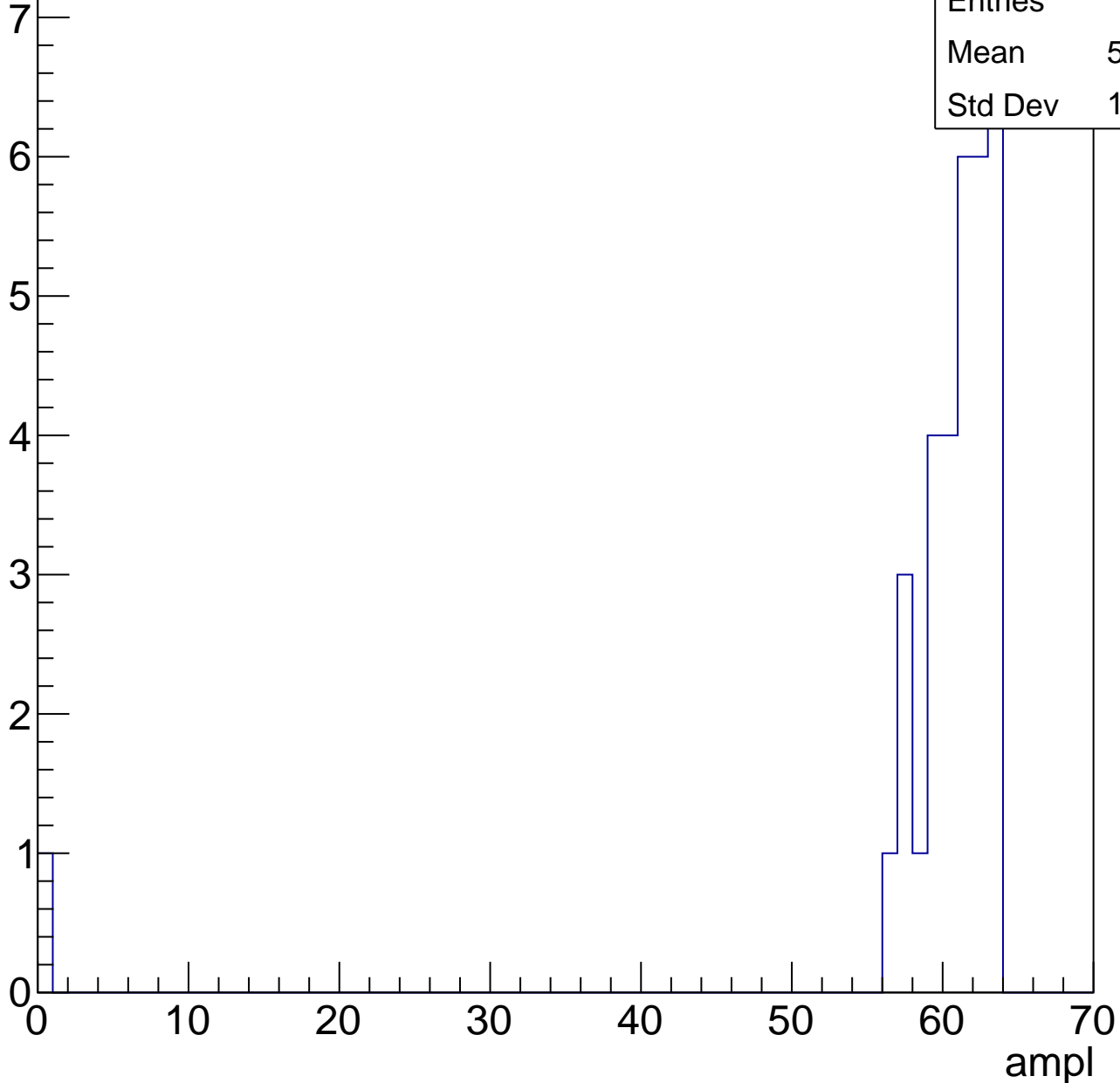


B1L003S, U13-ch31, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.79
Std Dev	10.58



B1L003S, U13-ch31, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch31, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch32, adc0

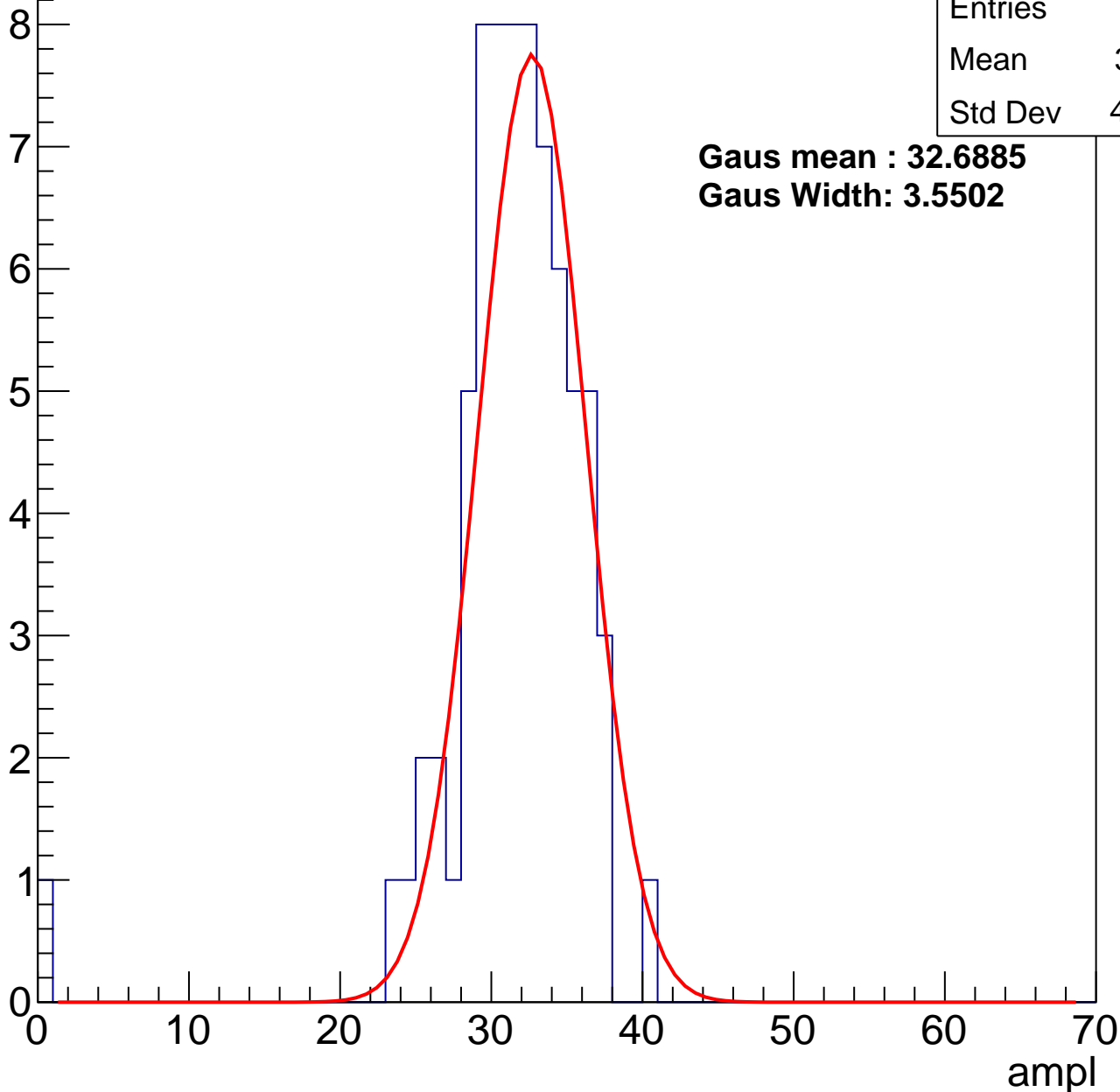
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	31.01
Std Dev	4.974

Gaus mean : 32.6885

Gaus Width: 3.5502



B1L003S, U13-ch32, adc1

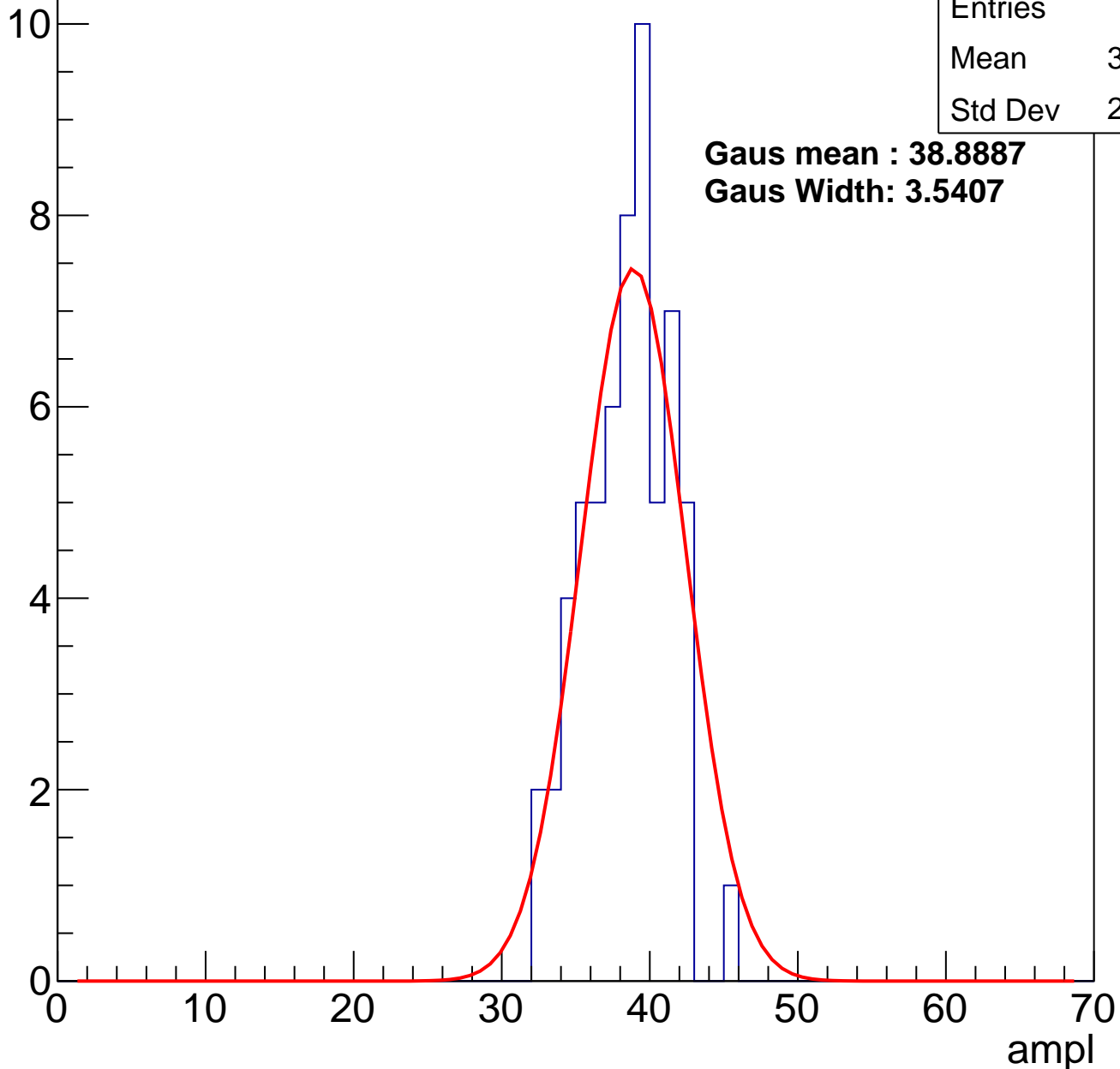
calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	37.98
Std Dev	2.825

Gaus mean : 38.8887

Gaus Width: 3.5407

Entry



B1L003S, U13-ch32, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	44.55
Std Dev	3.019

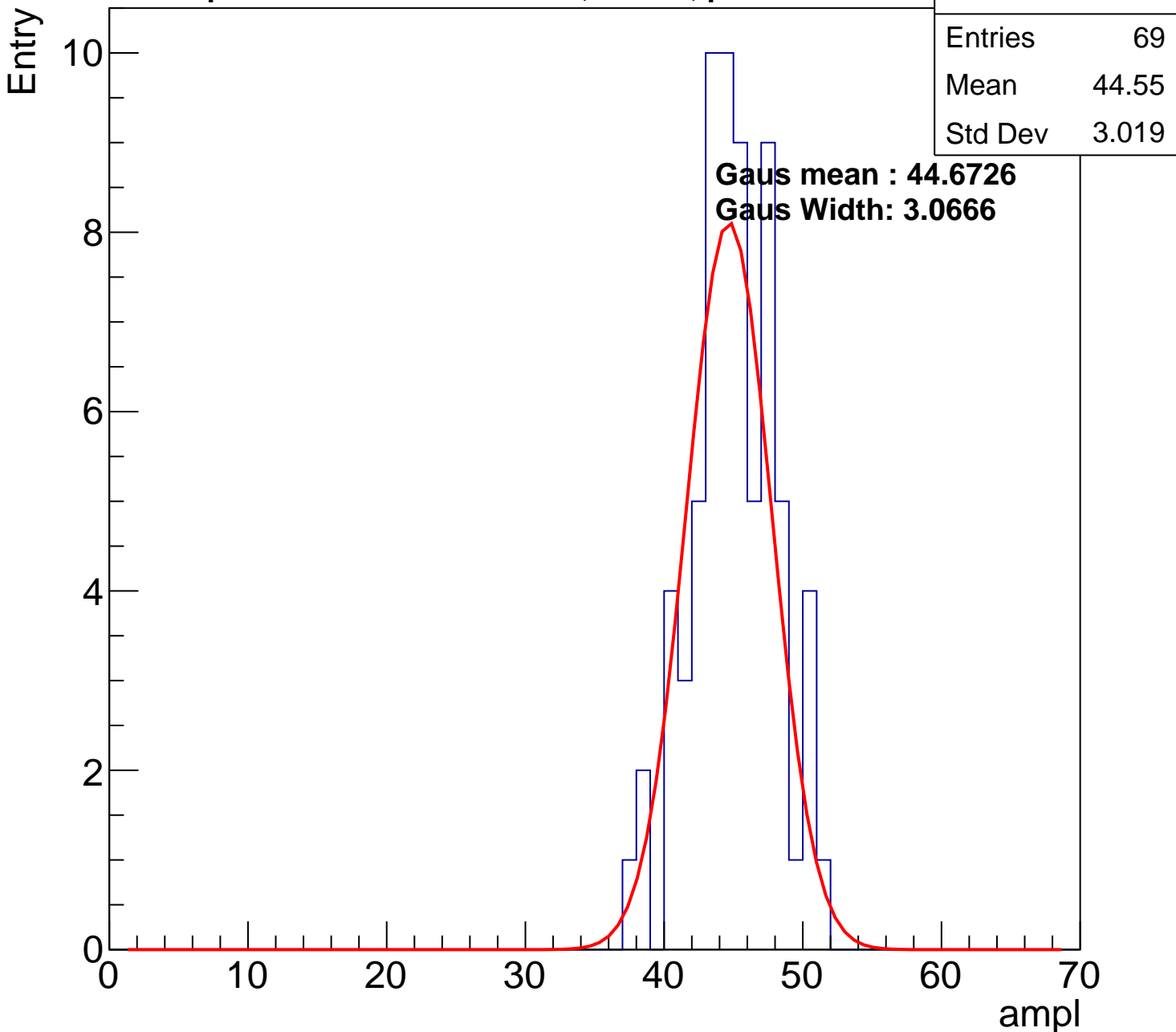
Gaus mean : 44.6726

Gaus Width: 3.0666

10
8
6
4
2
0

ampl

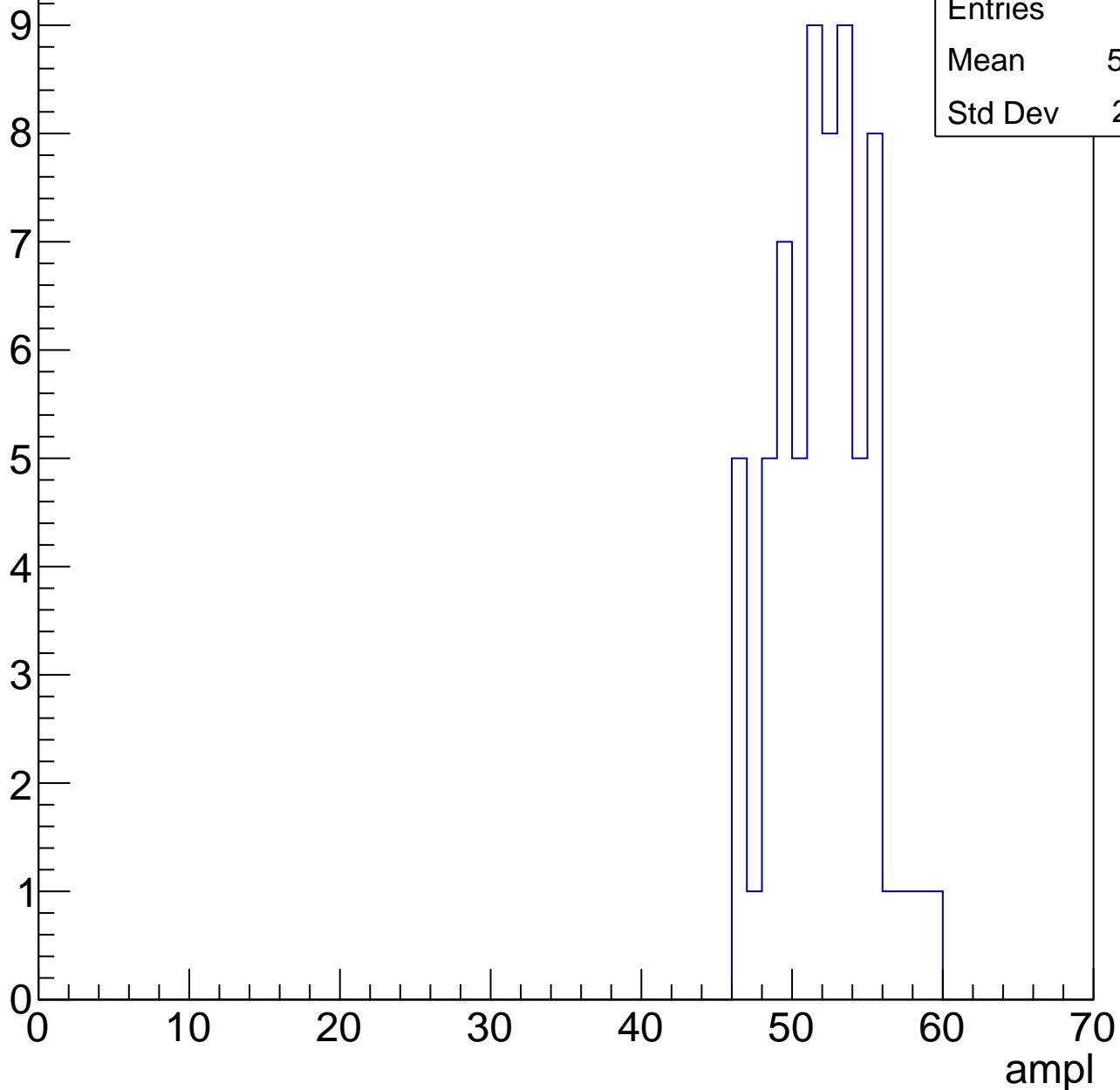
0 10 20 30 40 50 60 70



B1L003S, U13-ch32, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



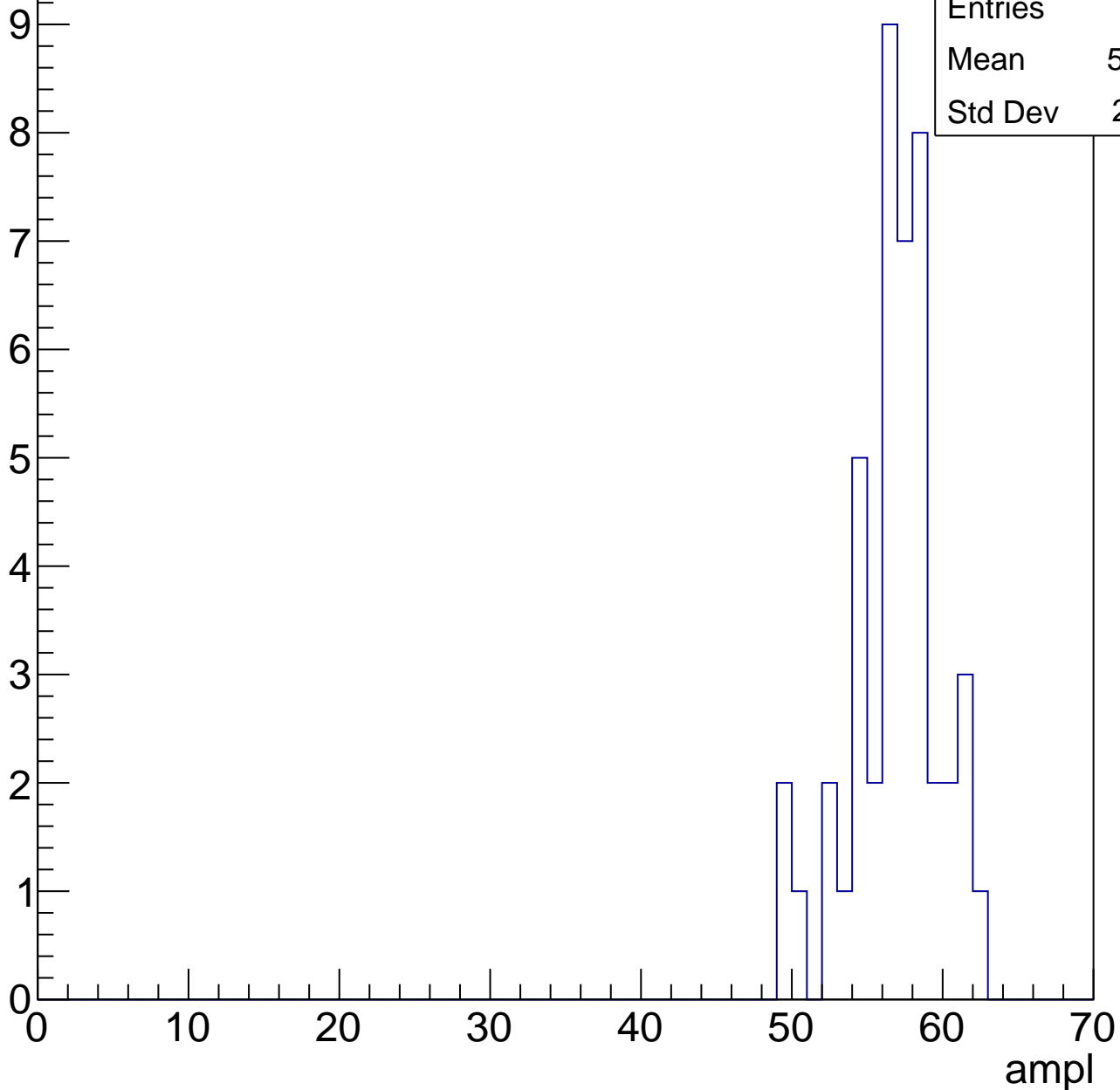
Entries	66
Mean	51.55
Std Dev	2.991

B1L003S, U13-ch32, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

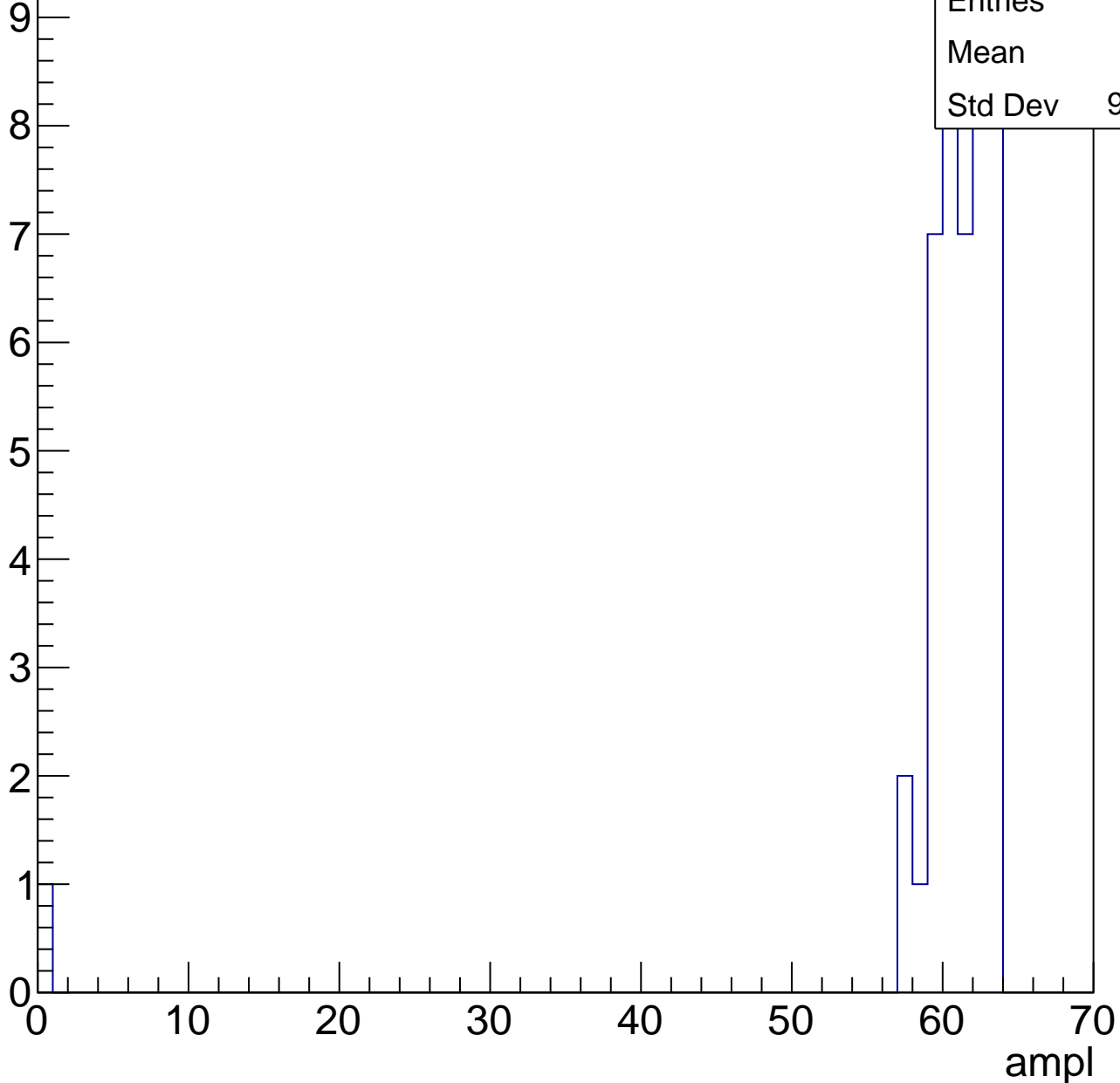
Entries	45
Mean	56.33
Std Dev	2.951



B1L003S, U13-ch32, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch32, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch32, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch33, adc0

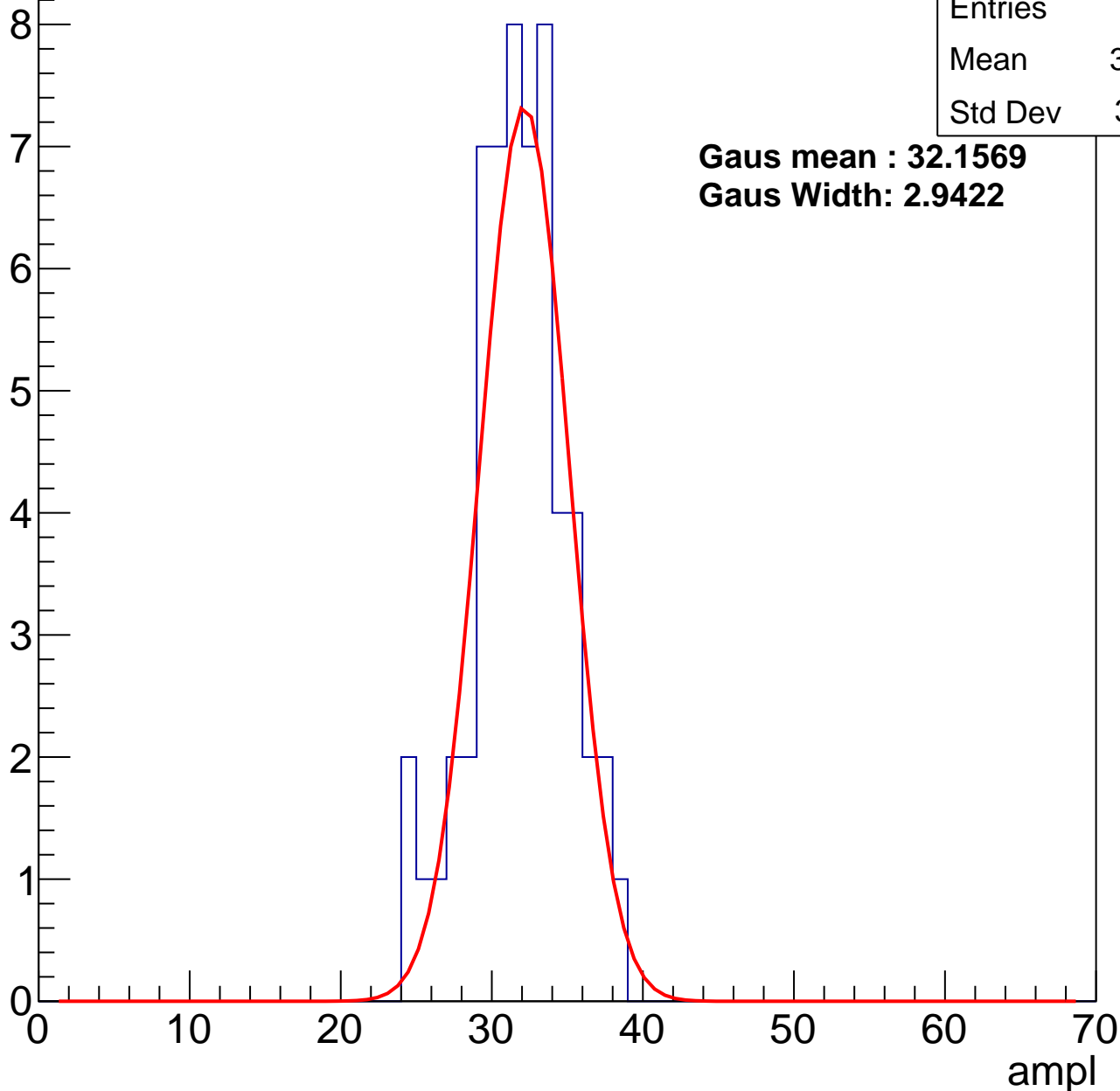
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	31.34
Std Dev	3.071

Gaus mean : 32.1569

Gaus Width: 2.9422



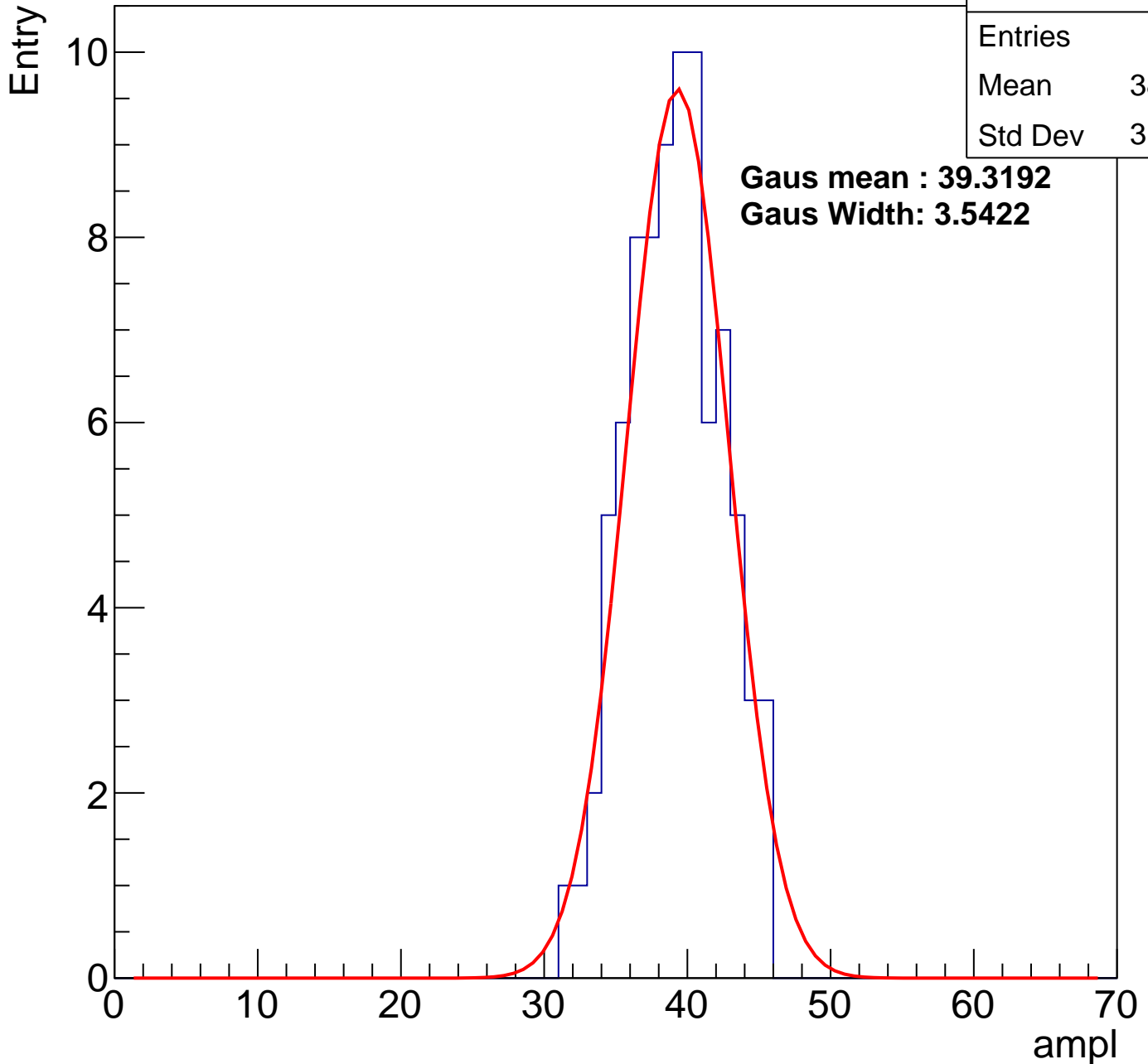
B1L003S, U13-ch33, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	84
Mean	38.65
Std Dev	3.216

Gaus mean : 39.3192

Gaus Width: 3.5422



B1L003S, U13-ch33, adc2

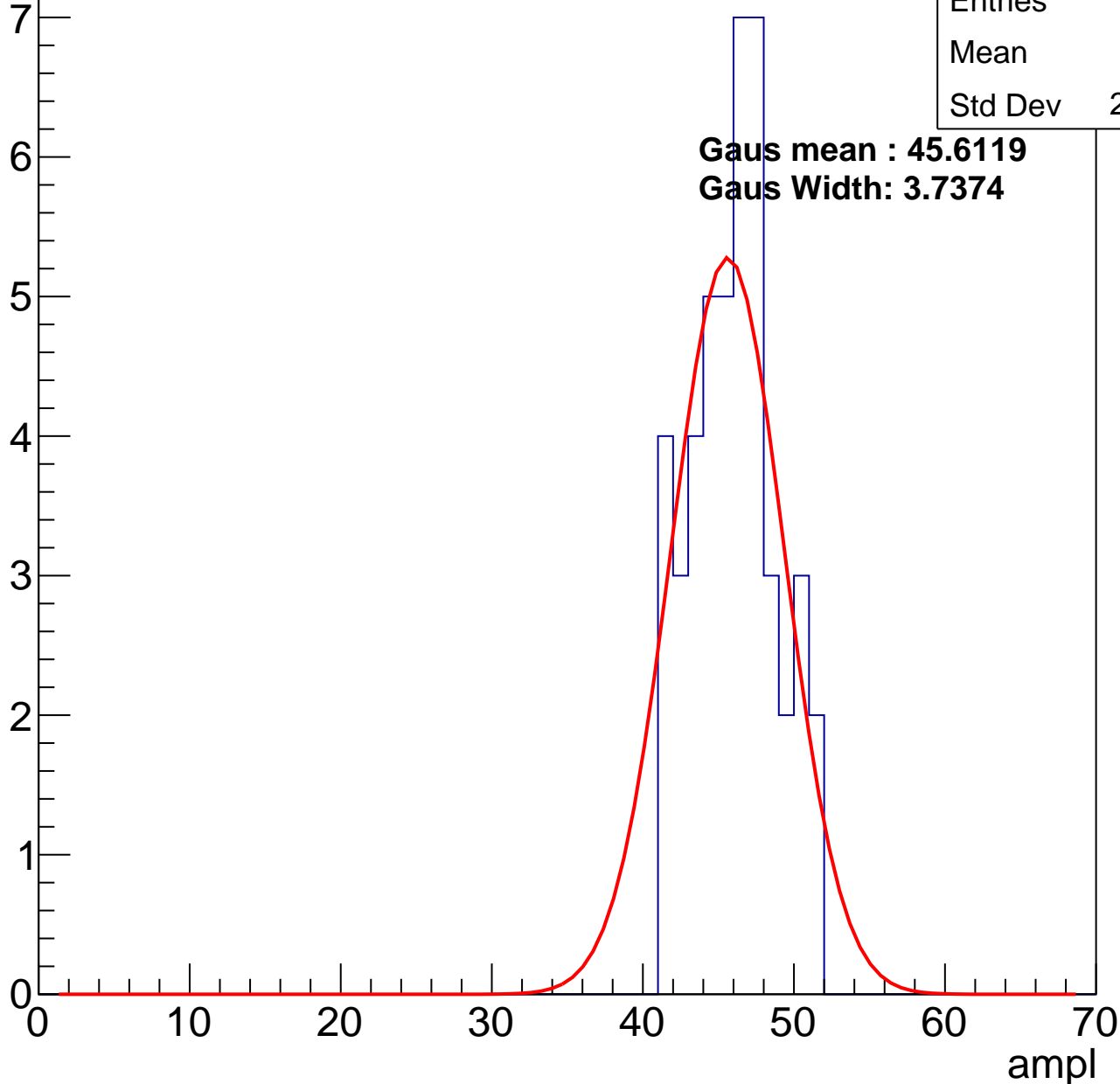
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	45.6
Std Dev	2.736

Gaus mean : 45.6119

Gaus Width: 3.7374

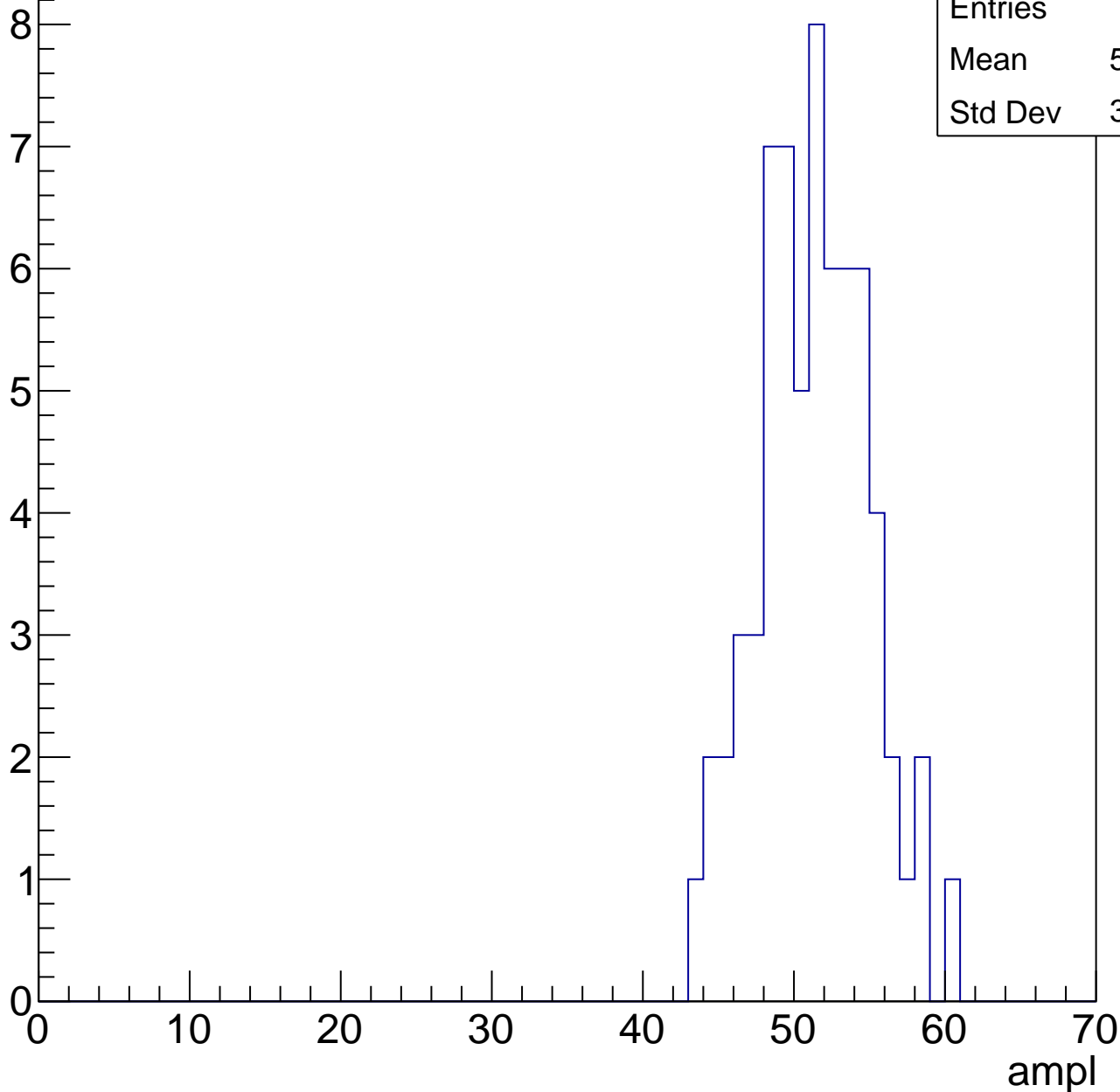


B1L003S, U13-ch33, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

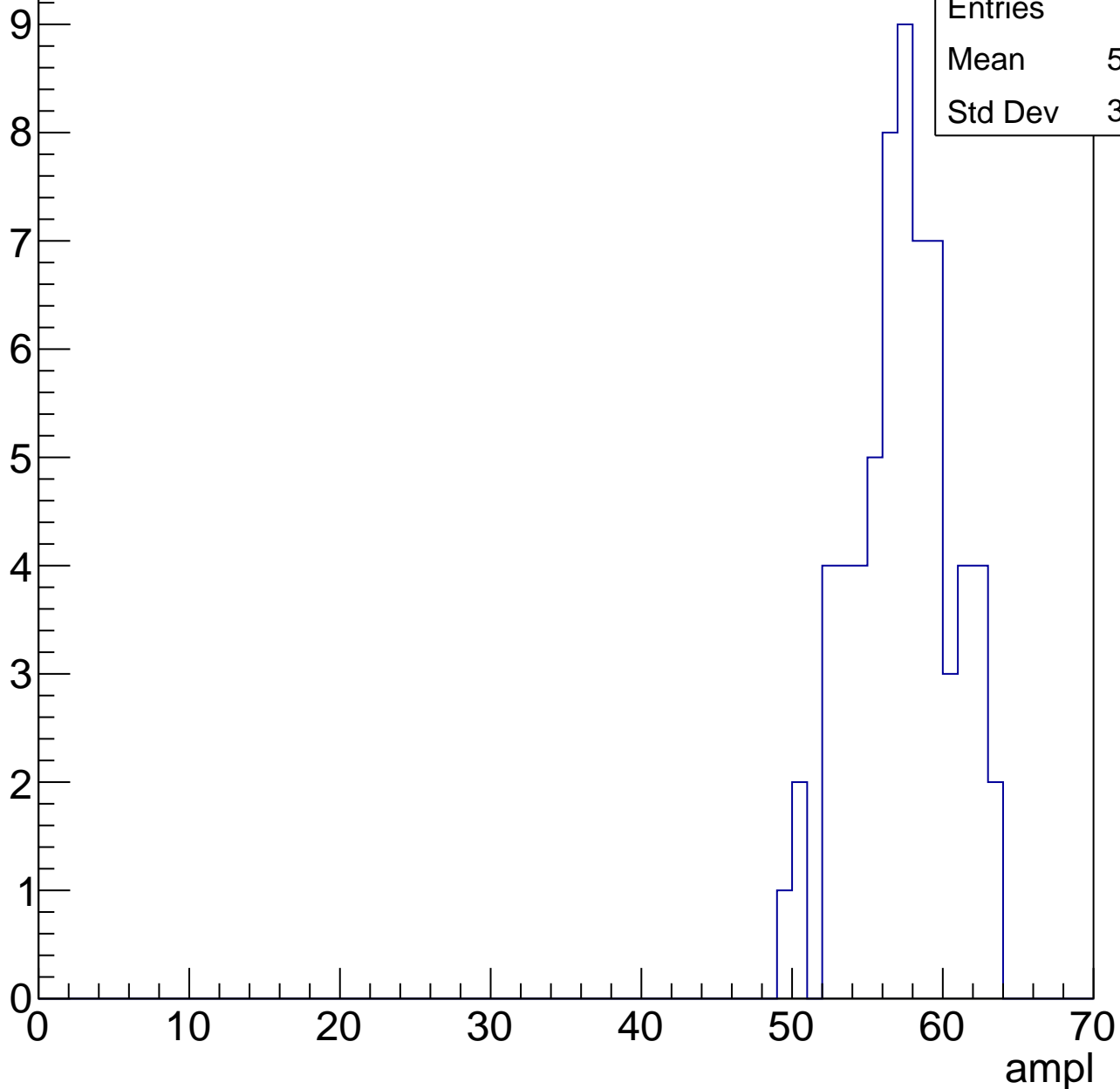
Entries	66
Mean	50.85
Std Dev	3.619



B1L003S, U13-ch33, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

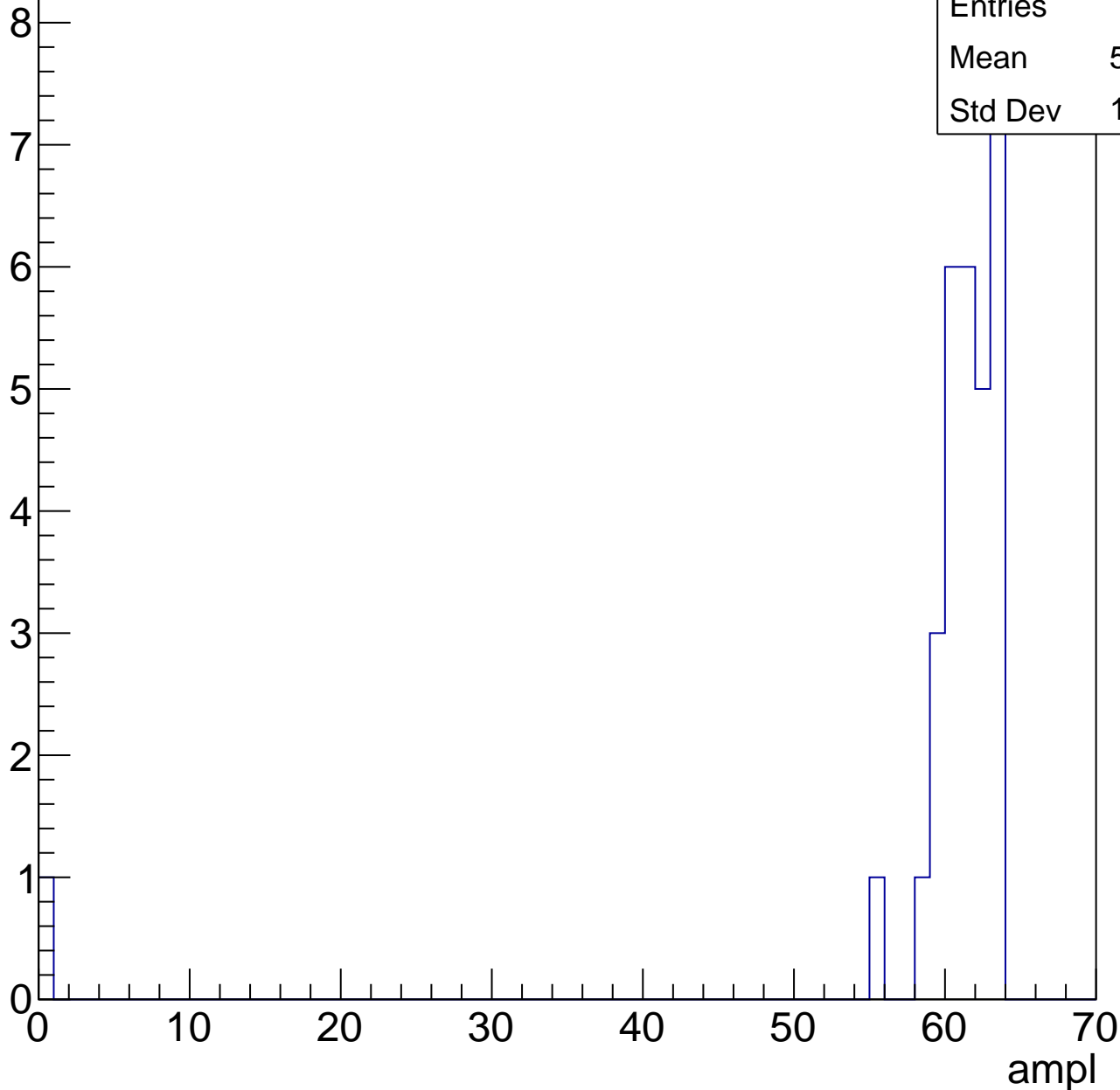


B1L003S, U13-ch33, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	31
Mean	59.03
Std Dev	10.93



B1L003S, U13-ch33, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch33, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch34, adc0

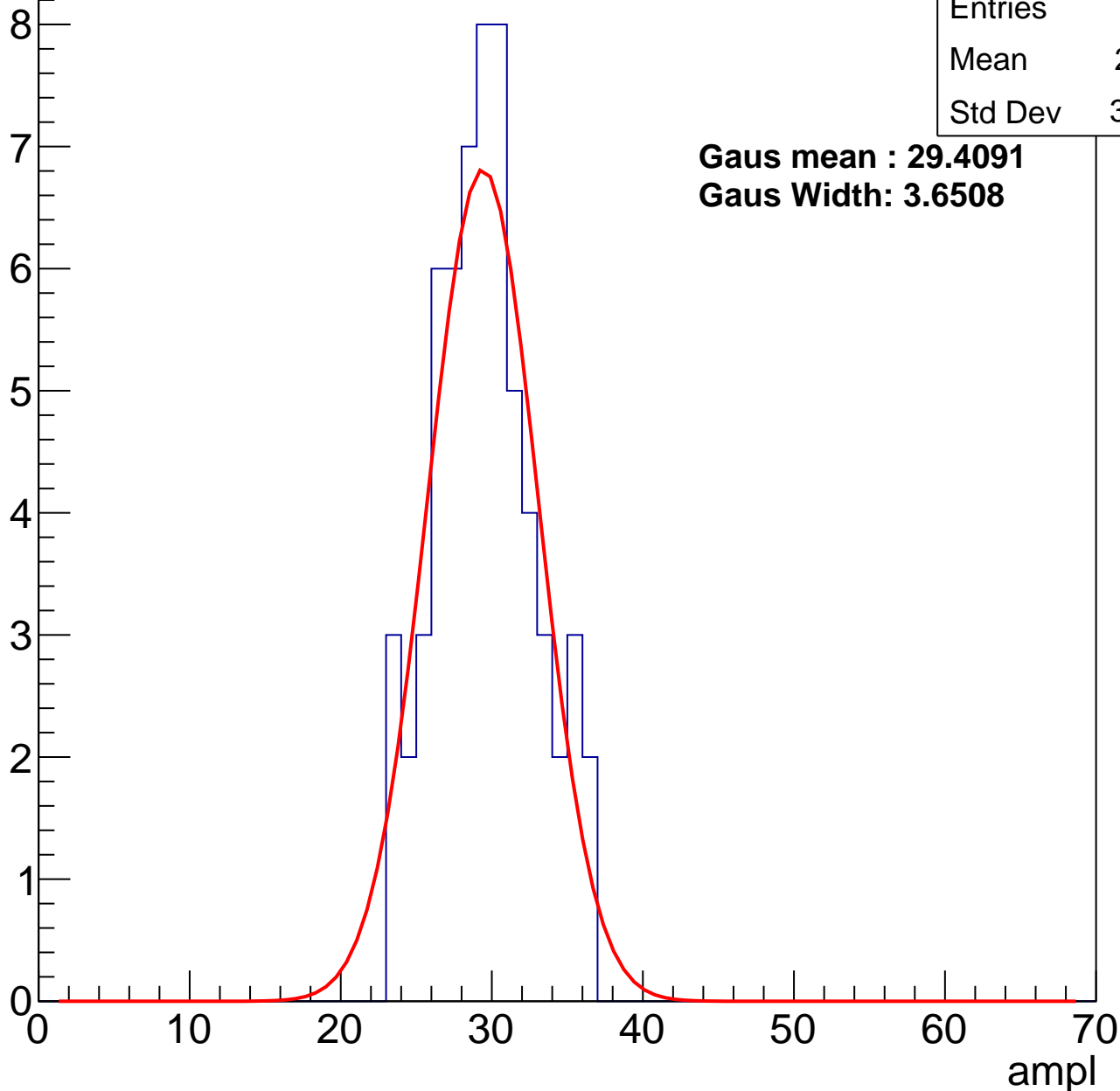
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.11
Std Dev	3.258

Gaus mean : 29.4091

Gaus Width: 3.6508



B1L003S, U13-ch34, adc1

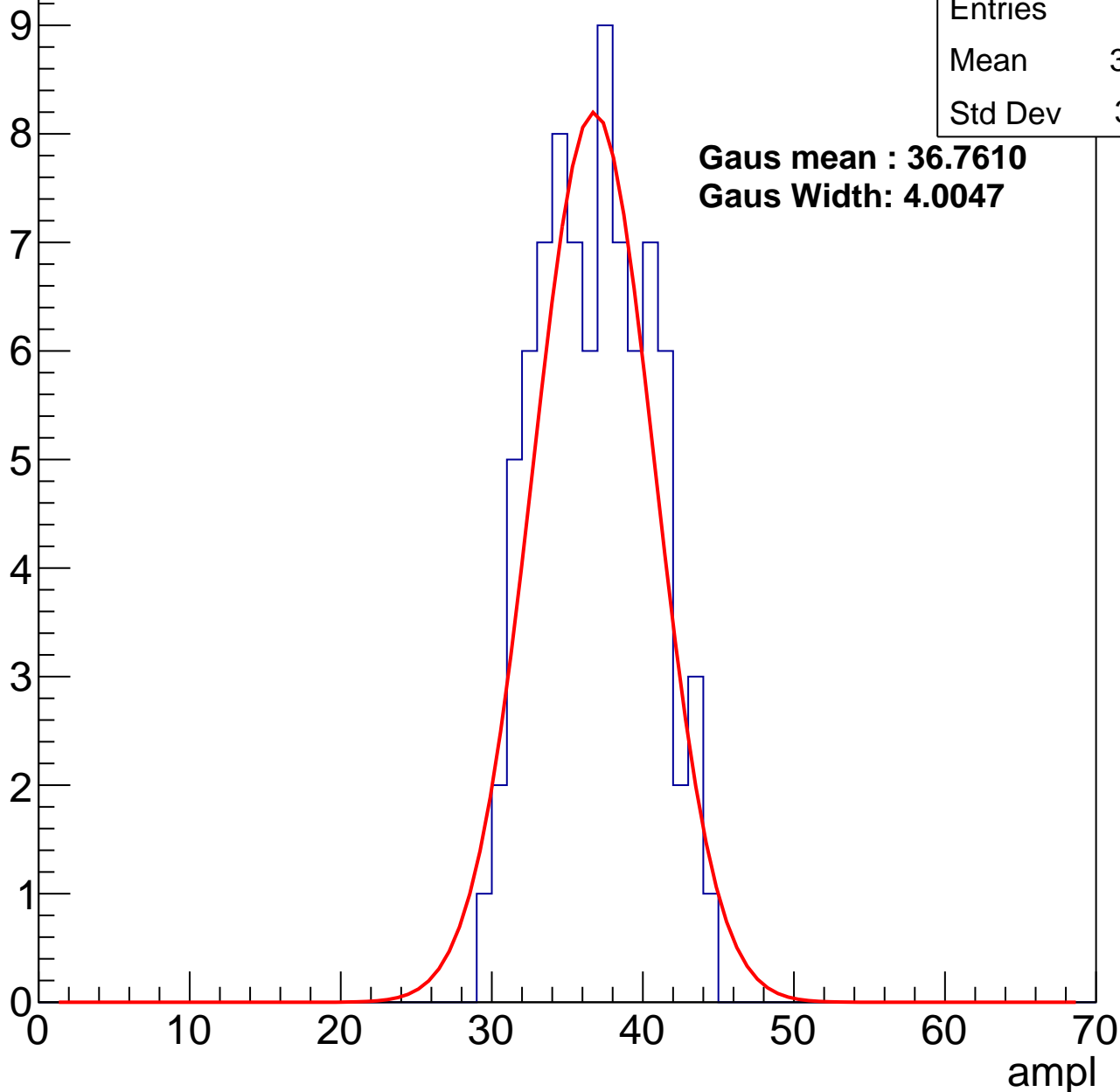
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	36.34
Std Dev	3.591

Gaus mean : 36.7610

Gaus Width: 4.0047



B1L003S, U13-ch34, adc2

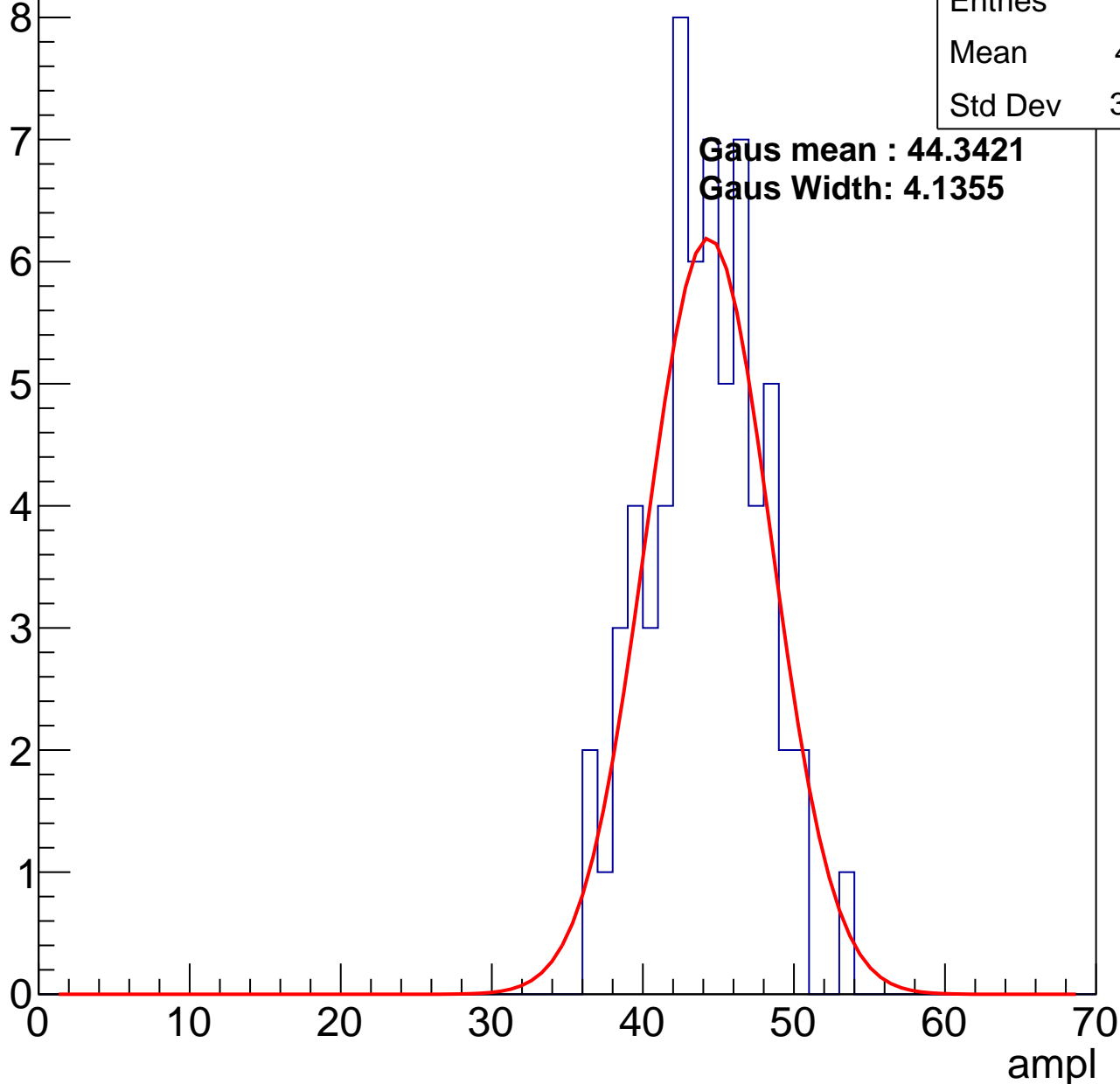
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.61
Std Dev	3.643

Gaus mean : 44.3421

Gaus Width: 4.1355

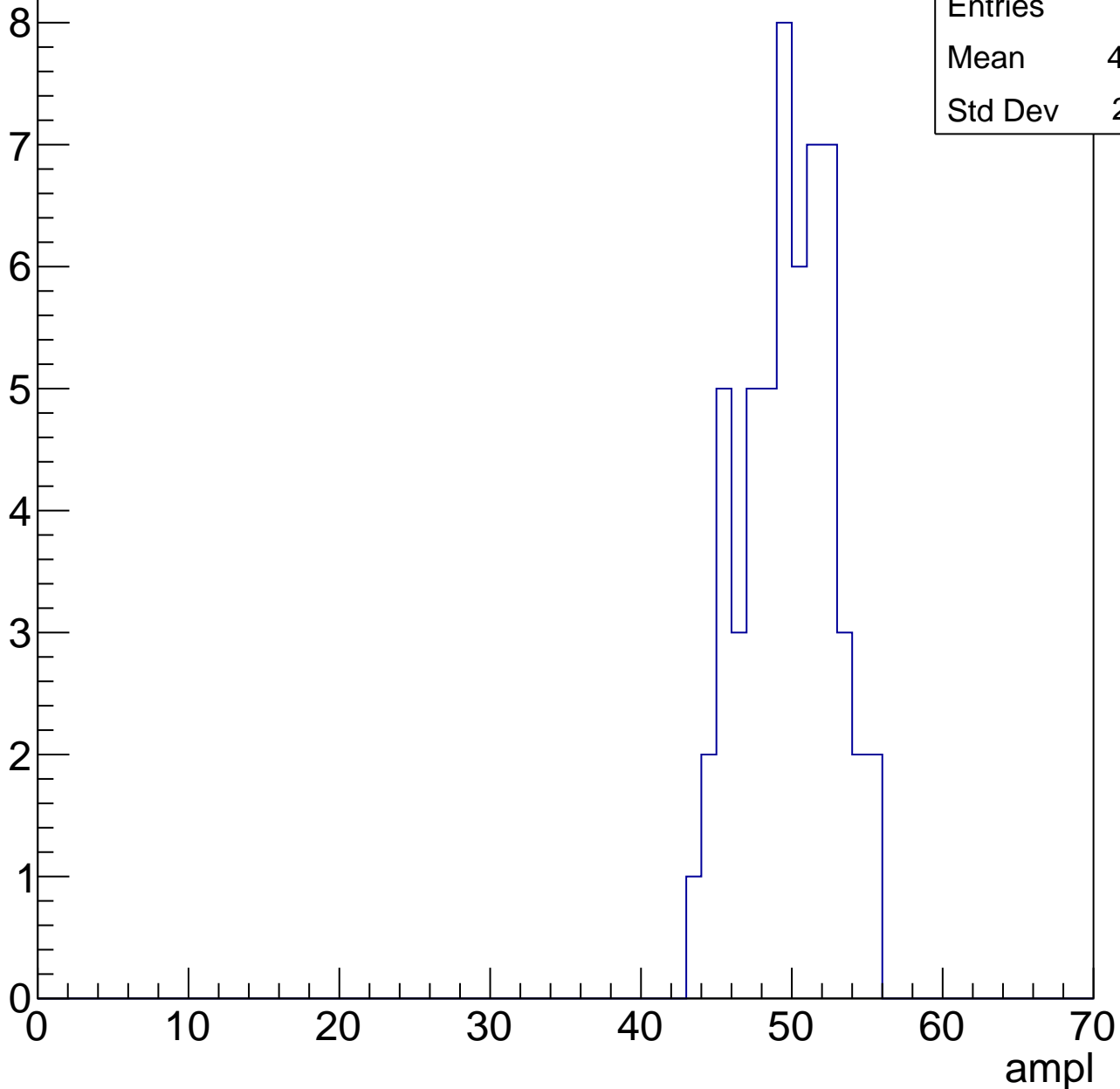


B1L003S, U13-ch34, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

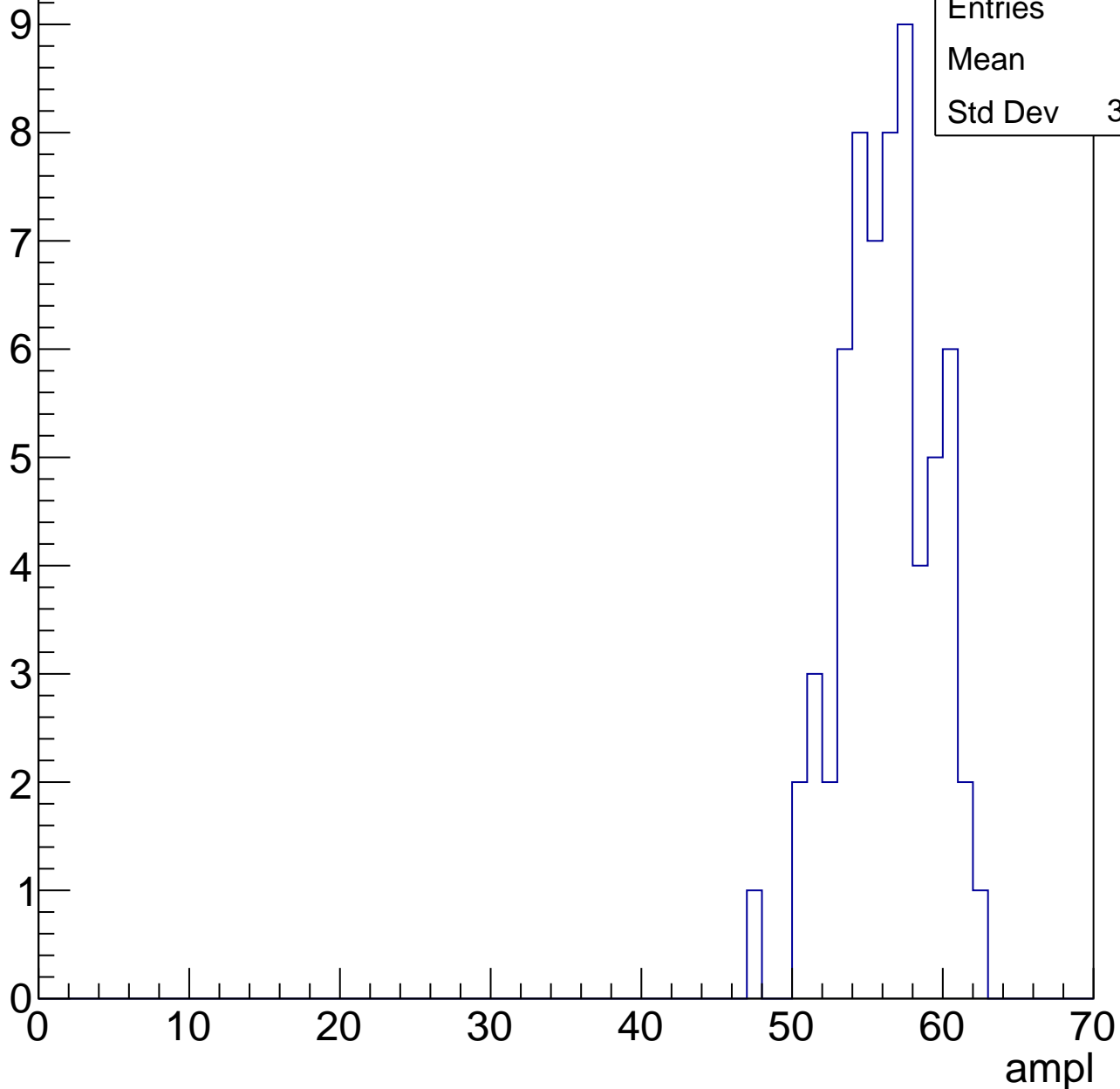
Entries	56
Mean	49.27
Std Dev	2.931



B1L003S, U13-ch34, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



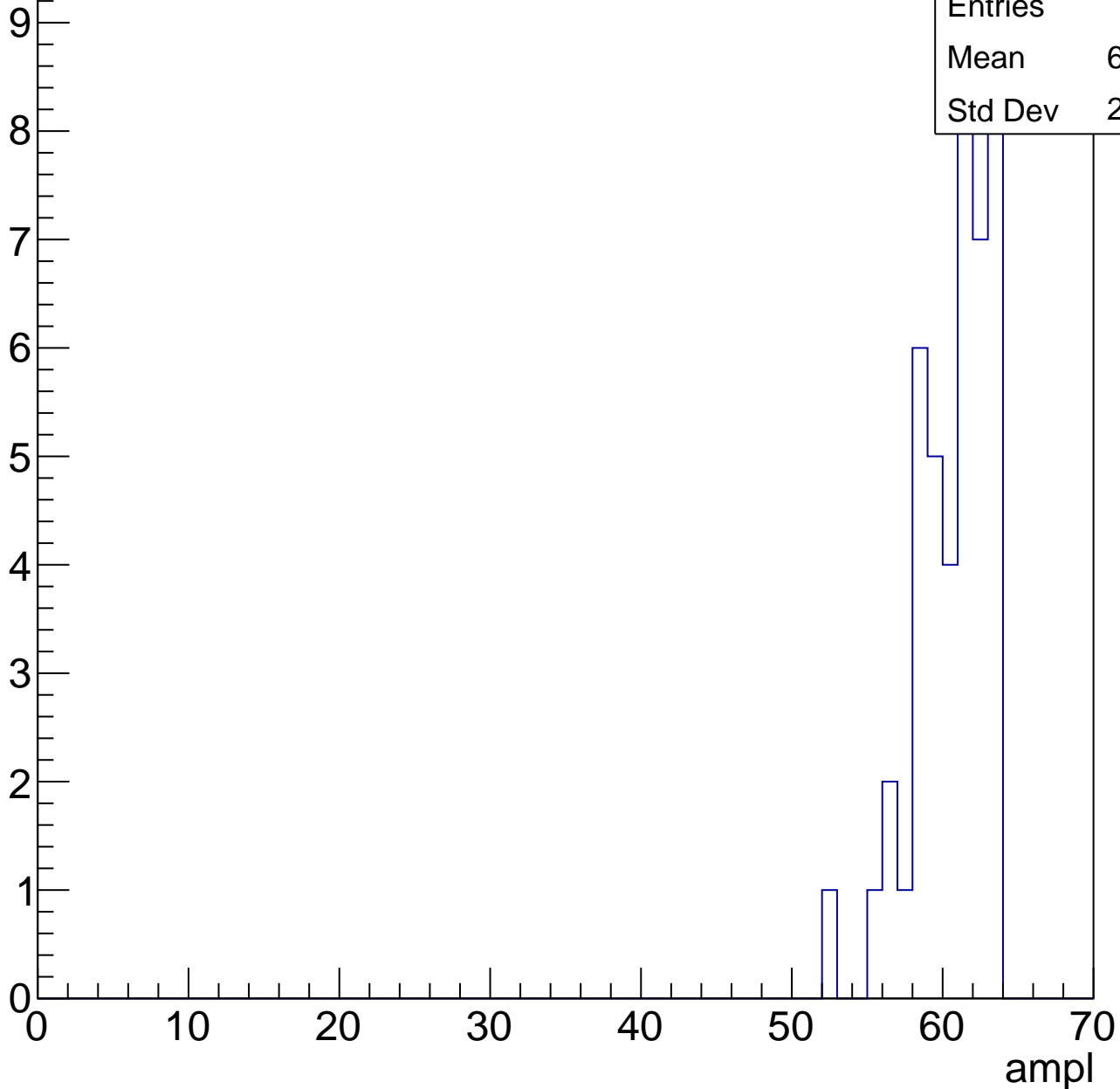
Entries	64
Mean	55.8
Std Dev	3.068

B1L003S, U13-ch34, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	60.18
Std Dev	2.498



B1L003S, U13-ch34, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch34, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L003S, U13-ch35, adc0

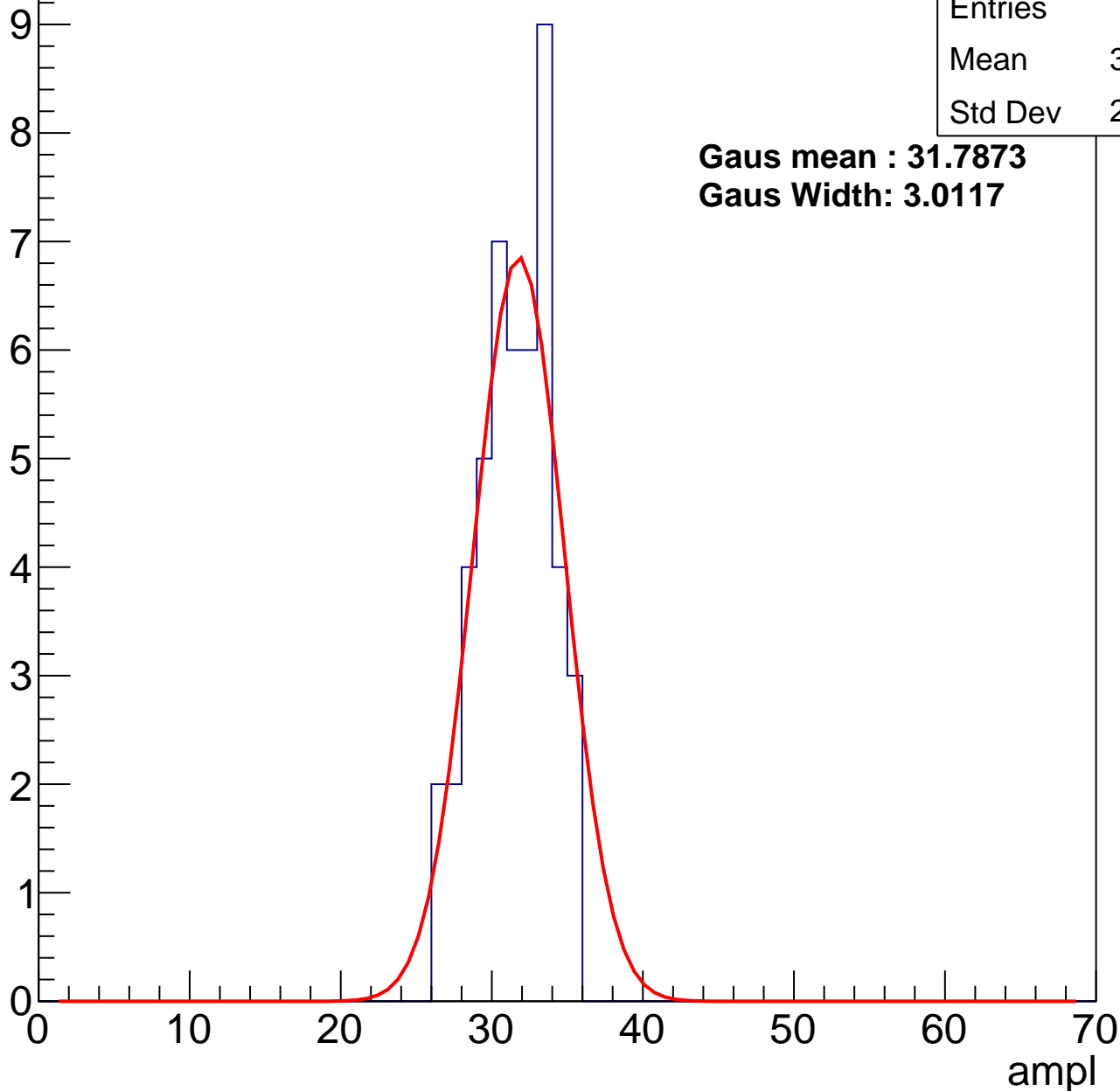
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	31.02
Std Dev	2.376

Gaus mean : 31.7873

Gaus Width: 3.0117



B1L003S, U13-ch35, adc1

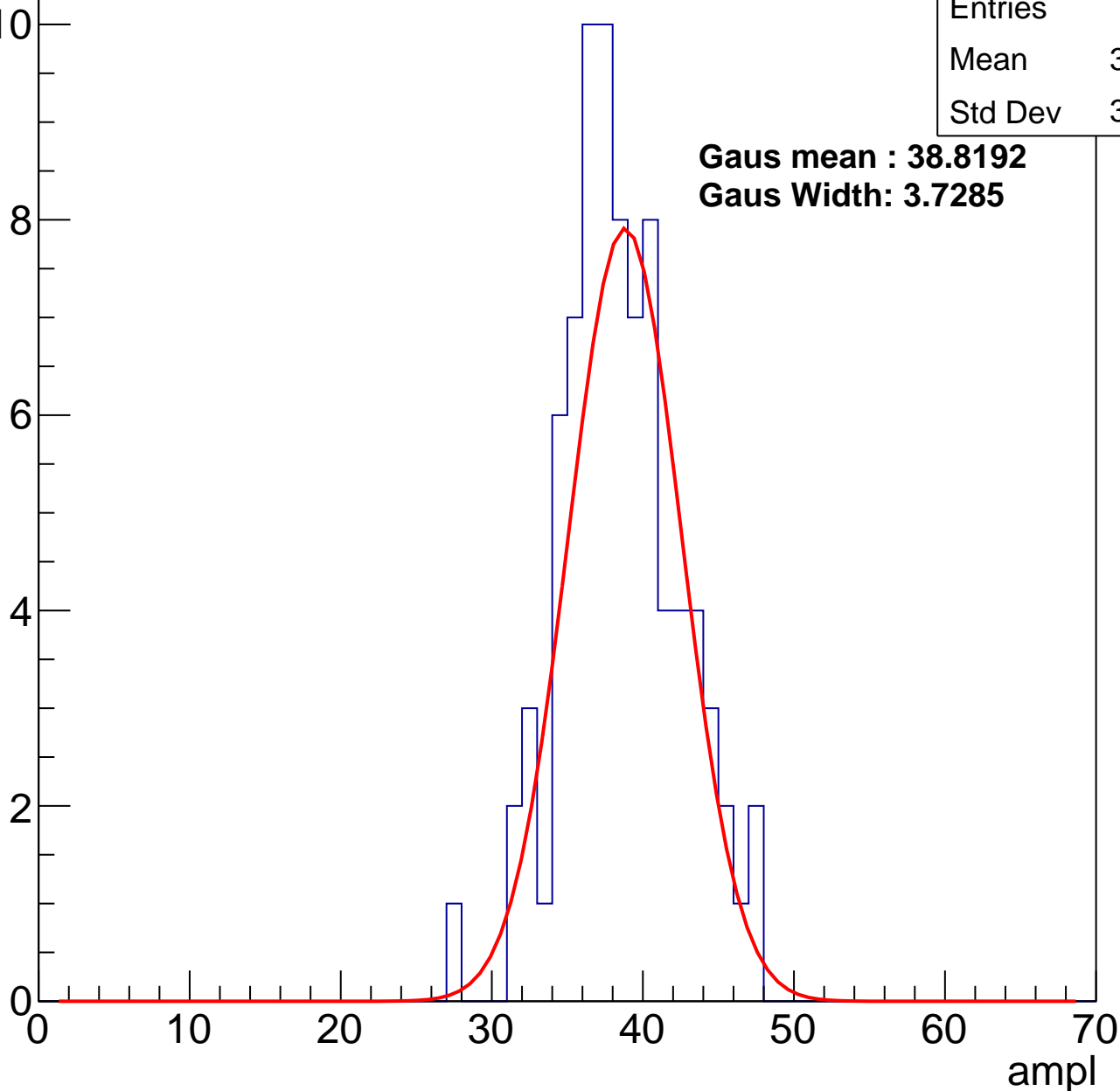
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	38.07
Std Dev	3.852

Gaus mean : 38.8192

Gaus Width: 3.7285



B1L003S, U13-ch35, adc2

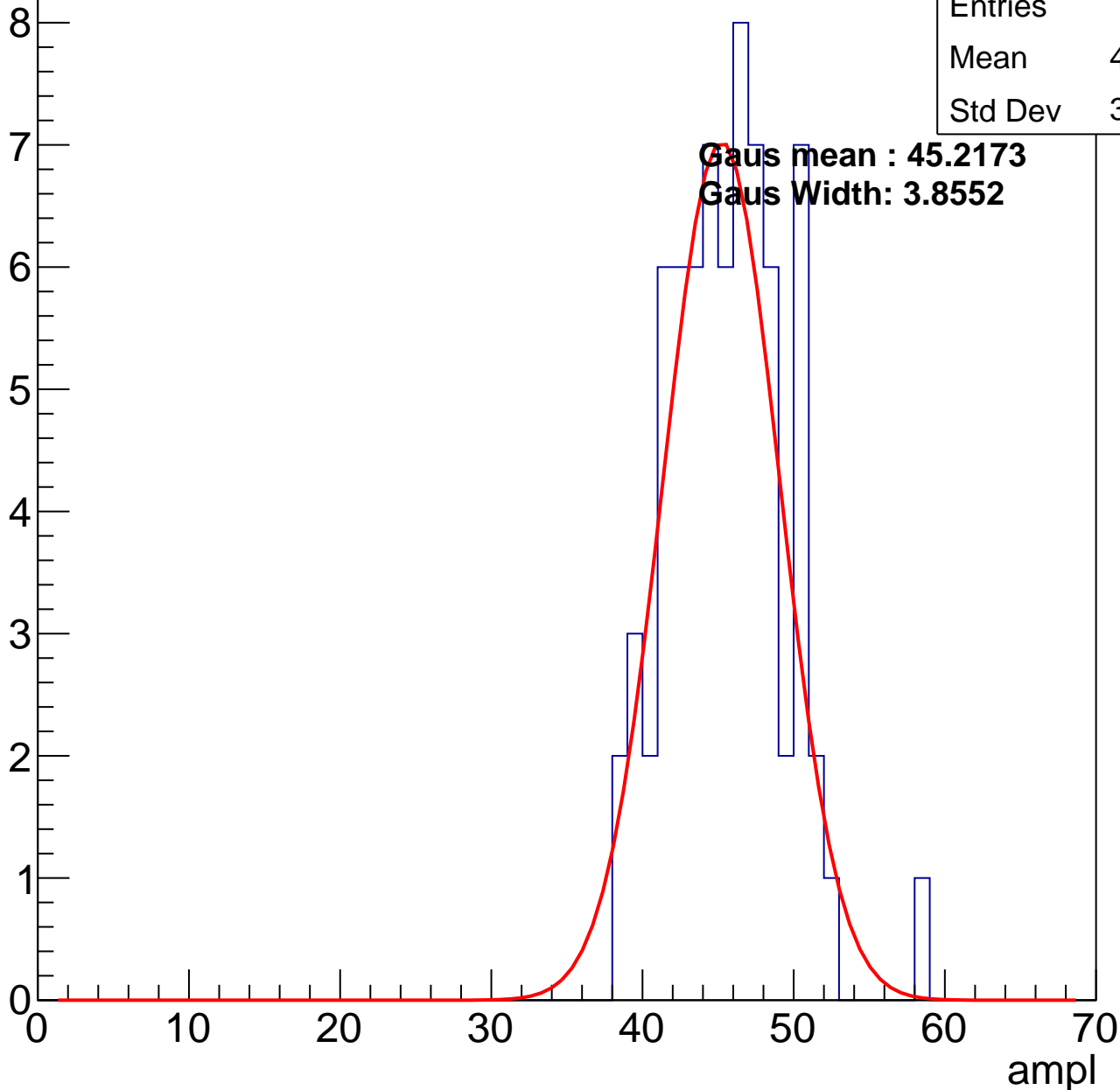
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	45.17
Std Dev	3.768

Gaus mean : 45.2173

Gaus Width: 3.8552

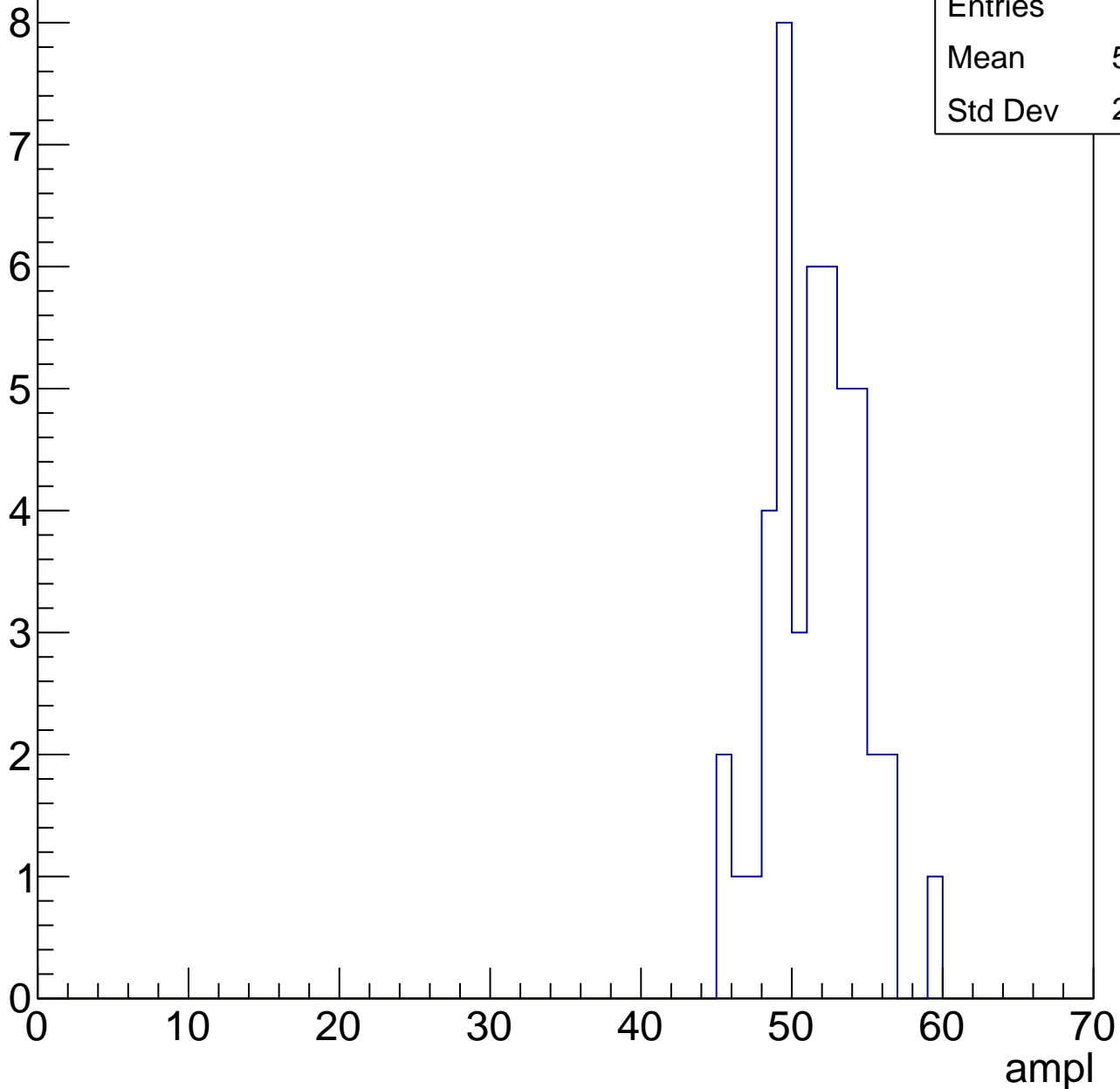


B1L003S, U13-ch35, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	51.11
Std Dev	2.951



B1L003S, U13-ch35, adc4

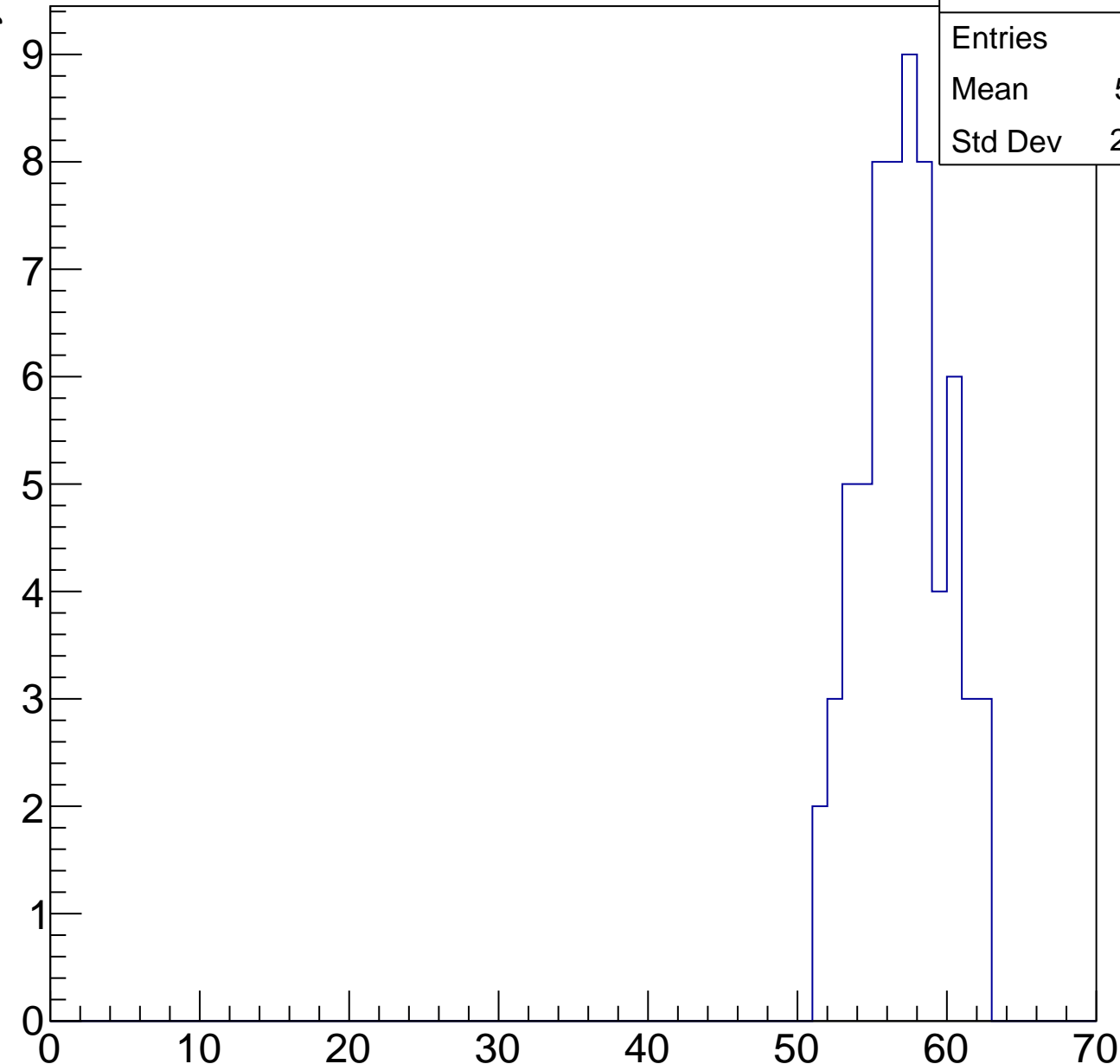
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	56.61
Std Dev	2.804

ampl

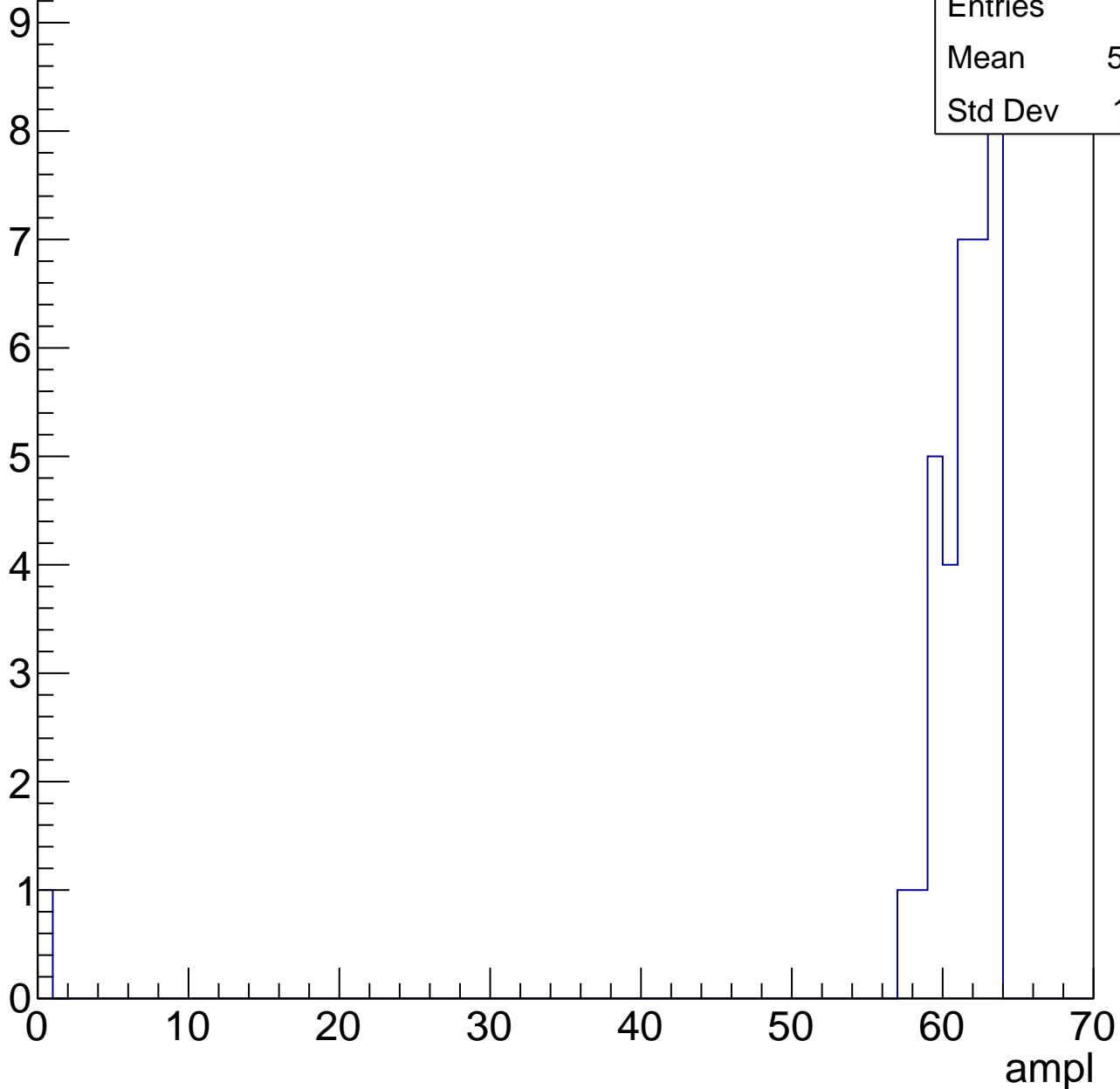


B1L003S, U13-ch35, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	59.37
Std Dev	10.31



B1L003S, U13-ch35, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U13-ch35, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch36, adc0

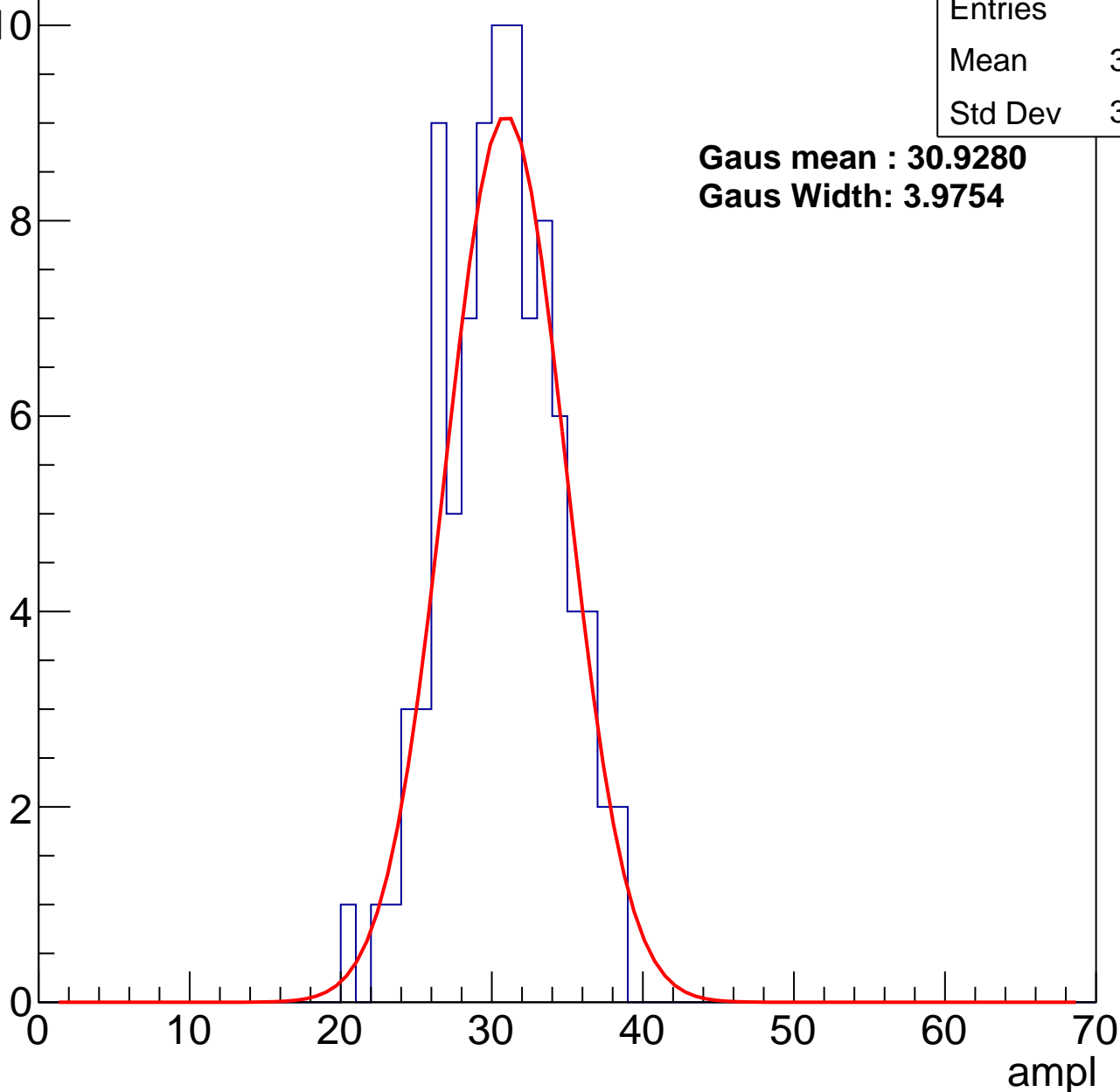
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	92
Mean	30.15
Std Dev	3.753

Gaus mean : 30.9280

Gaus Width: 3.9754



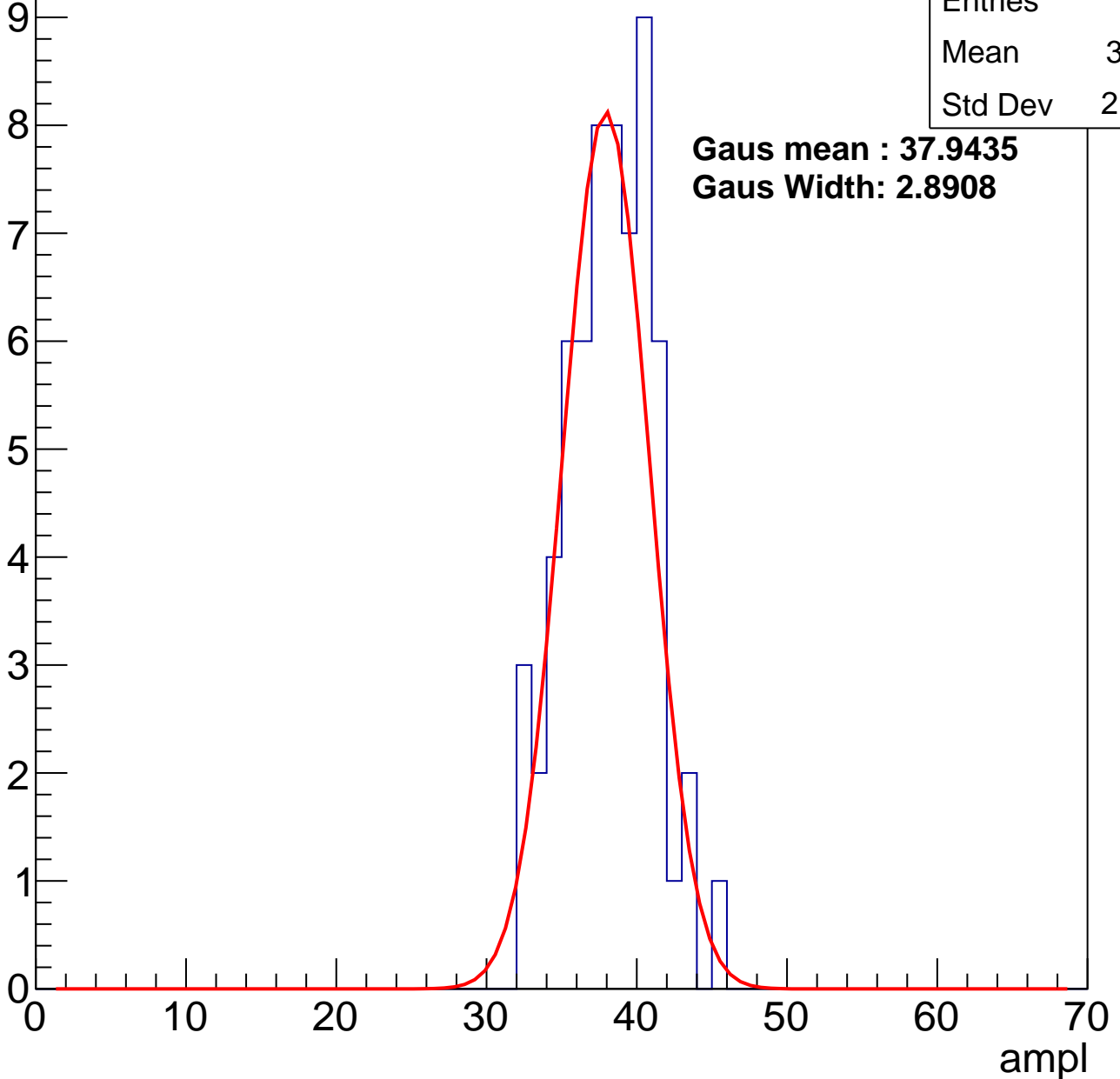
B1L003S, U13-ch36, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	37.71
Std Dev	2.859

Gaus mean : 37.9435
Gaus Width: 2.8908



B1L003S, U13-ch36, adc2

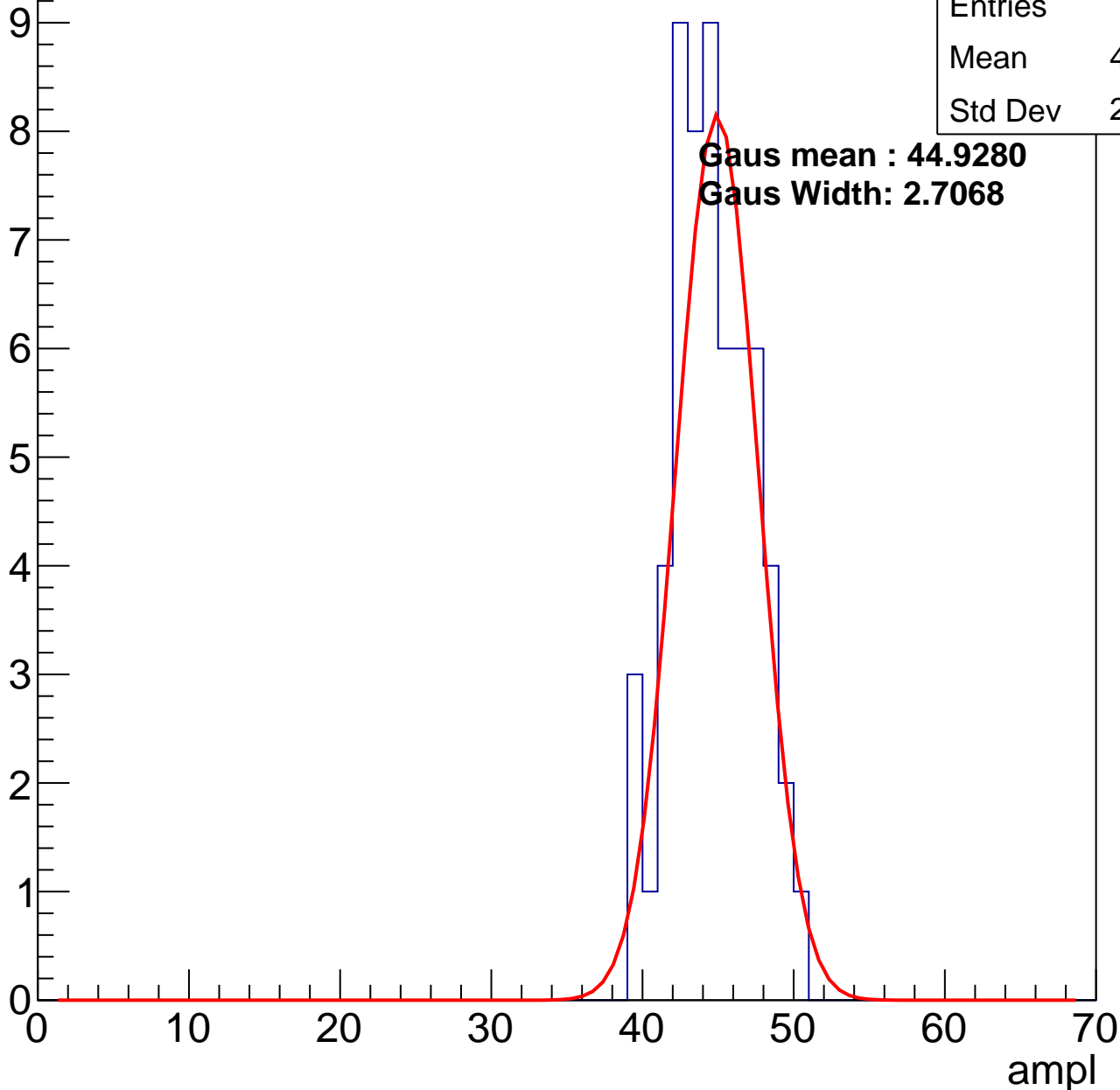
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.19
Std Dev	2.613

Gaus mean : 44.9280

Gaus Width: 2.7068

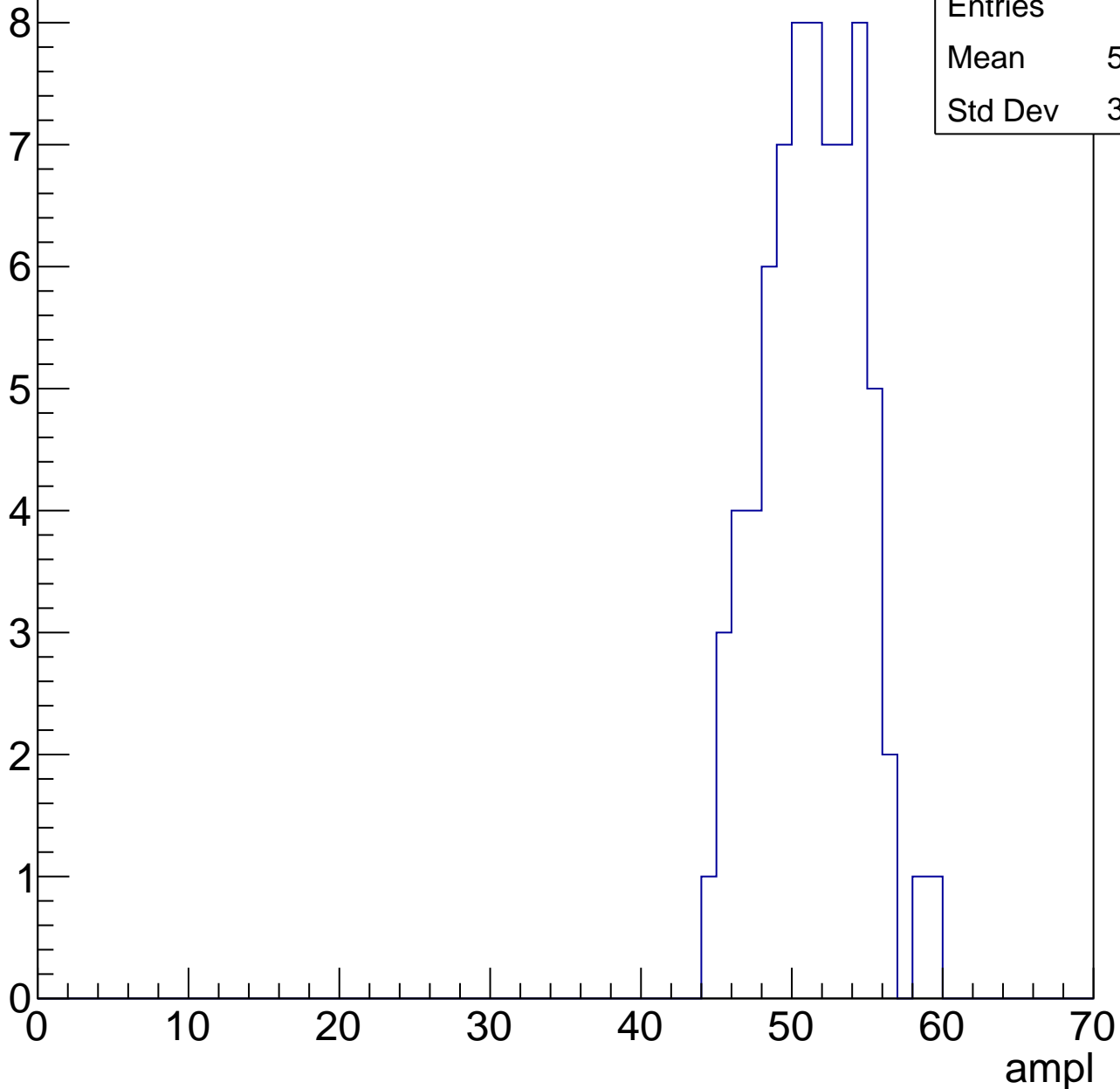


B1L003S, U13-ch36, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	50.85
Std Dev	3.247



B1L003S, U13-ch36, adc4

calib_packv5_042523_0143.root, FC#13, port D2

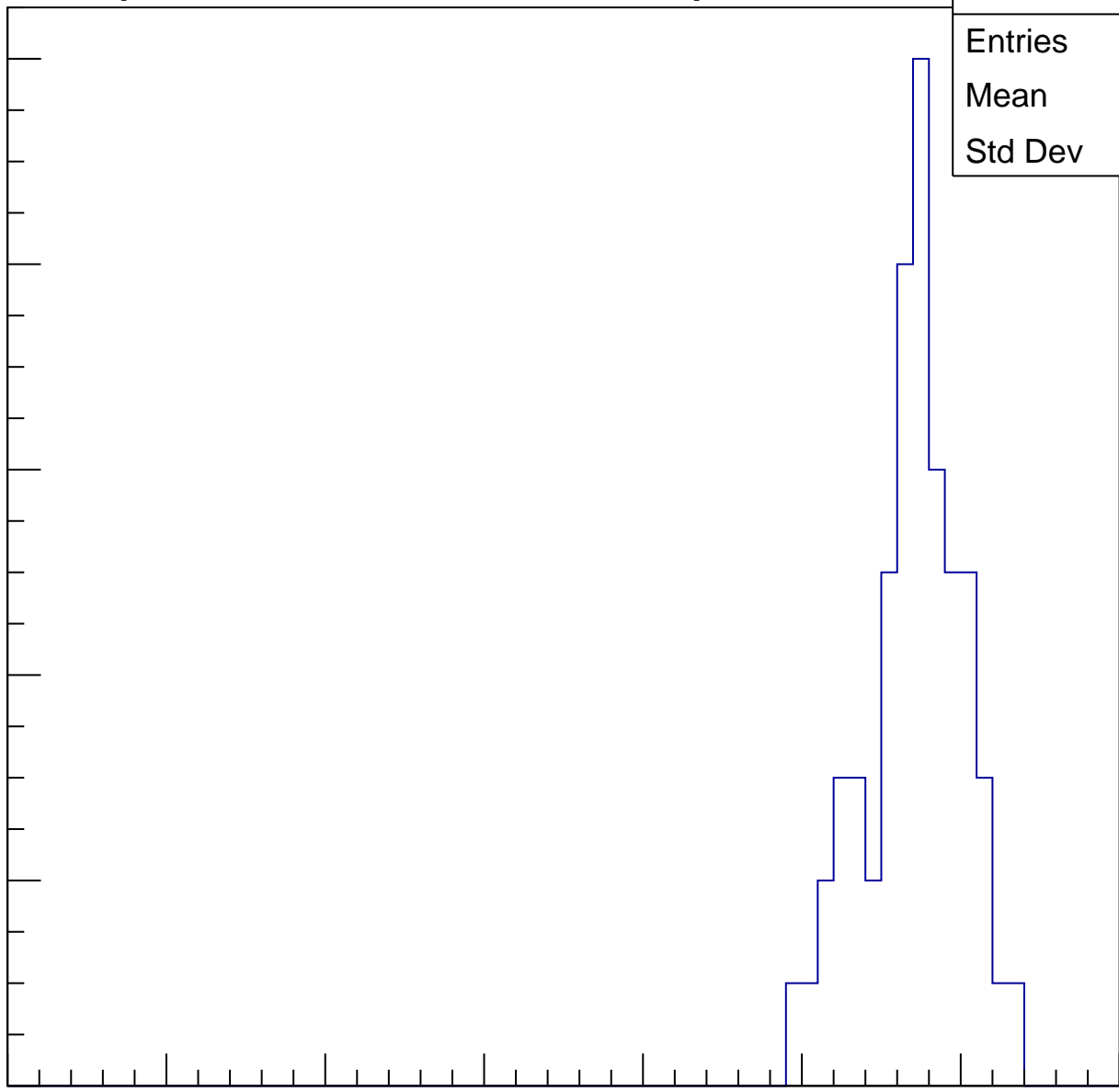
Entries	56
Mean	56.57
Std Dev	3.058

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

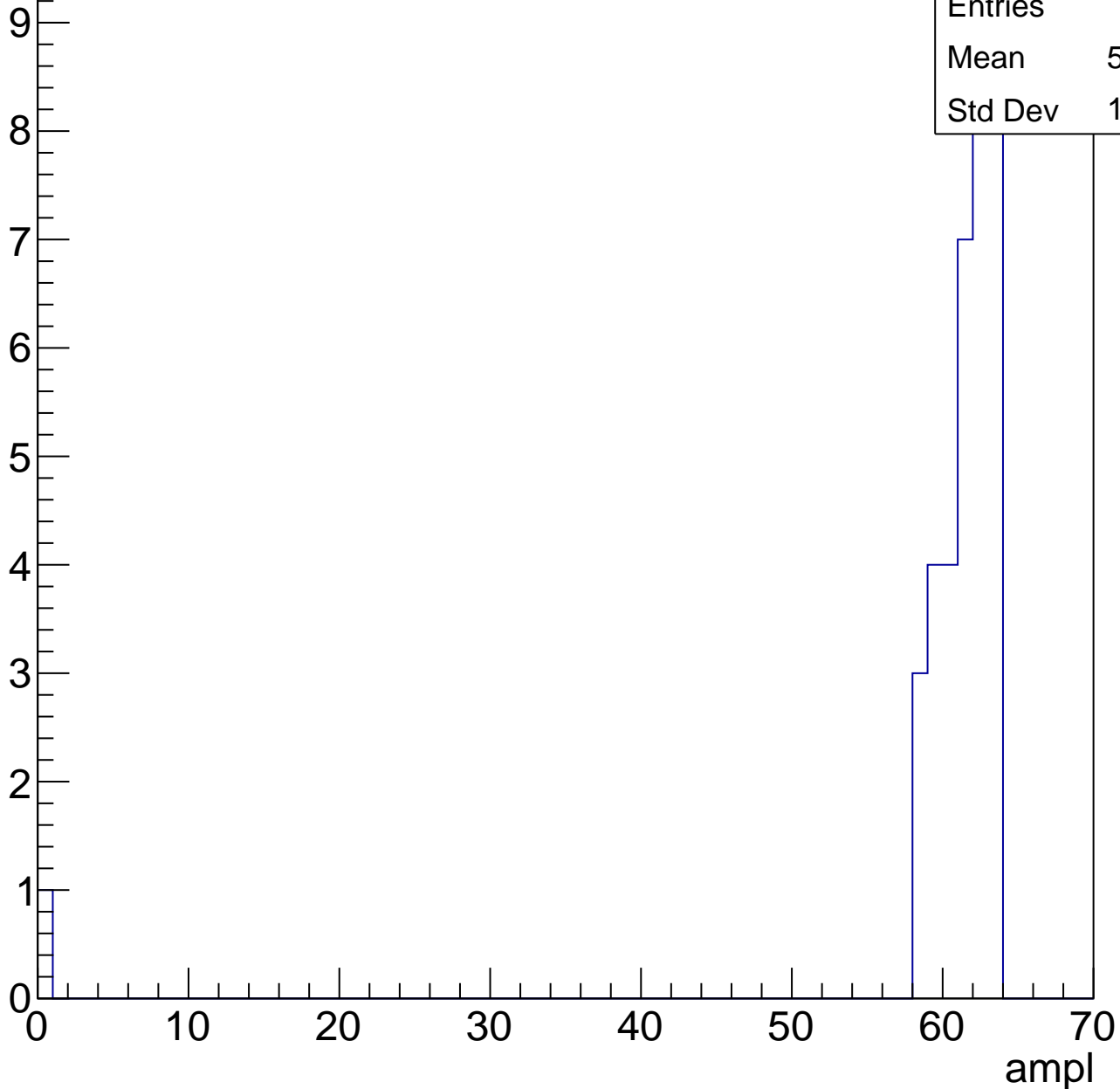


B1L003S, U13-ch36, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.42
Std Dev	10.16



B1L003S, U13-ch36, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch36, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch37, adc0

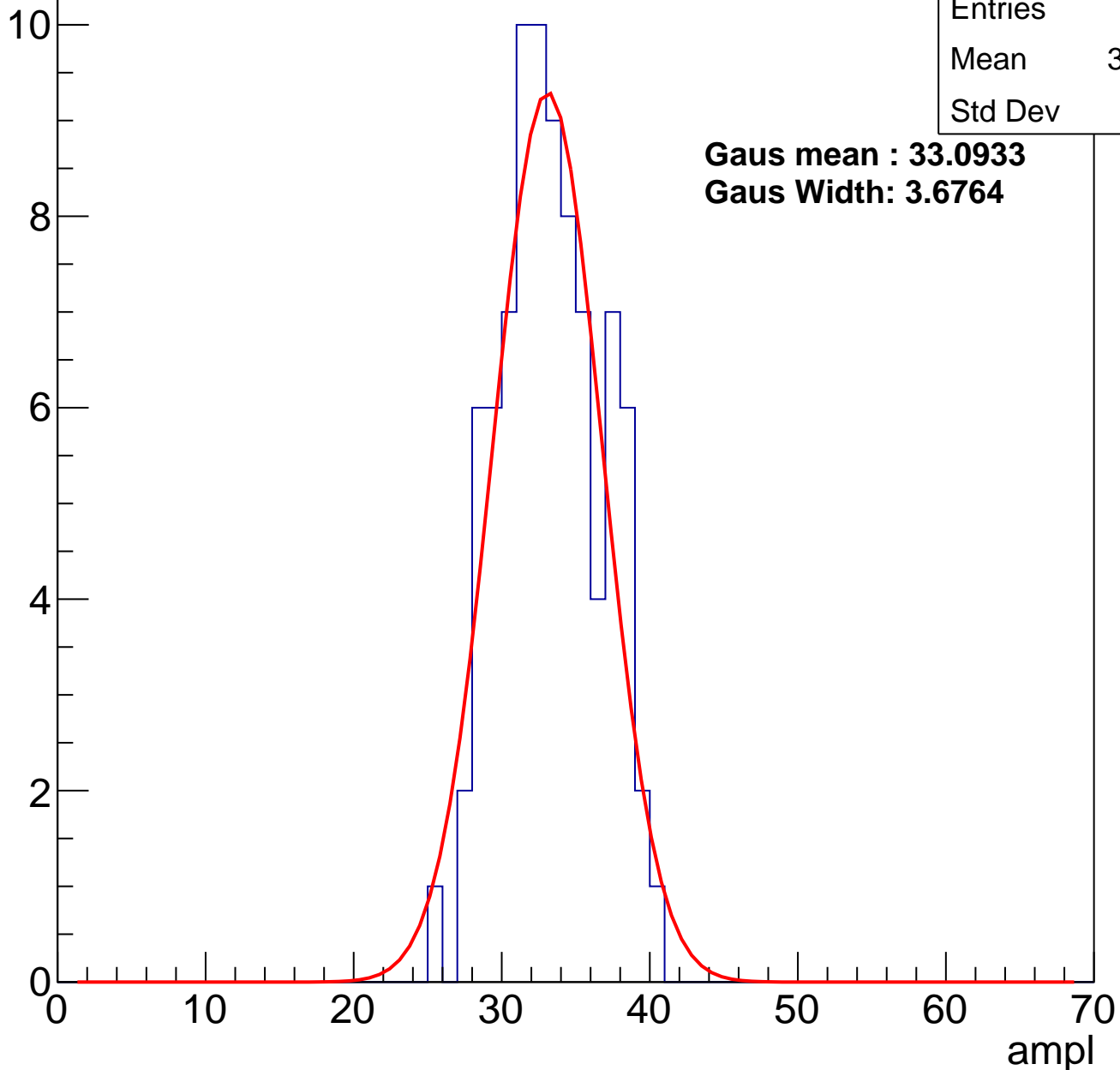
calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	32.84
Std Dev	3.32

Gaus mean : 33.0933

Gaus Width: 3.6764

Entry



B1L003S, U13-ch37, adc1

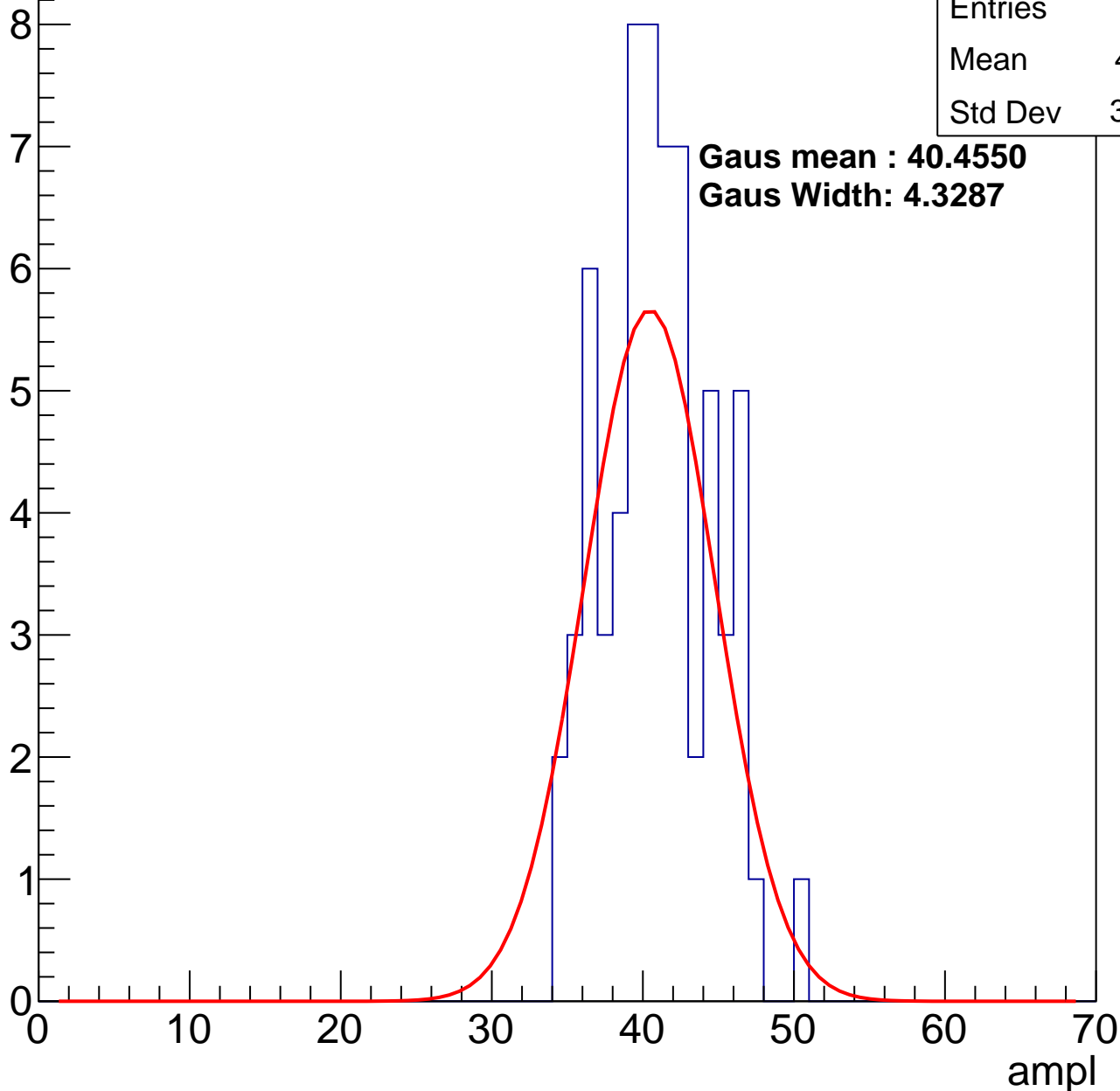
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	40.51
Std Dev	3.539

Gaus mean : 40.4550

Gaus Width: 4.3287



B1L003S, U13-ch37, adc2

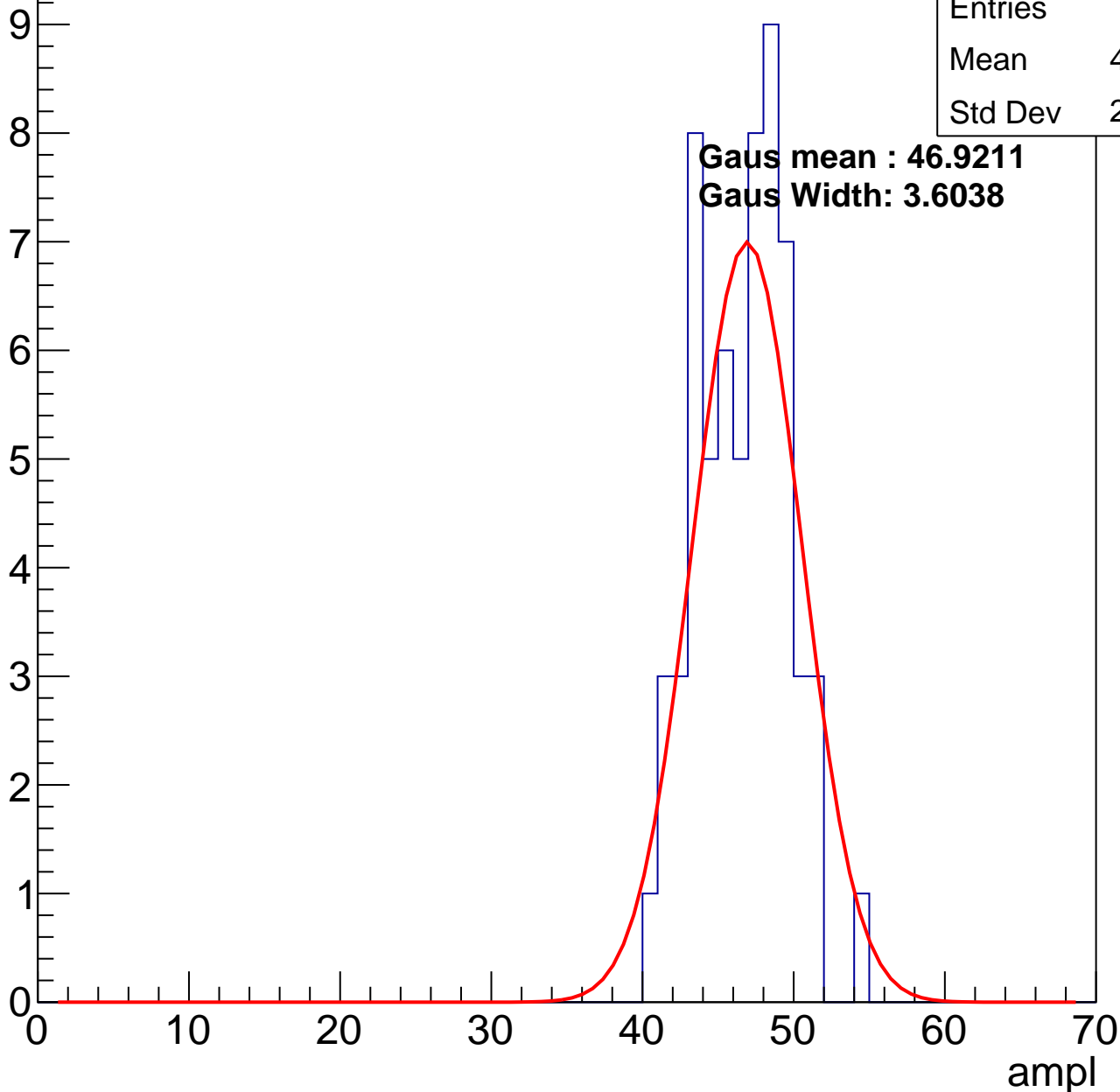
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	46.15
Std Dev	2.978

Gaus mean : 46.9211

Gaus Width: 3.6038

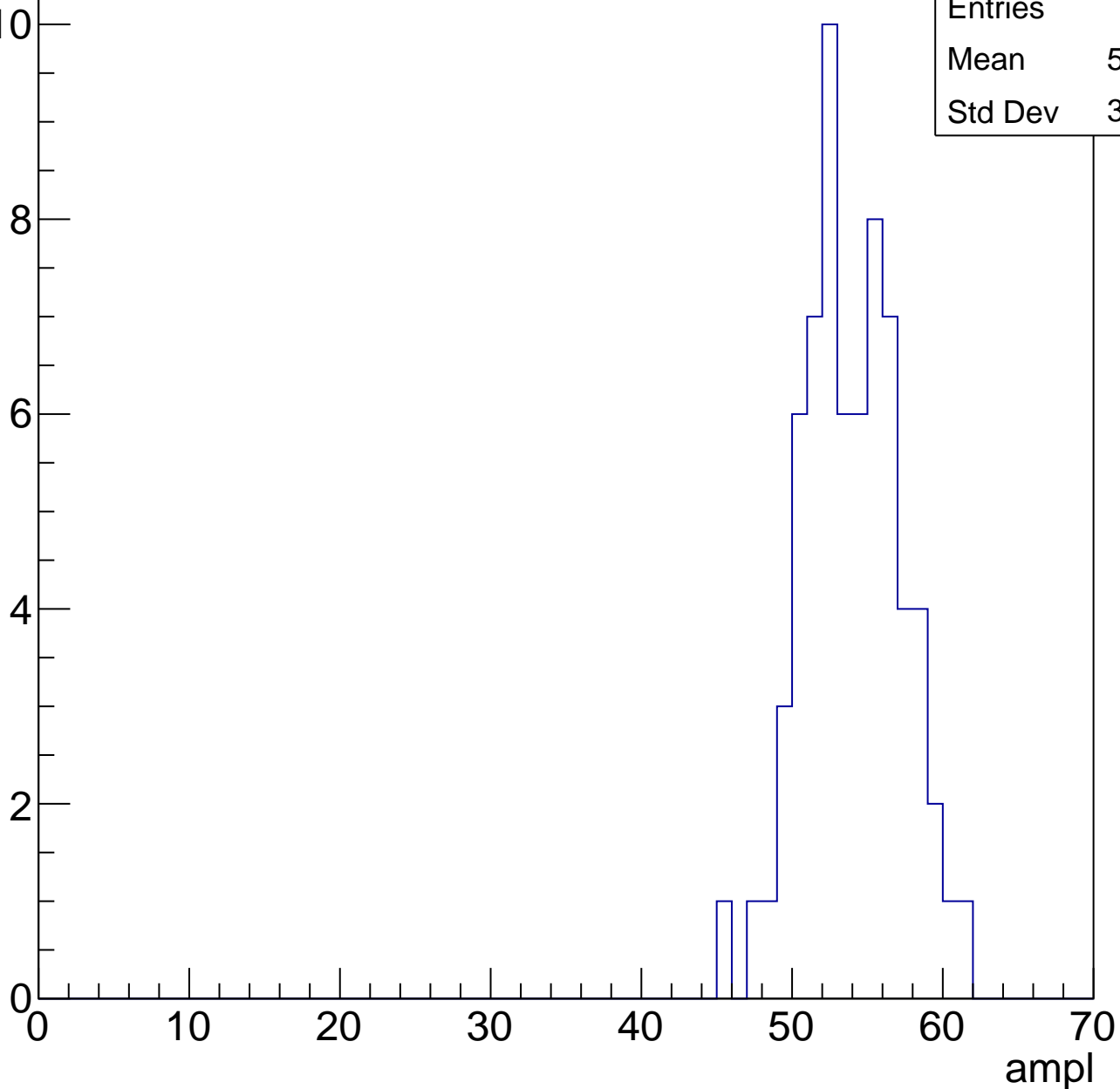


B1L003S, U13-ch37, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	53.49
Std Dev	3.206

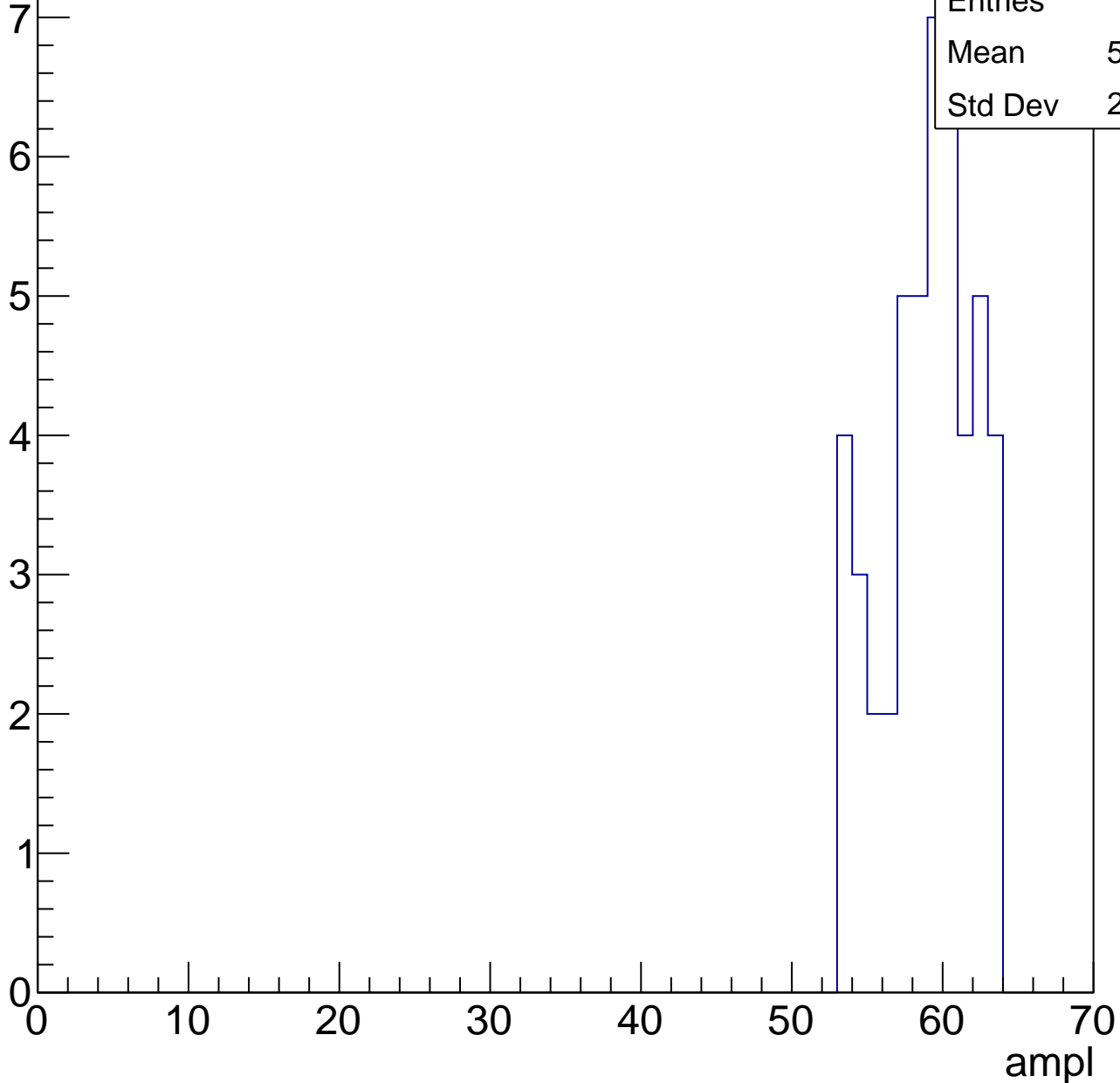


B1L003S, U13-ch37, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

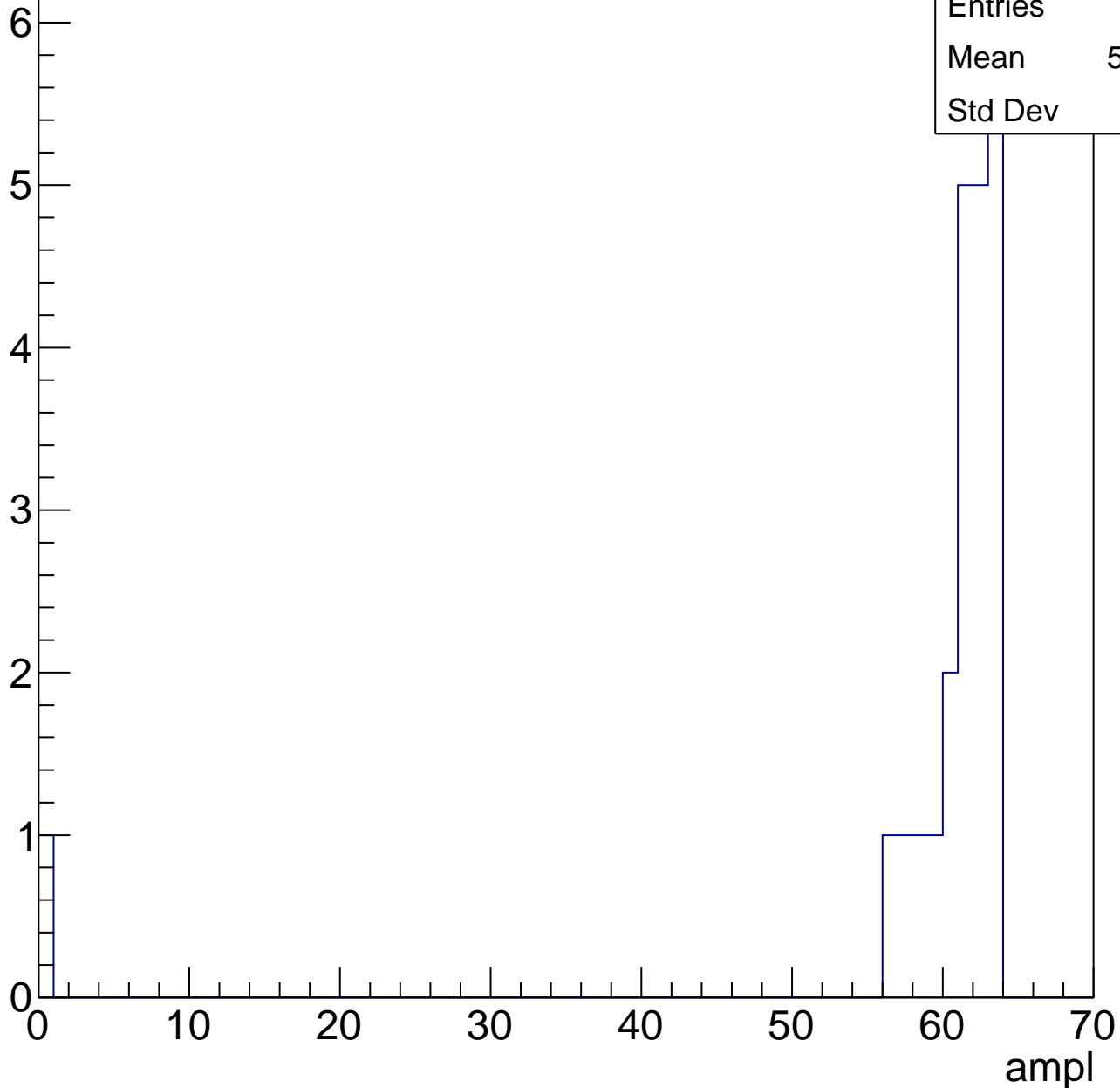
Entries	48
Mean	58.54
Std Dev	2.944



B1L003S, U13-ch37, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch37, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch37, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch38, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.82
Std Dev	4.866

Gaus mean : 30.6475

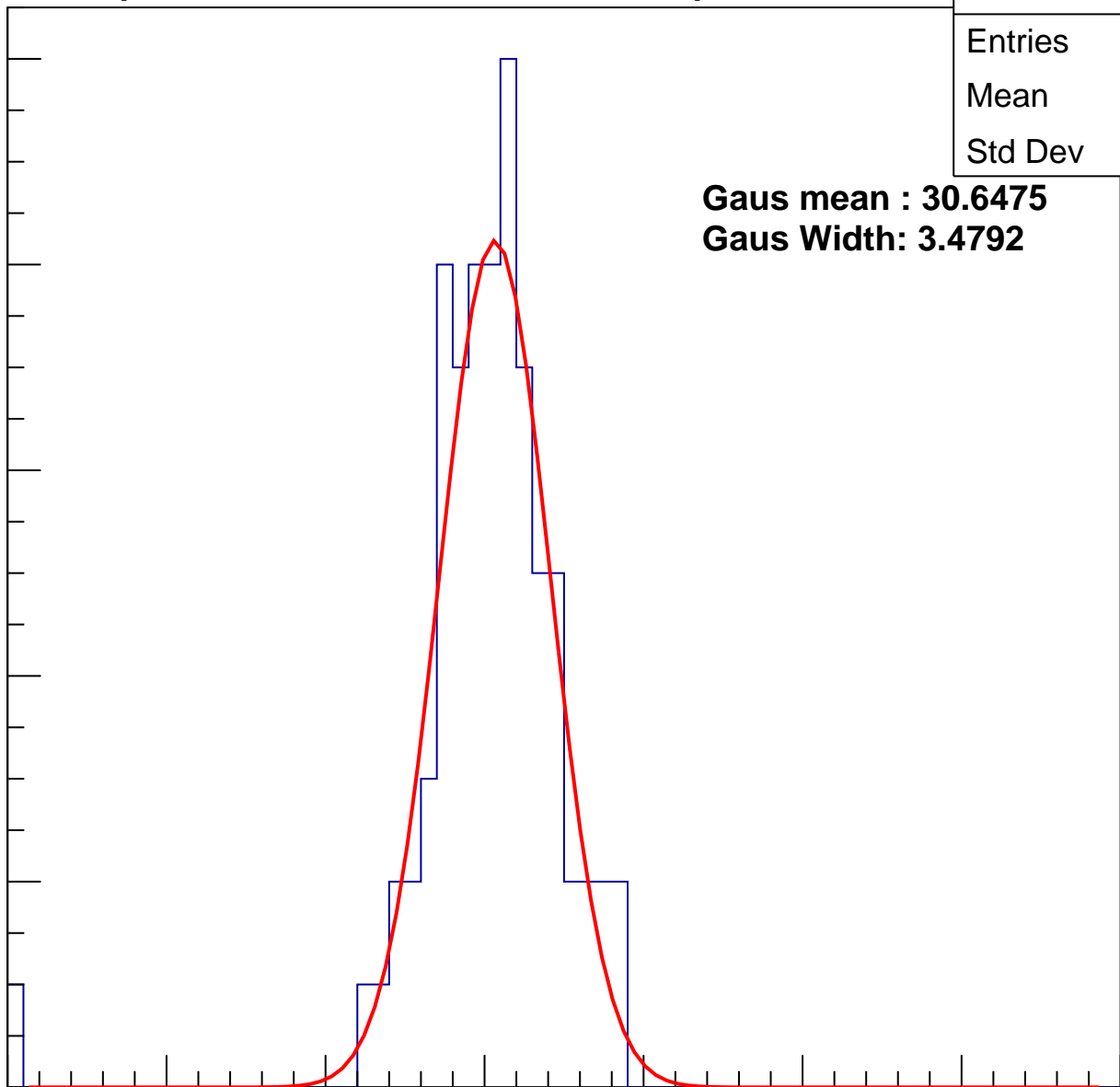
Gaus Width: 3.4792

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



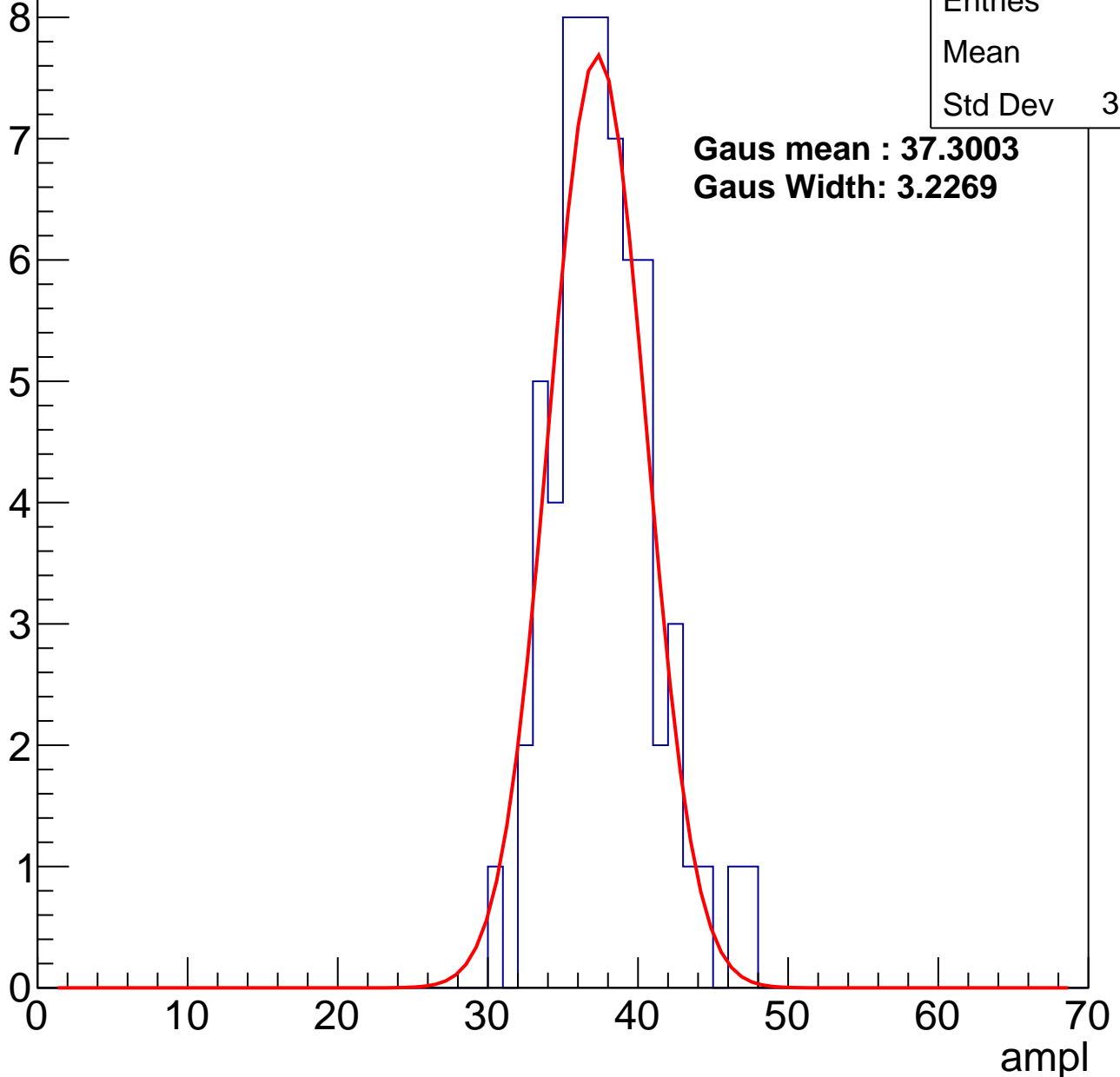
B1L003S, U13-ch38, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.3
Std Dev	3.325

Gaus mean : 37.3003
Gaus Width: 3.2269



B1L003S, U13-ch38, adc2

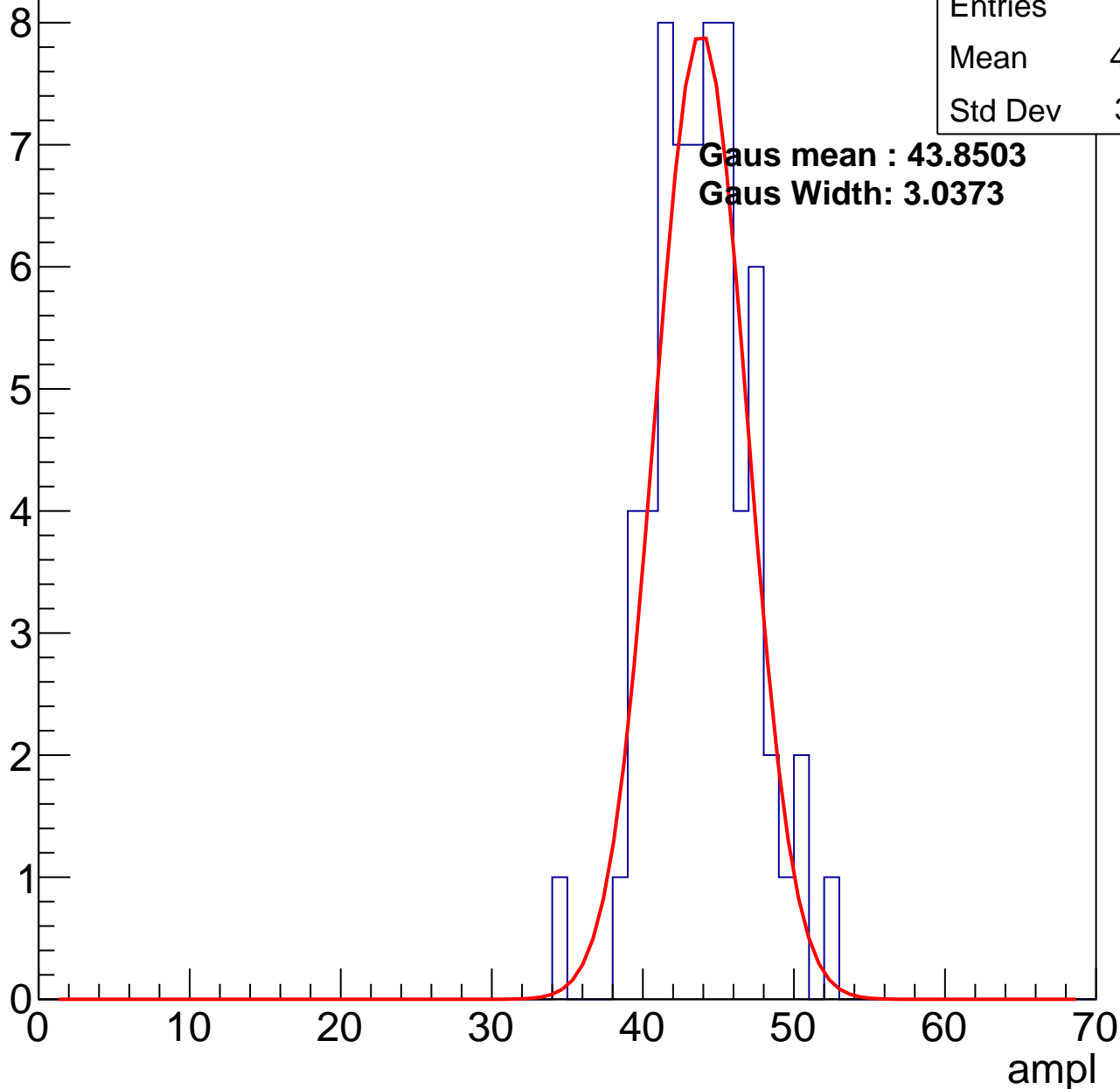
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.53
Std Dev	3.221

Gaus mean : 43.8503

Gaus Width: 3.0373

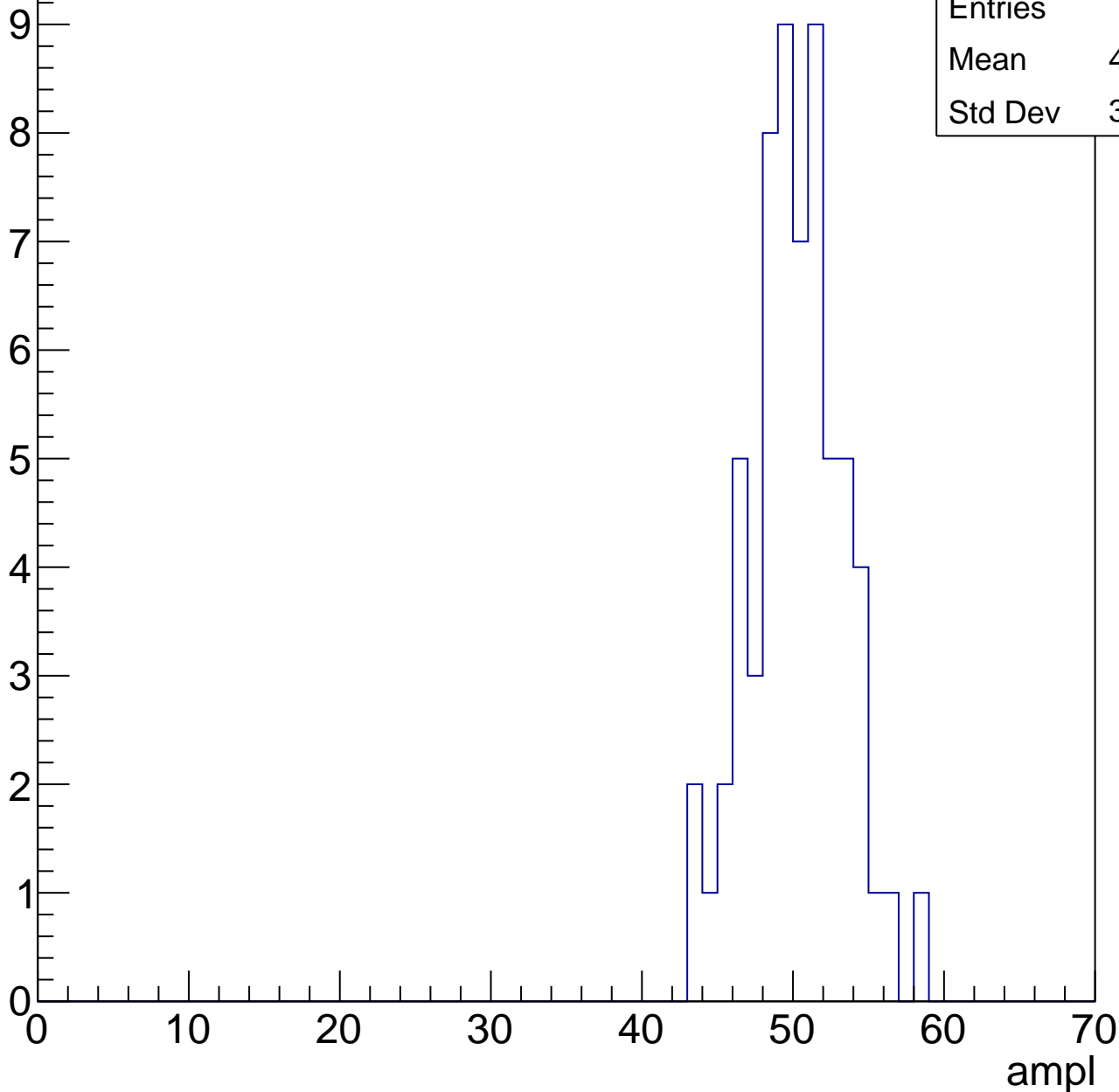


B1L003S, U13-ch38, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.76
Std Dev	3.064

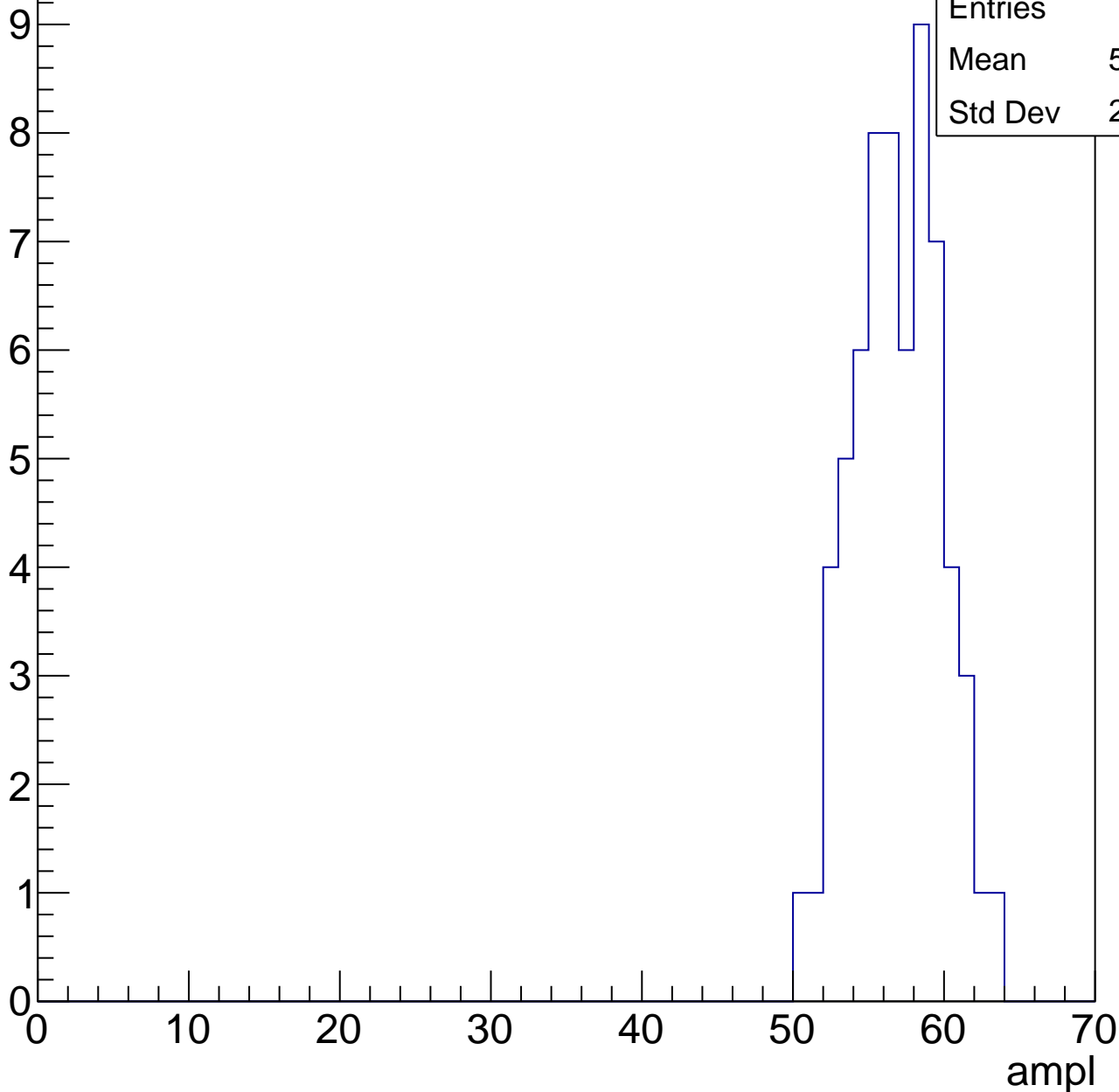


B1L003S, U13-ch38, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	56.42
Std Dev	2.849

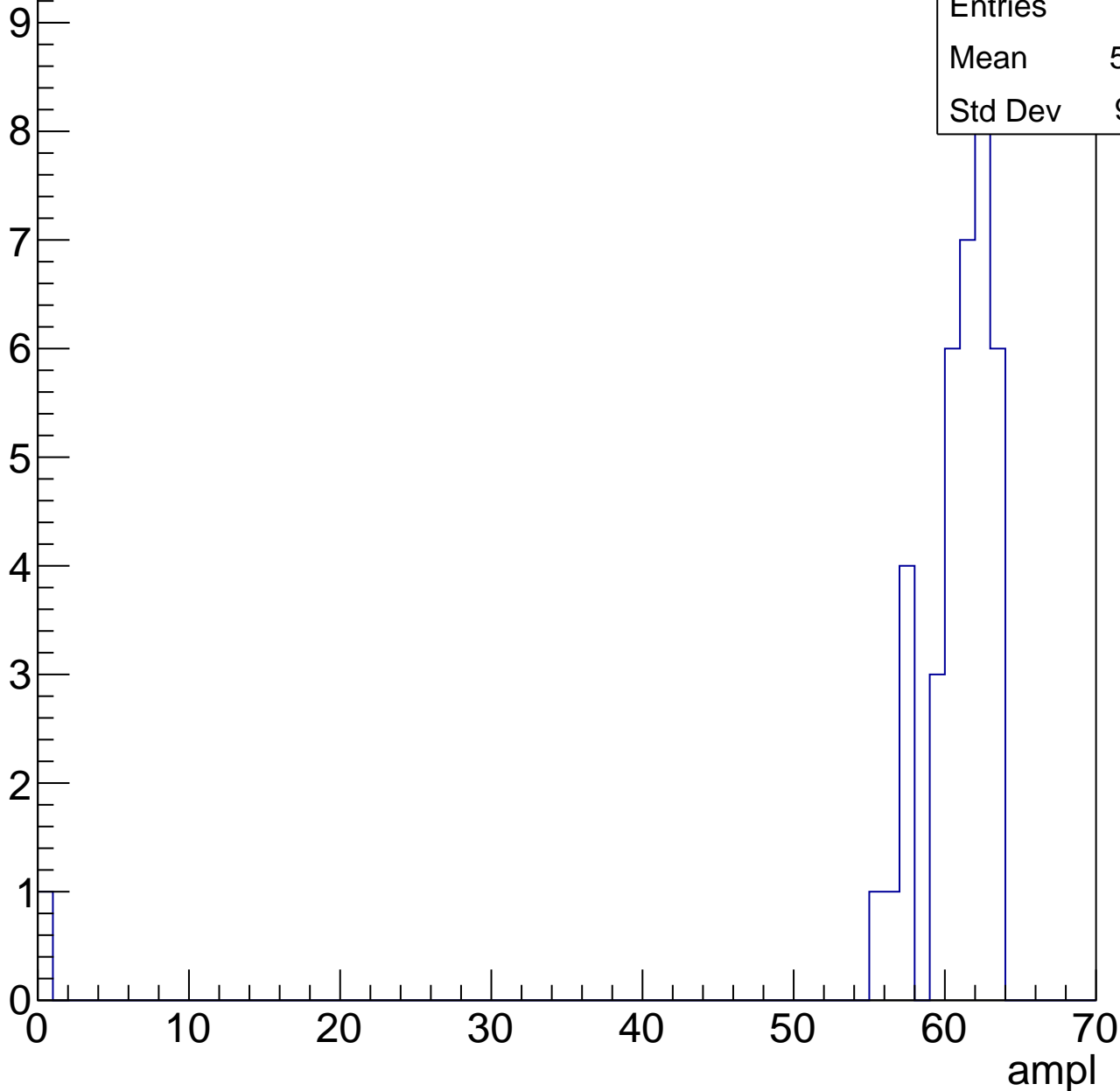


B1L003S, U13-ch38, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	58.92
Std Dev	9.911



B1L003S, U13-ch38, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch38, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch39, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	29.74
Std Dev	4.761

Gaus mean : 30.8465

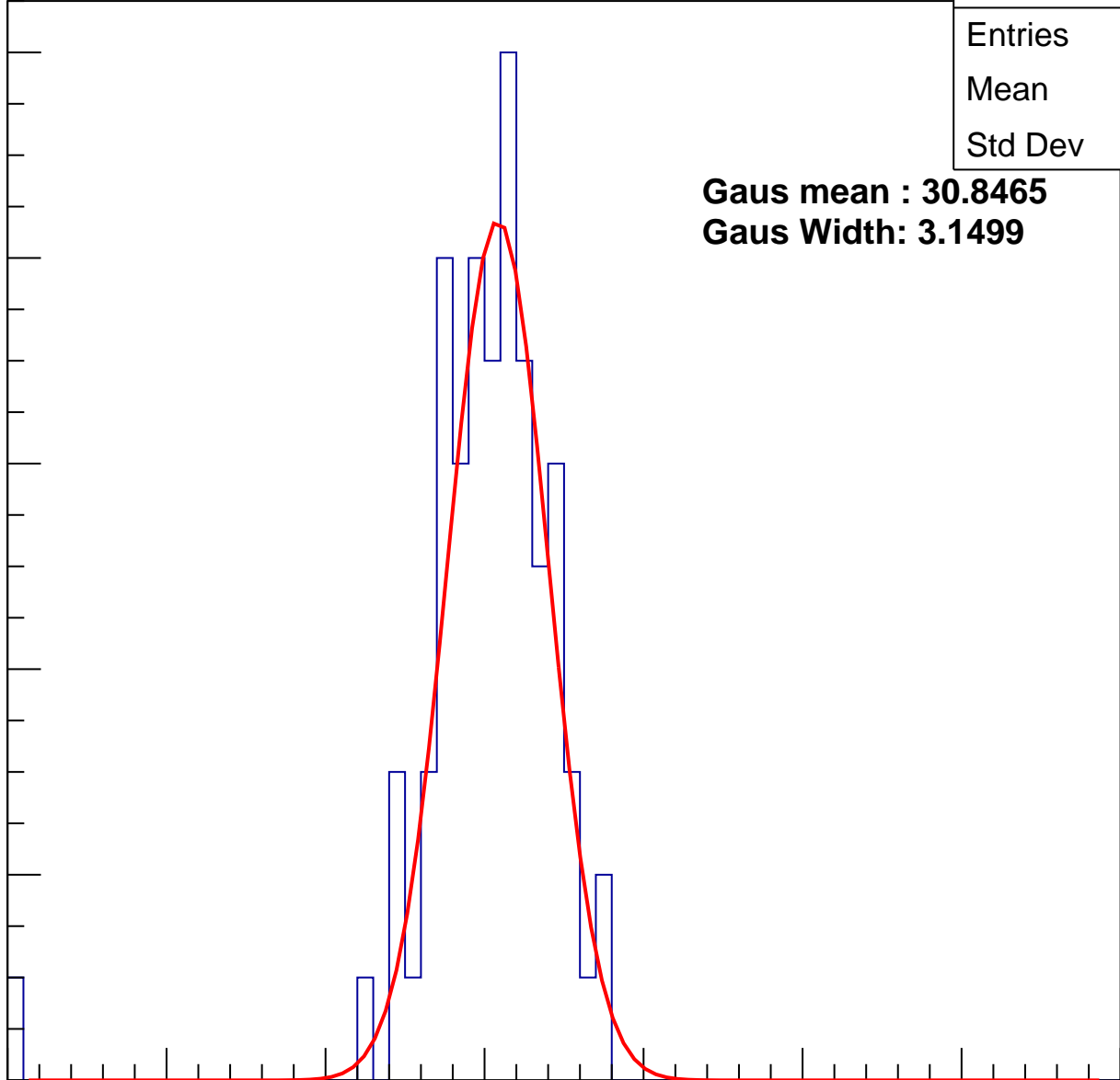
Gaus Width: 3.1499

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

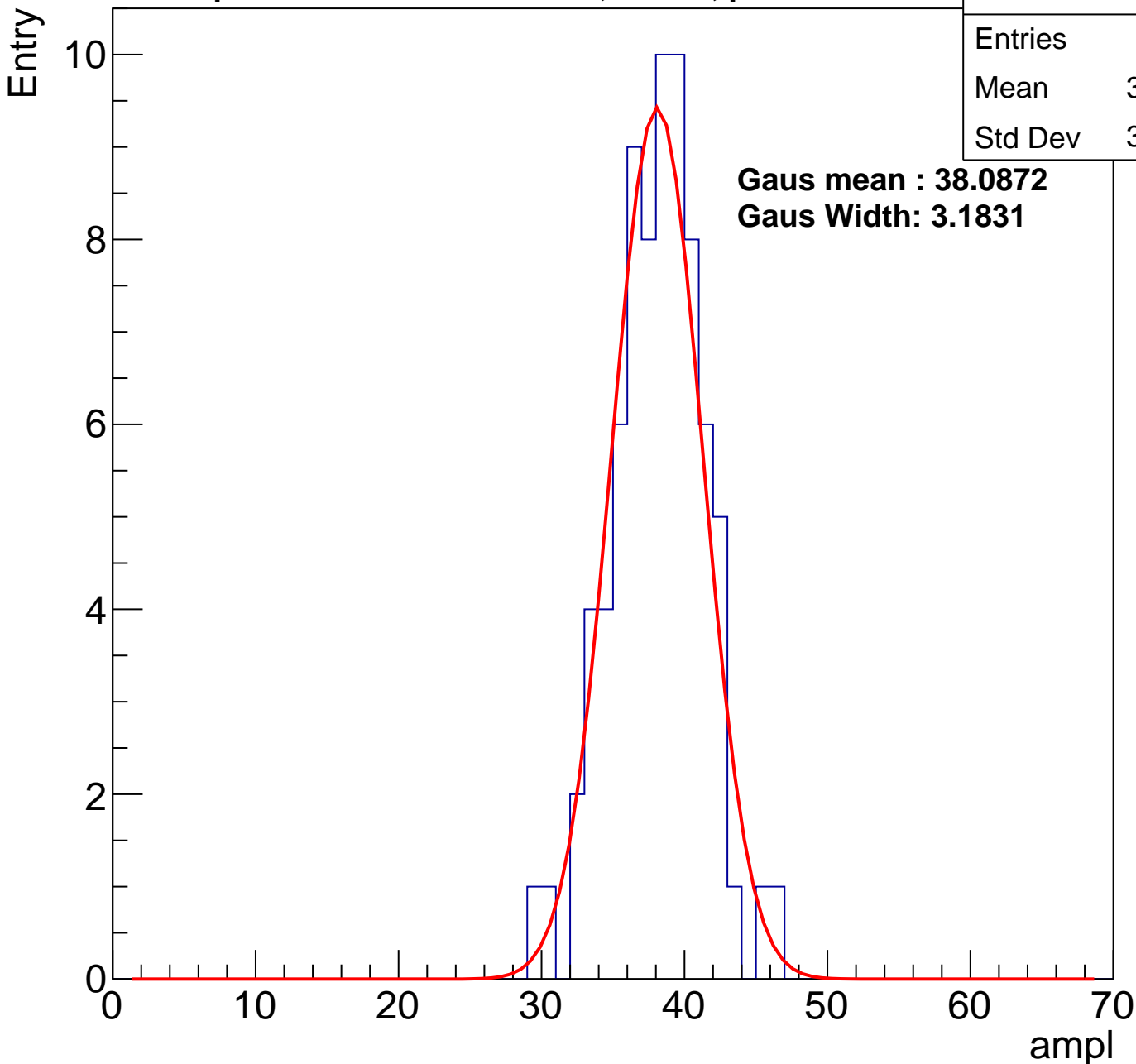


B1L003S, U13-ch39, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	37.68
Std Dev	3.193

Gaus mean : 38.0872
Gaus Width: 3.1831



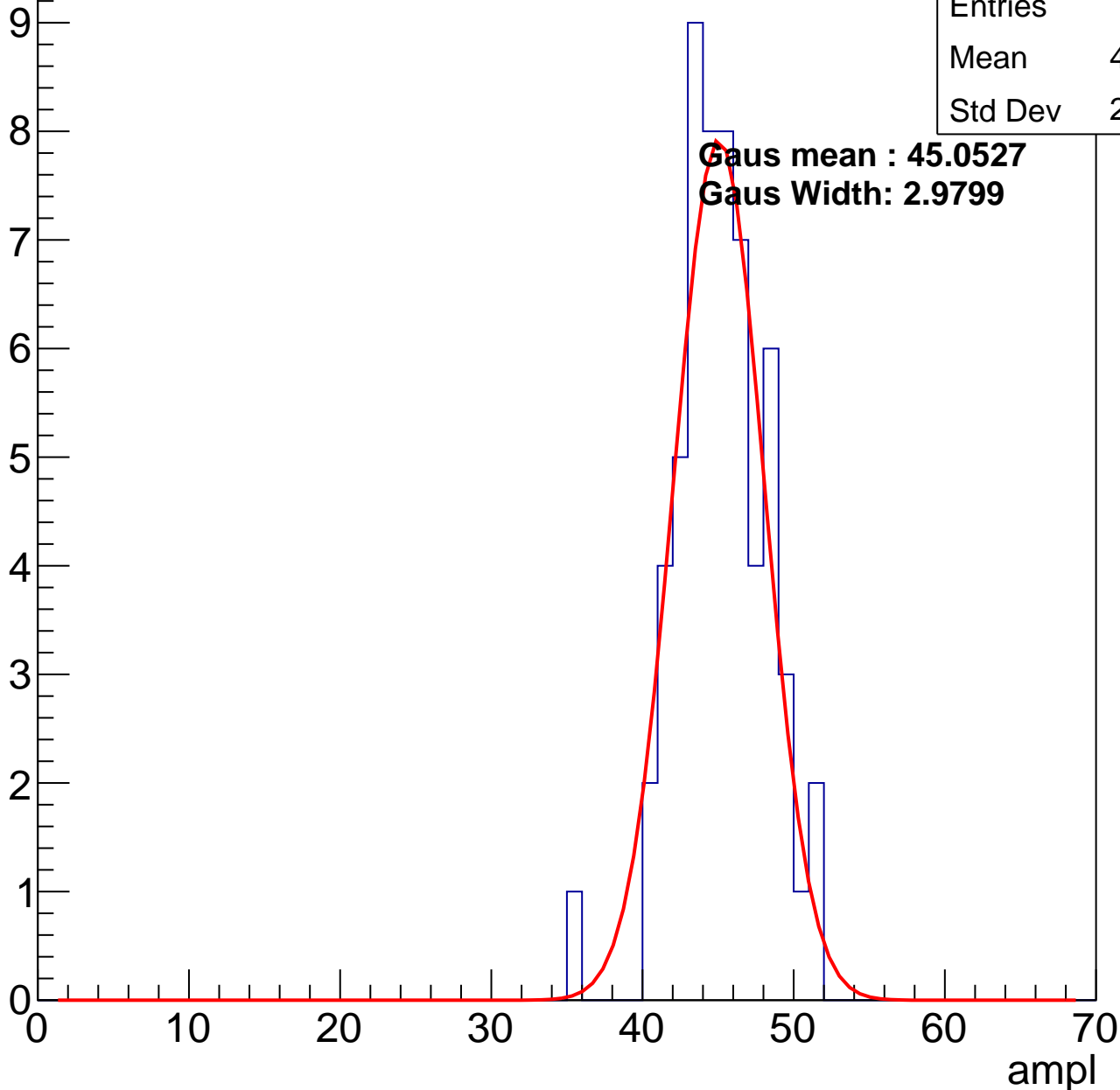
B1L003S, U13-ch39, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	44.75
Std Dev	2.947

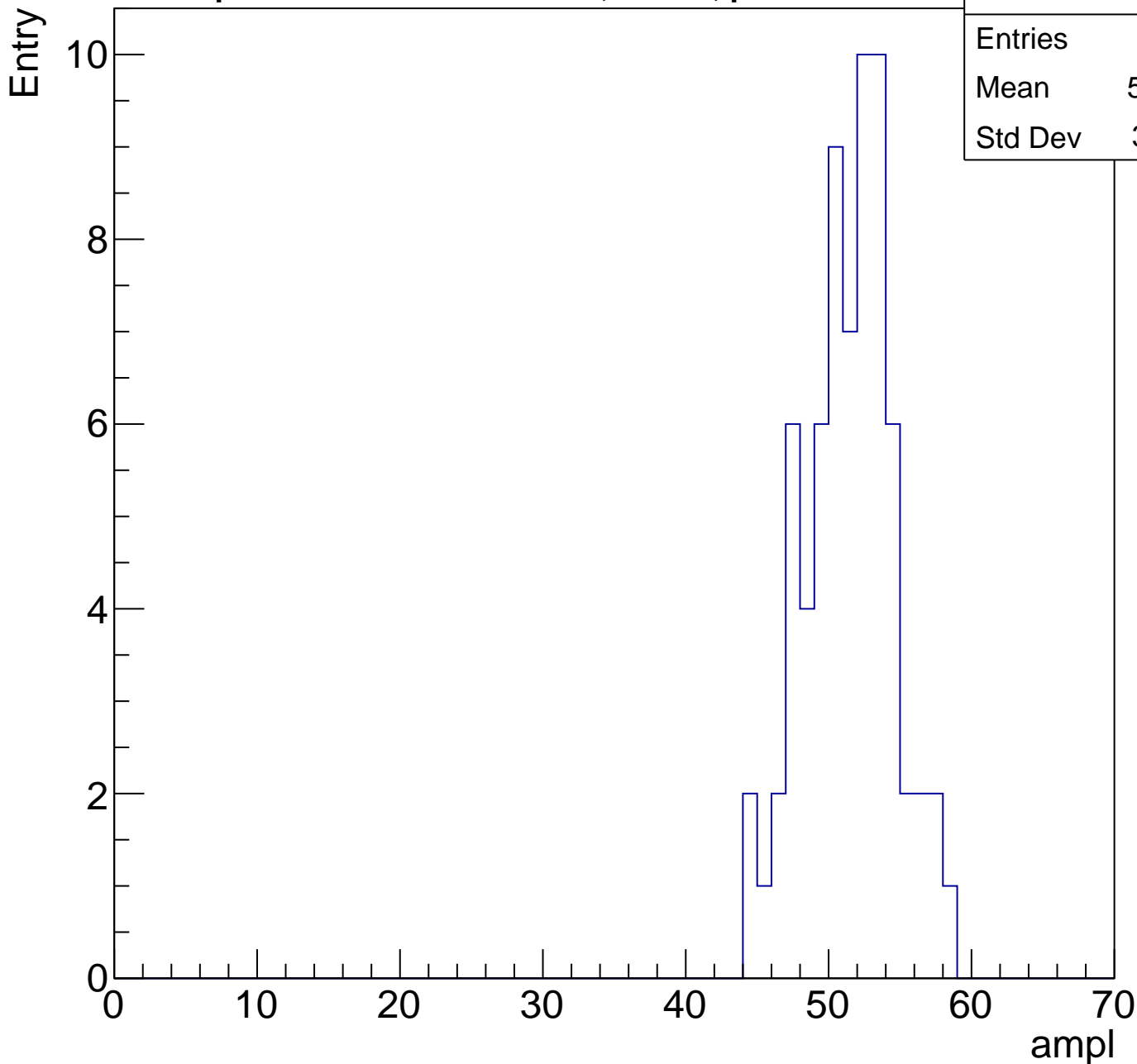
Gaus mean : 45.0527
Gaus Width: 2.9799



B1L003S, U13-ch39, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	50.97
Std Dev	3.061

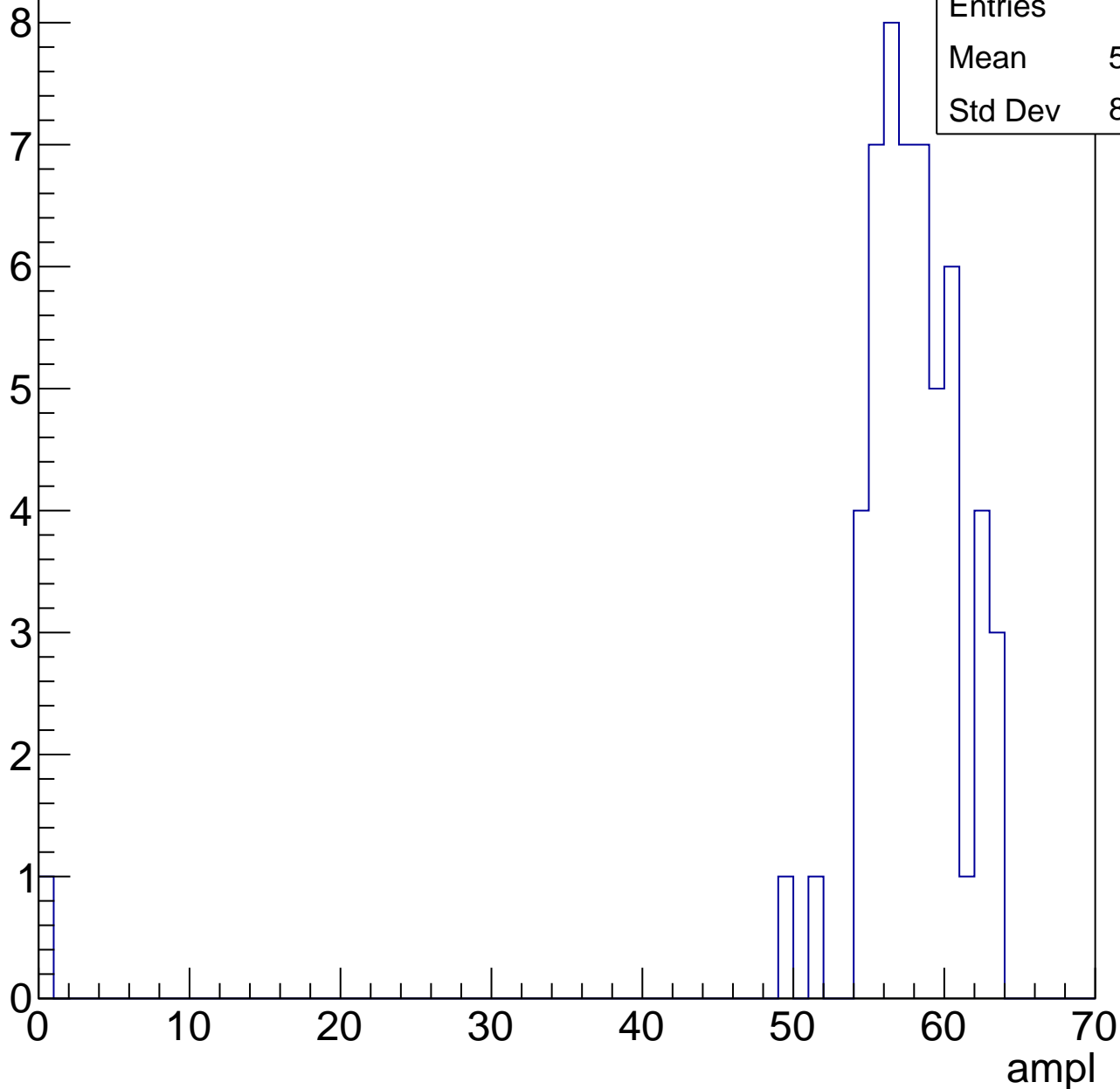


B1L003S, U13-ch39, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	56.49
Std Dev	8.215

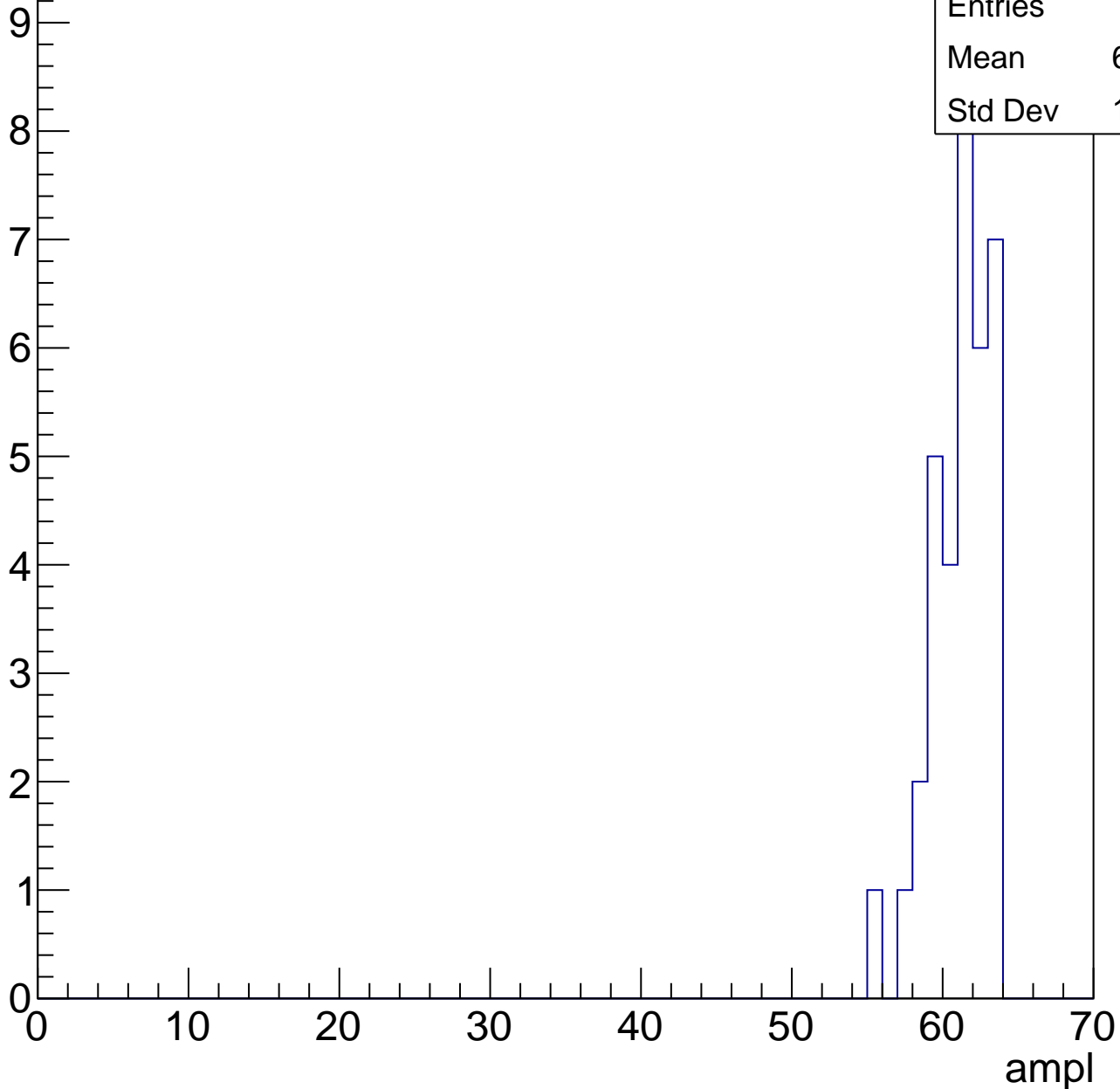


B1L003S, U13-ch39, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	60.71
Std Dev	1.891



B1L003S, U13-ch39, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch39, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch40, adc0

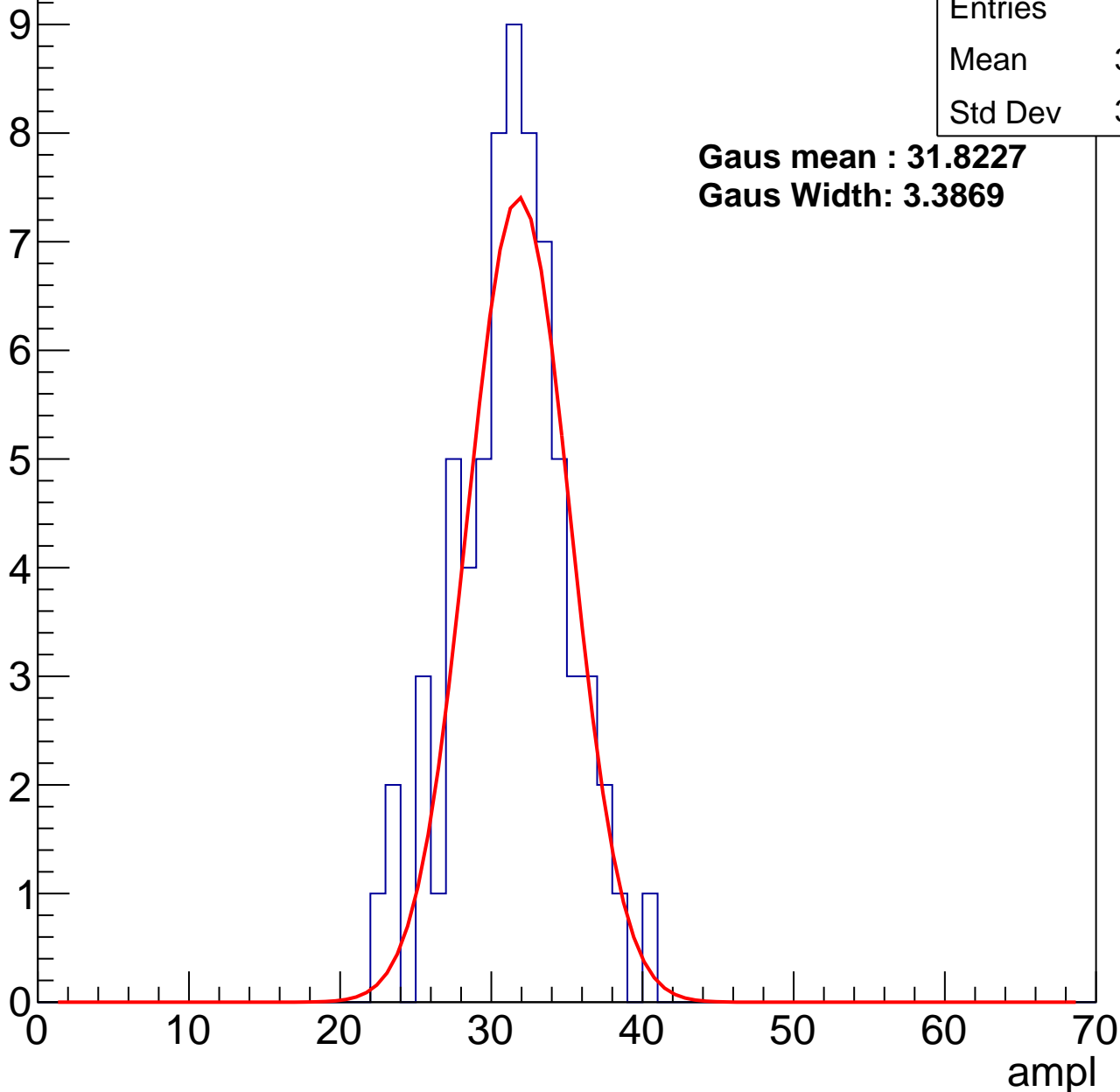
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.91
Std Dev	3.621

Gaus mean : 31.8227

Gaus Width: 3.3869



B1L003S, U13-ch40, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	37.33
Std Dev	2.84

Gaus mean : 37.9148

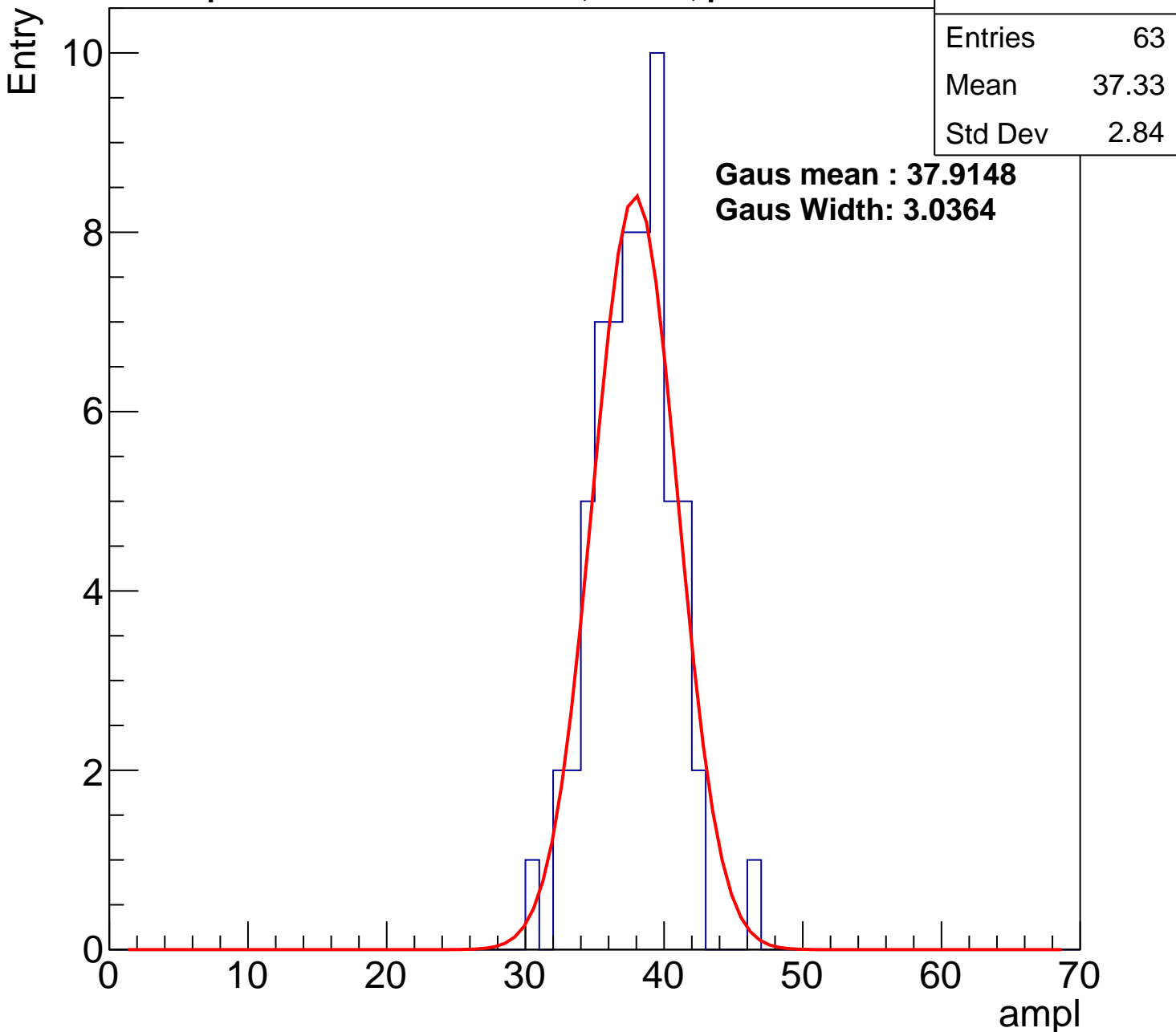
Gaus Width: 3.0364

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch40, adc2

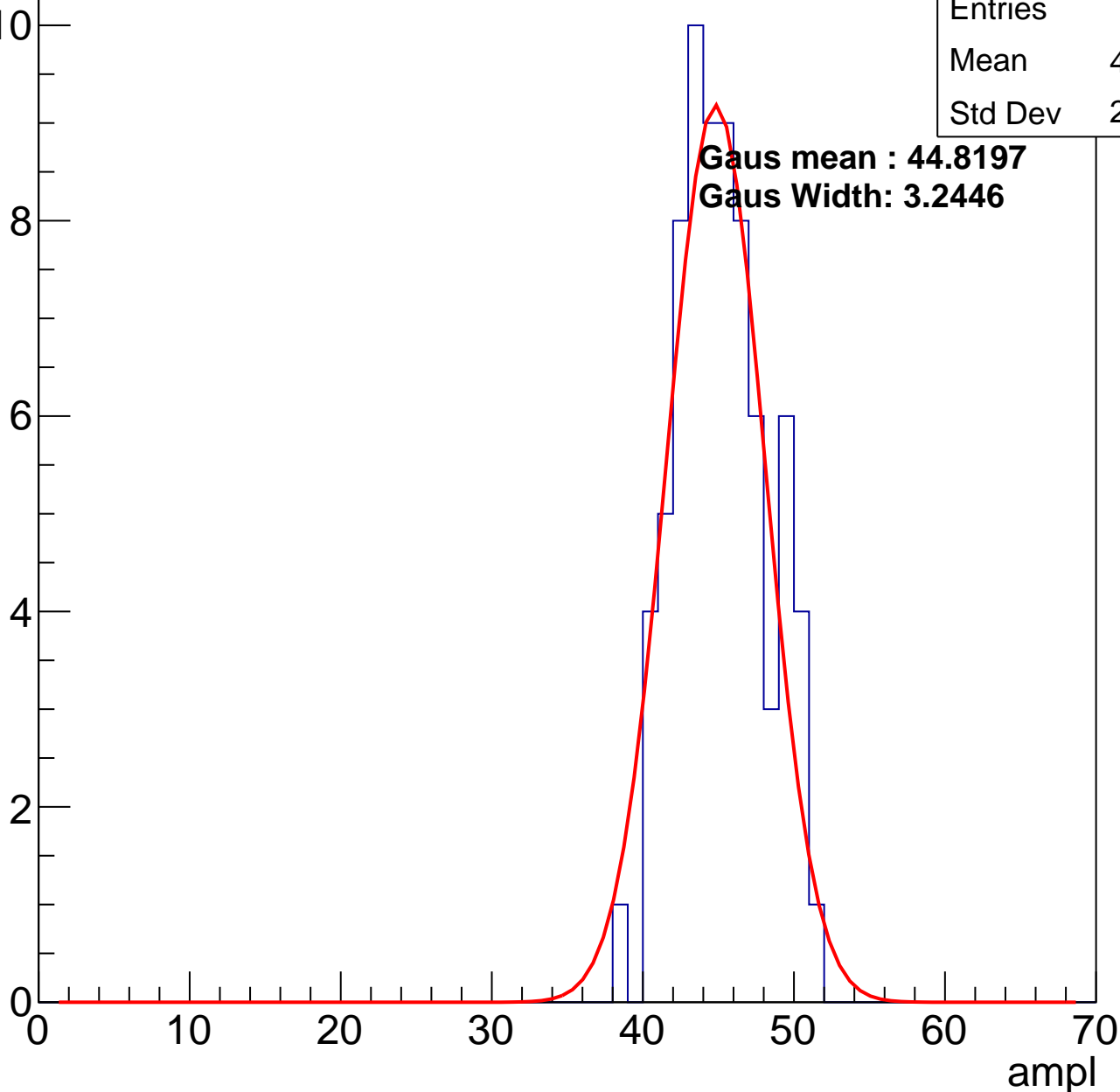
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	44.72
Std Dev	2.929

Gaus mean : 44.8197

Gaus Width: 3.2446

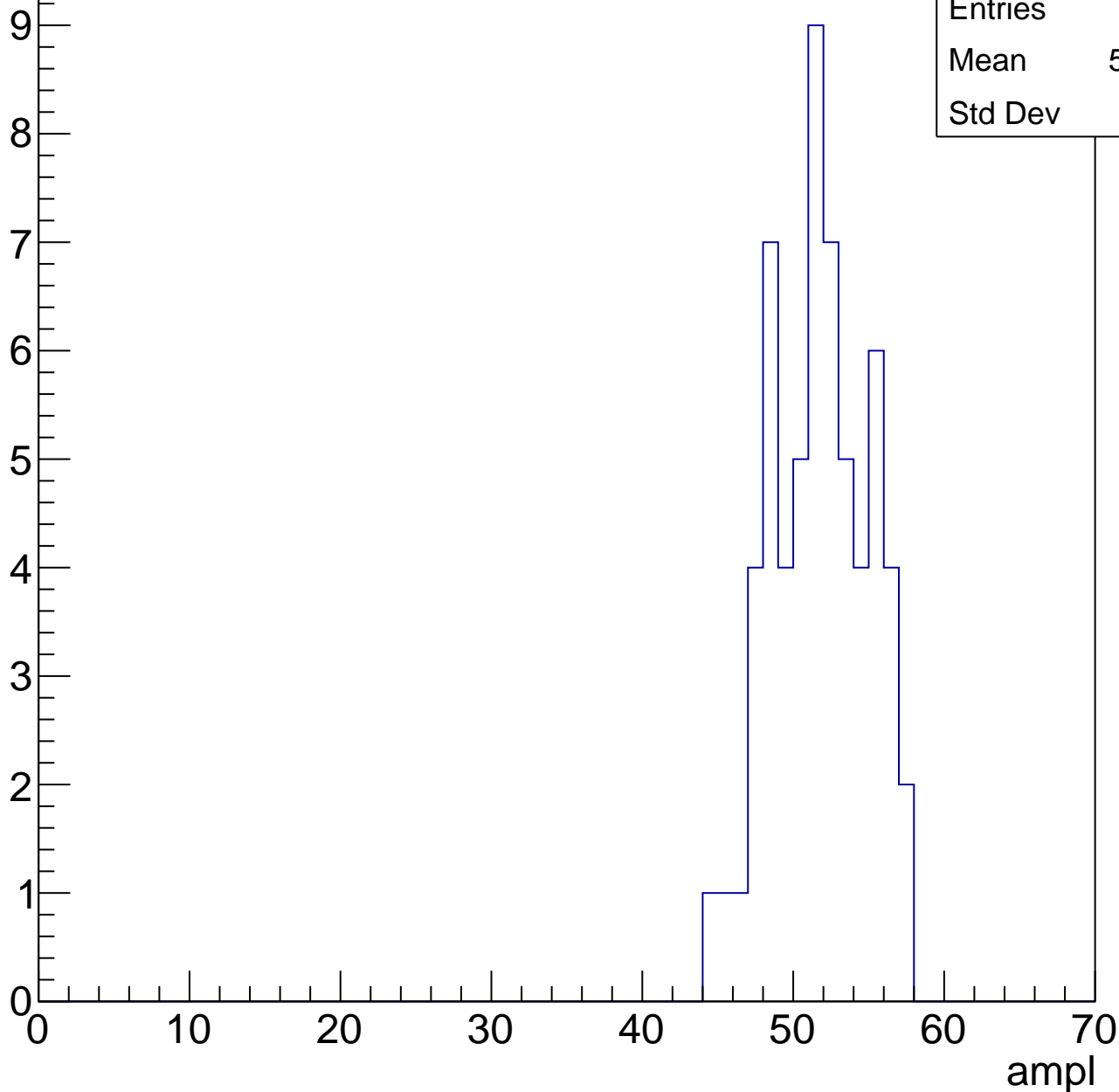


B1L003S, U13-ch40, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	51.28
Std Dev	3.12

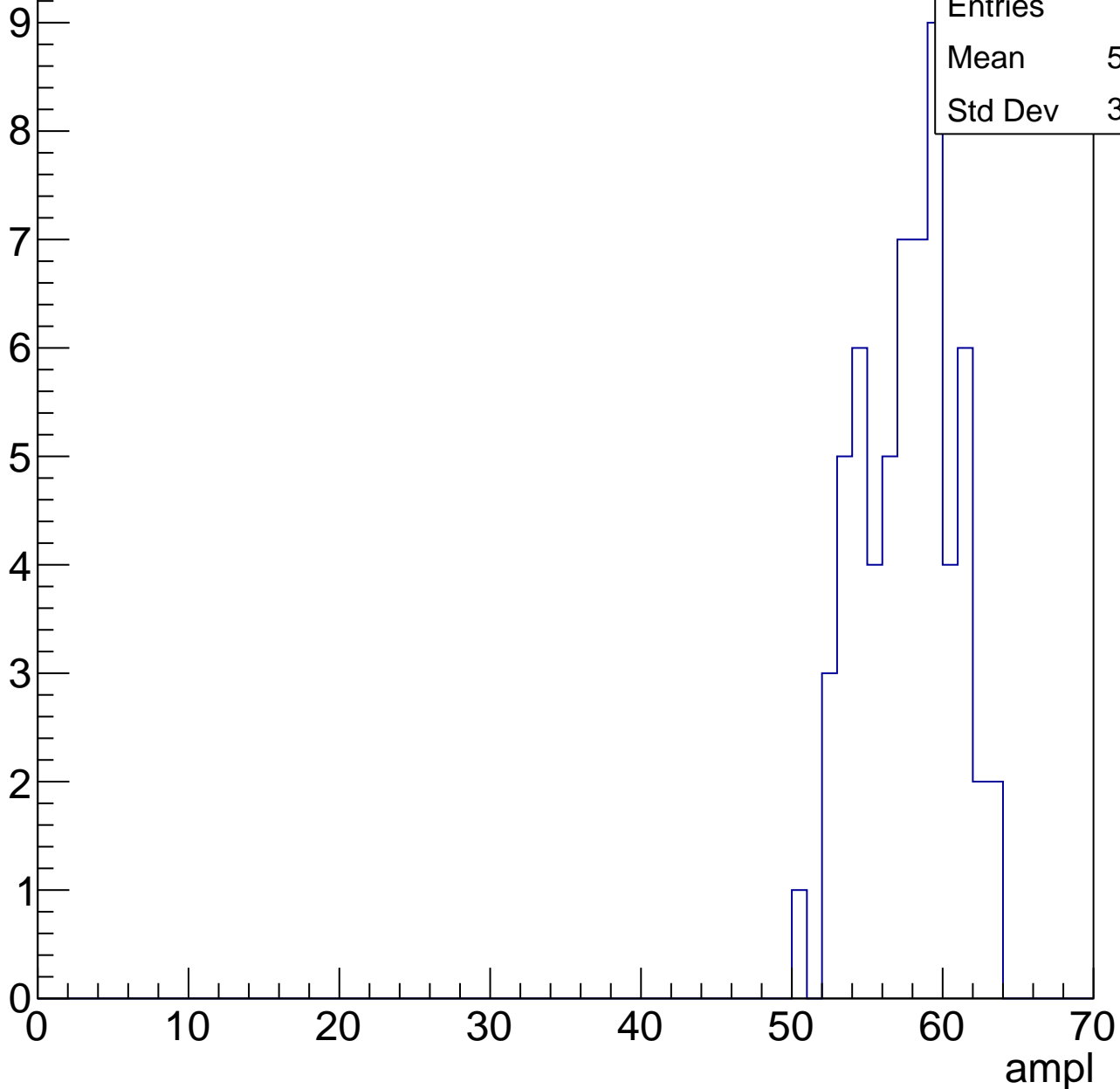


B1L003S, U13-ch40, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	57.16
Std Dev	3.069

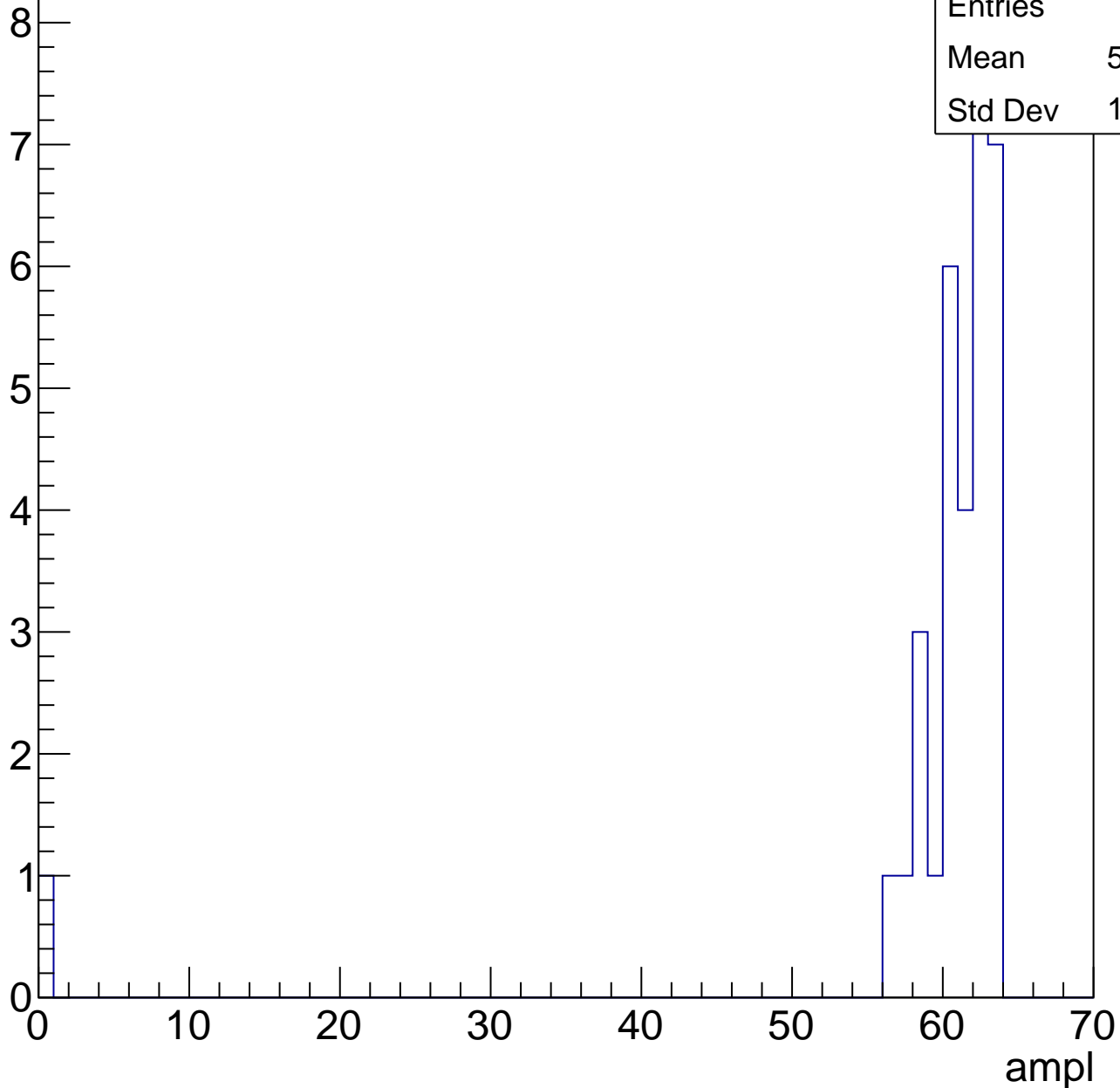


B1L003S, U13-ch40, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	32
Mean	58.97
Std Dev	10.76



B1L003S, U13-ch40, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U13-ch40, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch41, adc0

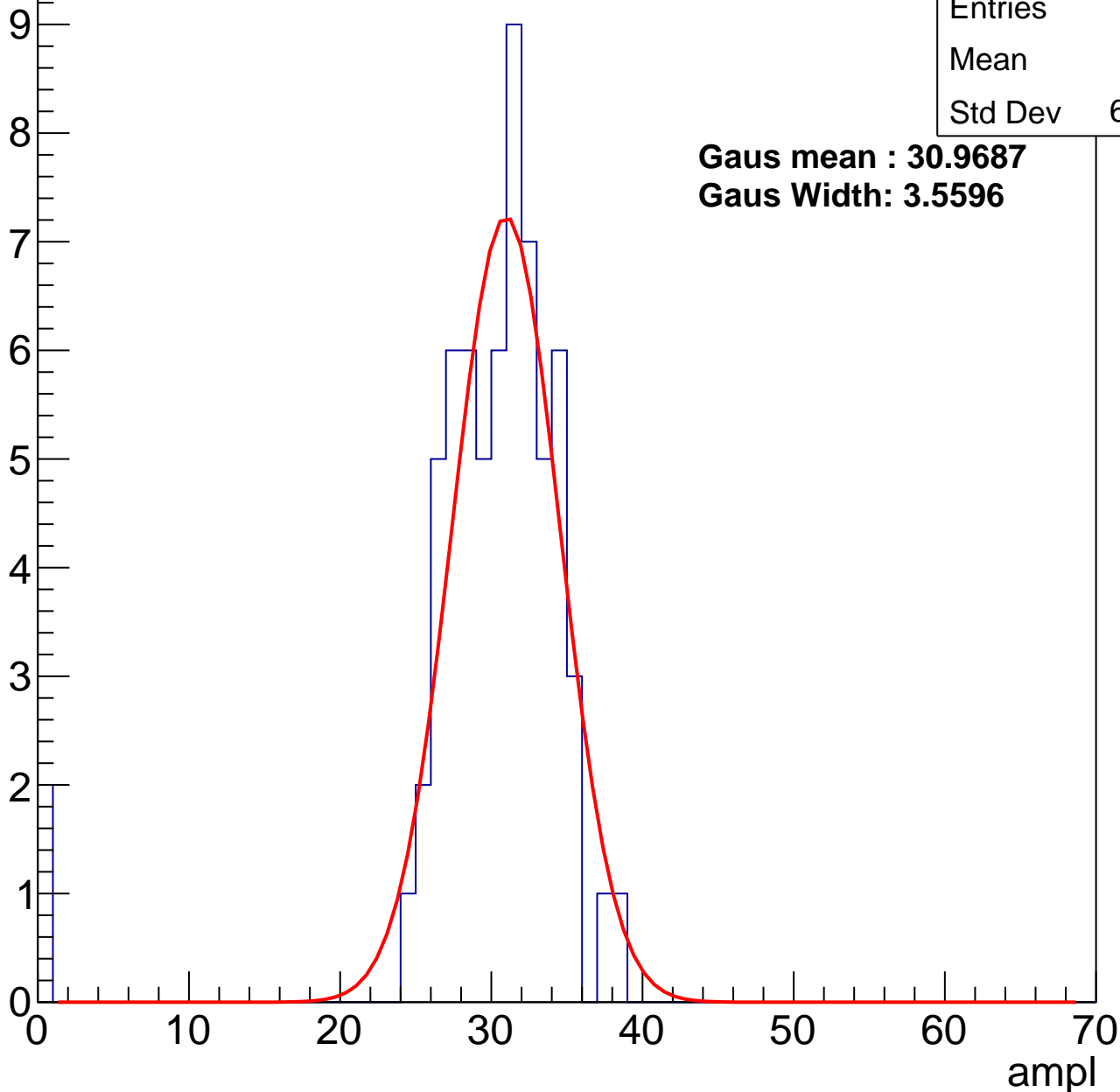
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.4
Std Dev	6.066

Gaus mean : 30.9687

Gaus Width: 3.5596

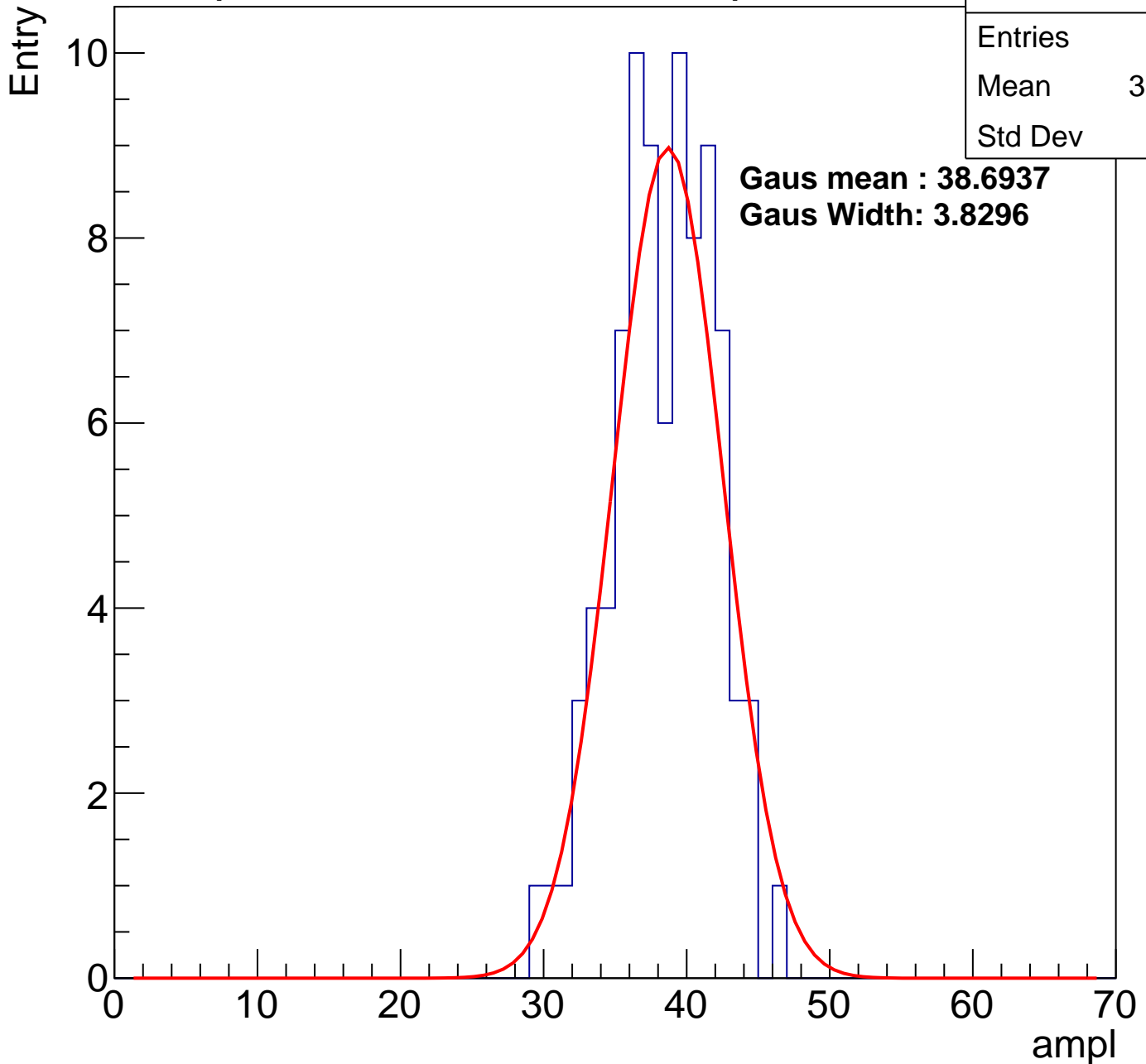


B1L003S, U13-ch41, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	37.93
Std Dev	3.49

Gaus mean : 38.6937
Gaus Width: 3.8296



B1L003S, U13-ch41, adc2

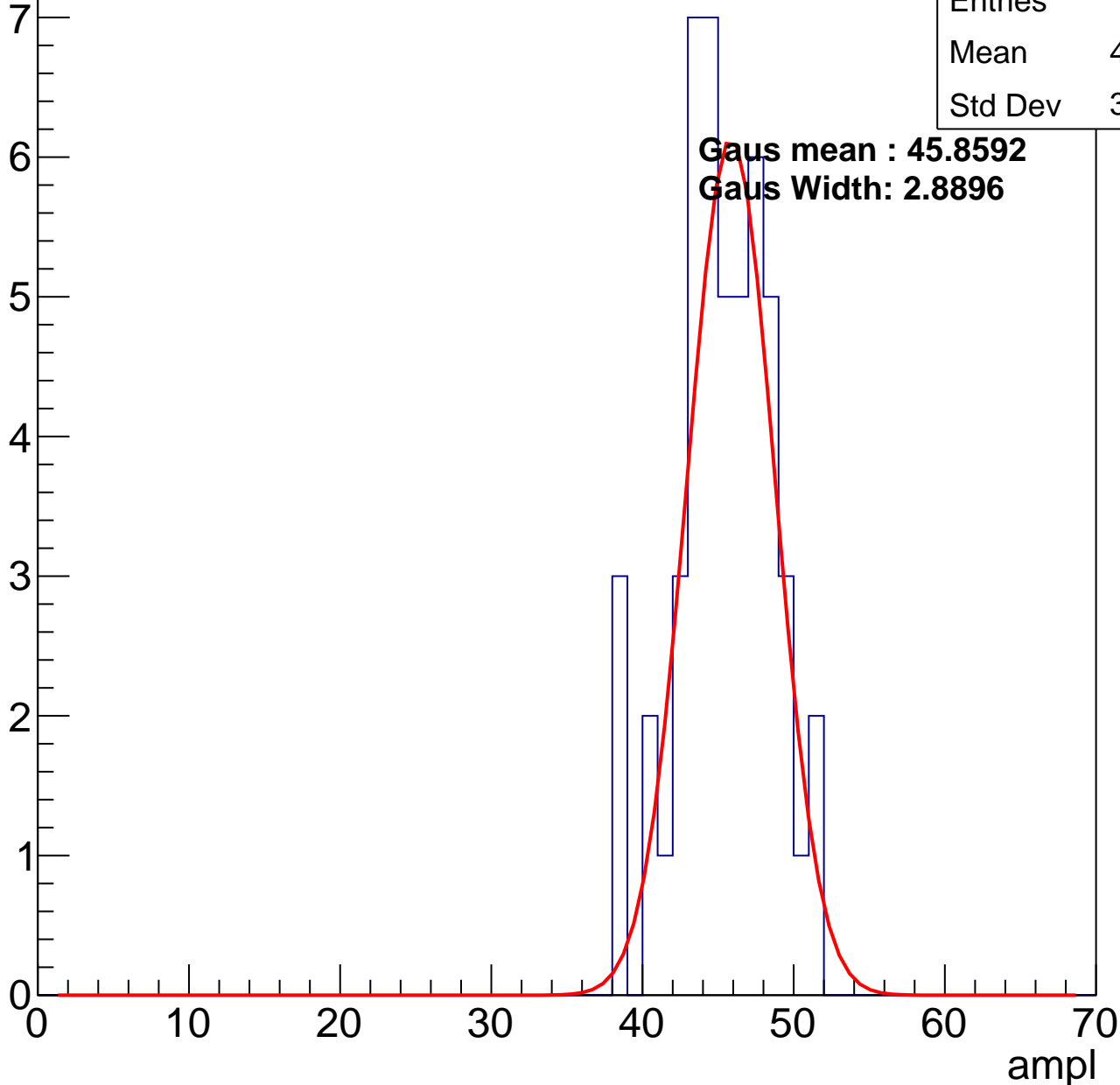
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	44.92
Std Dev	3.142

Gaus mean : 45.8592

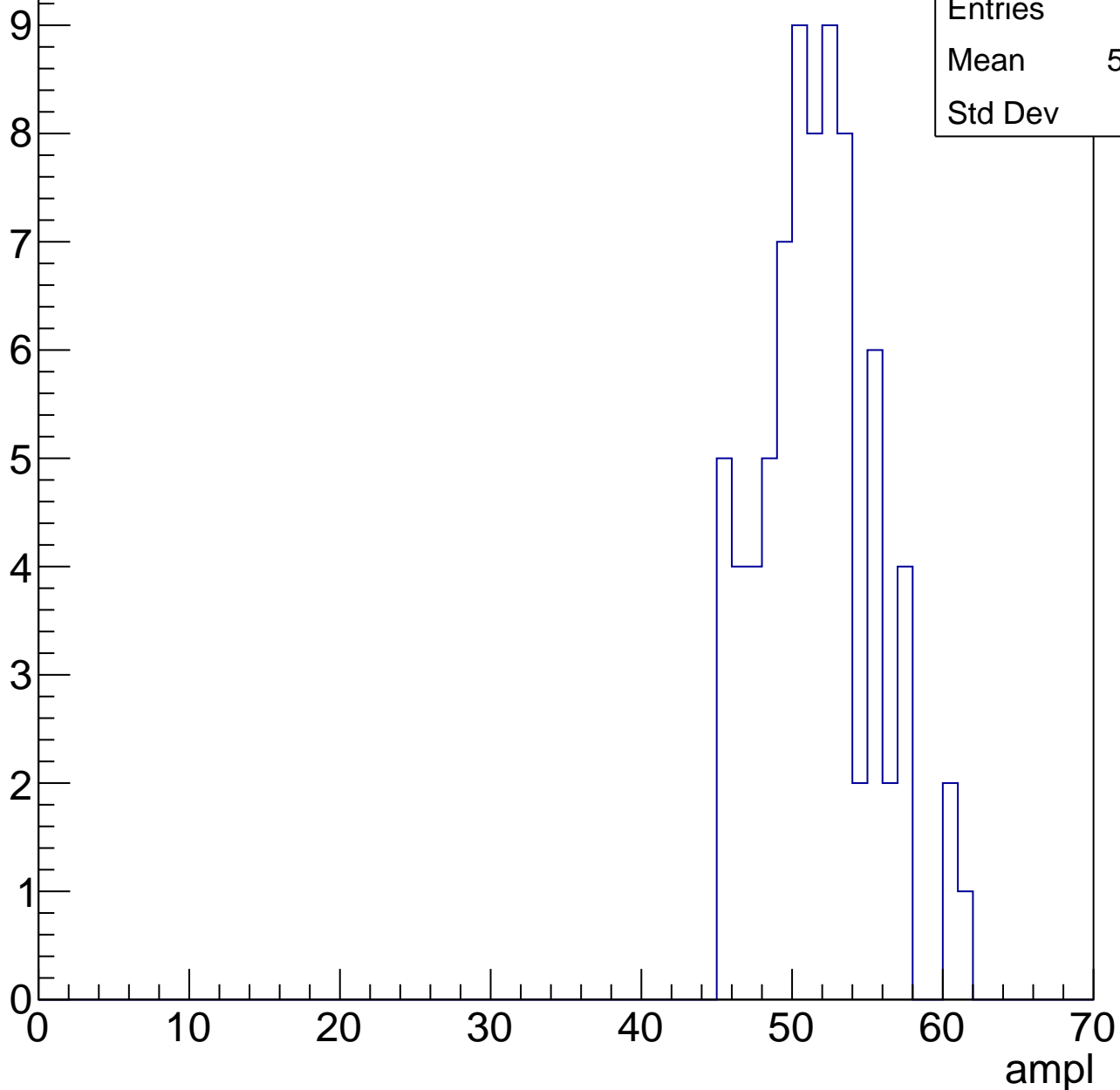
Gaus Width: 2.8896



B1L003S, U13-ch41, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

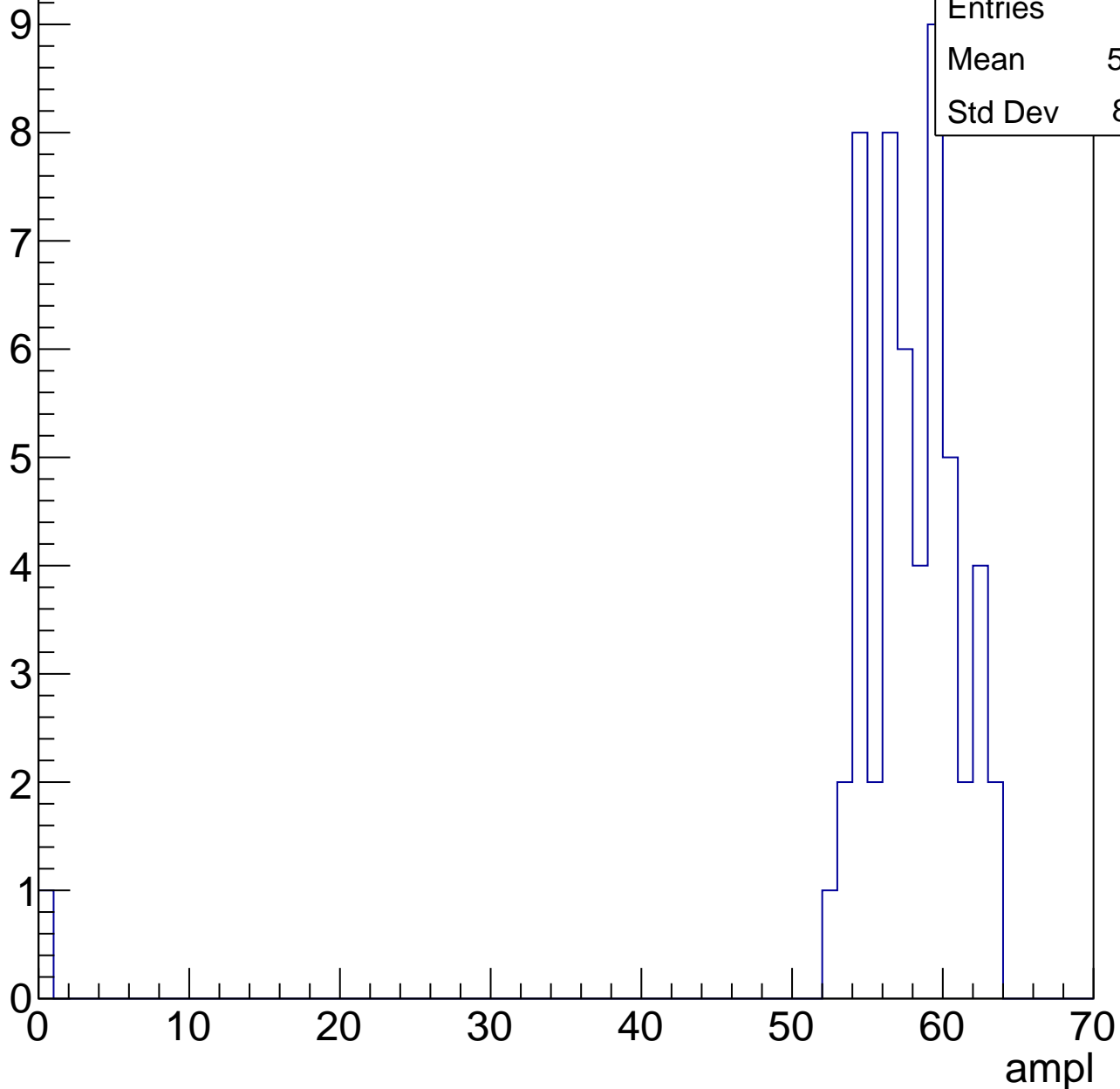


Entries	76
Mean	51.17
Std Dev	3.69

B1L003S, U13-ch41, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

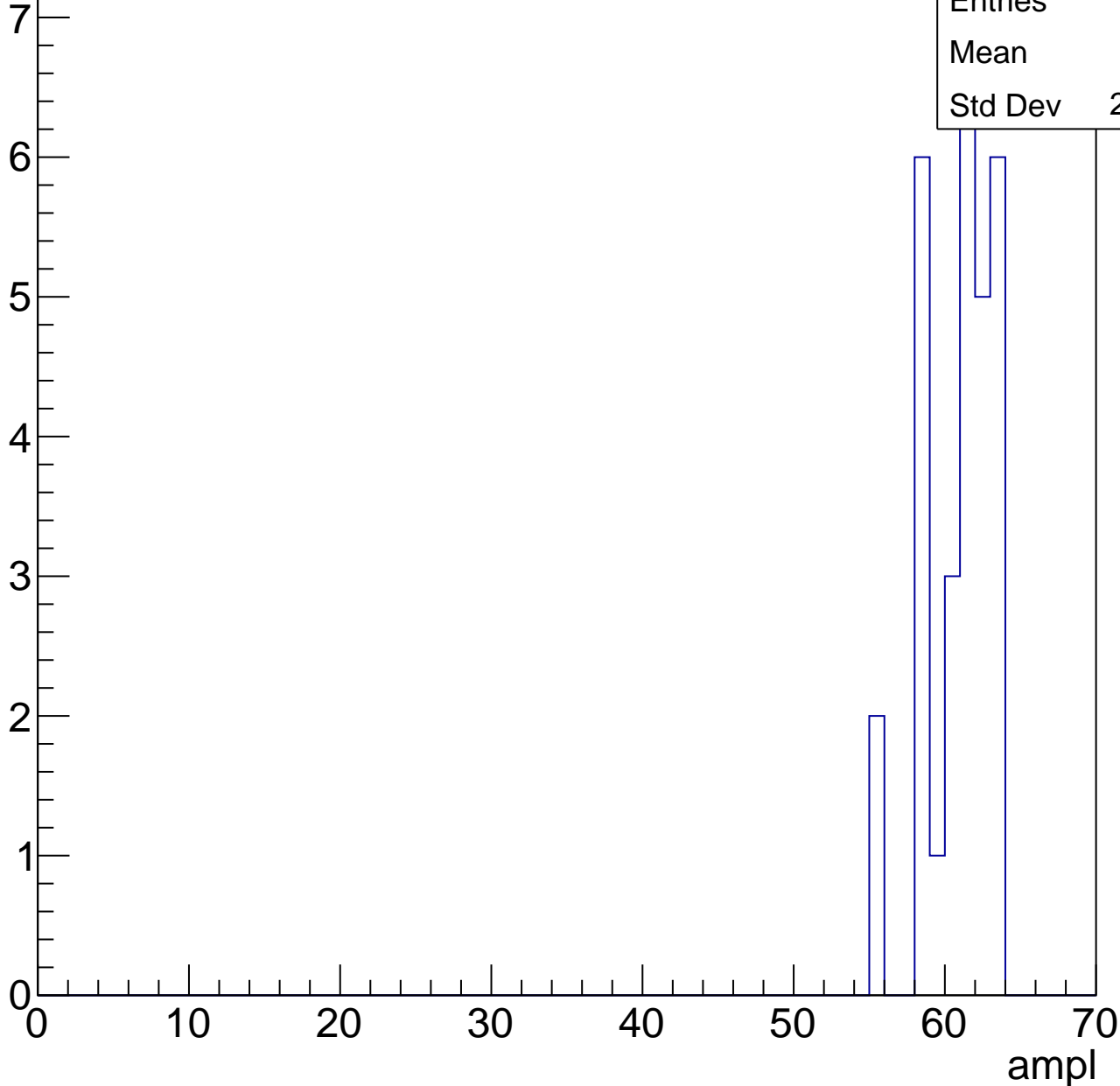


B1L003S, U13-ch41, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	30
Mean	60.4
Std Dev	2.245



B1L003S, U13-ch41, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L003S, U13-ch41, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch42, adc0

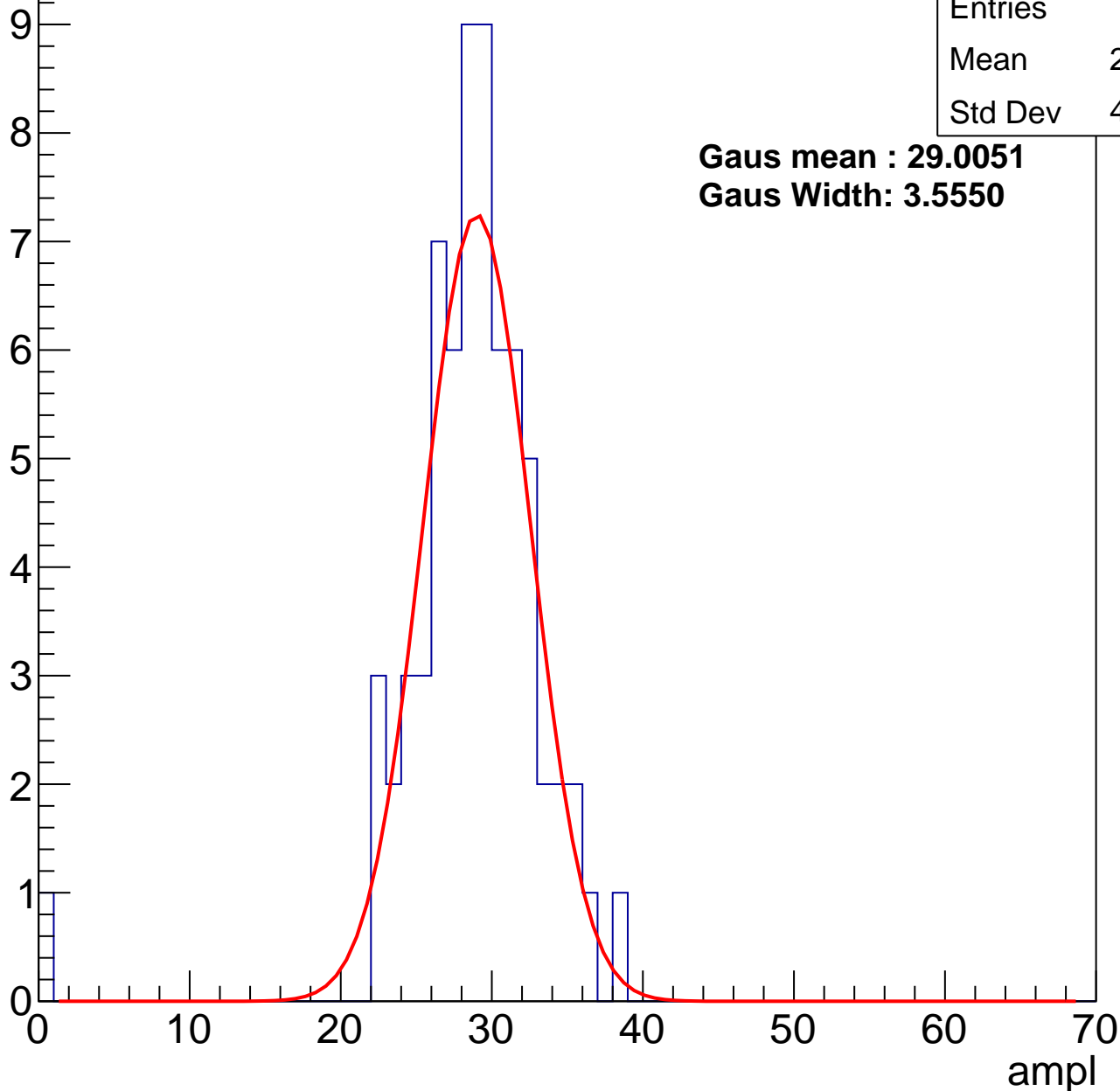
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.24
Std Dev	4.848

Gaus mean : 29.0051

Gaus Width: 3.5550



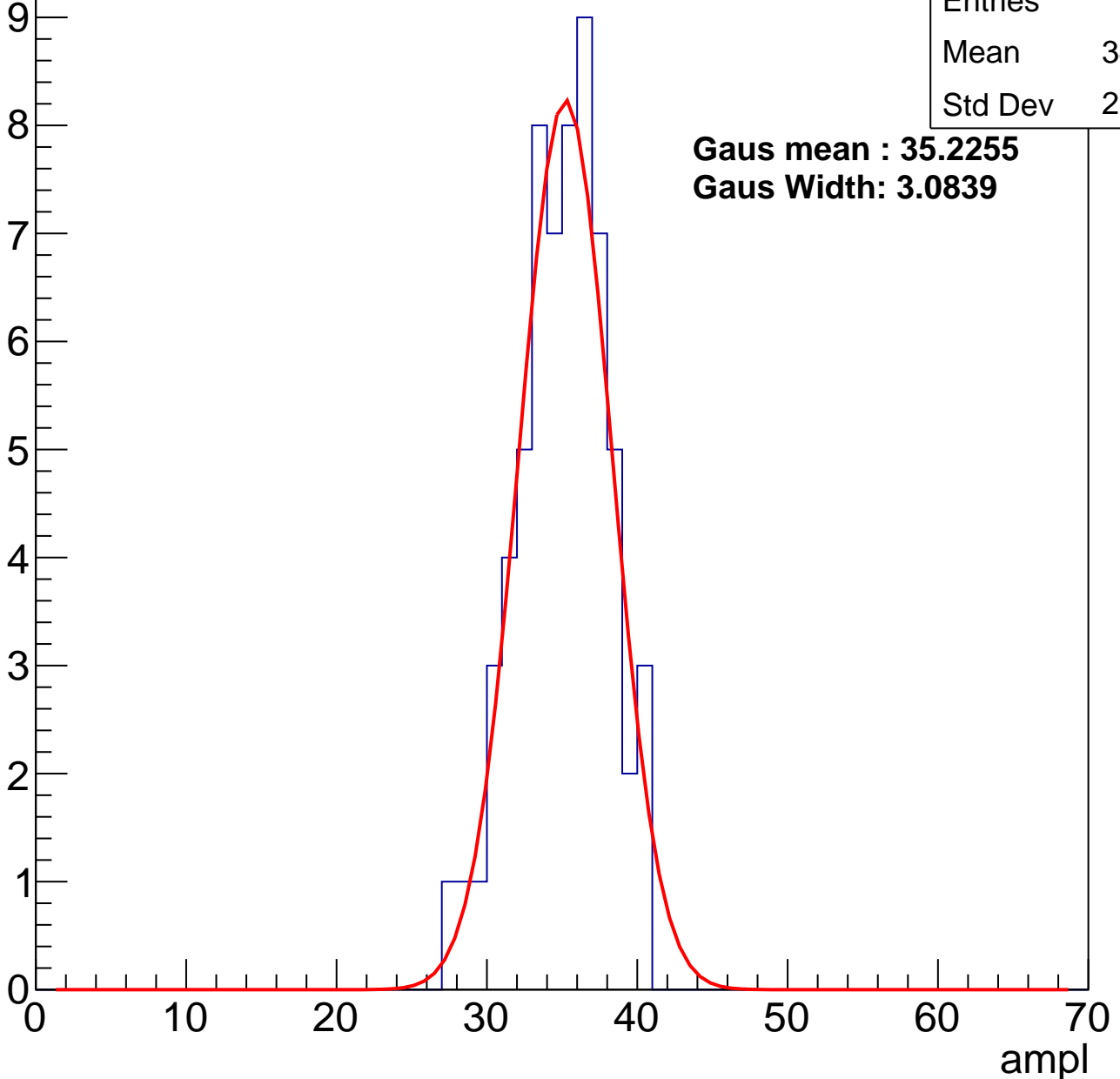
B1L003S, U13-ch42, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	34.55
Std Dev	2.926

Gaus mean : 35.2255
Gaus Width: 3.0839



B1L003S, U13-ch42, adc2

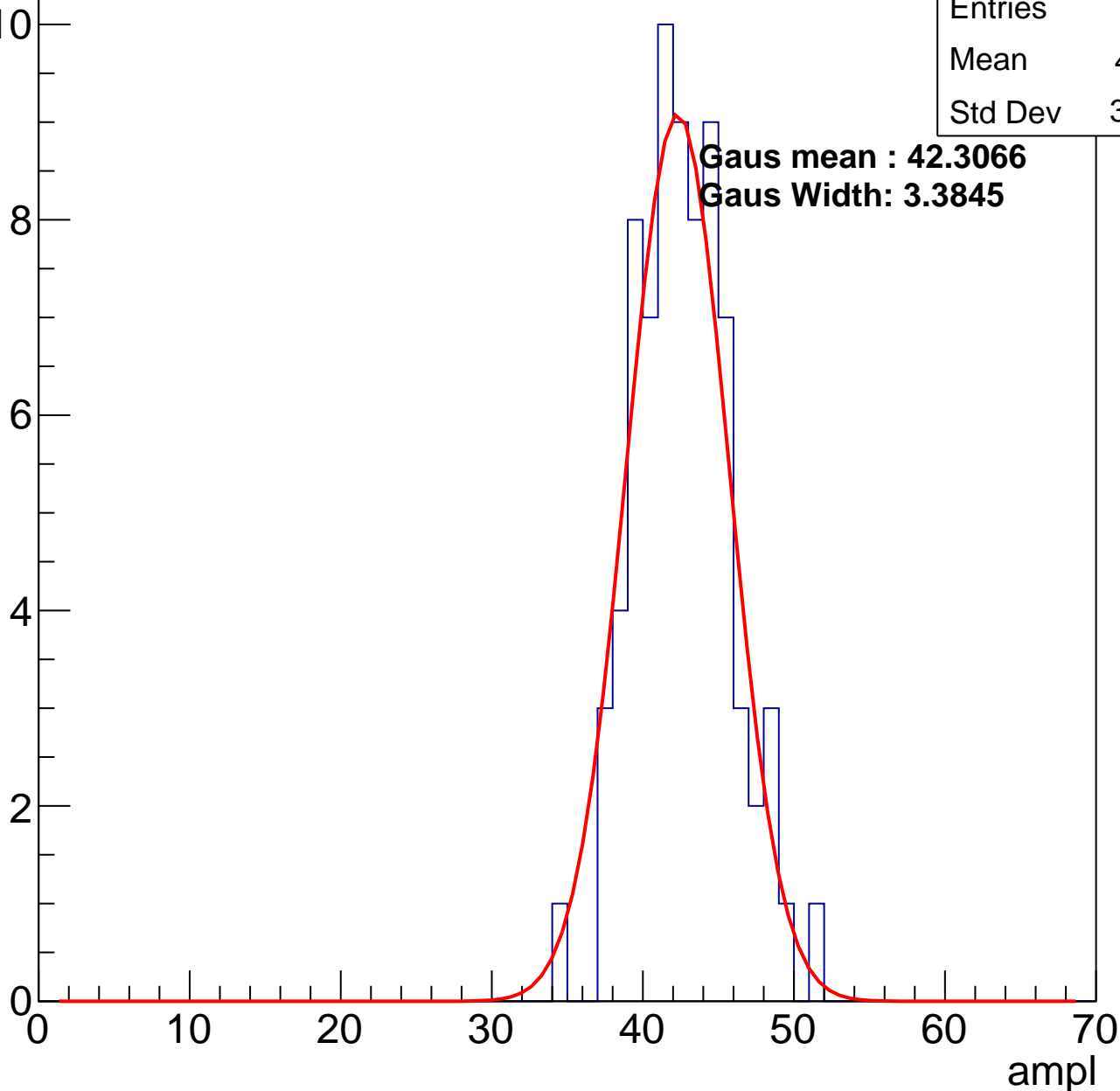
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.21
Std Dev	3.147

Gaus mean : 42.3066

Gaus Width: 3.3845

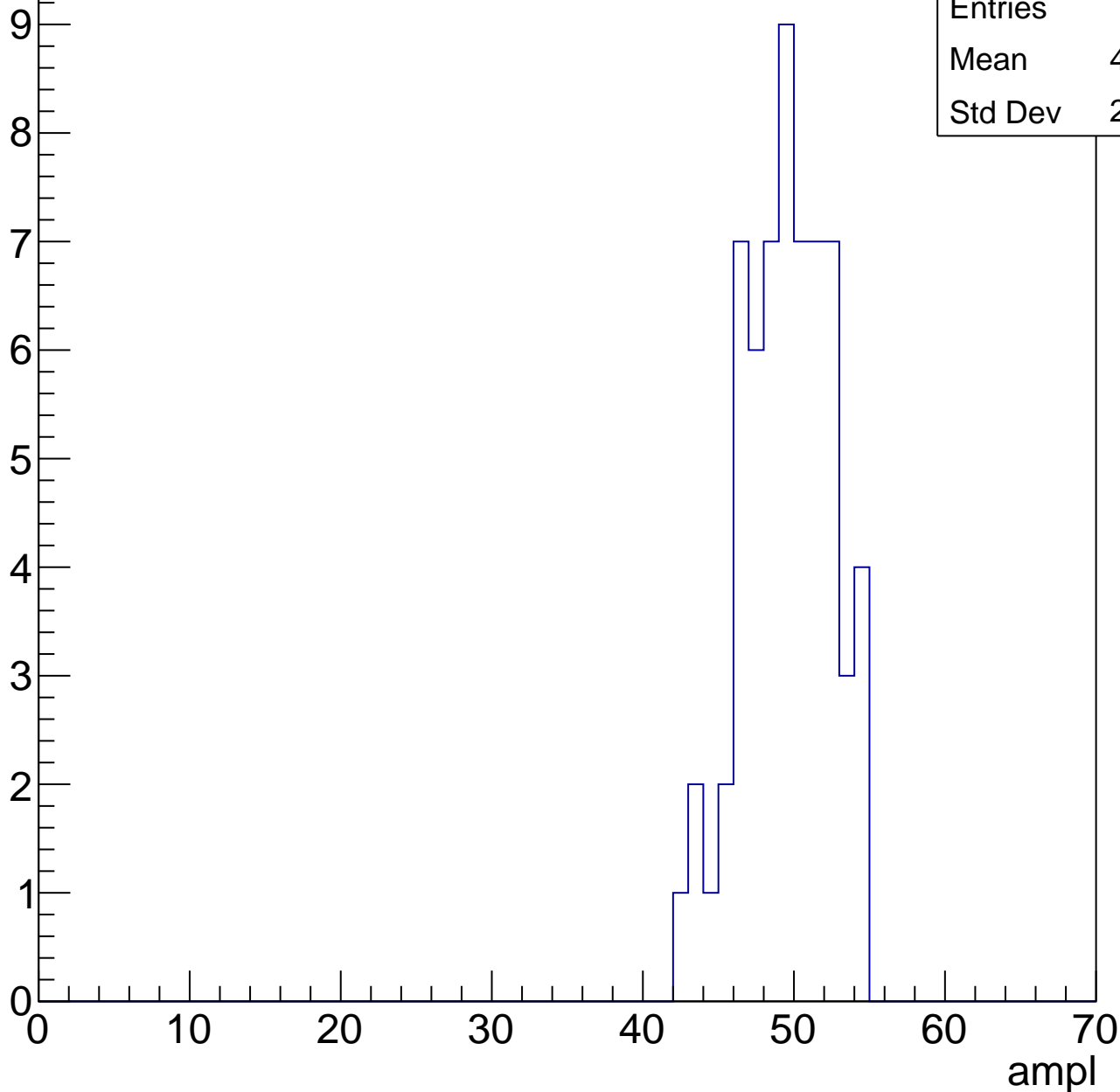


B1L003S, U13-ch42, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.03
Std Dev	2.867

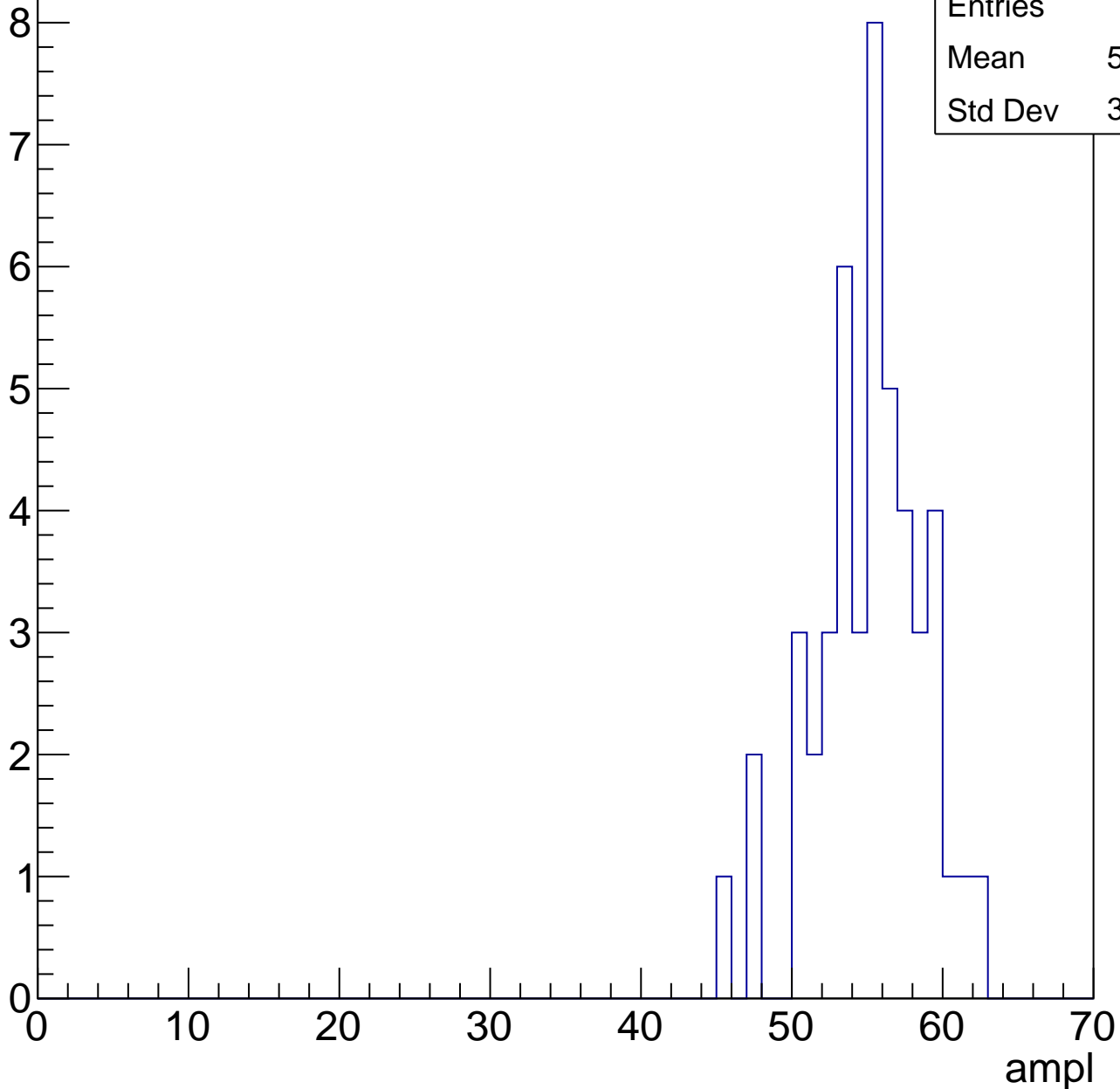


B1L003S, U13-ch42, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	54.64
Std Dev	3.587

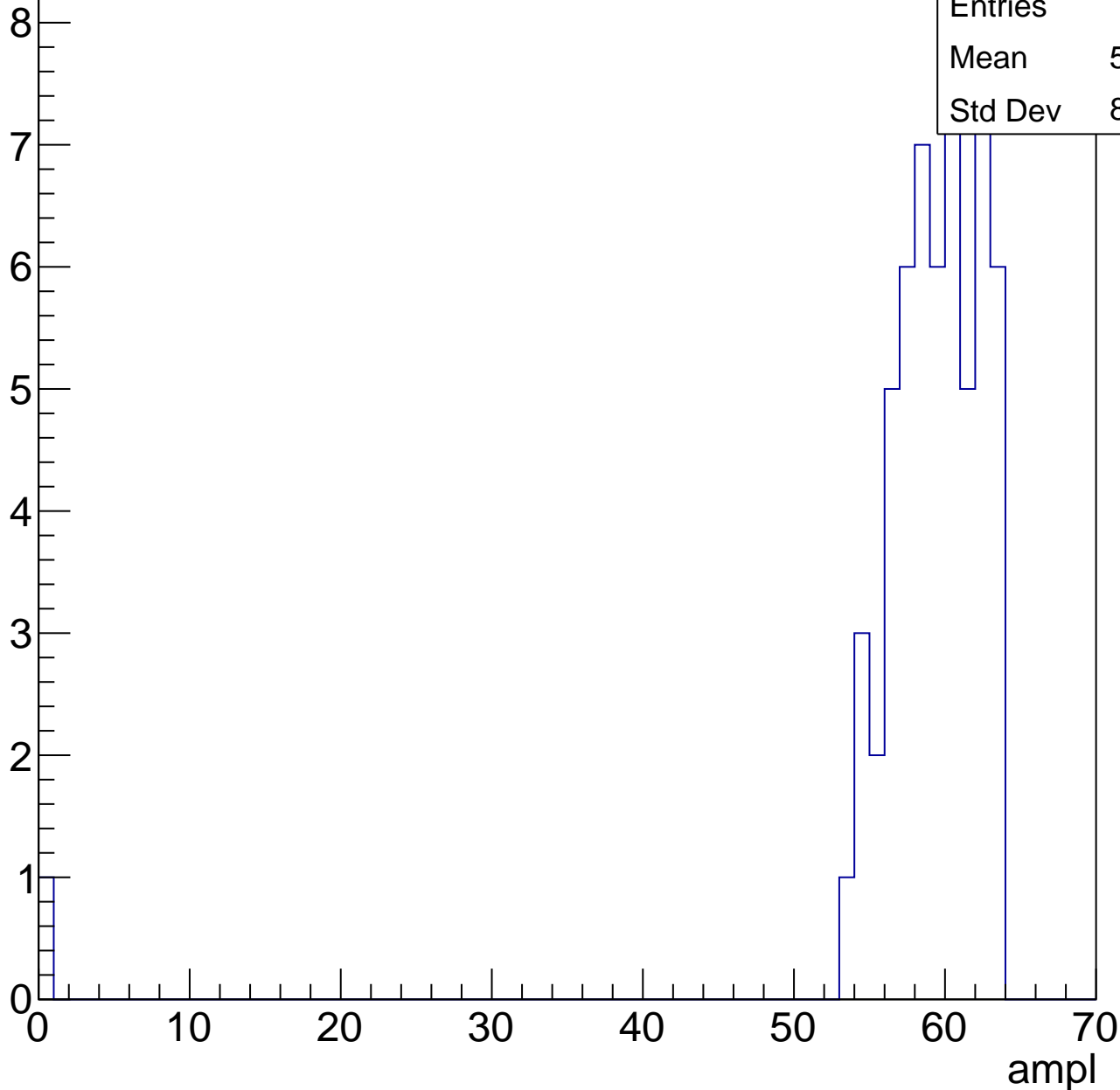


B1L003S, U13-ch42, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

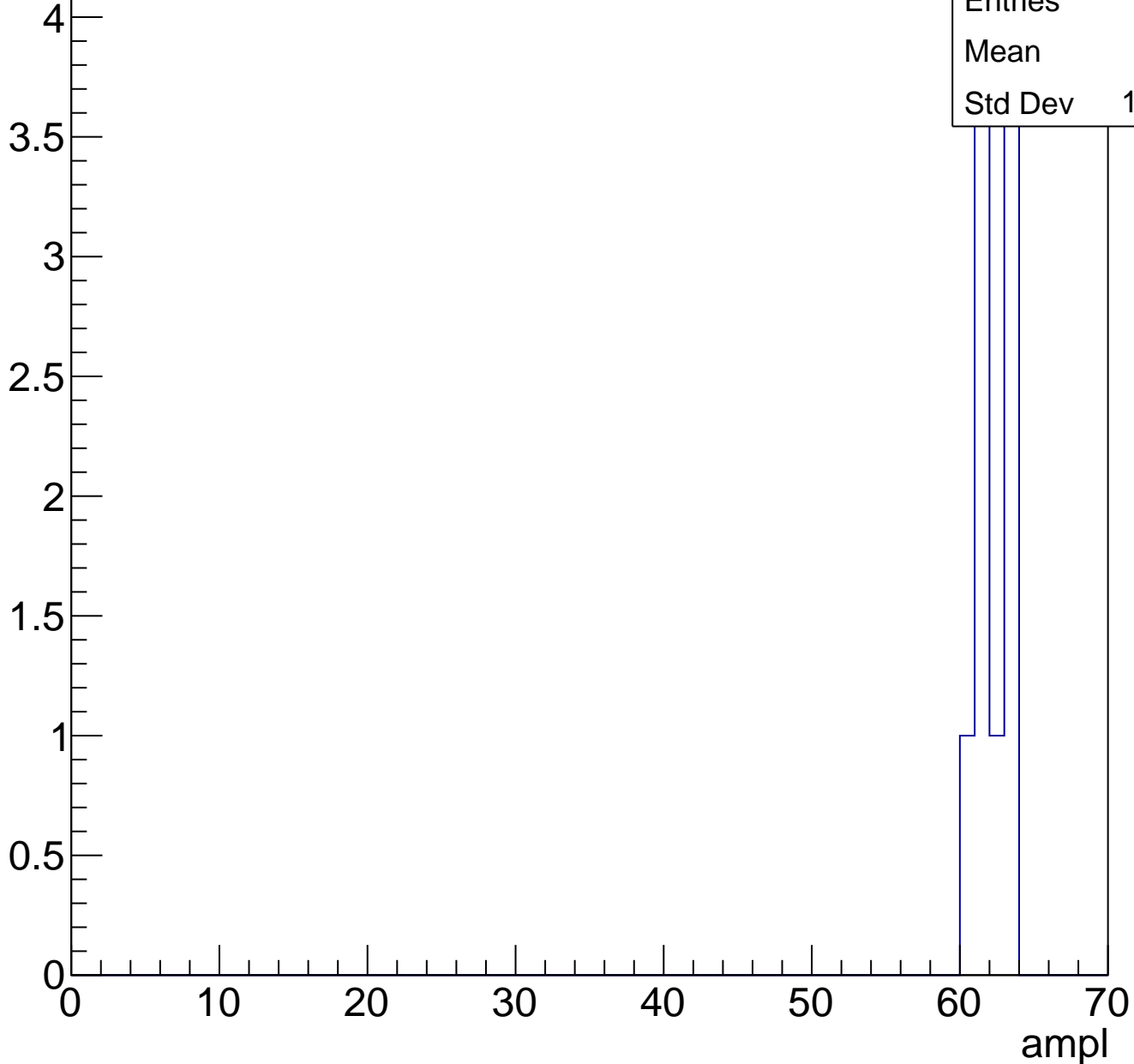
Entries	58
Mean	58.03
Std Dev	8.139



B1L003S, U13-ch42, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch42, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch43, adc0

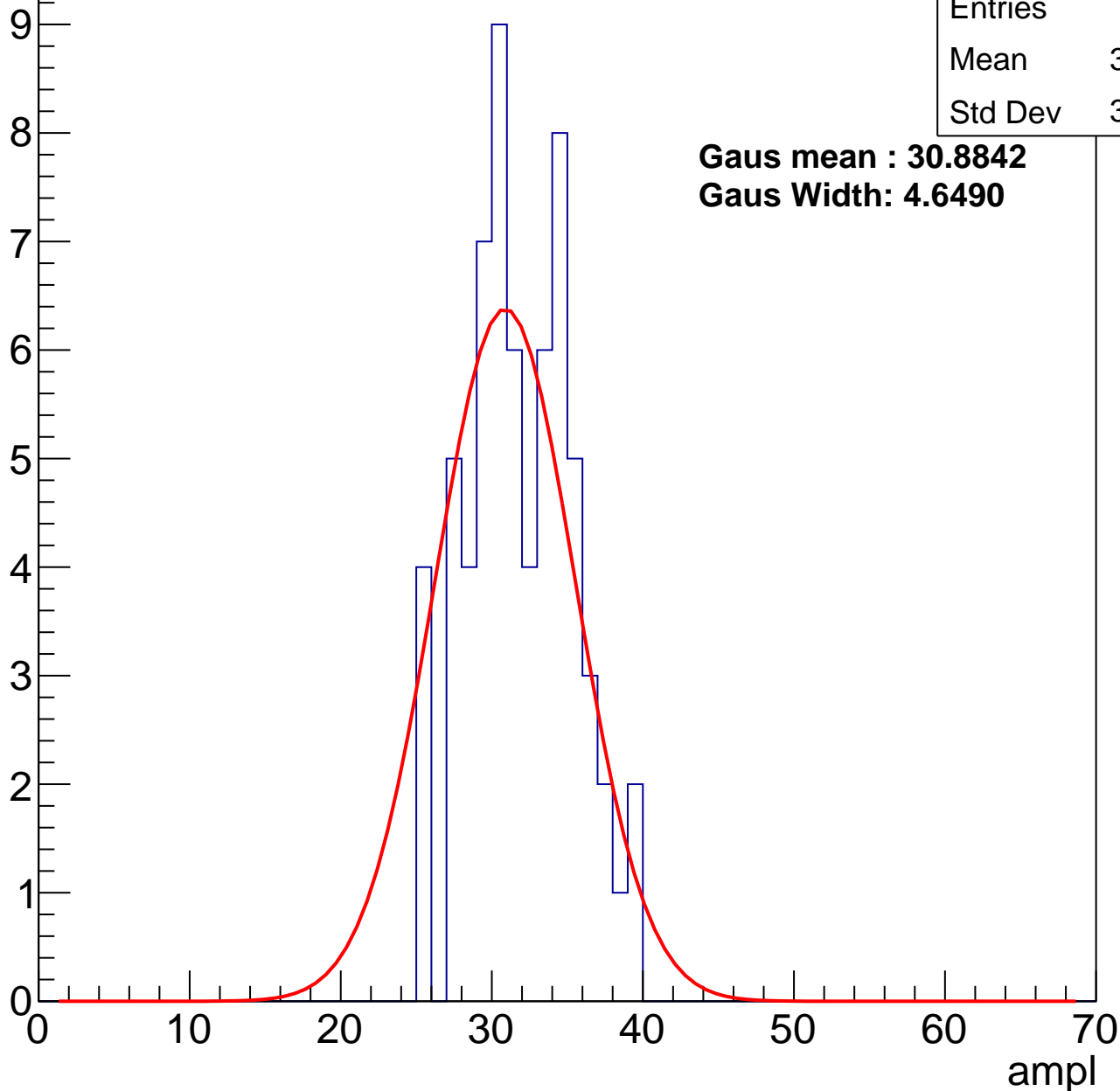
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	31.47
Std Dev	3.452

Gaus mean : 30.8842

Gaus Width: 4.6490



B1L003S, U13-ch43, adc1

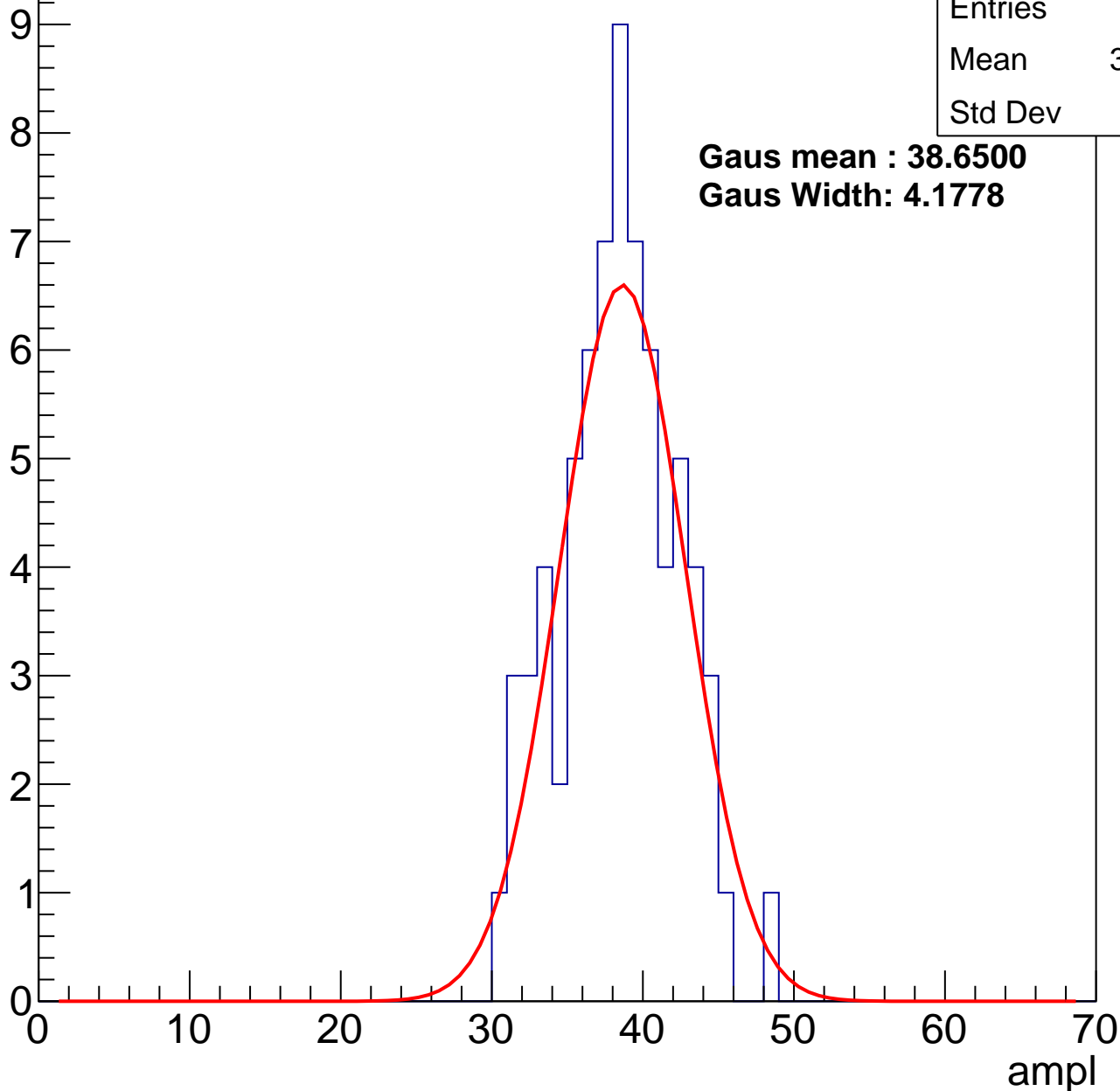
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	37.96
Std Dev	3.81

Gaus mean : 38.6500

Gaus Width: 4.1778



B1L003S, U13-ch43, adc2

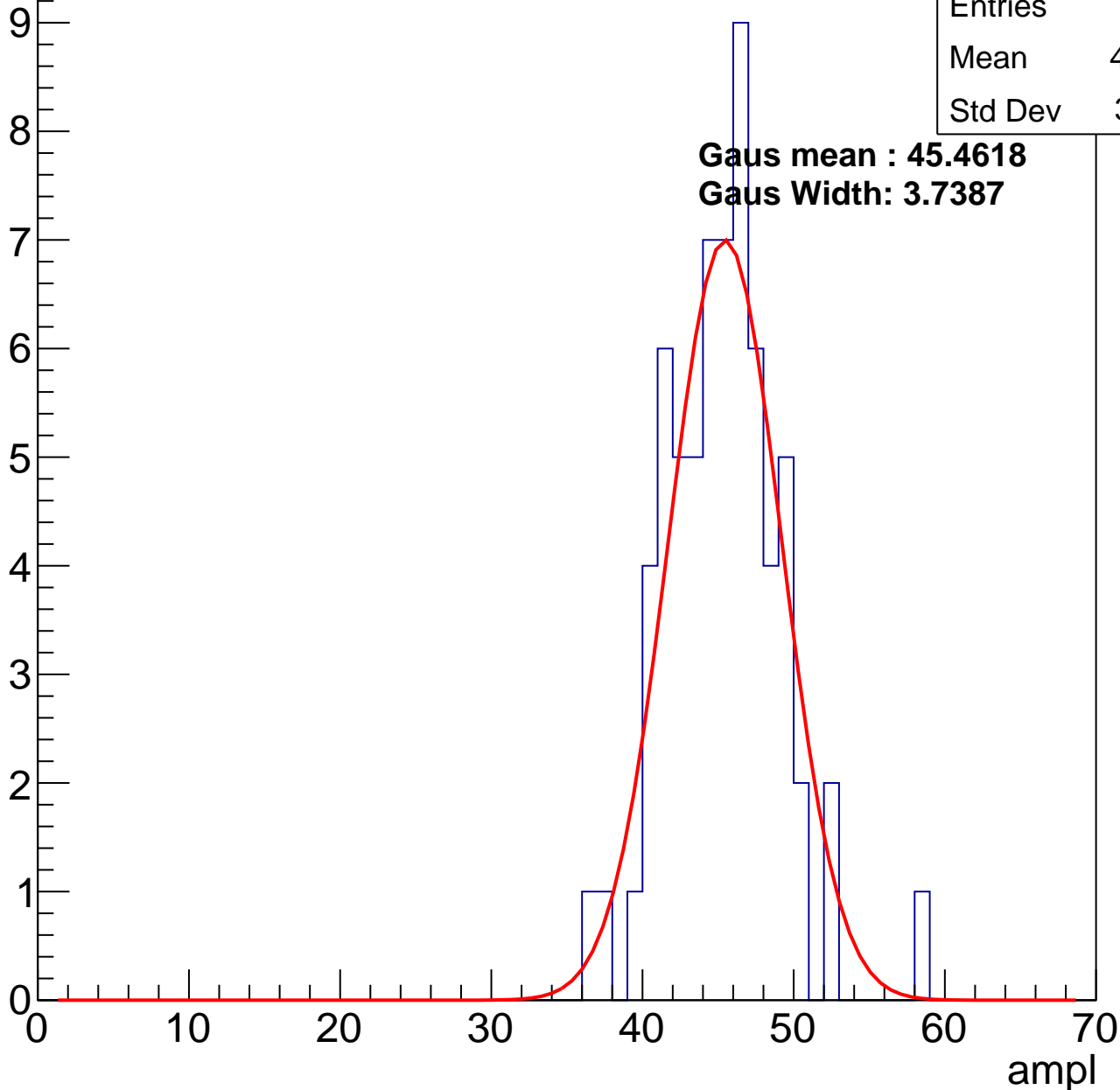
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.86
Std Dev	3.721

Gaus mean : 45.4618

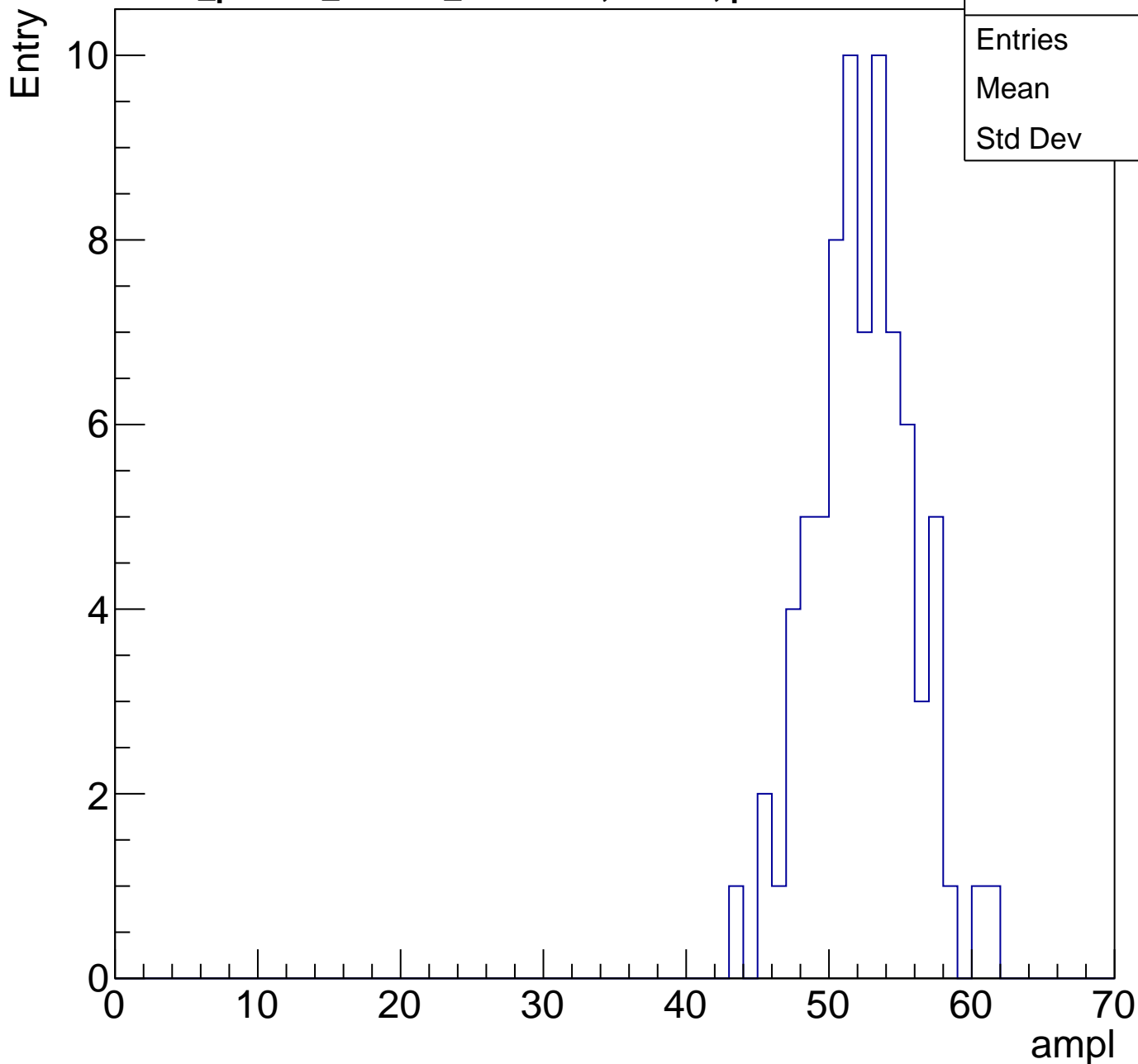
Gaus Width: 3.7387



B1L003S, U13-ch43, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	51.9
Std Dev	3.47

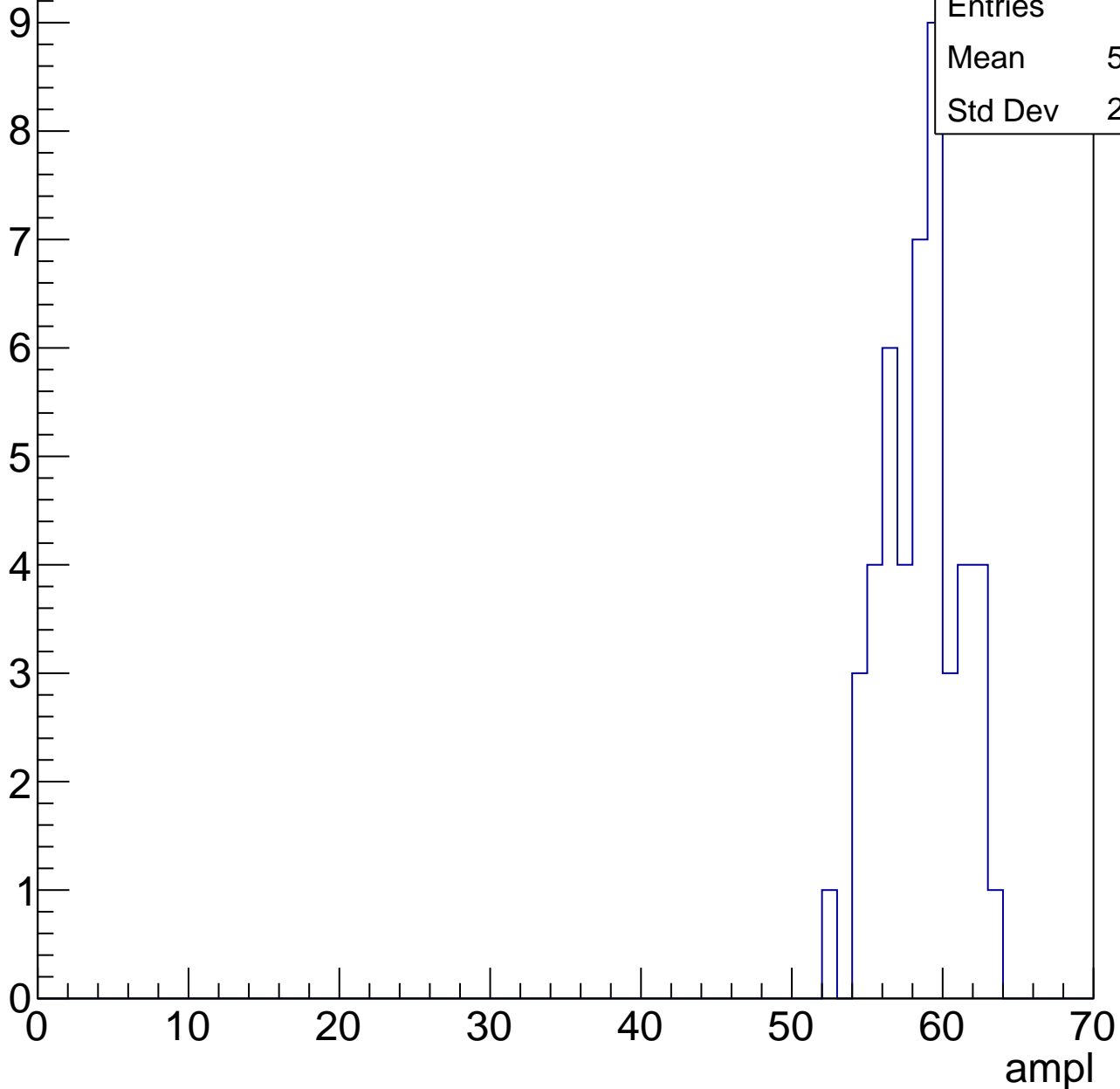


B1L003S, U13-ch43, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

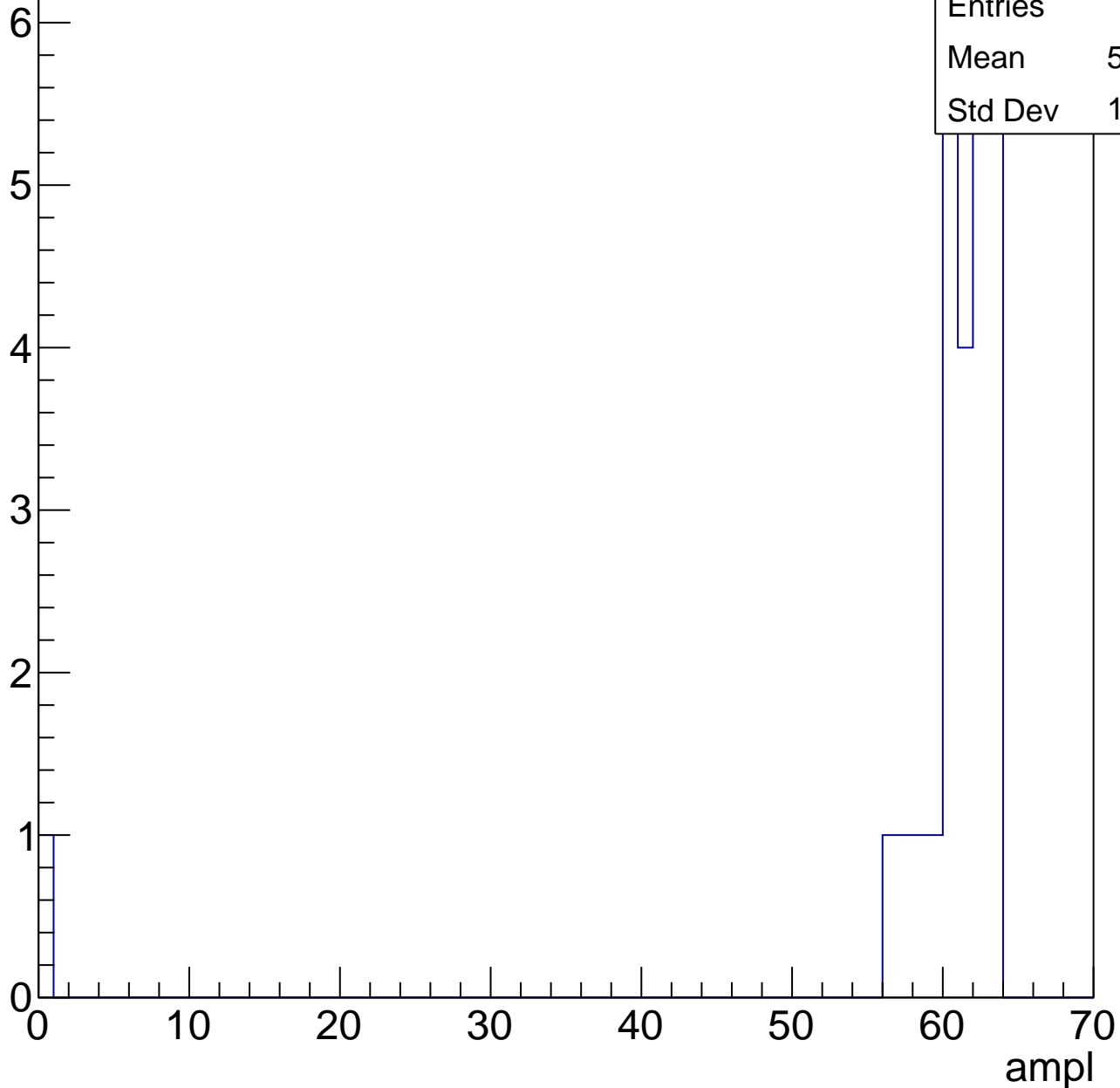
Entries	46
Mean	58.04
Std Dev	2.528



B1L003S, U13-ch43, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	27
Mean	58.67
Std Dev	11.65

B1L003S, U13-ch43, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch43, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch44, adc0

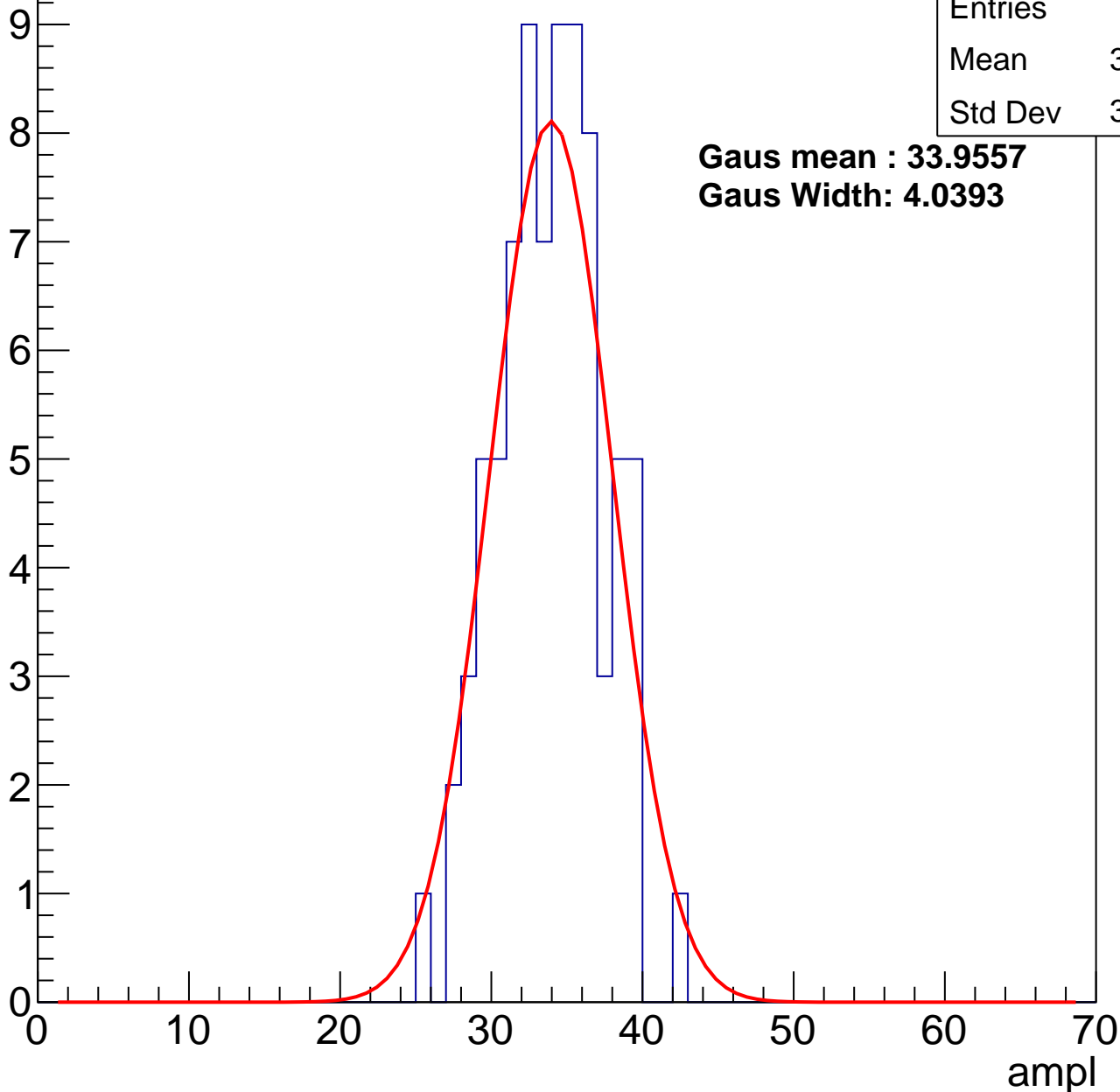
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	33.43
Std Dev	3.393

Gaus mean : 33.9557

Gaus Width: 4.0393



B1L003S, U13-ch44, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	58
Mean	40.28
Std Dev	2.558

Gaus mean : 40.8533

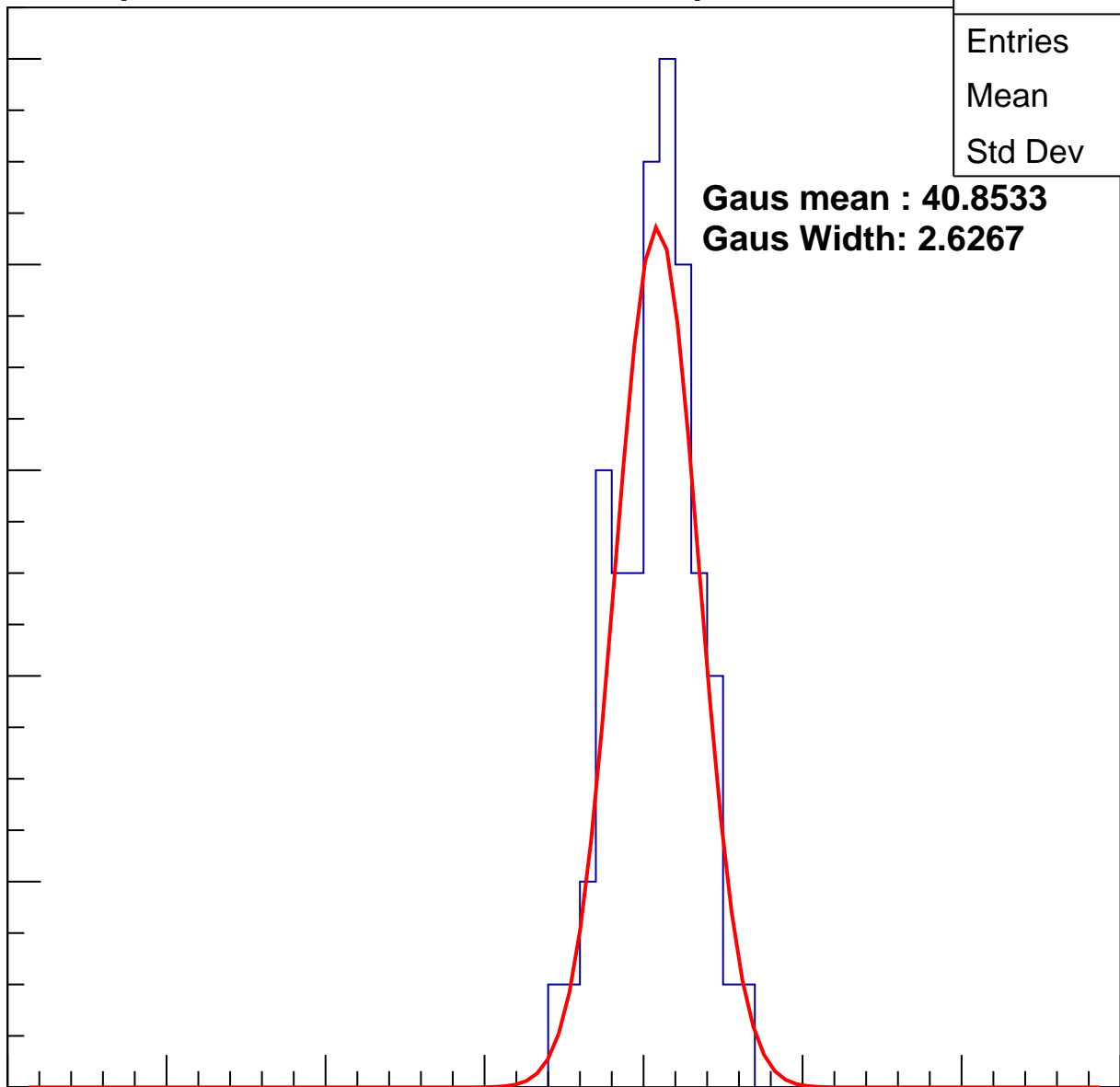
Gaus Width: 2.6267

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch44, adc2

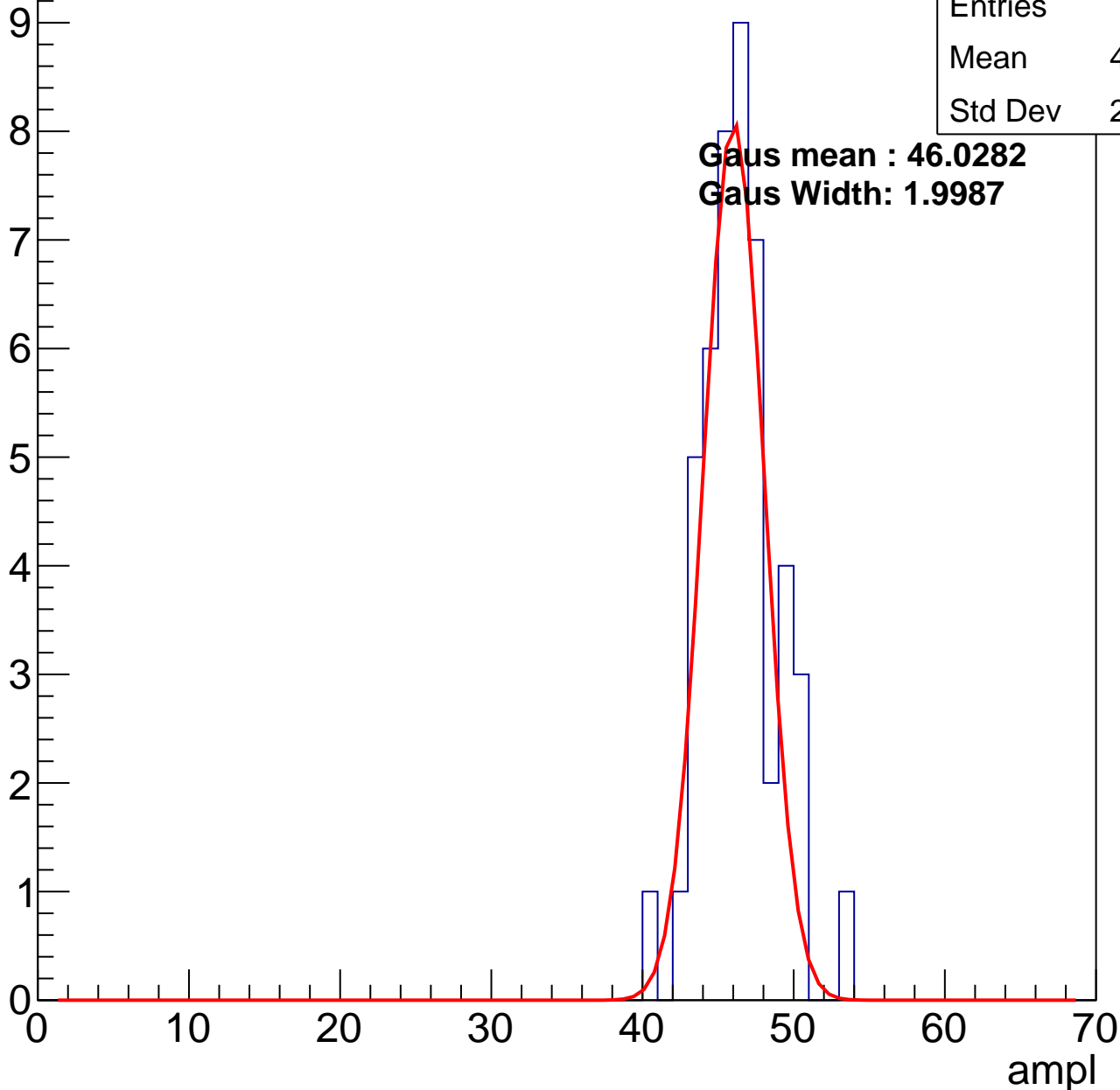
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	45.94
Std Dev	2.427

Gaus mean : 46.0282

Gaus Width: 1.9987

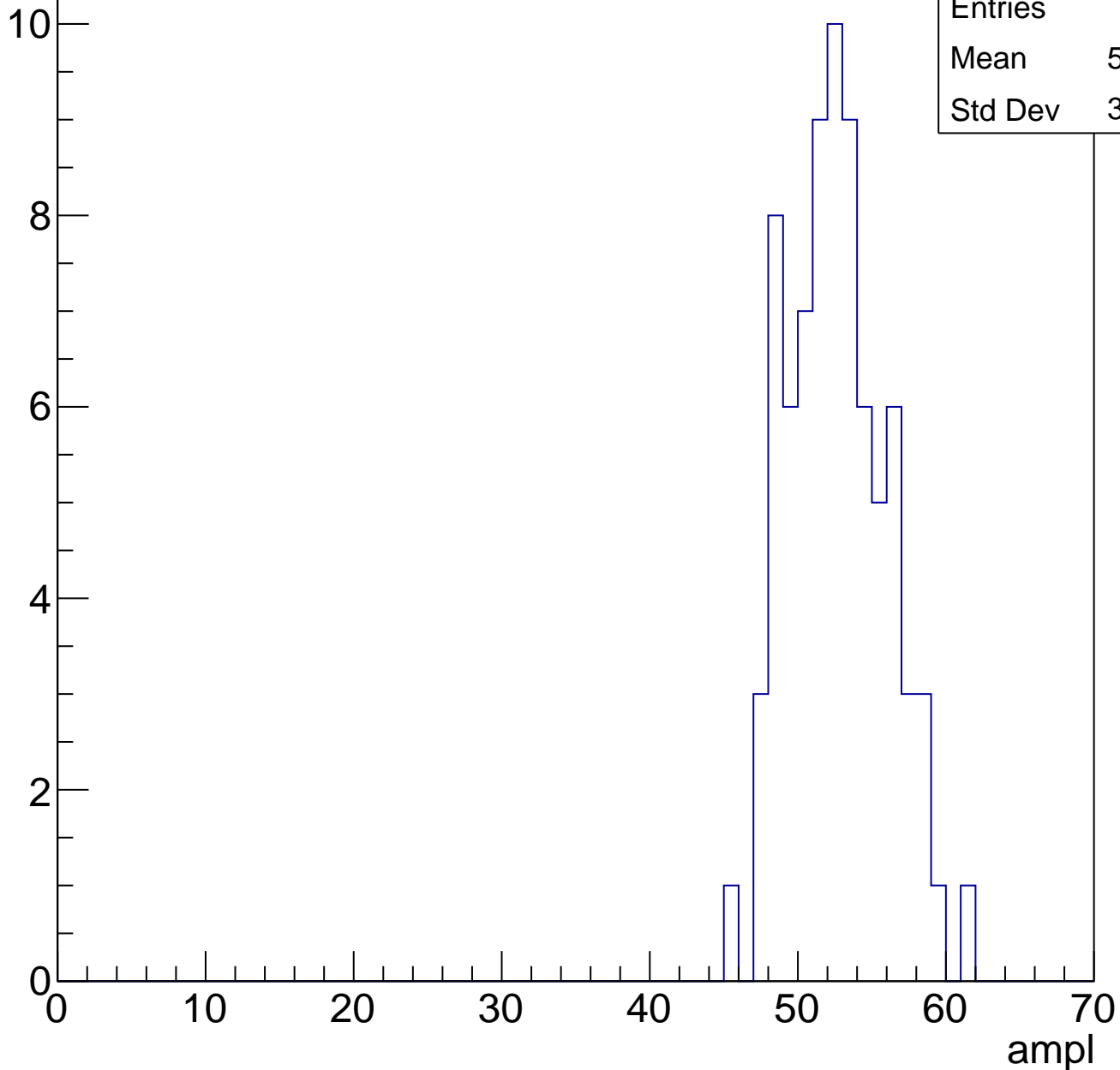


B1L003S, U13-ch44, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	52.18
Std Dev	3.257

Entry



B1L003S, U13-ch44, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	50
Mean	58.3
Std Dev	2.78

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

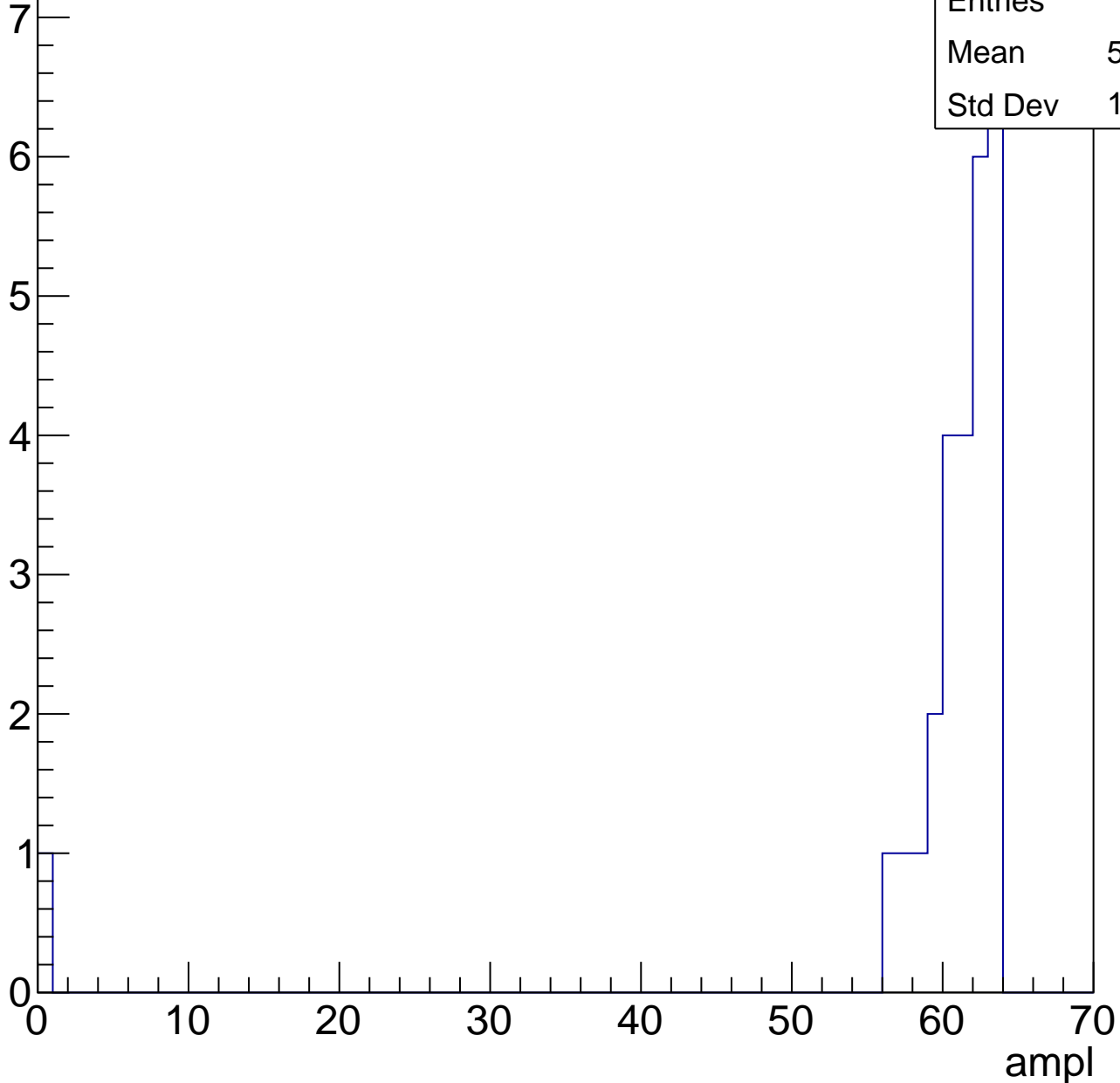
ampl

B1L003S, U13-ch44, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	27
Mean	58.74
Std Dev	11.67



B1L003S, U13-ch44, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch44, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch45, adc0

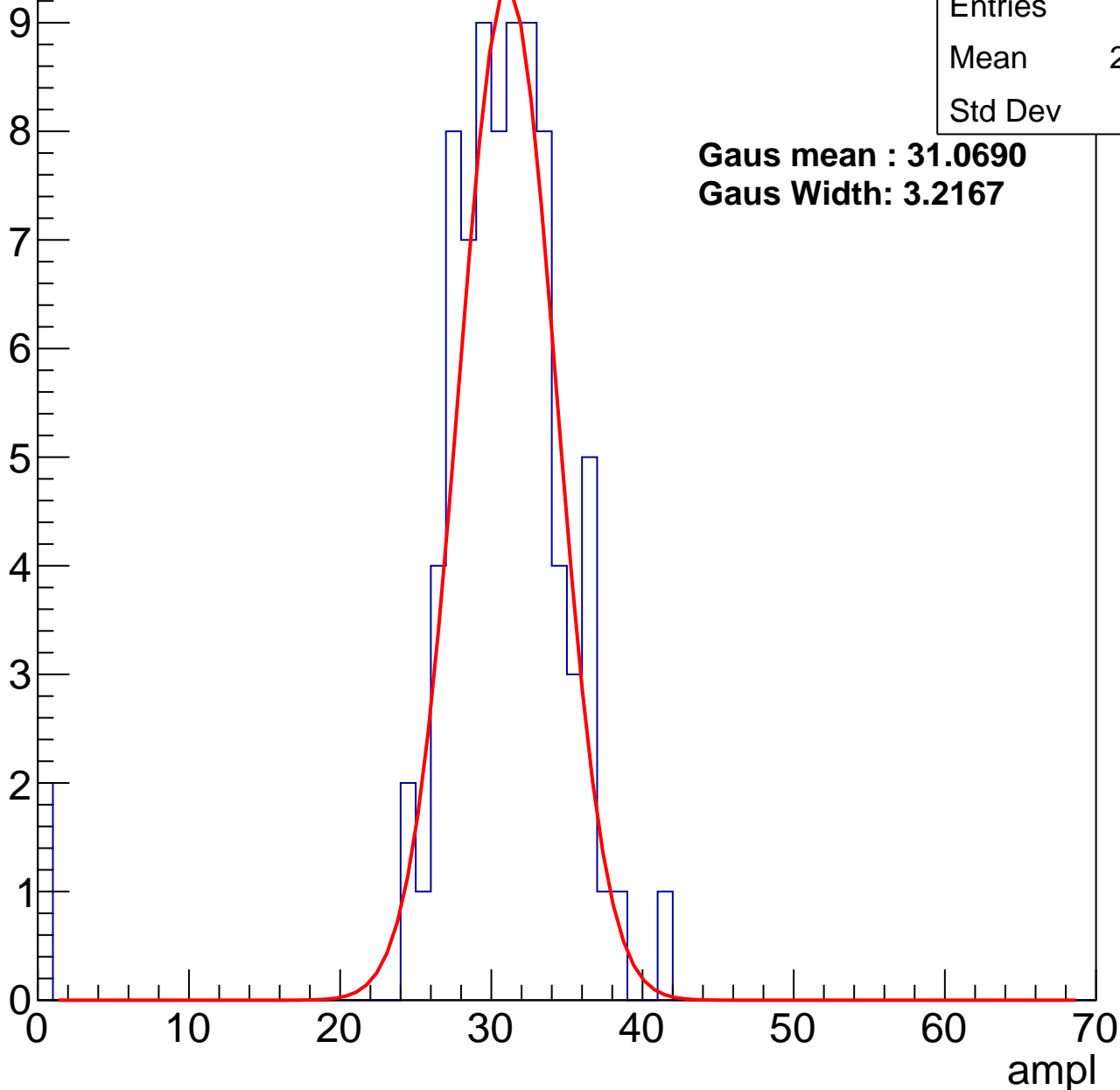
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	29.98
Std Dev	5.78

Gaus mean : 31.0690

Gaus Width: 3.2167



B1L003S, U13-ch45, adc1

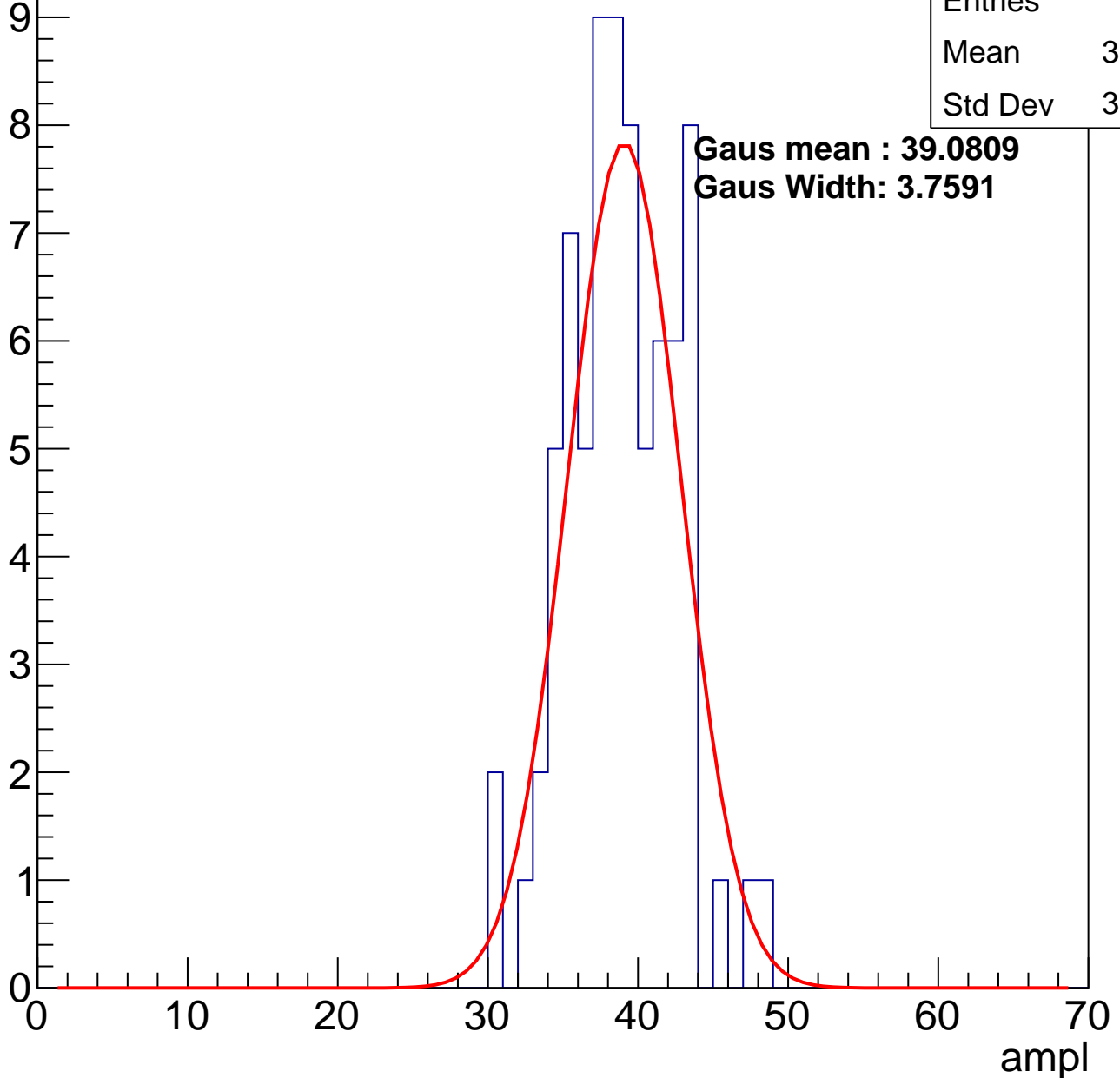
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	38.45
Std Dev	3.589

Gaus mean : 39.0809

Gaus Width: 3.7591



B1L003S, U13-ch45, adc2

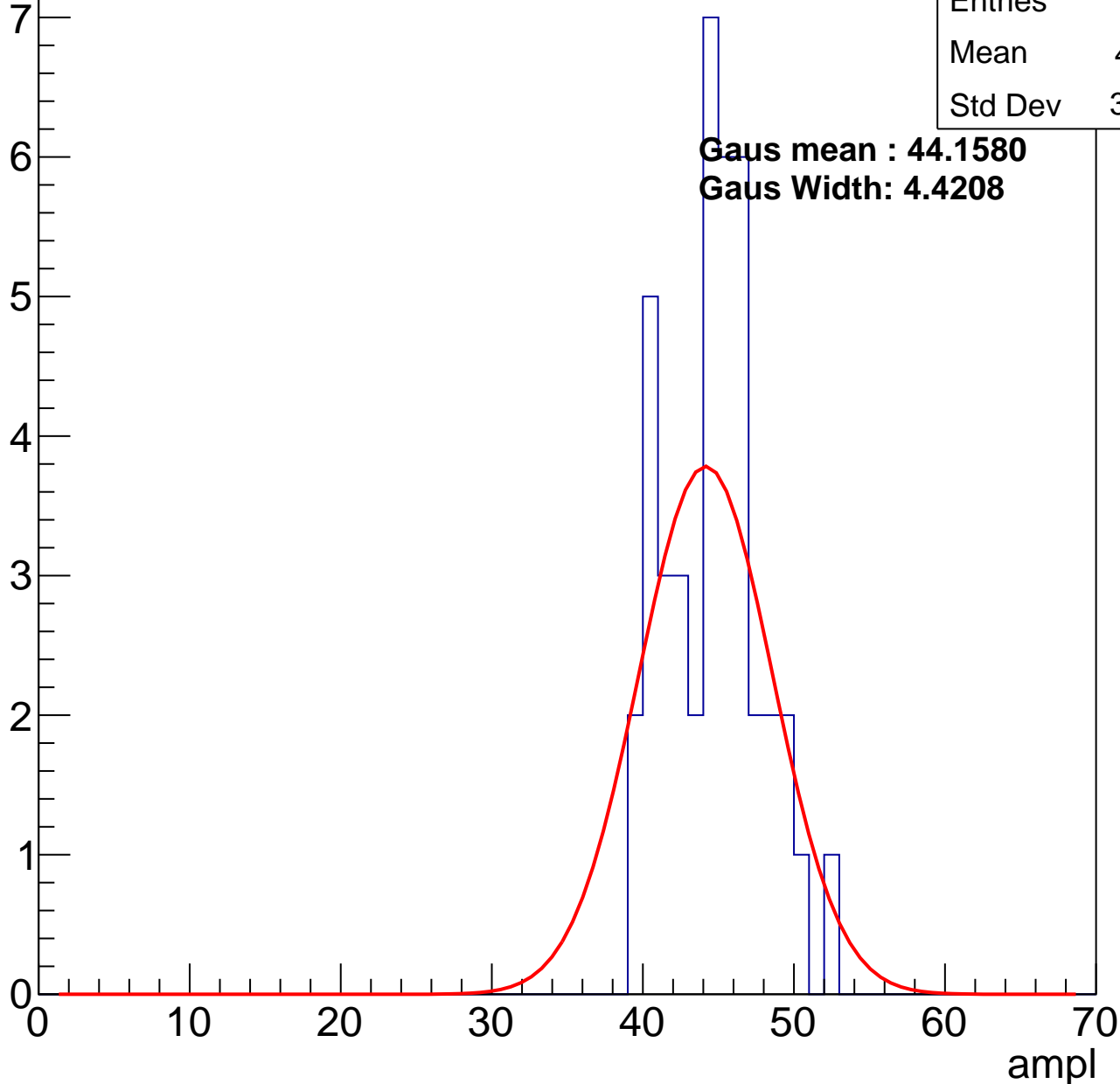
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	44.21
Std Dev	3.082

Gaus mean : 44.1580

Gaus Width: 4.4208

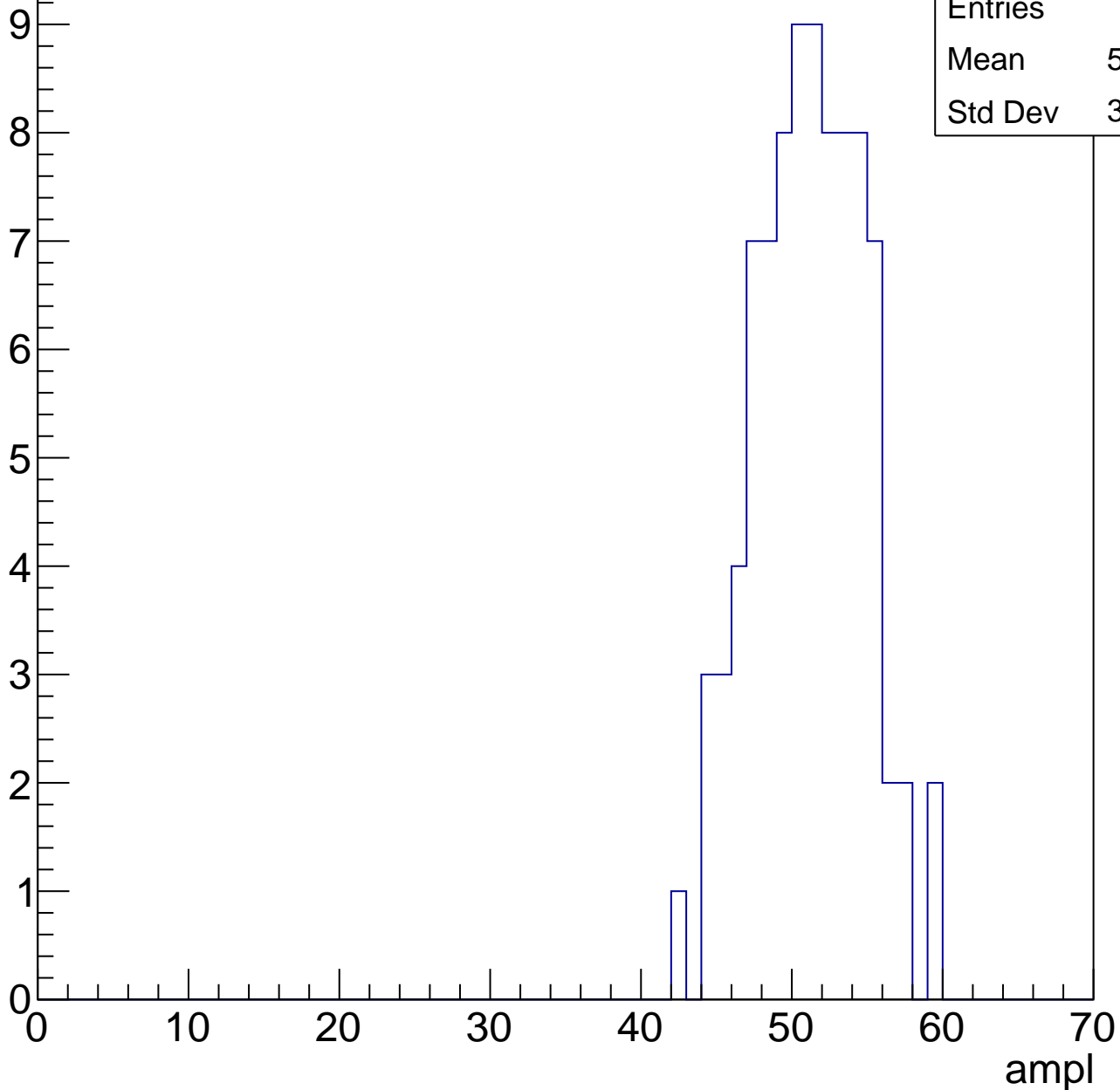


B1L003S, U13-ch45, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	88
Mean	50.68
Std Dev	3.563

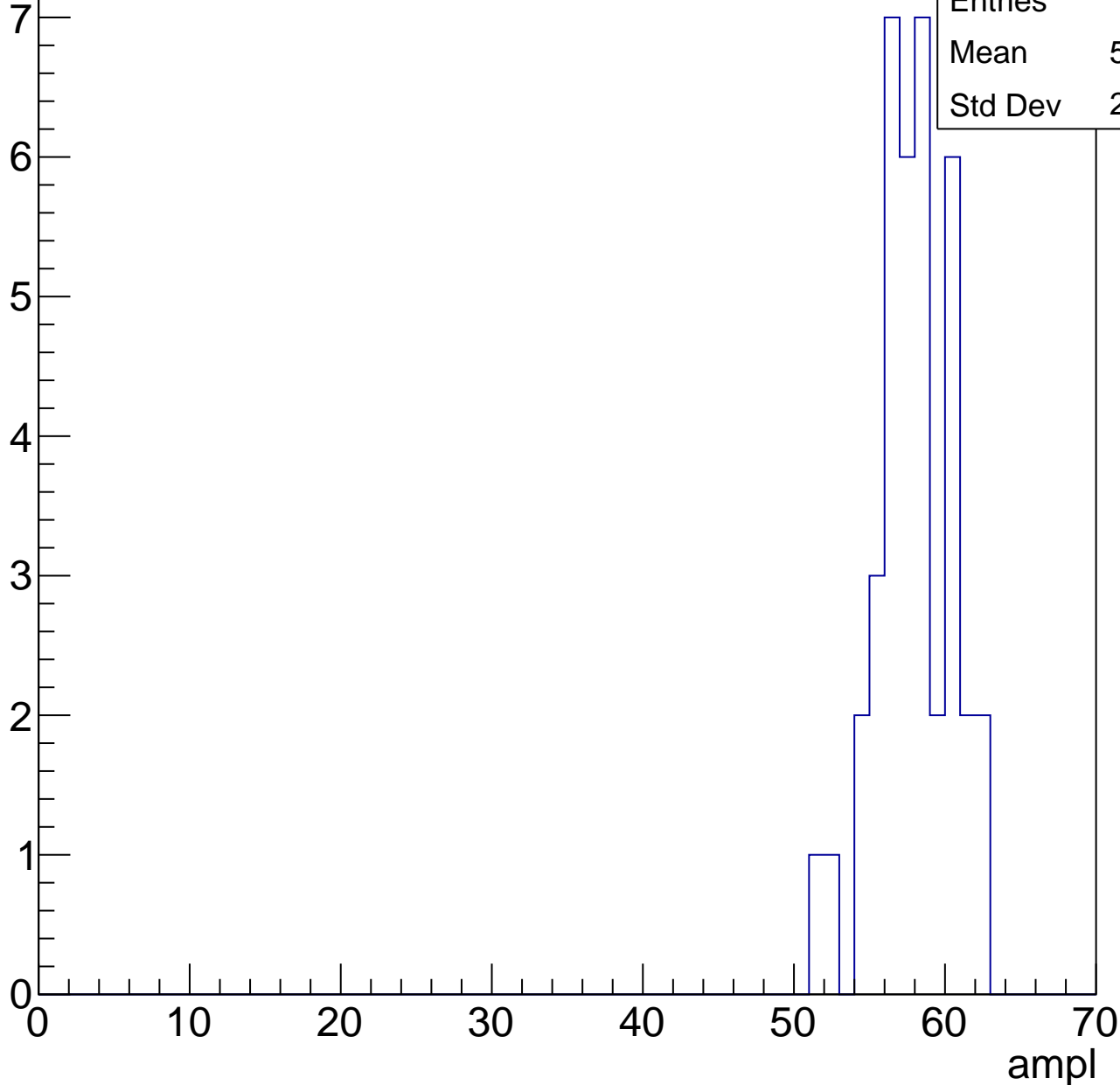


B1L003S, U13-ch45, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	57.44
Std Dev	2.489

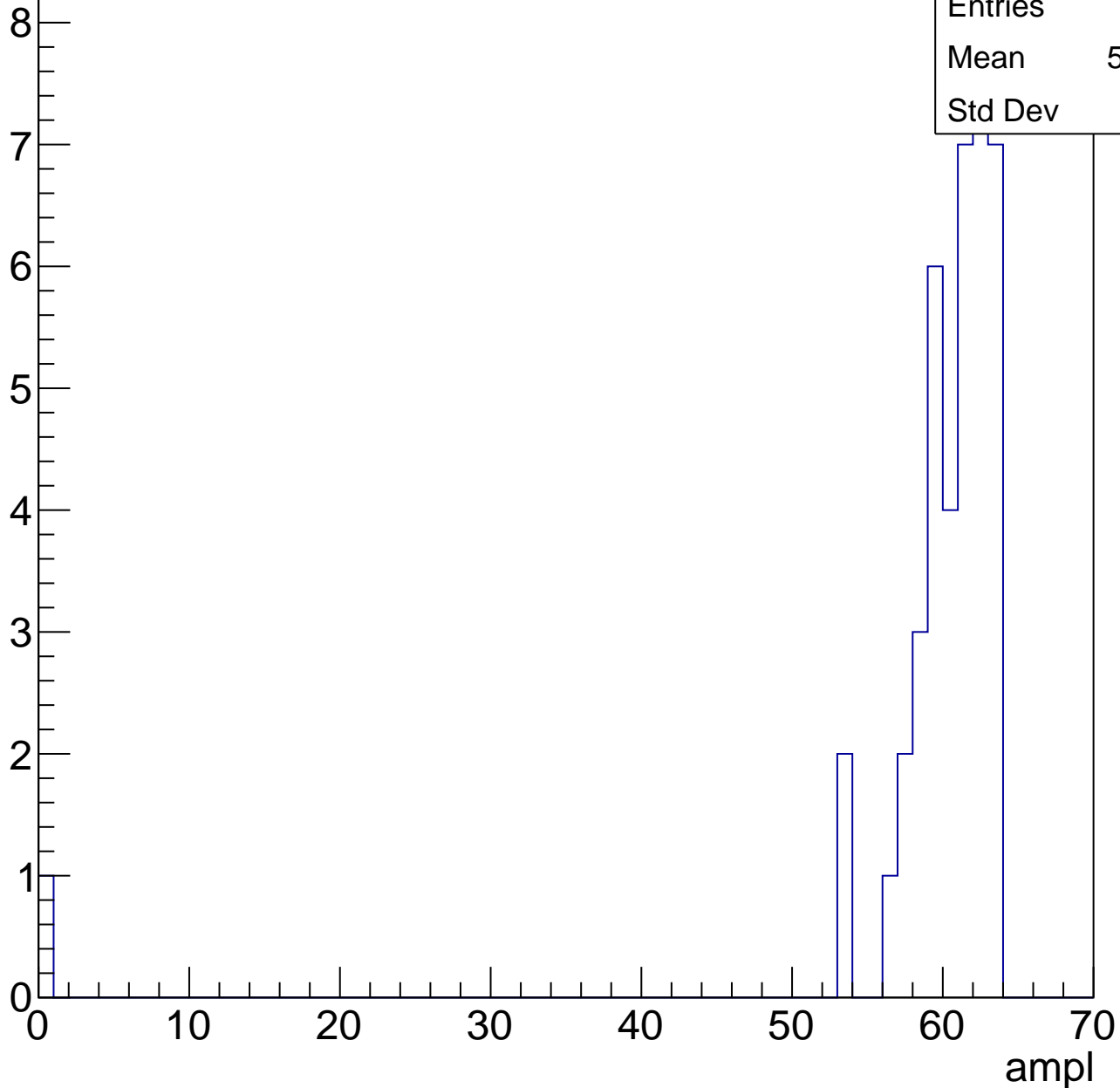


B1L003S, U13-ch45, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.73
Std Dev	9.61



B1L003S, U13-ch45, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch45, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch46, adc0

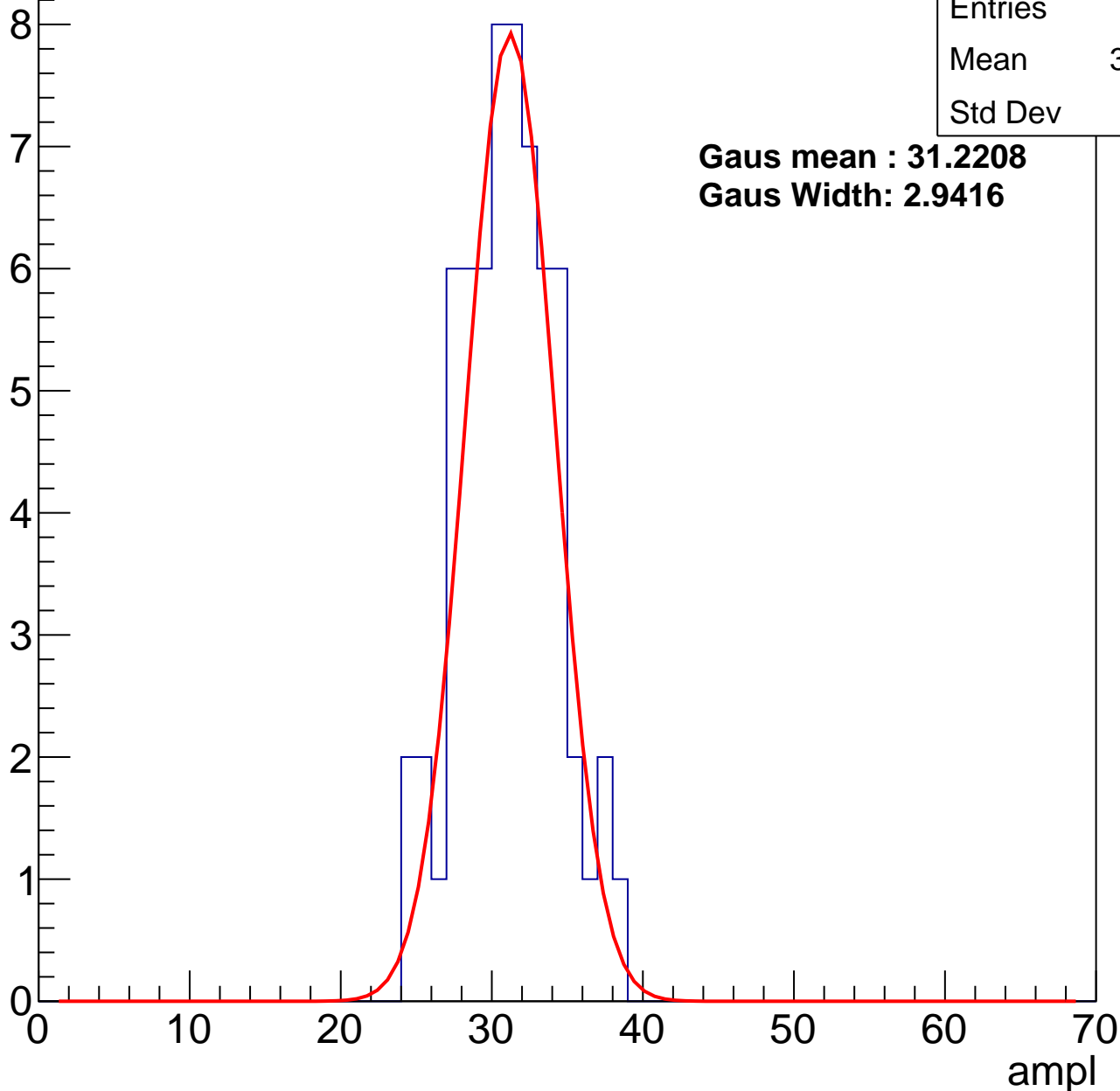
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.62
Std Dev	3.14

Gaus mean : 31.2208

Gaus Width: 2.9416



B1L003S, U13-ch46, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	38.04
Std Dev	3.2

Gaus mean : 38.4336

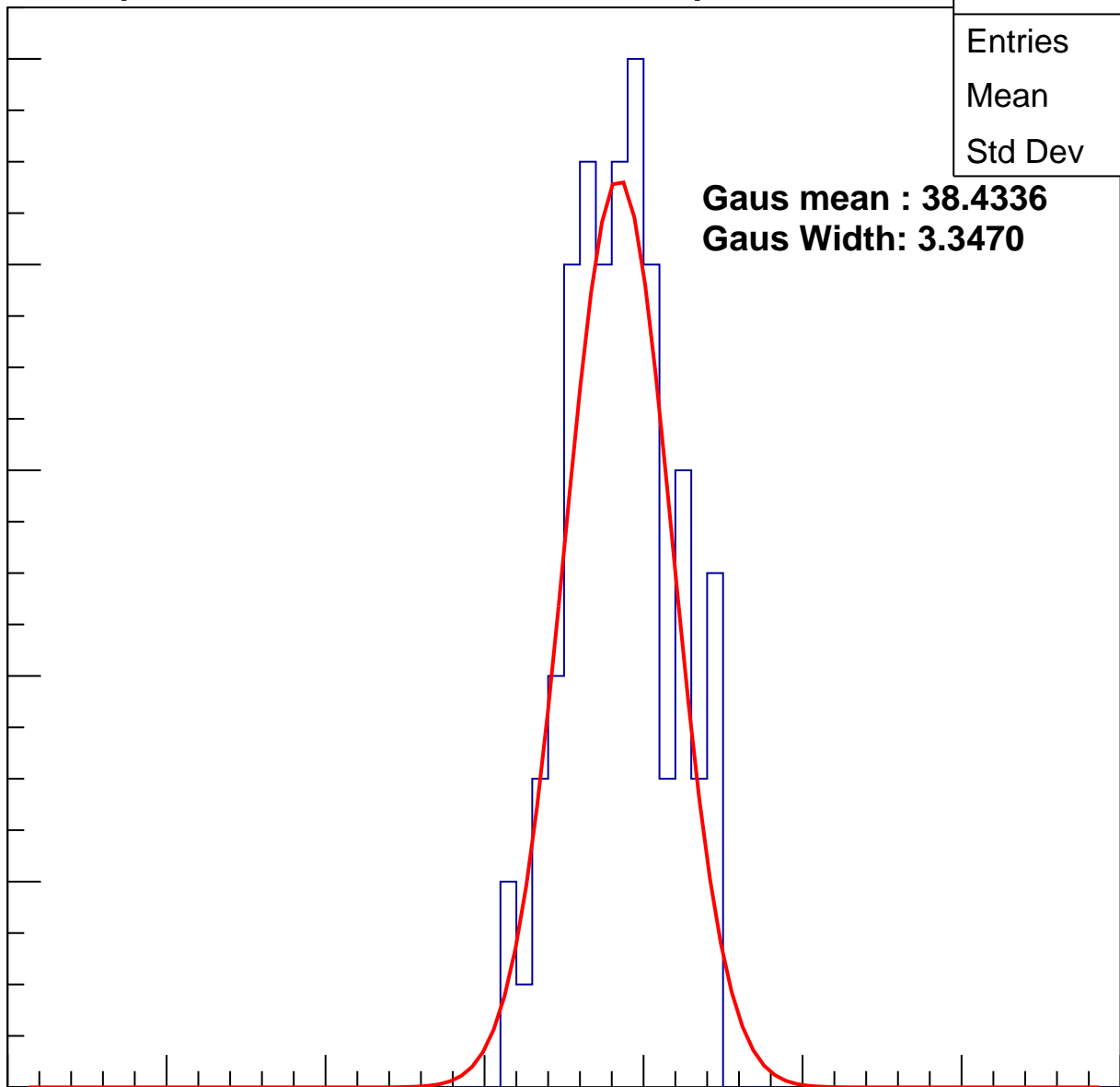
Gaus Width: 3.3470

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch46, adc2

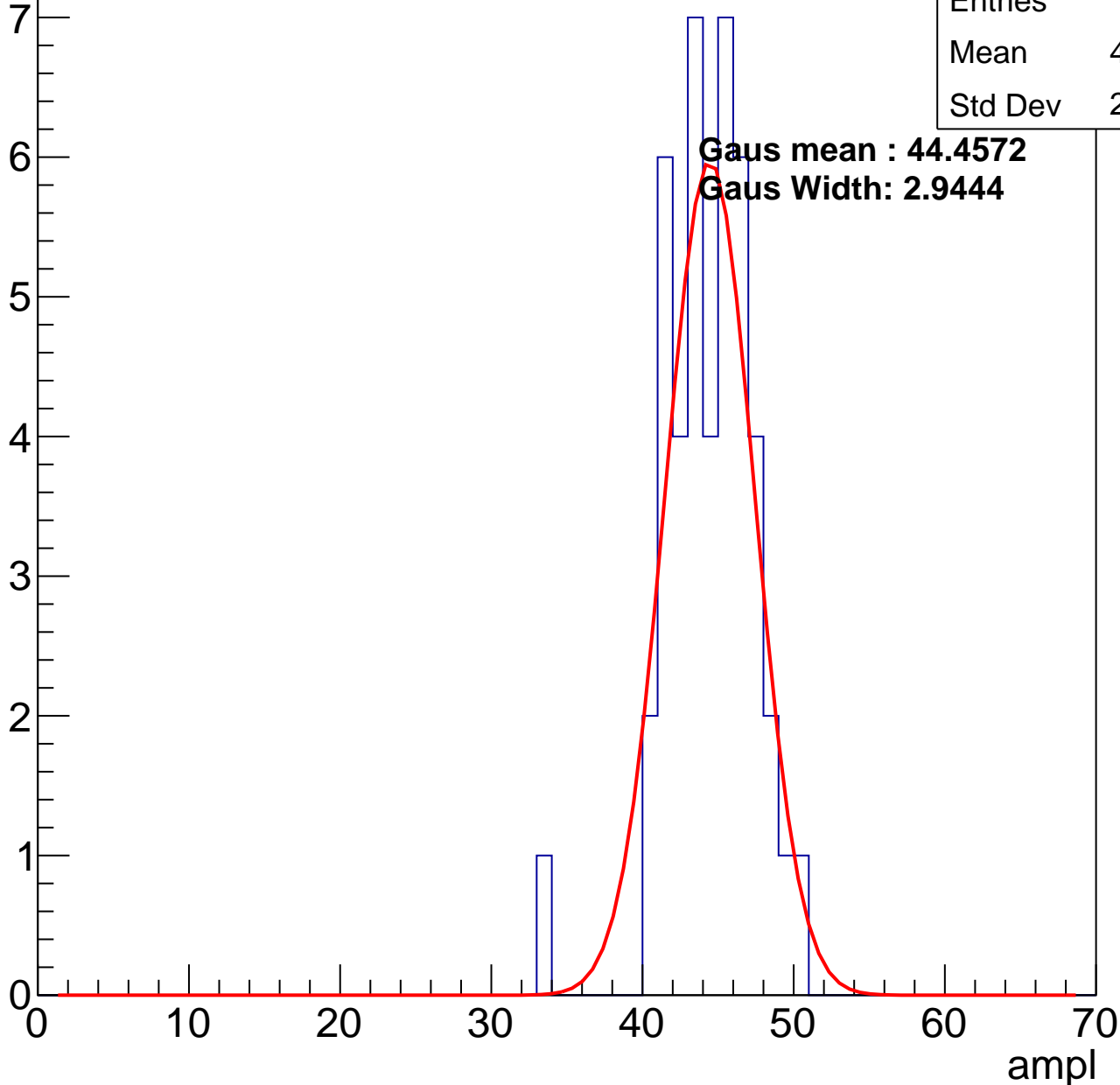
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	43.96
Std Dev	2.944

Gaus mean : 44.4572

Gaus Width: 2.9444

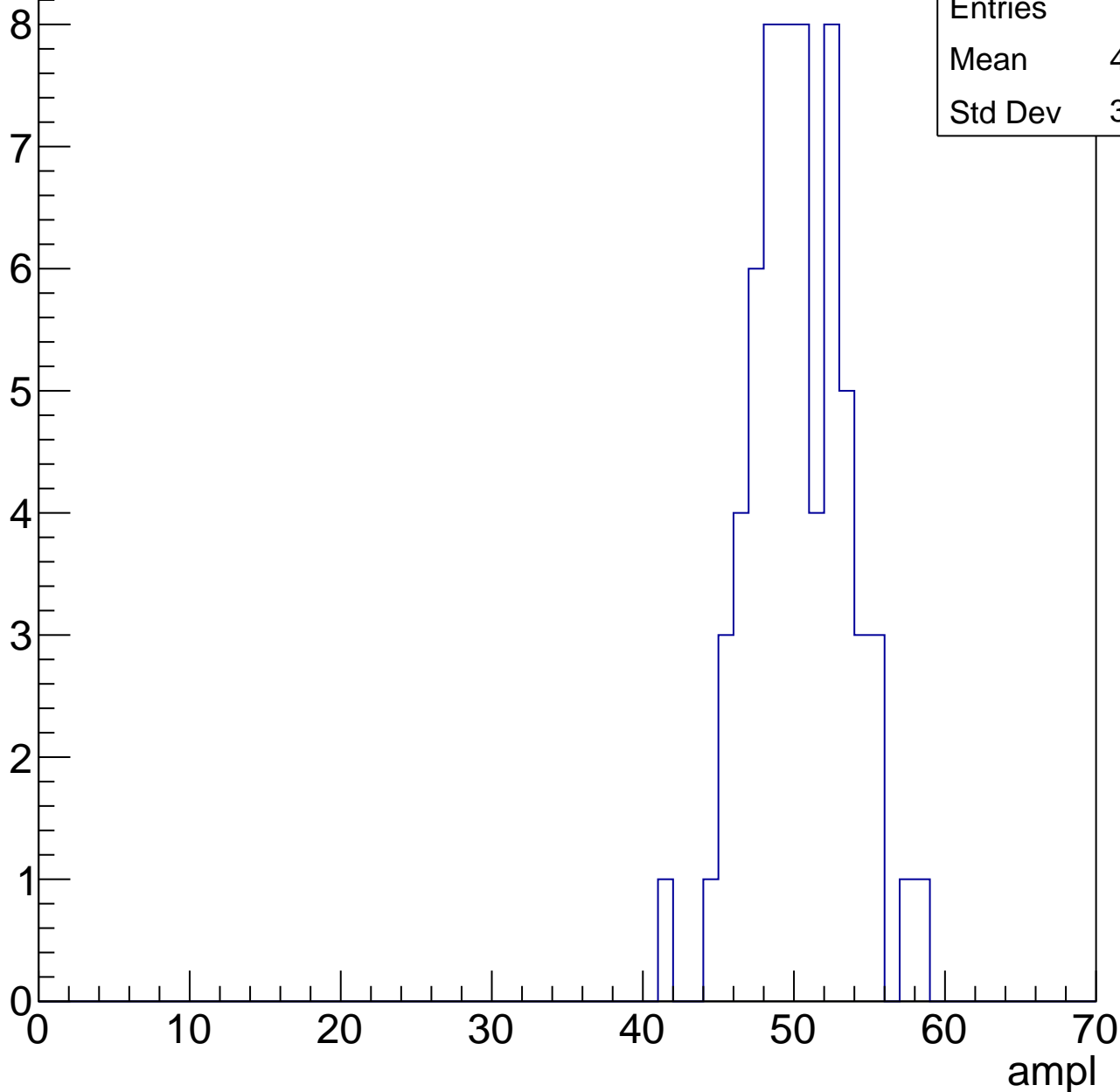


B1L003S, U13-ch46, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.83
Std Dev	3.224

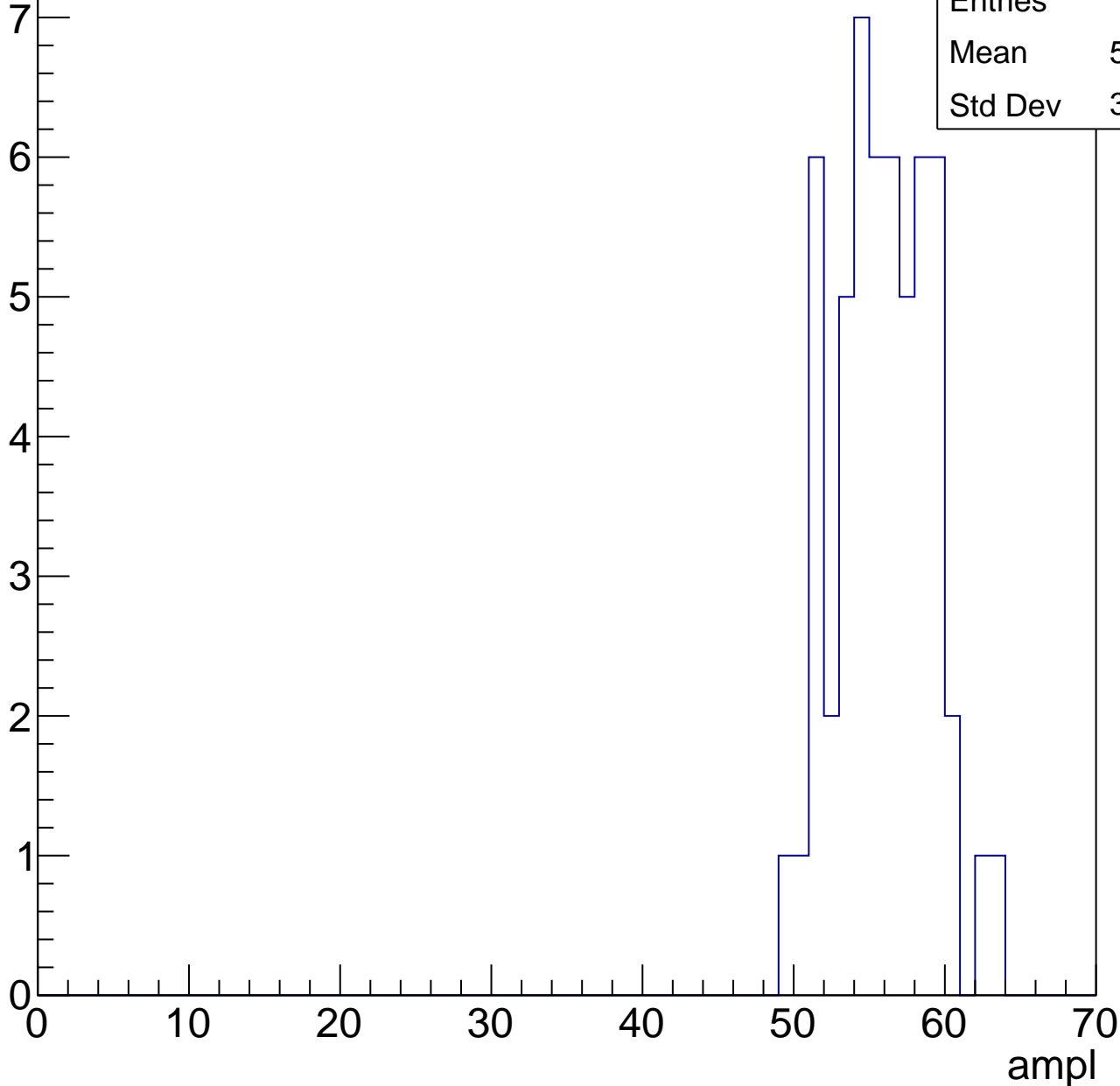


B1L003S, U13-ch46, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.45
Std Dev	3.103

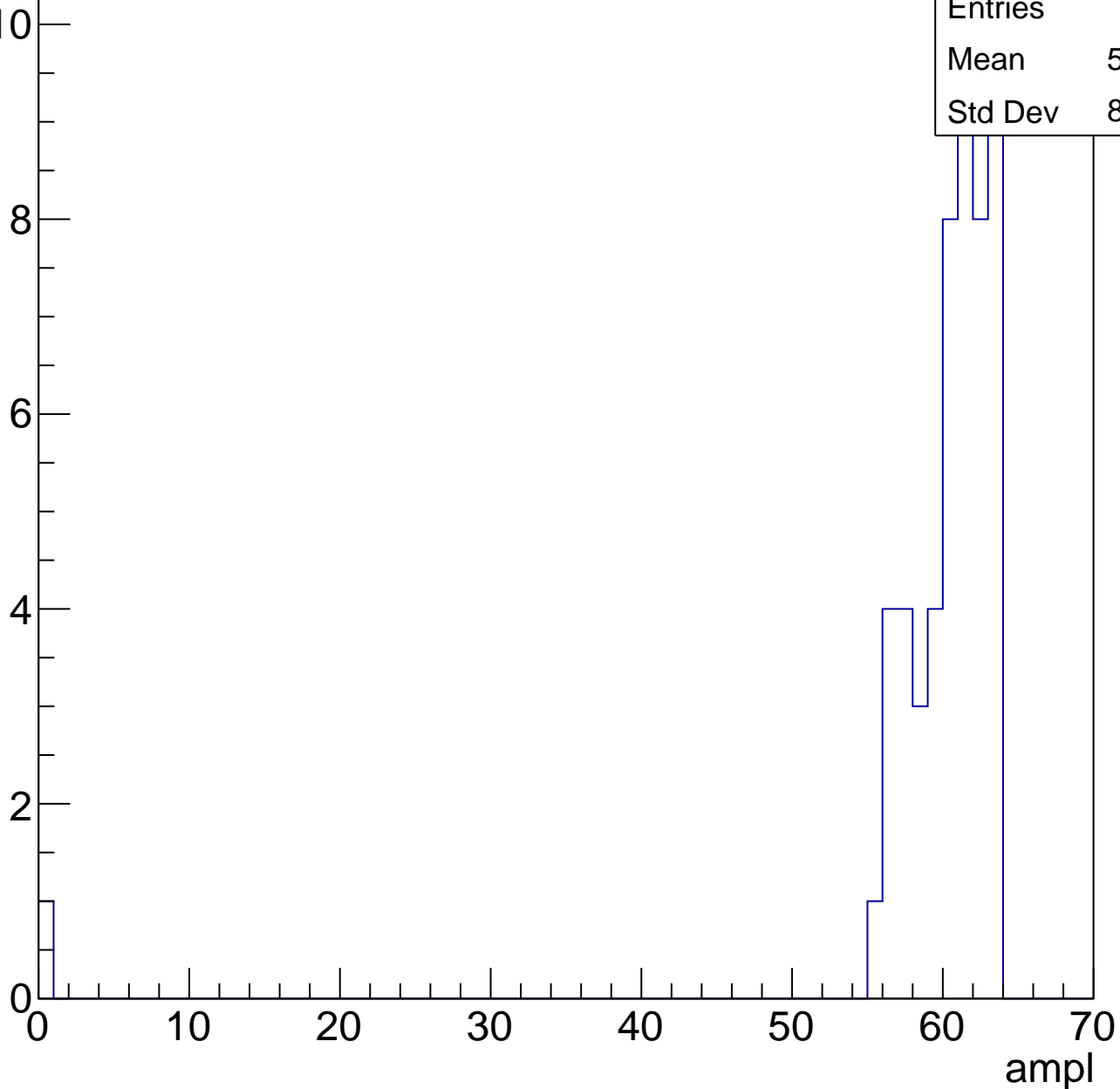


B1L003S, U13-ch46, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	59.04
Std Dev	8.566



B1L003S, U13-ch46, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L003S, U13-ch46, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch47, adc0

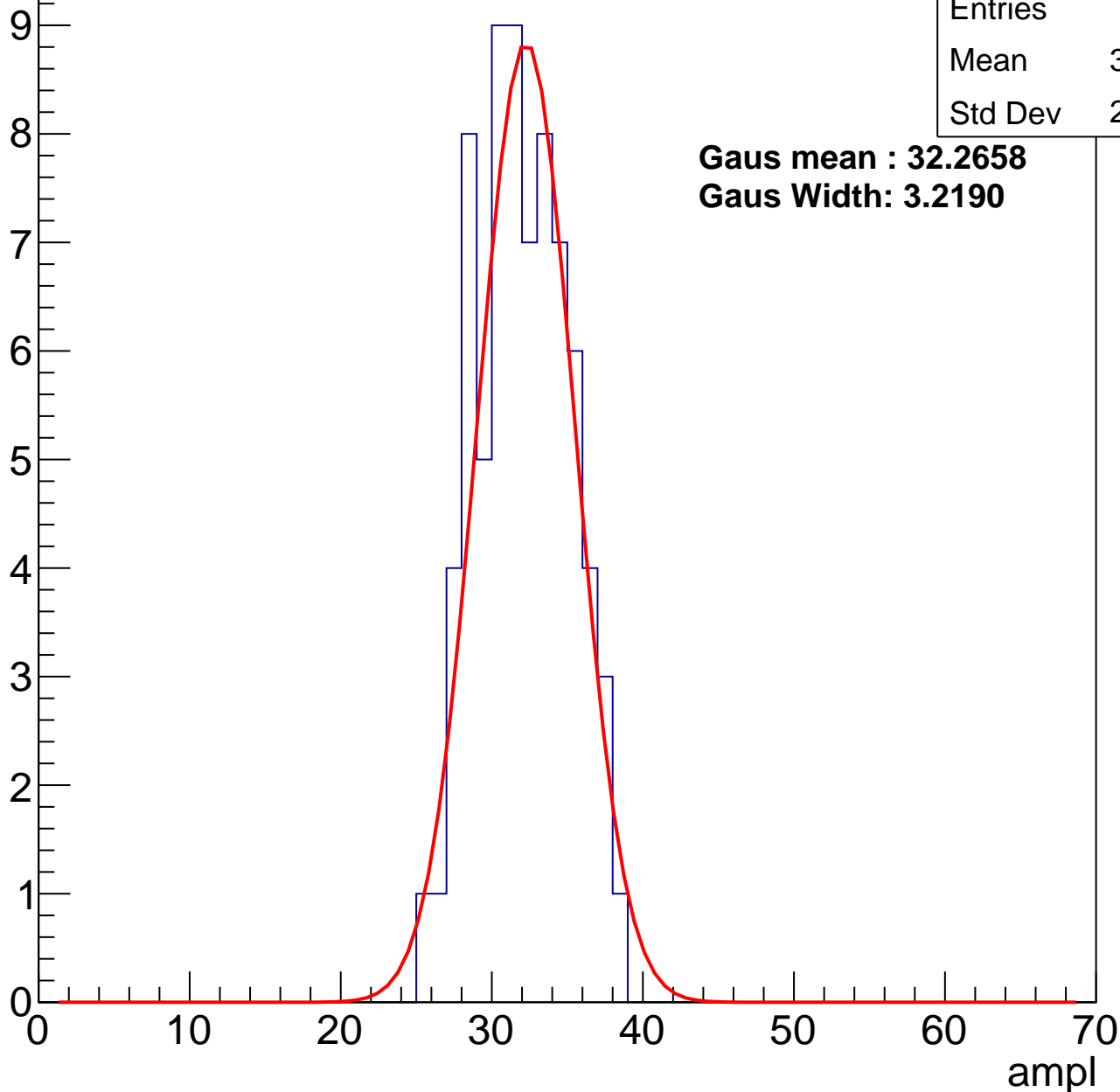
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	31.59
Std Dev	2.997

Gaus mean : 32.2658

Gaus Width: 3.2190



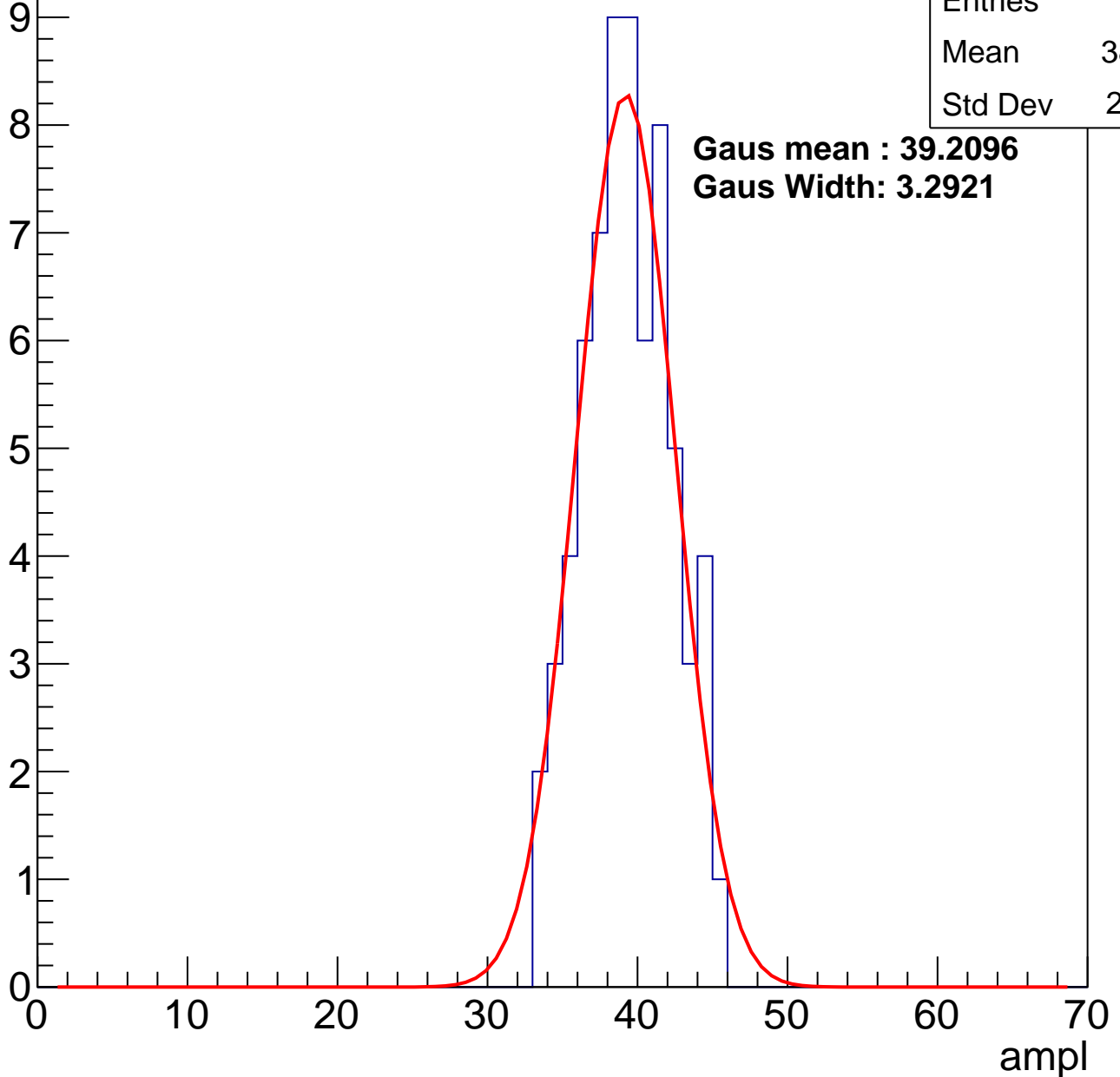
B1L003S, U13-ch47, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	38.87
Std Dev	2.911

Gaus mean : 39.2096
Gaus Width: 3.2921



B1L003S, U13-ch47, adc2

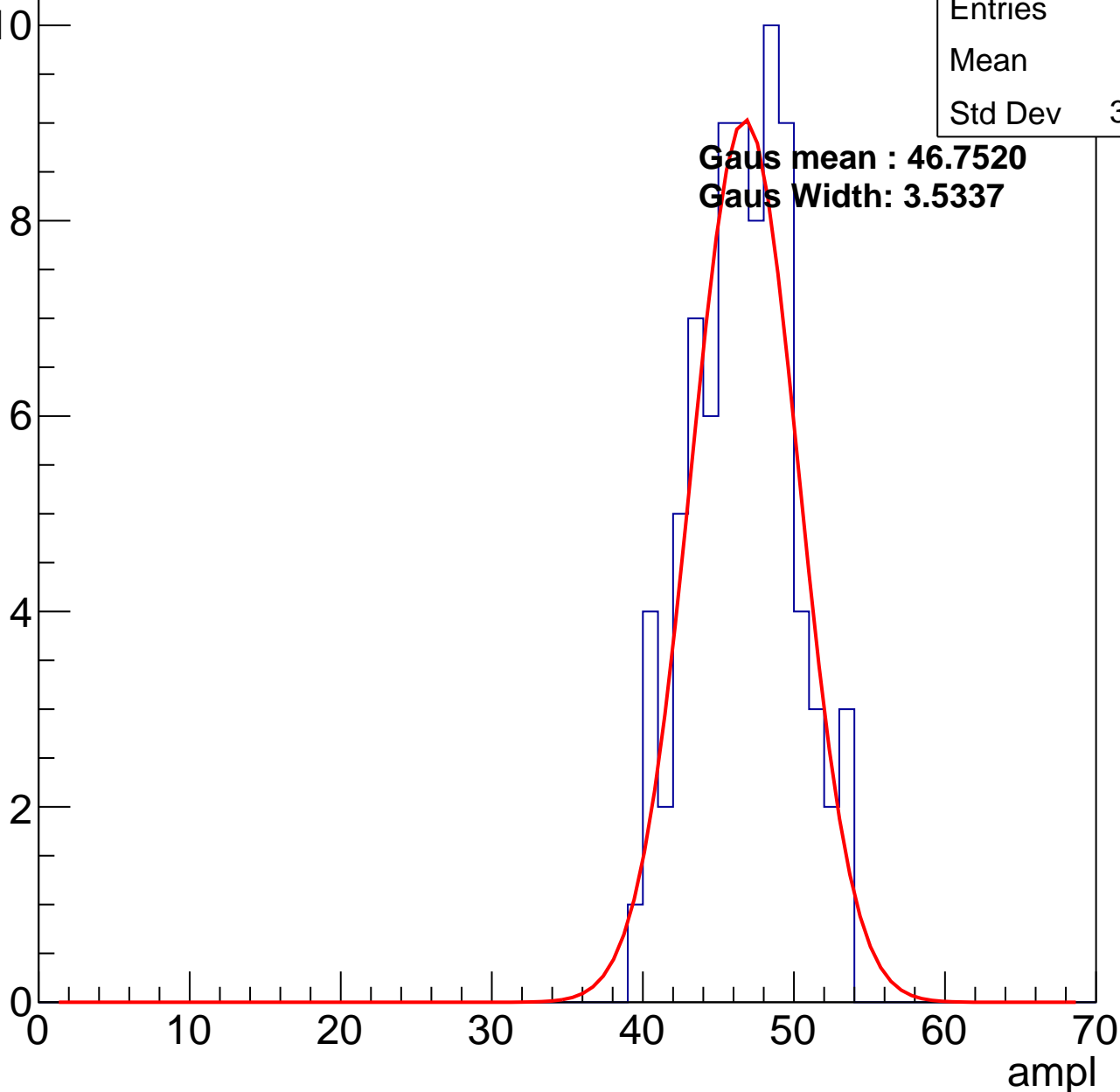
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	46.2
Std Dev	3.318

Gaus mean : 46.7520

Gaus Width: 3.5337



B1L003S, U13-ch47, adc3

calib_packv5_042523_0143.root, FC#13, port D2

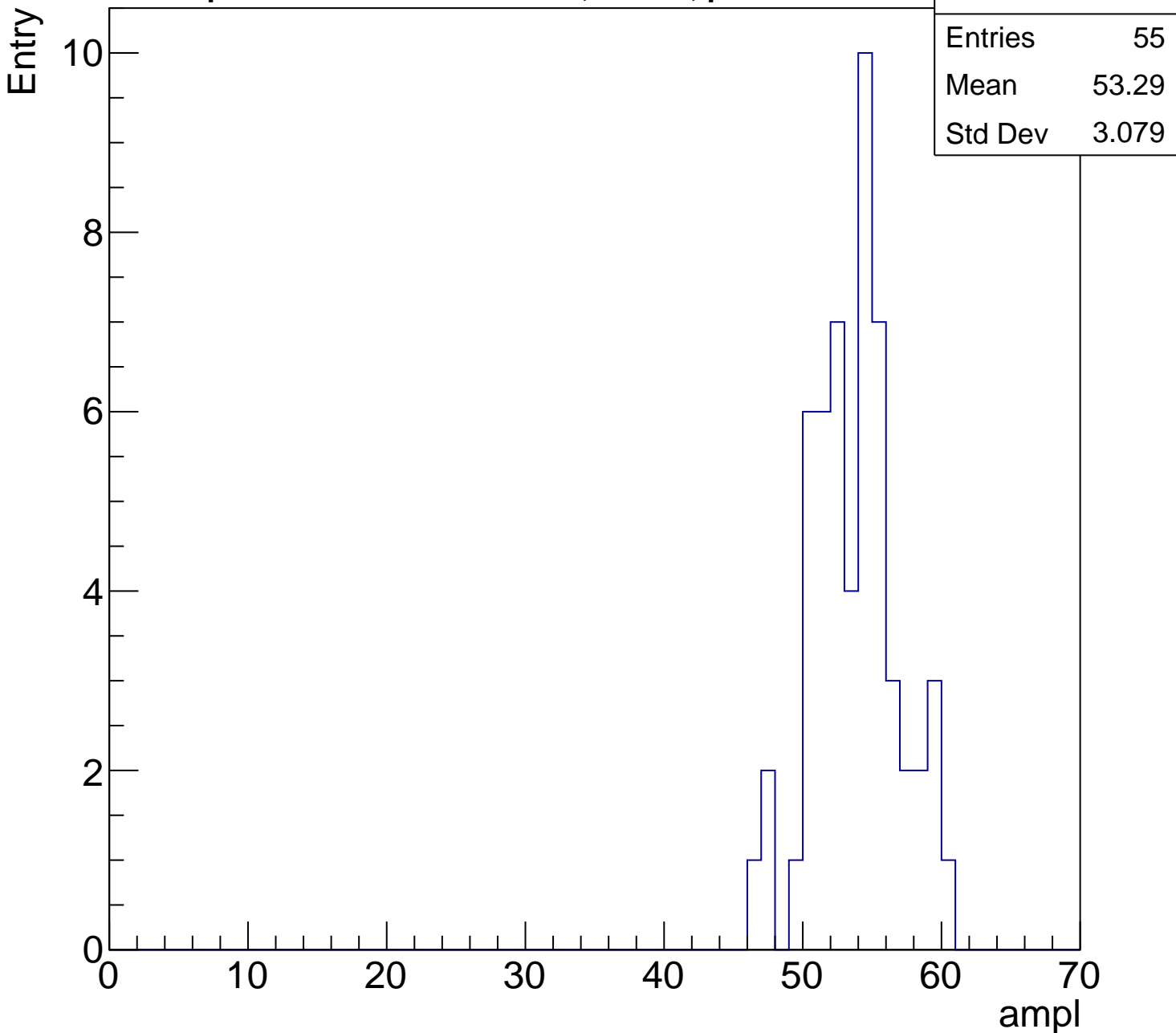
Entries	55
Mean	53.29
Std Dev	3.079

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

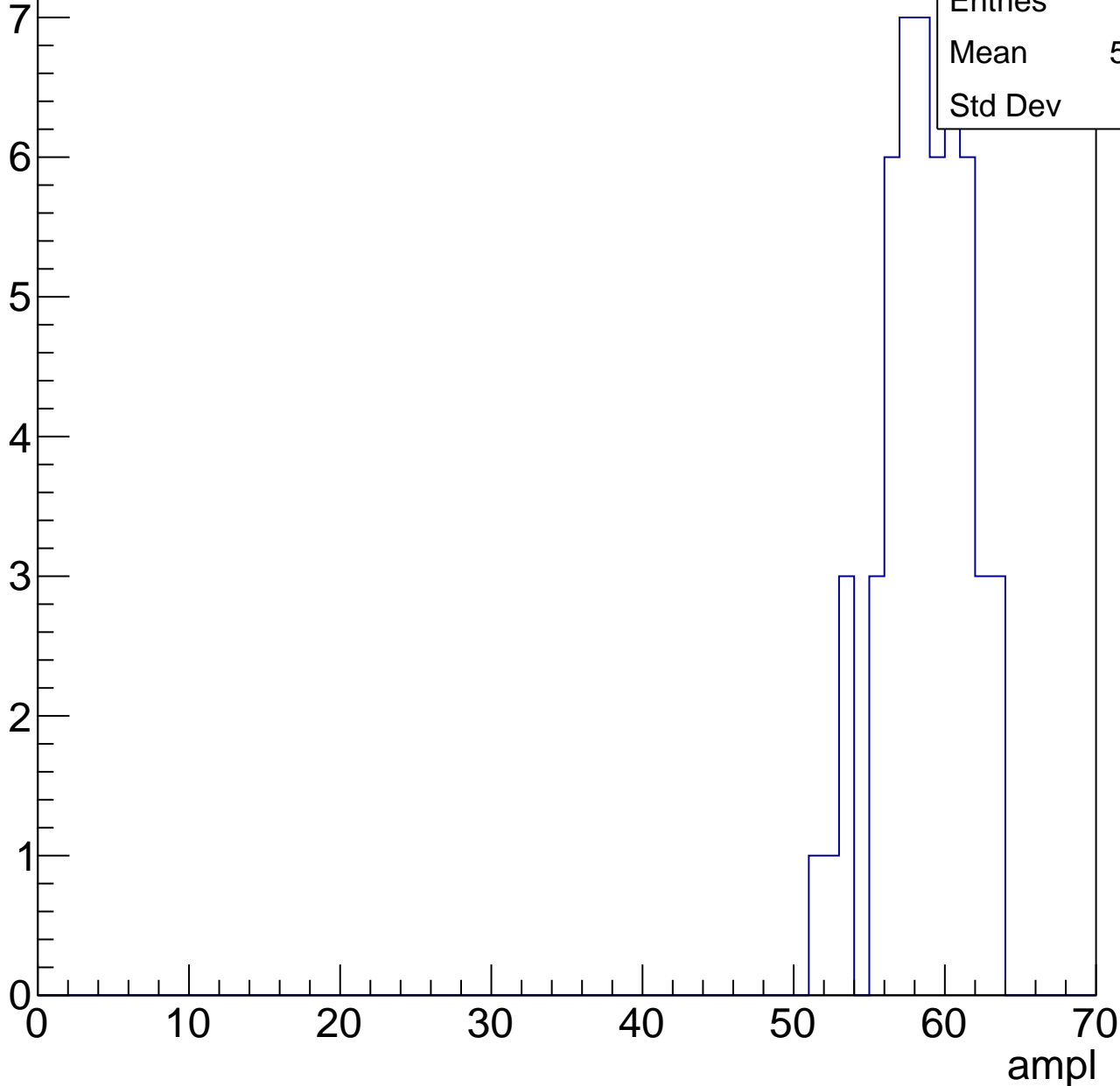


B1L003S, U13-ch47, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.17
Std Dev	2.84

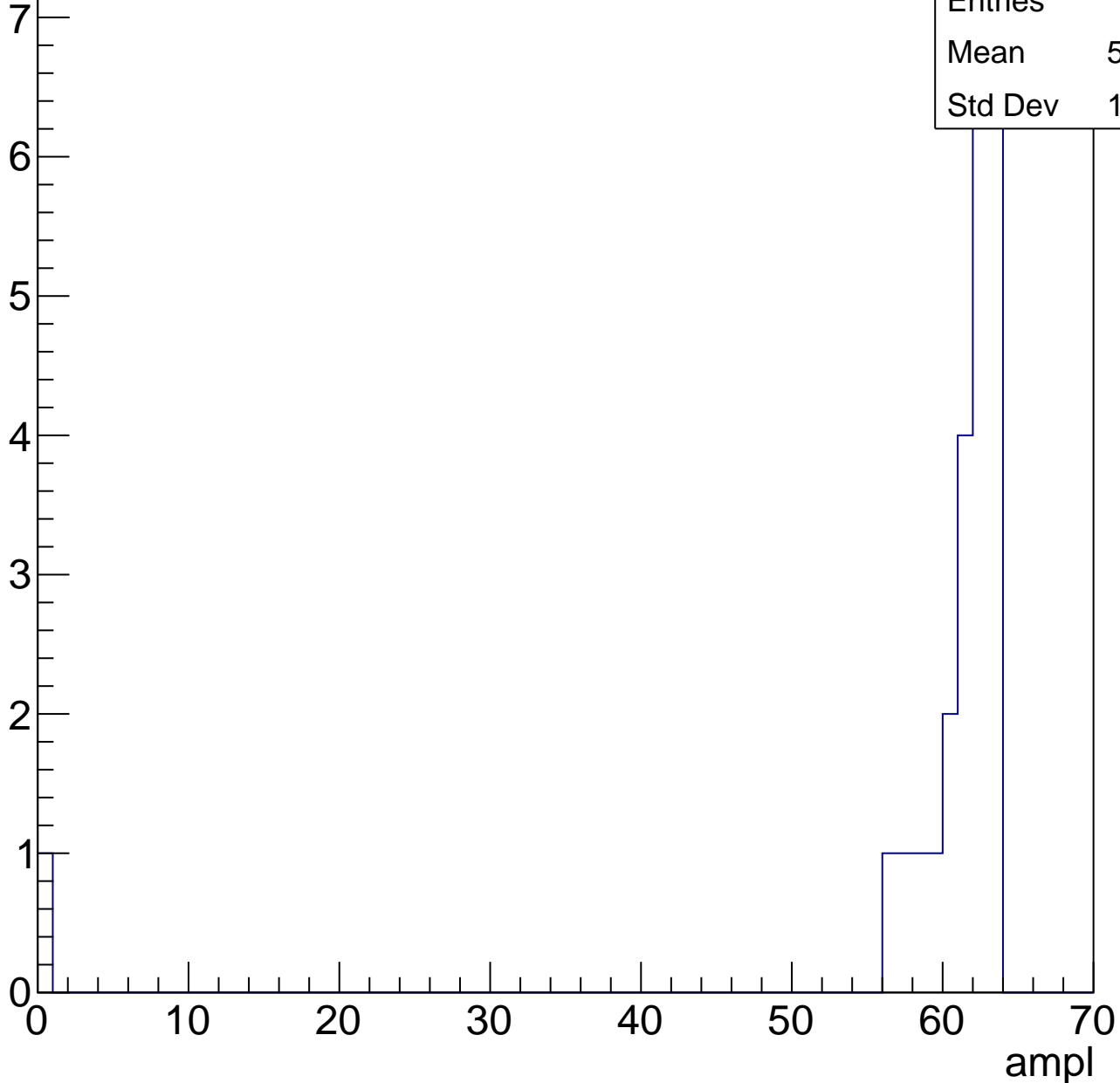


B1L003S, U13-ch47, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	25
Mean	58.76
Std Dev	12.14



B1L003S, U13-ch47, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch47, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch48, adc0

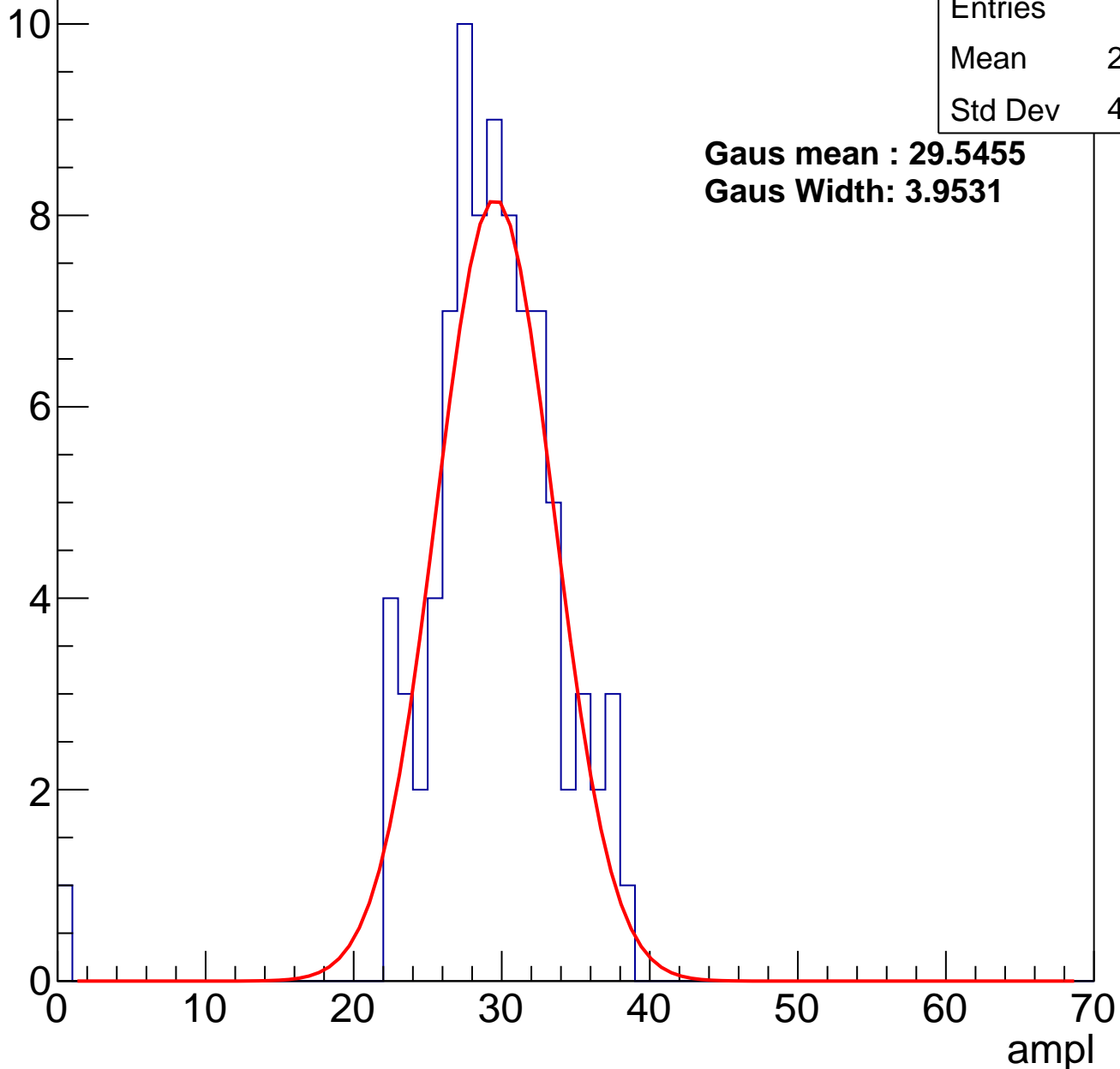
calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	28.86
Std Dev	4.914

Gaus mean : 29.5455

Gaus Width: 3.9531

Entry



B1L003S, U13-ch48, adc1

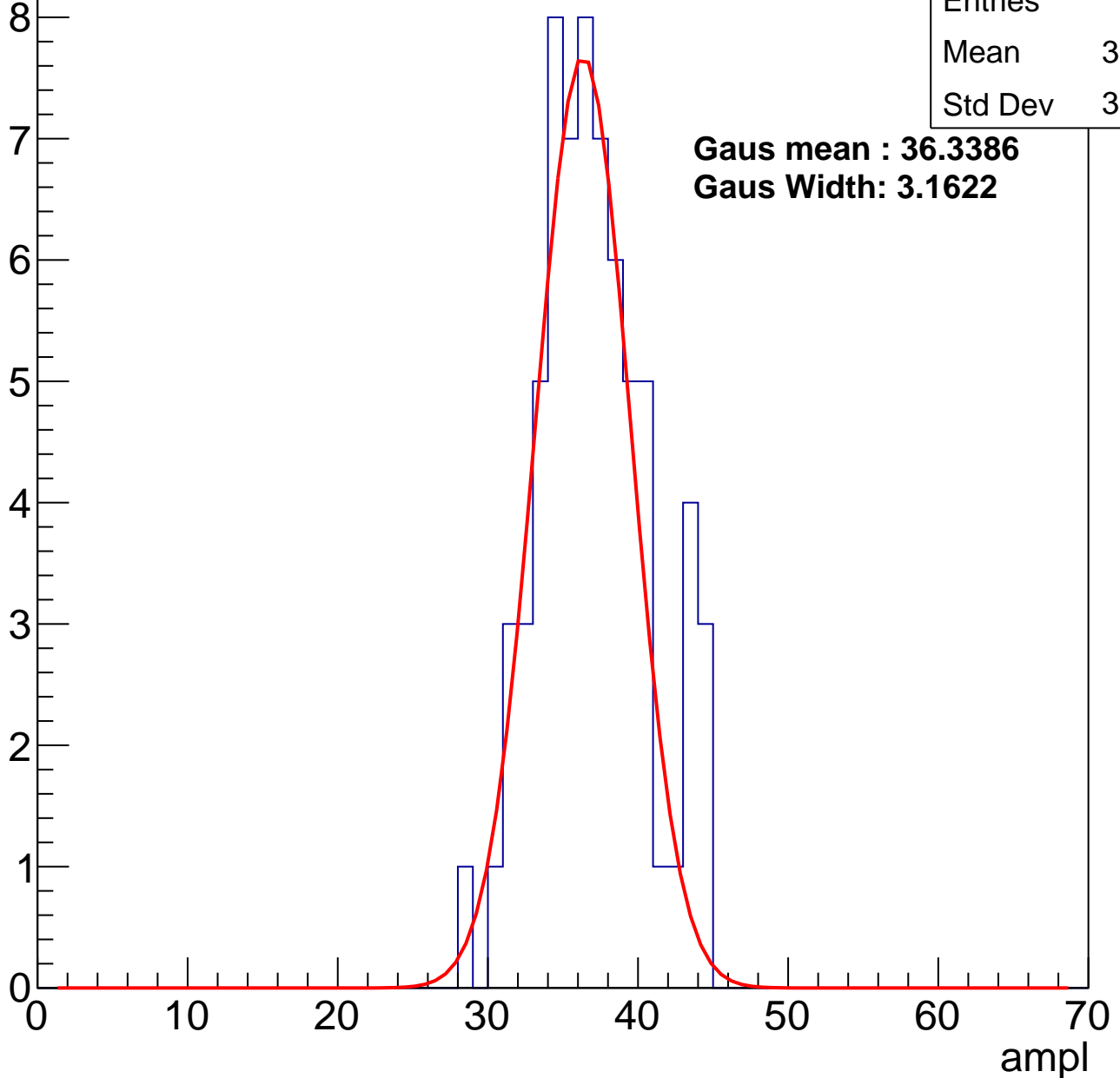
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.56
Std Dev	3.619

Gaus mean : 36.3386

Gaus Width: 3.1622



B1L003S, U13-ch48, adc2

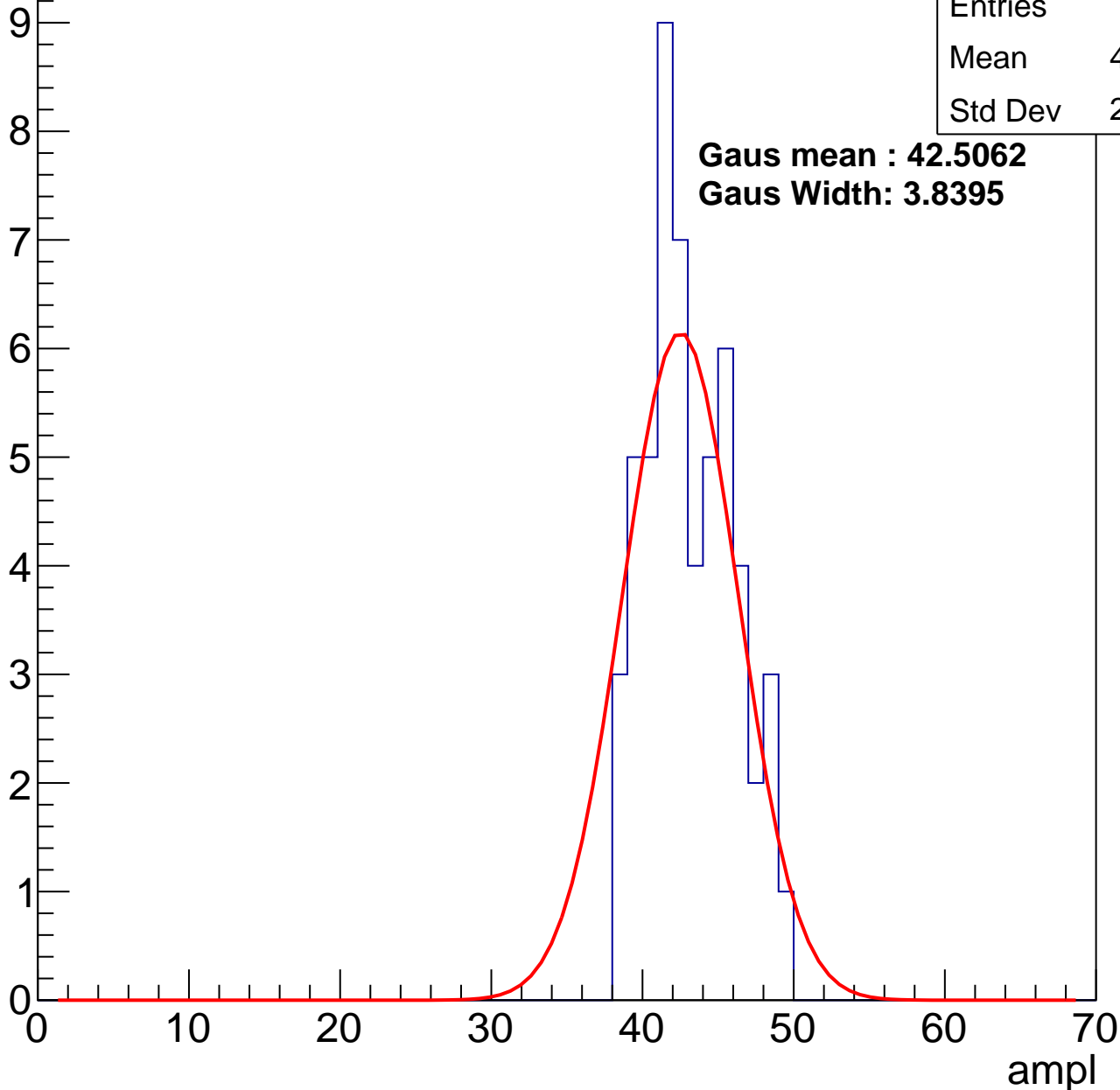
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.69
Std Dev	2.873

Gaus mean : 42.5062

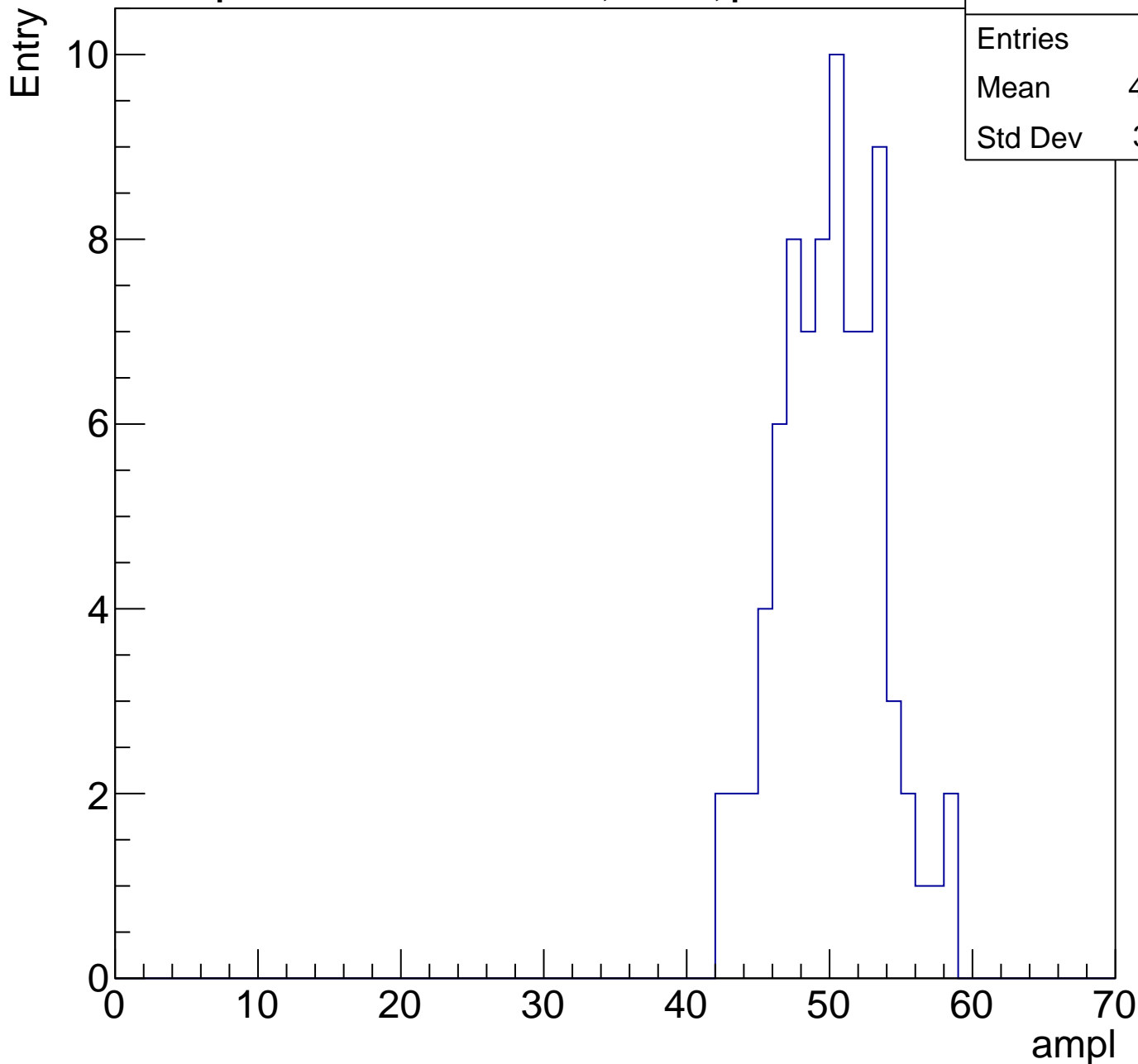
Gaus Width: 3.8395



B1L003S, U13-ch48, adc3

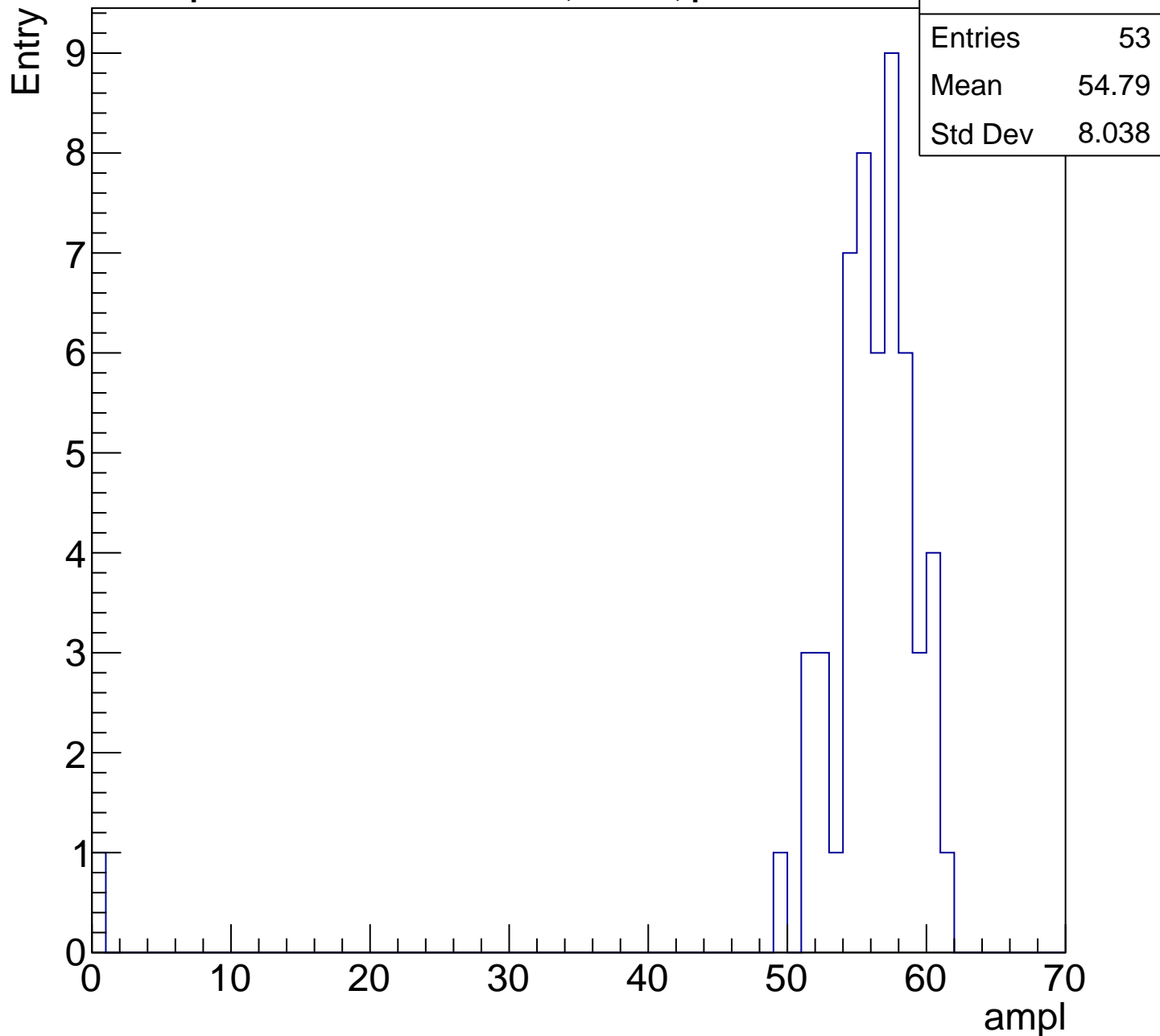
calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	49.59
Std Dev	3.541



B1L003S, U13-ch48, adc4

calib_packv5_042523_0143.root, FC#13, port D2

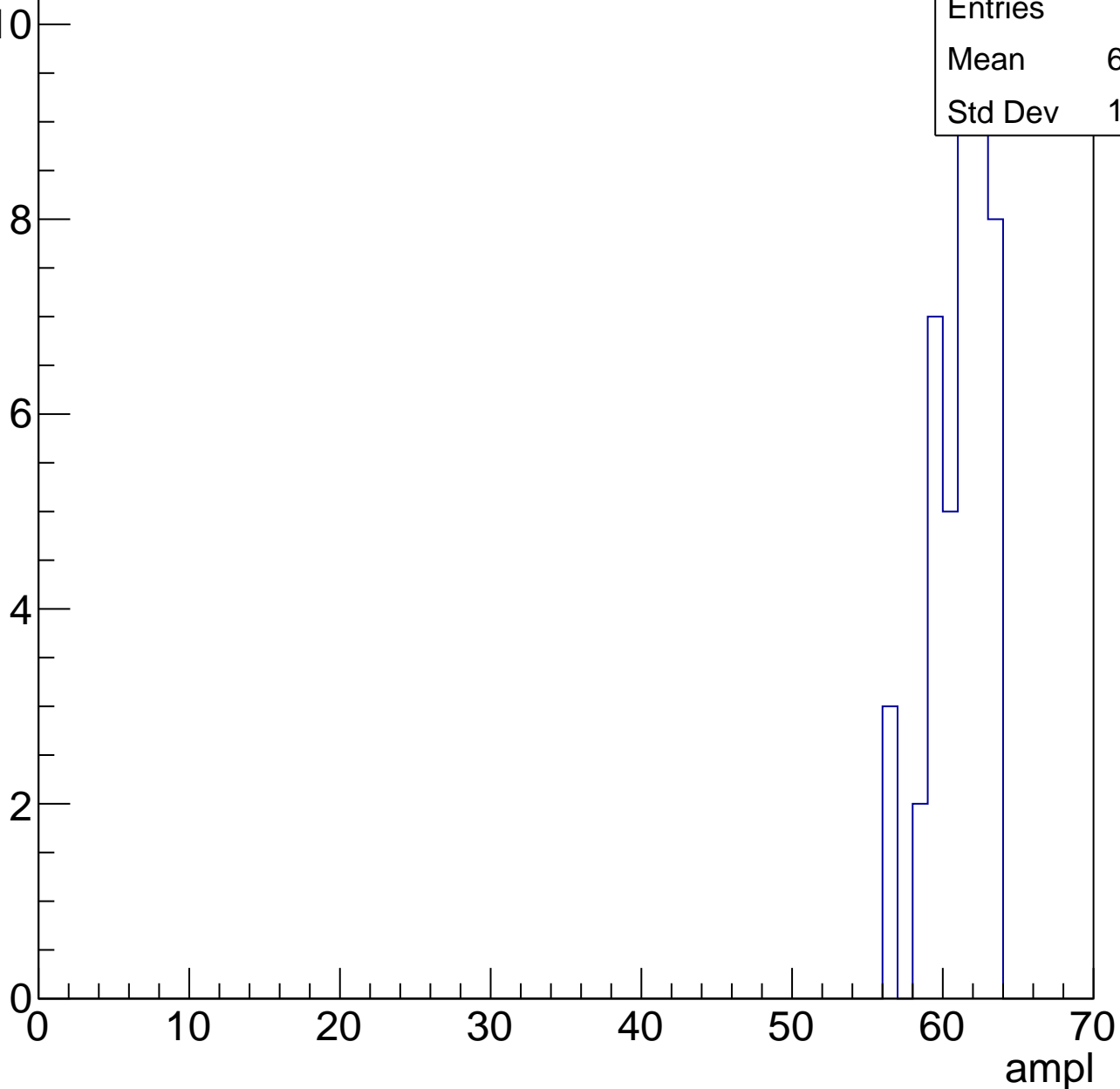


B1L003S, U13-ch48, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	60.68
Std Dev	1.928



B1L003S, U13-ch48, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L003S, U13-ch48, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch49, adc0

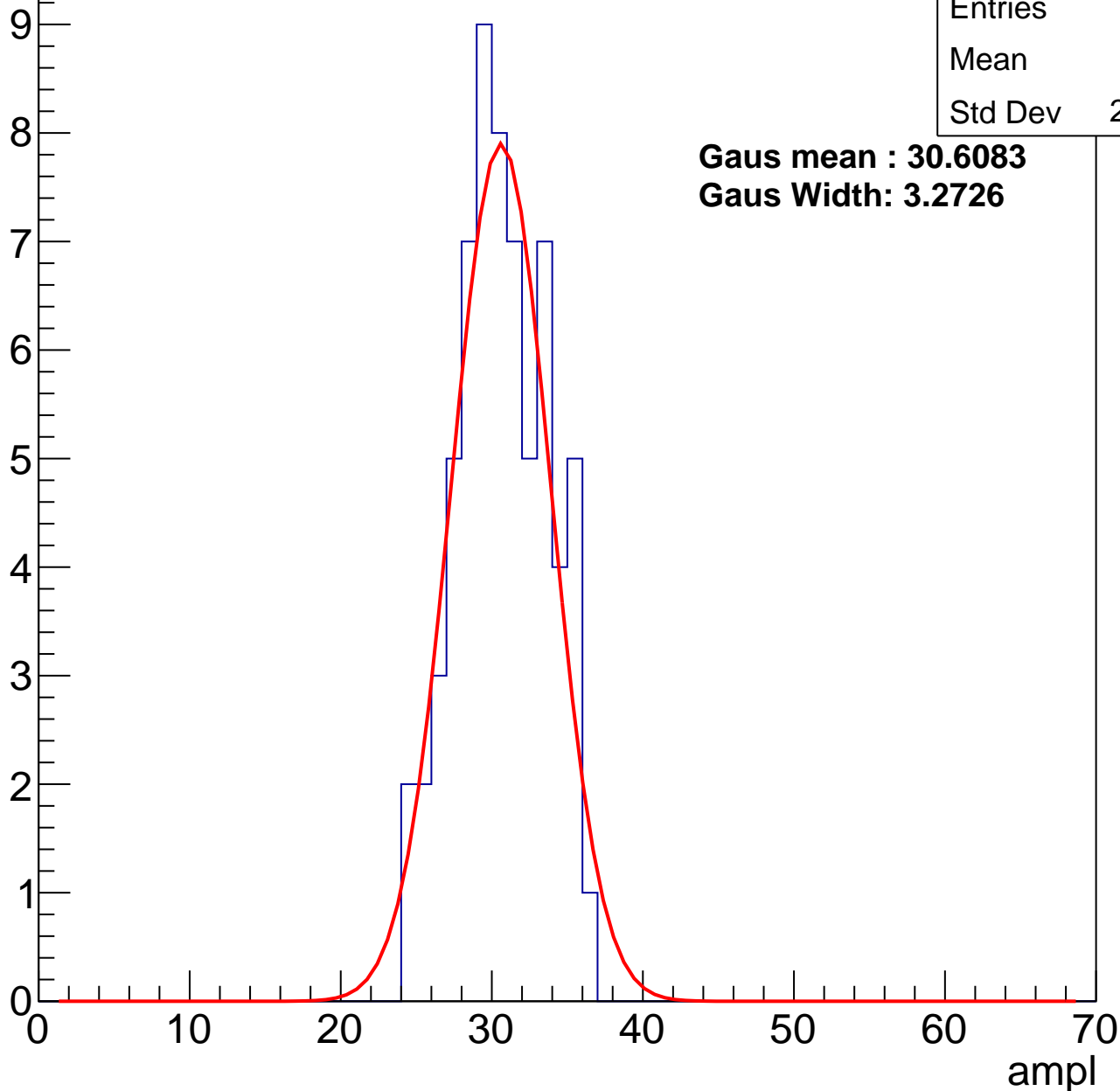
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.2
Std Dev	2.947

Gaus mean : 30.6083

Gaus Width: 3.2726



B1L003S, U13-ch49, adc1

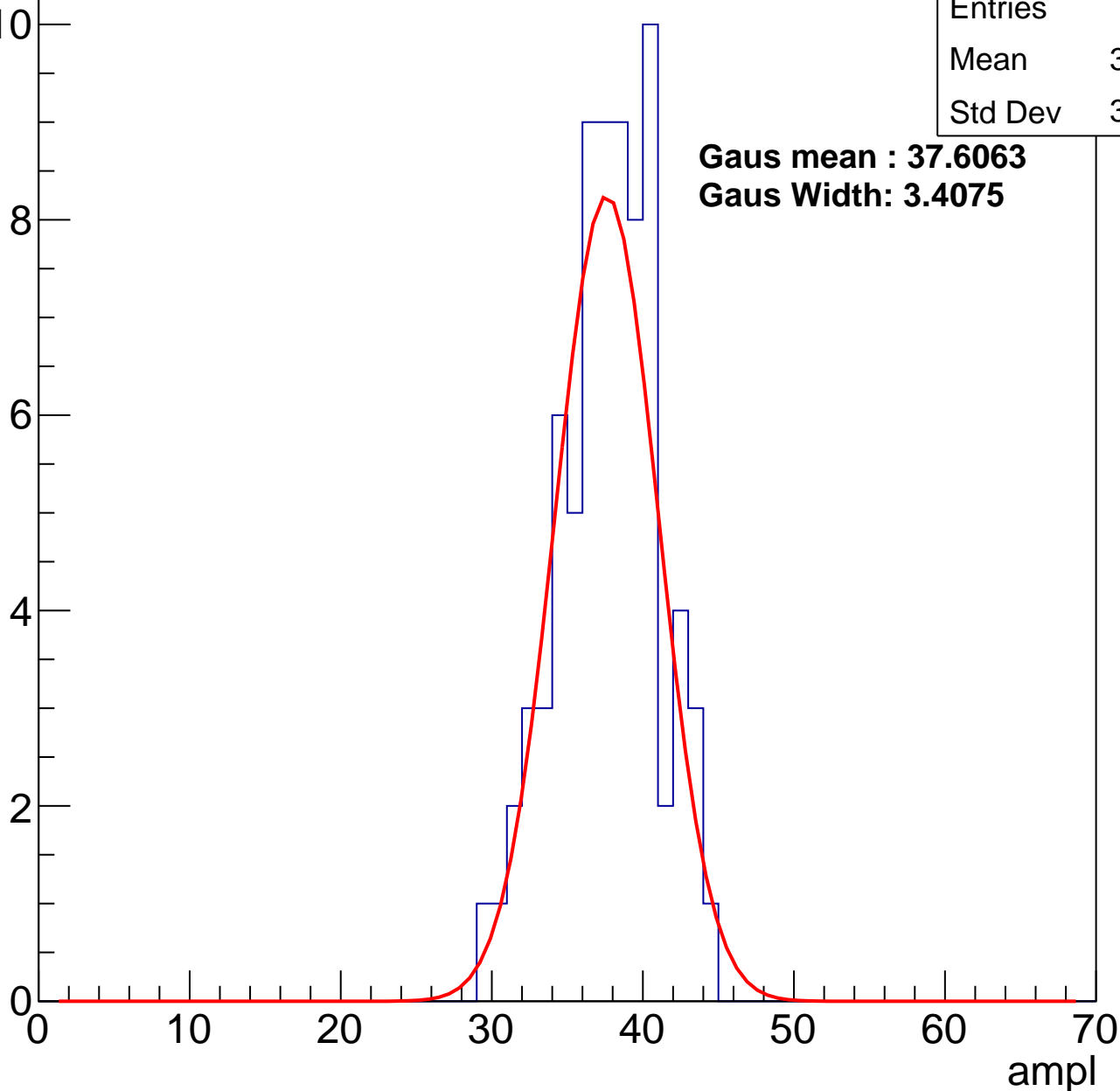
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	37.22
Std Dev	3.259

Gaus mean : 37.6063

Gaus Width: 3.4075



B1L003S, U13-ch49, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	44.83
Std Dev	3.287

Gaus mean : 44.7489

Gaus Width: 3.1953

Entry

10

8

6

4

2

0

0

10

20

30

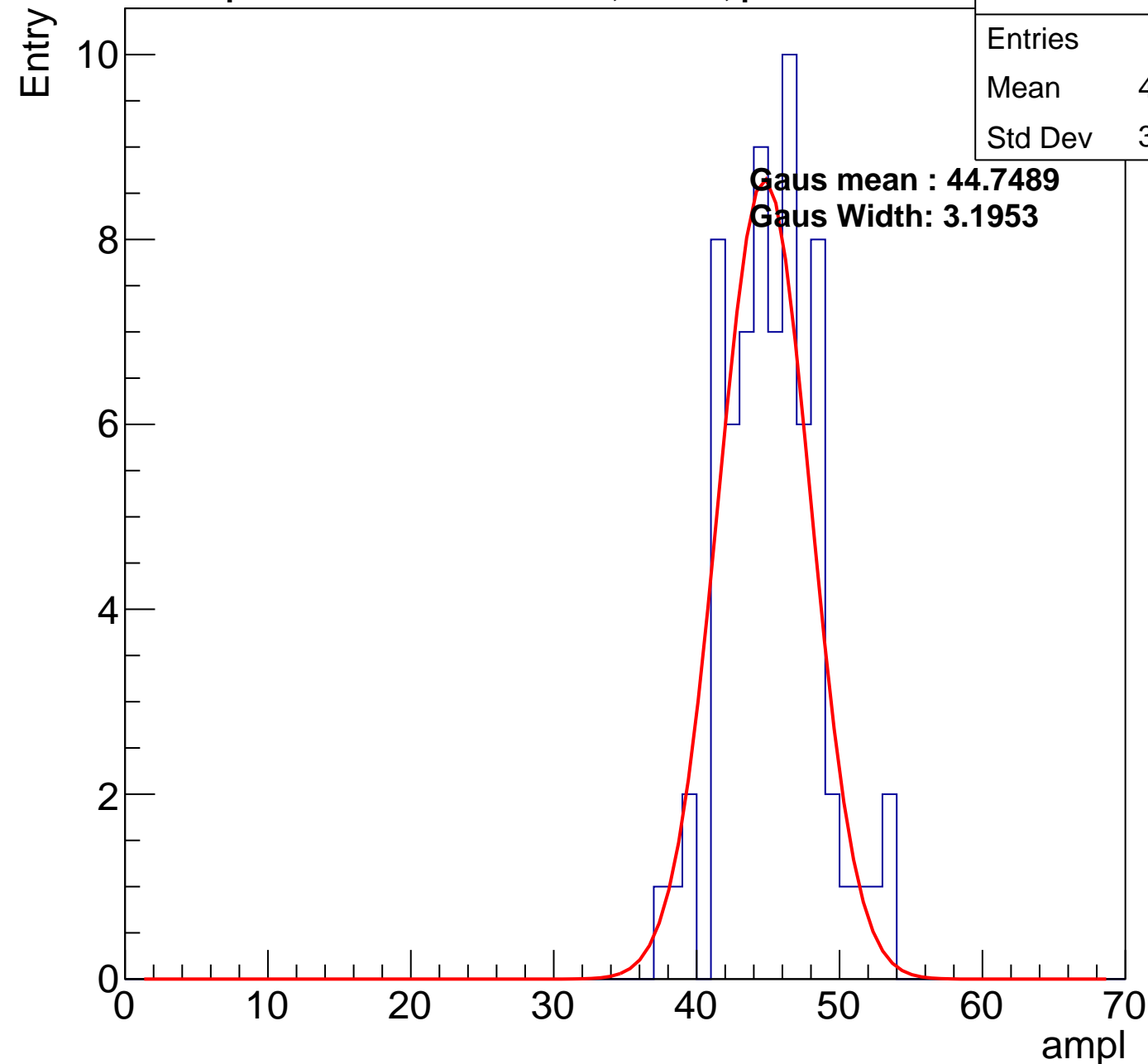
40

50

60

70

ampl

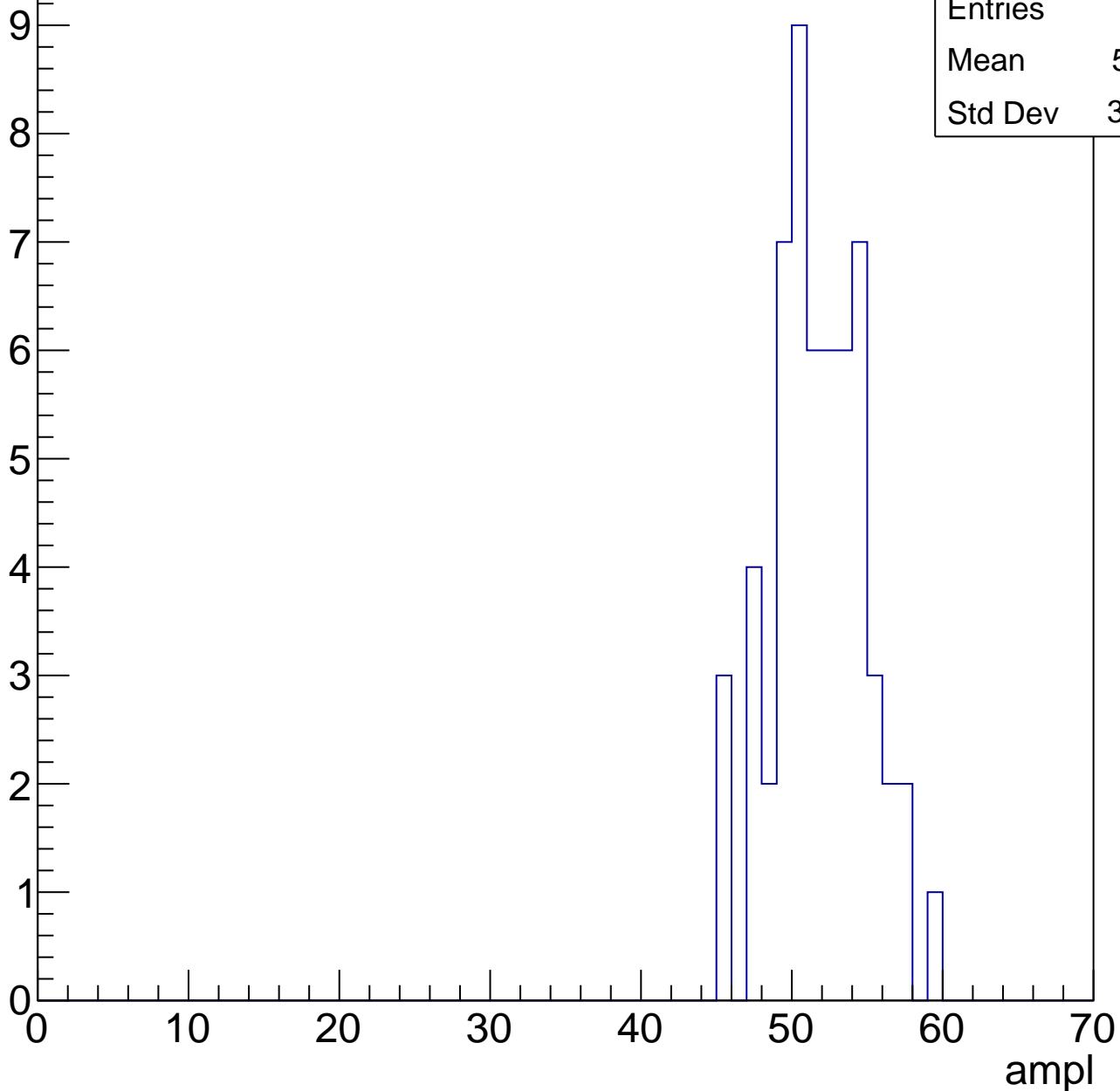


B1L003S, U13-ch49, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	51.31
Std Dev	3.075

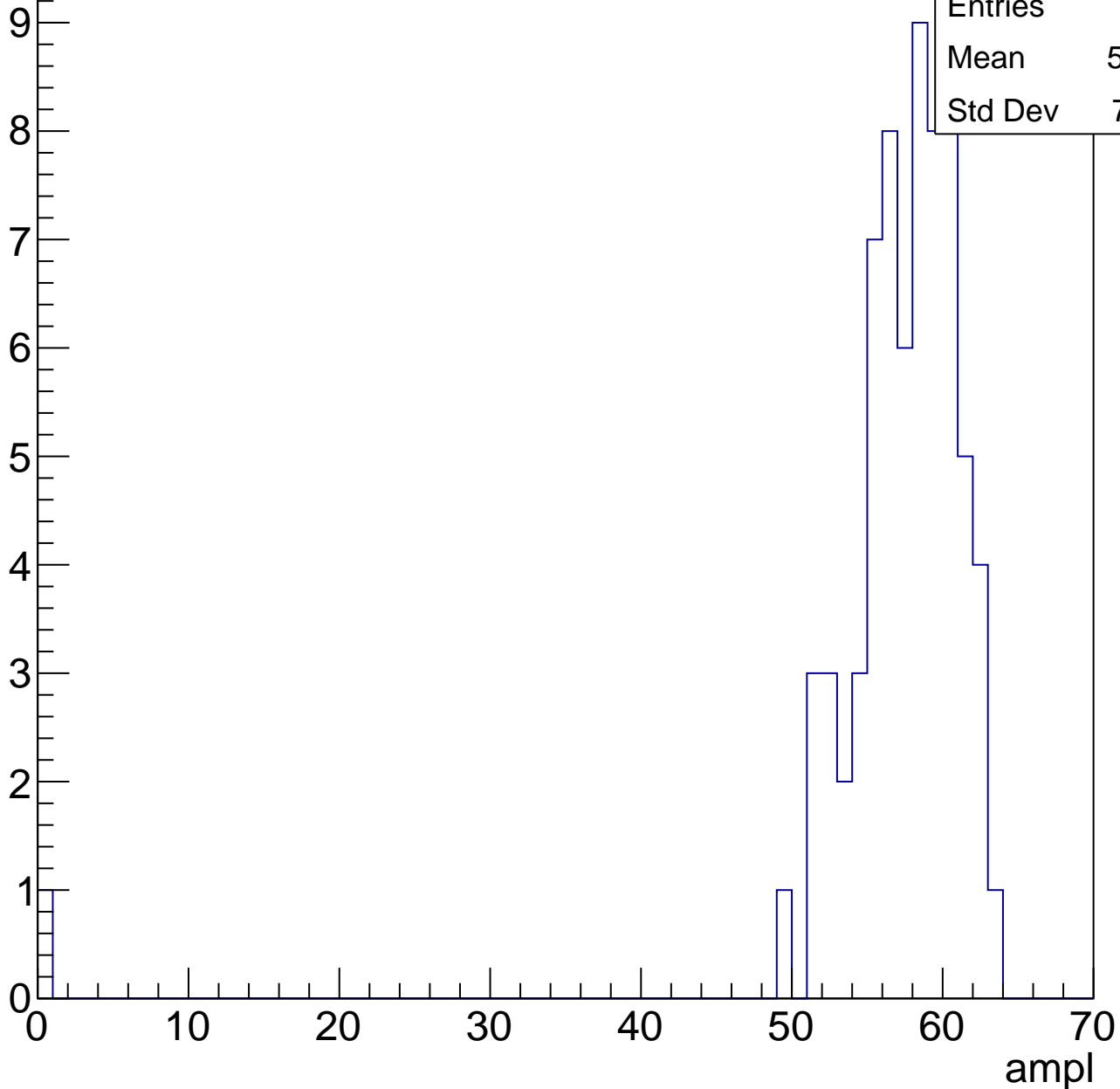


B1L003S, U13-ch49, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	56.44
Std Dev	7.471

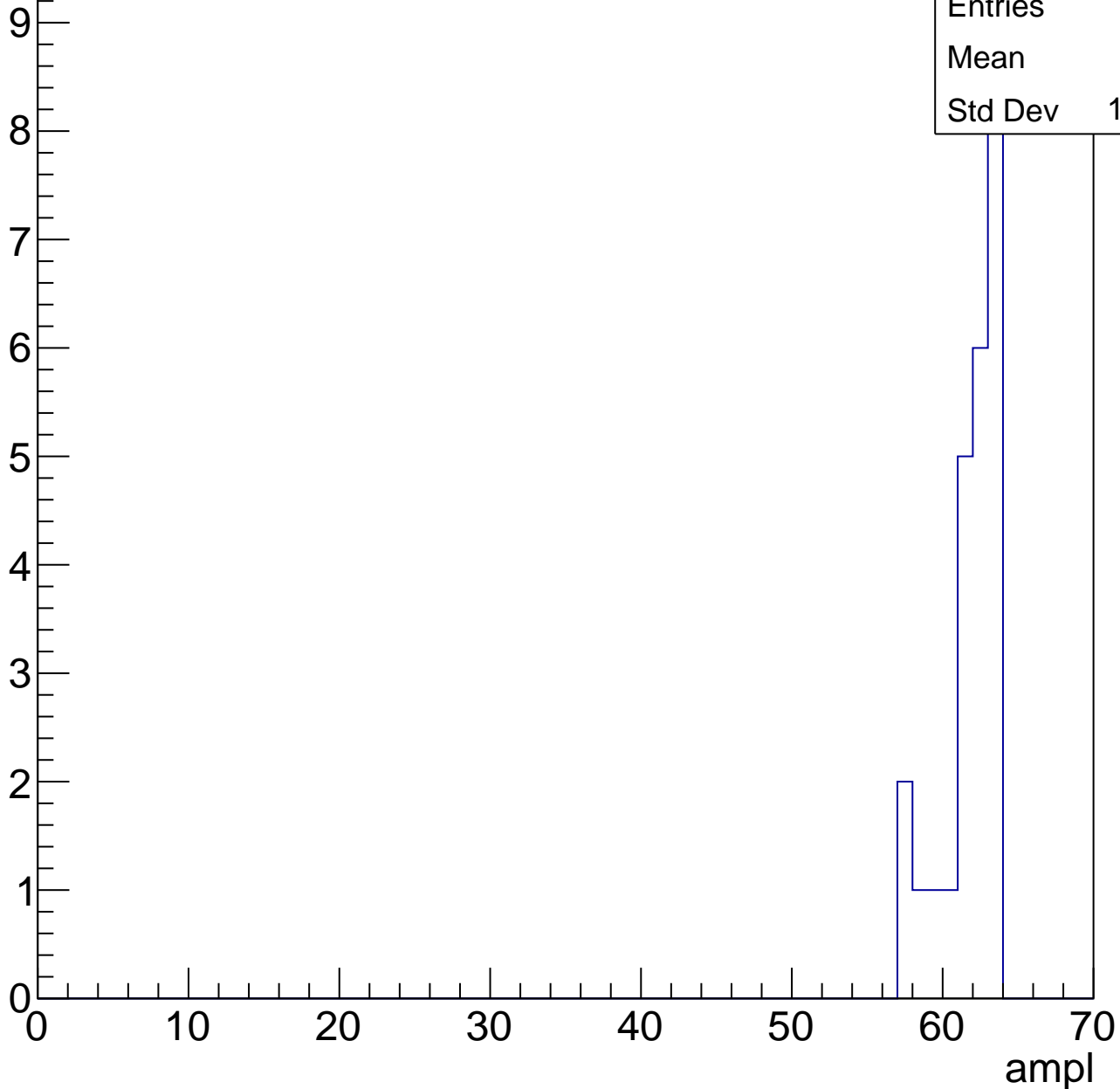


B1L003S, U13-ch49, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	25
Mean	61.4
Std Dev	1.833



B1L003S, U13-ch49, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch49, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U13-ch50, adc0

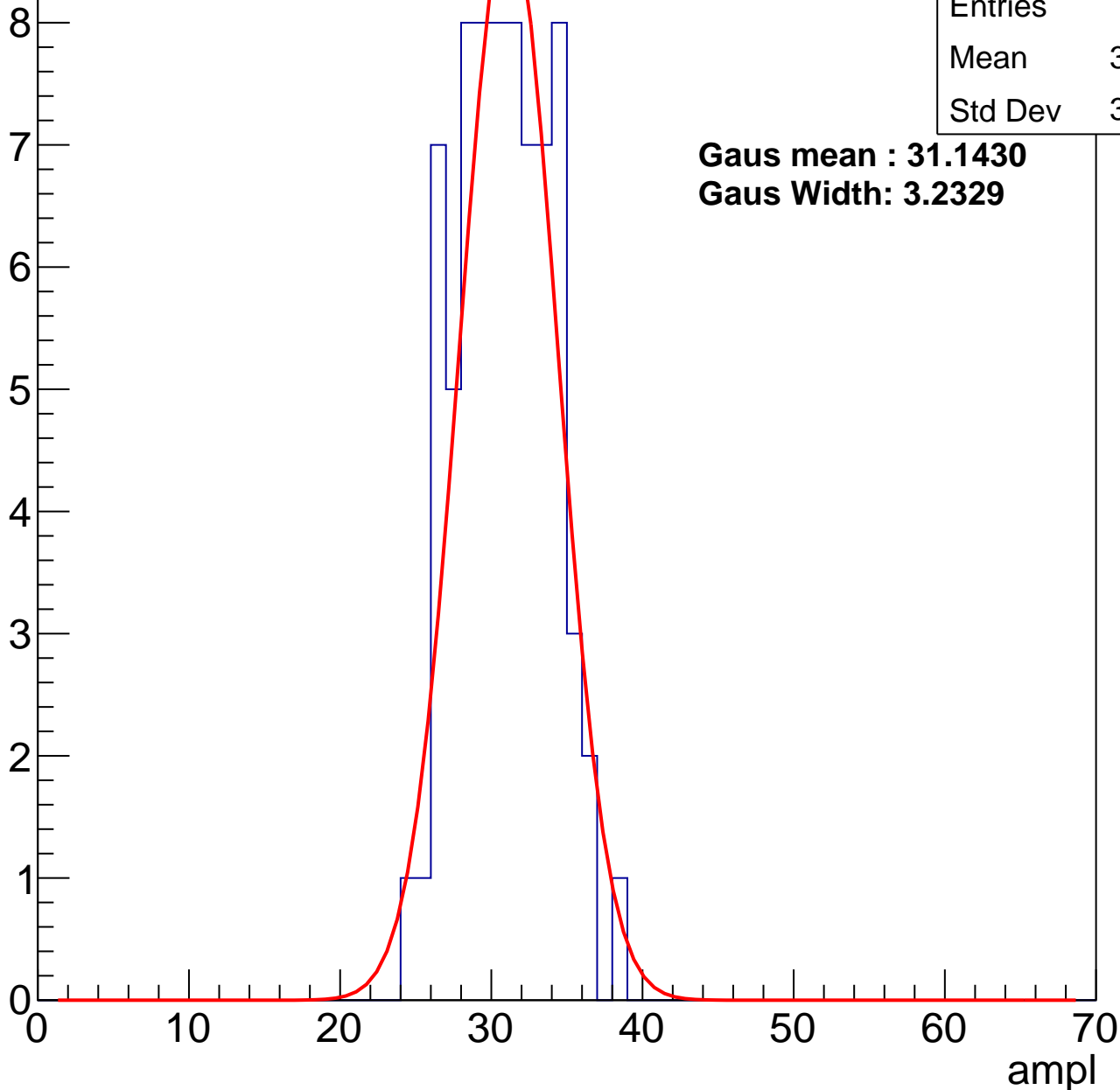
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.43
Std Dev	3.036

Gaus mean : 31.1430

Gaus Width: 3.2329



B1L003S, U13-ch50, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

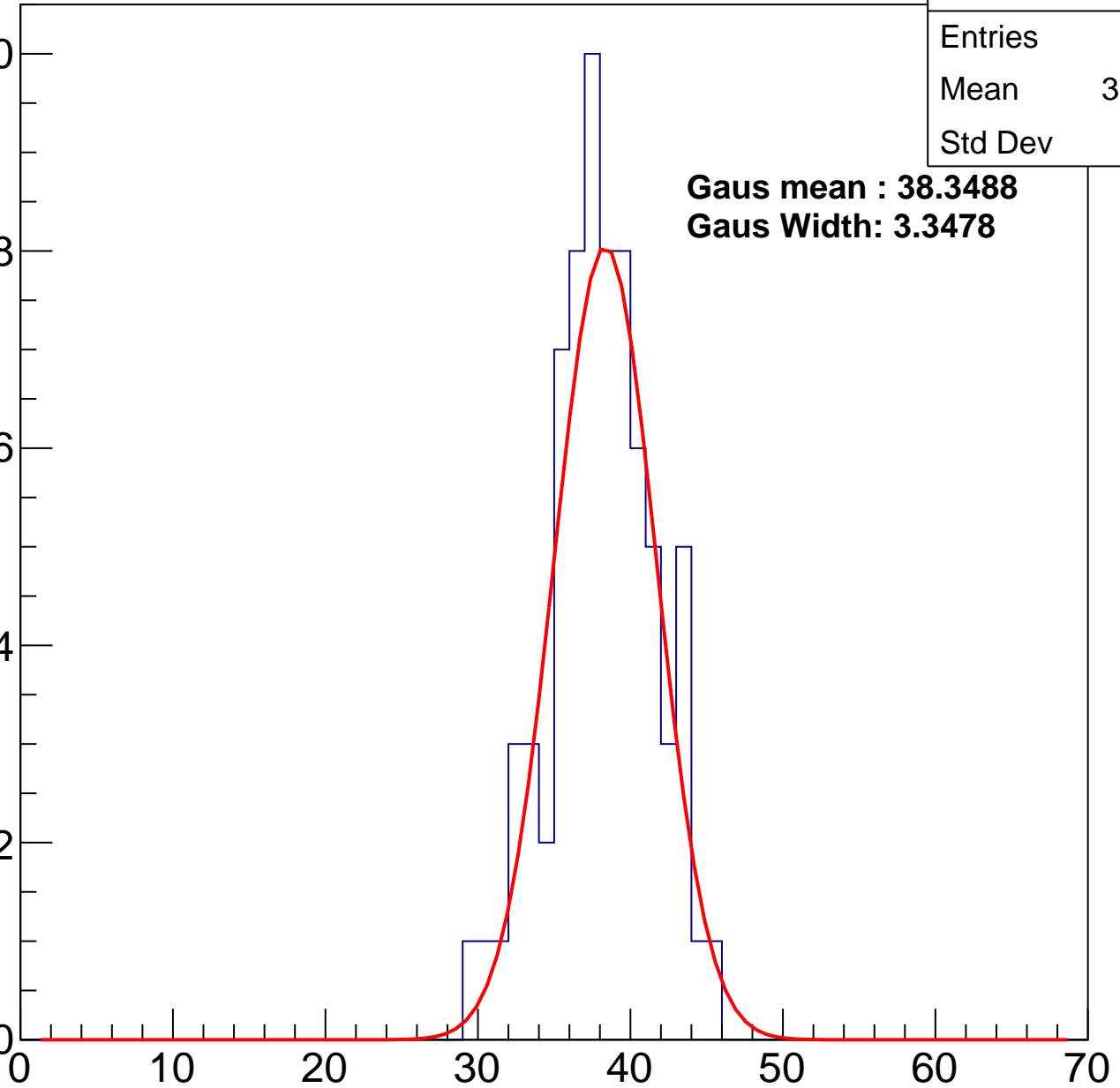
Entries	73
Mean	37.63
Std Dev	3.39

Gaus mean : 38.3488

Gaus Width: 3.3478

10
8
6
4
2
0

ampl



B1L003S, U13-ch50, adc2

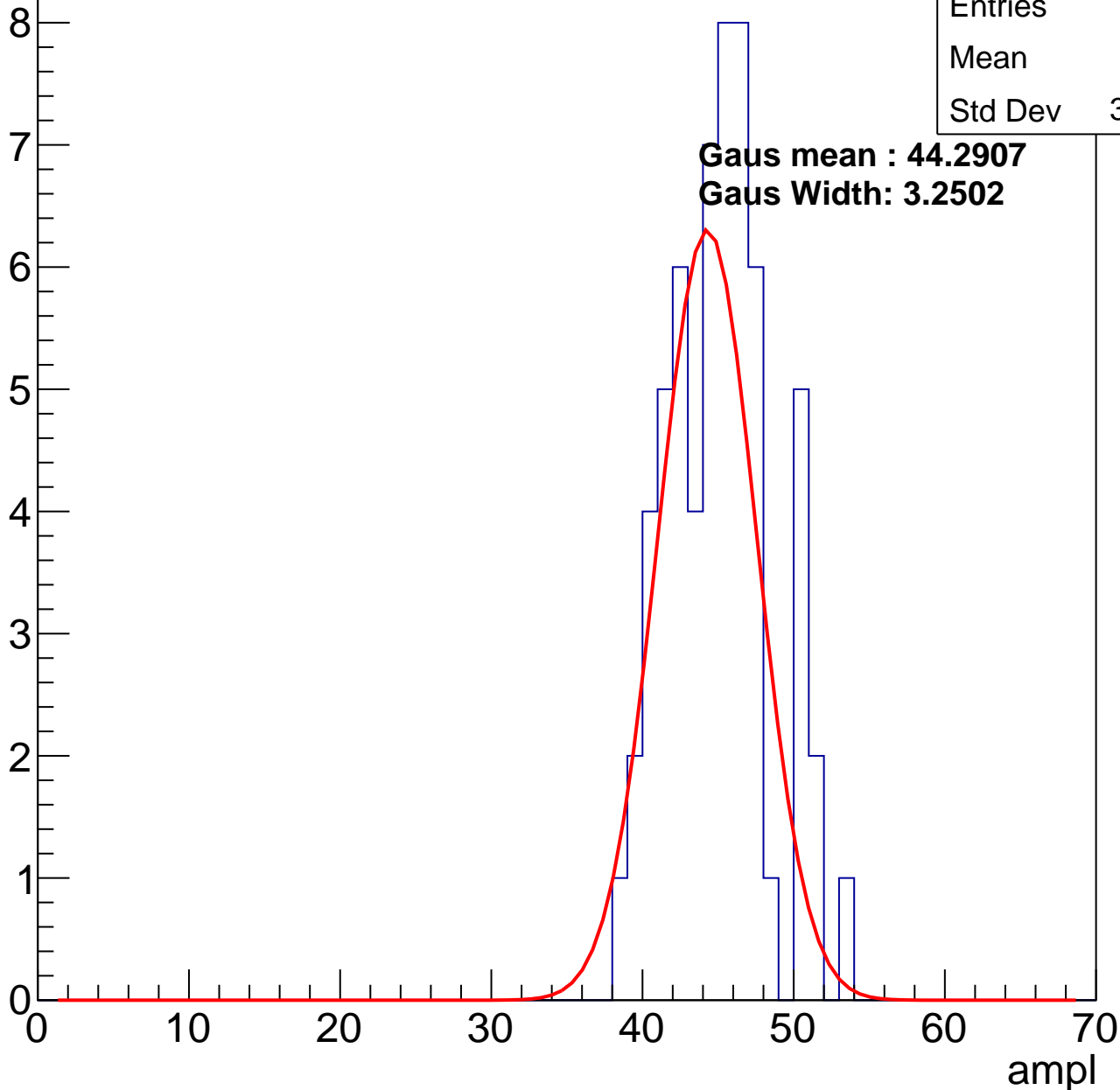
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	44.6
Std Dev	3.343

Gaus mean : 44.2907

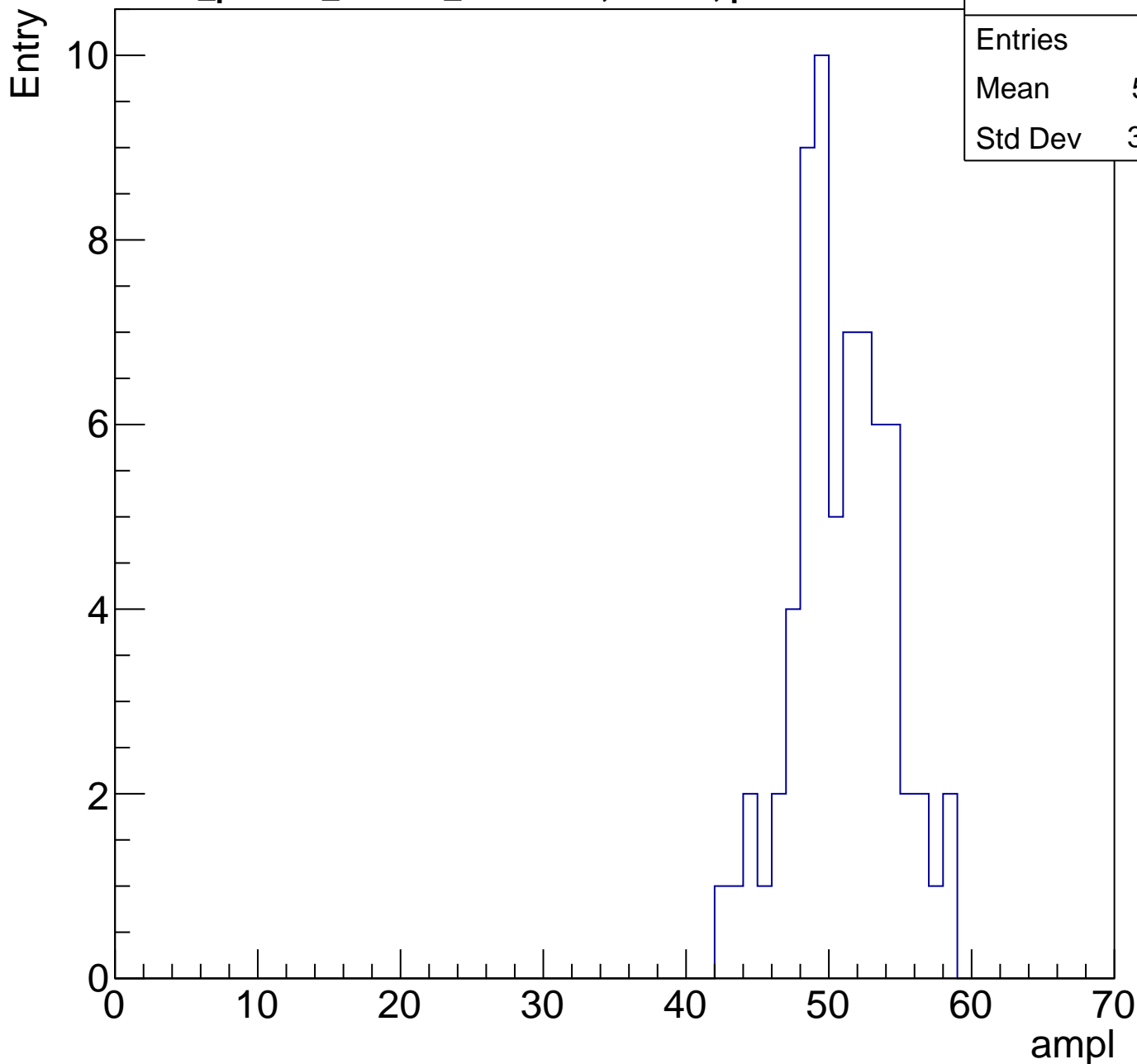
Gaus Width: 3.2502



B1L003S, U13-ch50, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	50.41
Std Dev	3.422

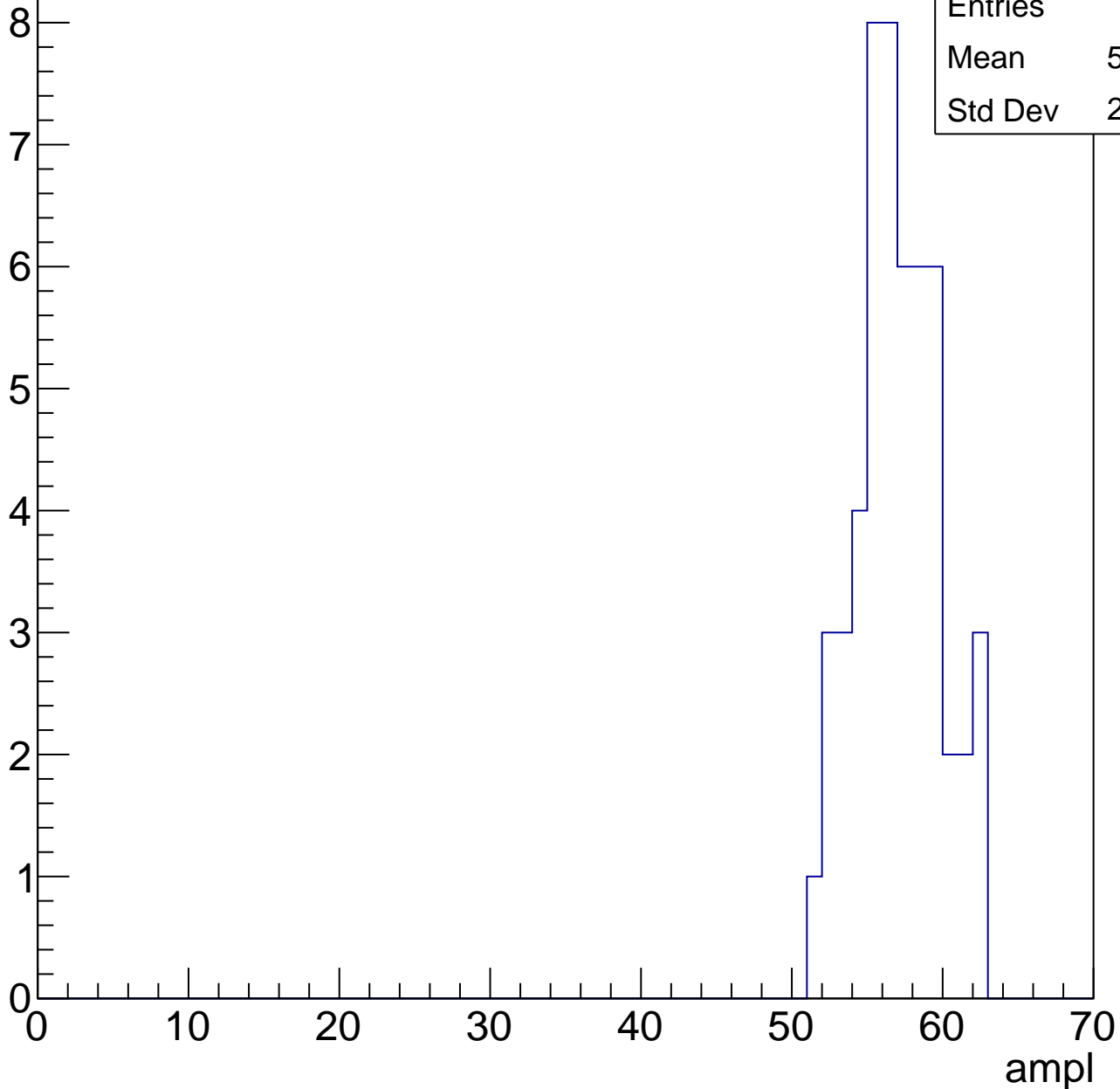


B1L003S, U13-ch50, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	56.58
Std Dev	2.706

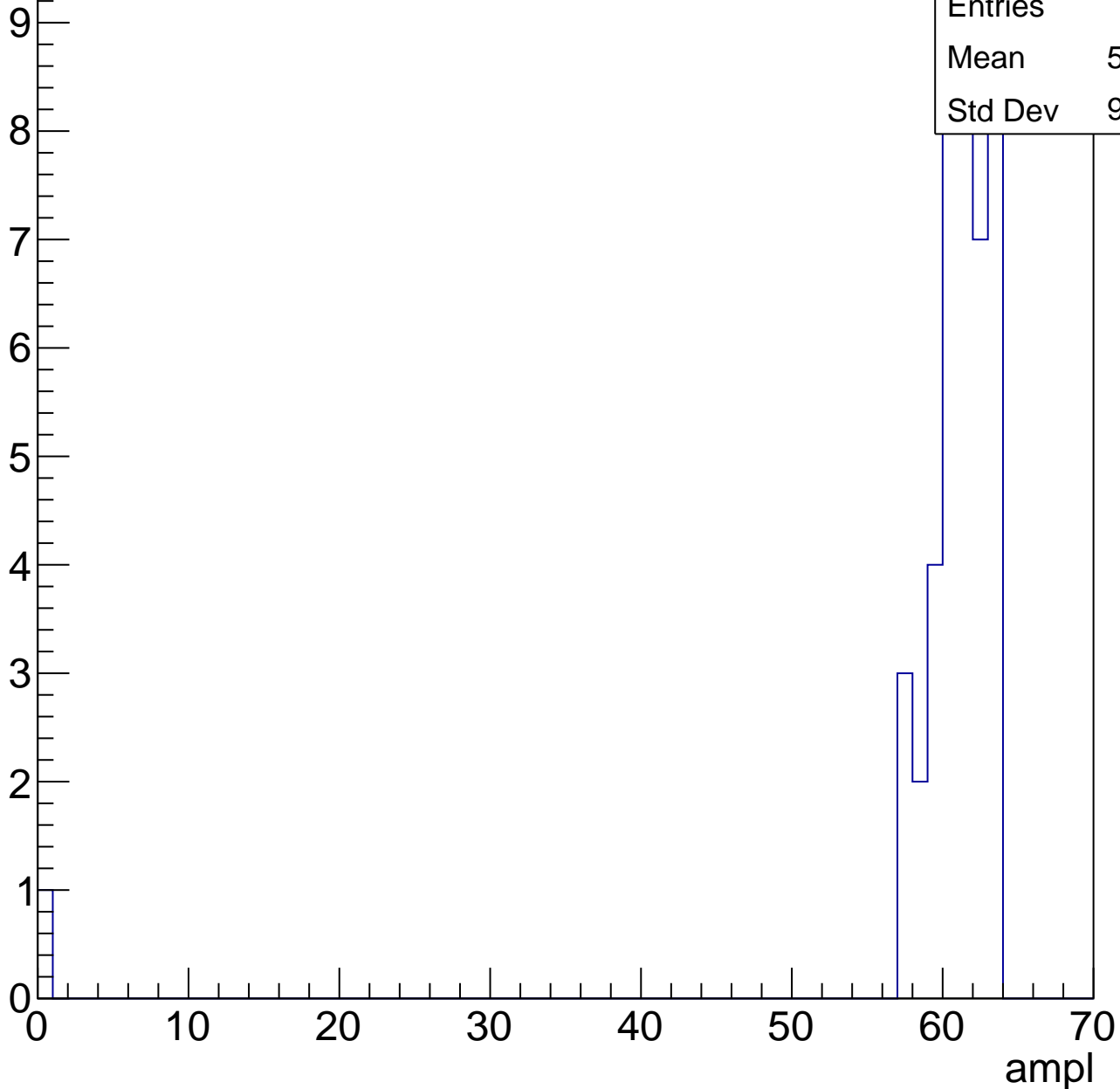


B1L003S, U13-ch50, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.33
Std Dev	9.433



B1L003S, U13-ch50, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch50, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L003S, U13-ch51, adc0

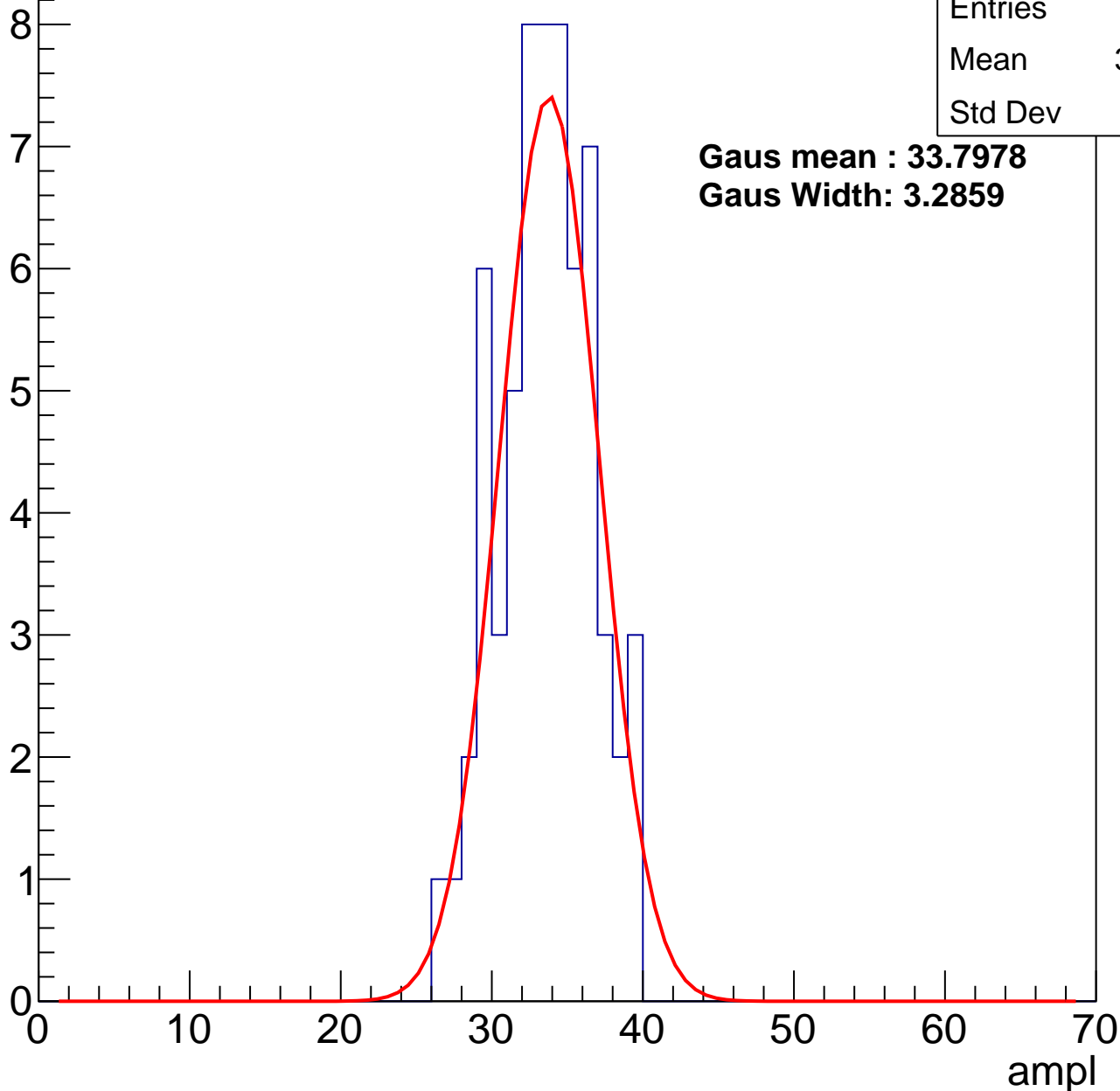
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	33.11
Std Dev	3.05

Gaus mean : 33.7978

Gaus Width: 3.2859



B1L003S, U13-ch51, adc1

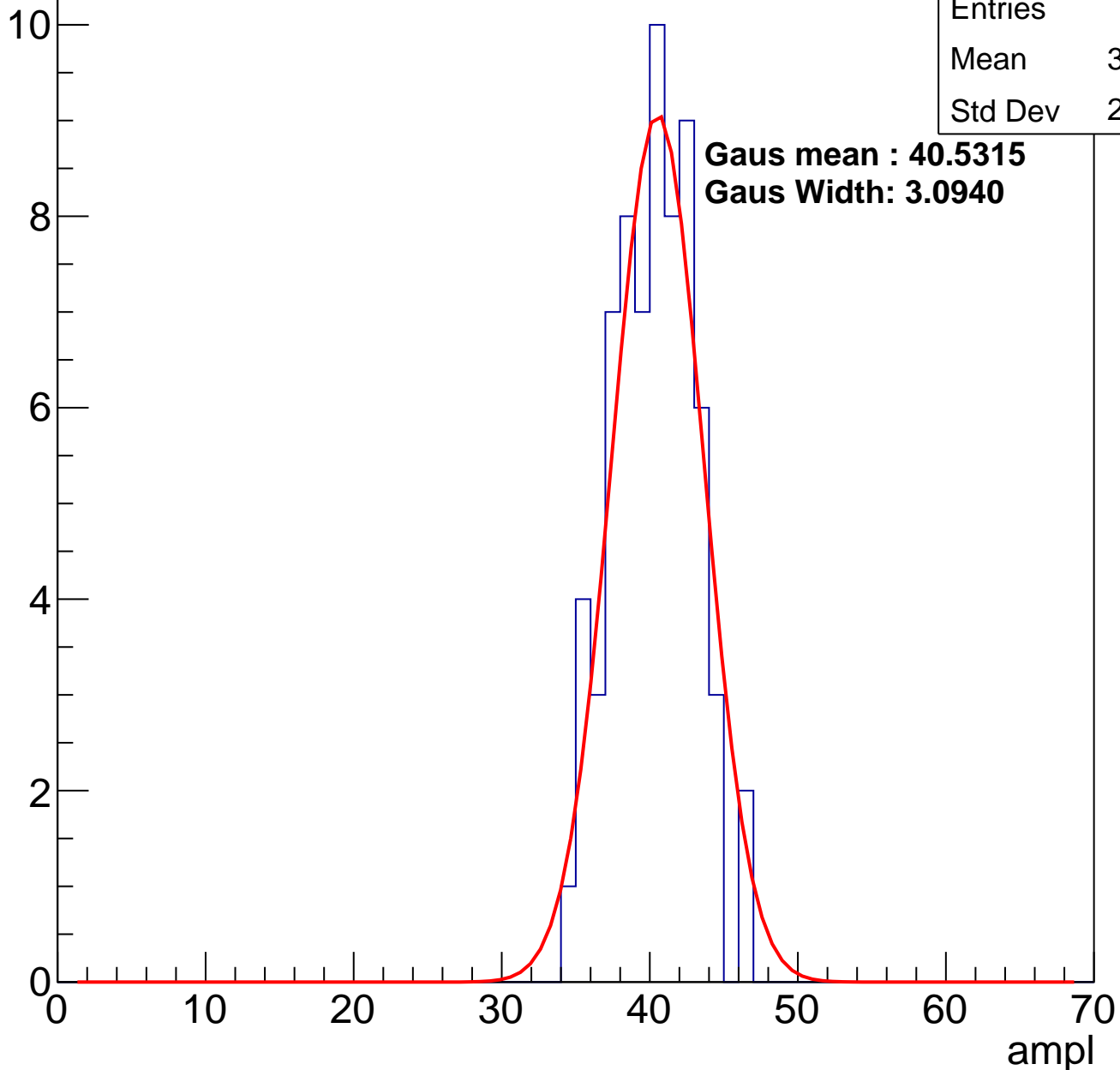
calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	39.79
Std Dev	2.715

Gaus mean : 40.5315

Gaus Width: 3.0940

Entry



B1L003S, U13-ch51, adc2

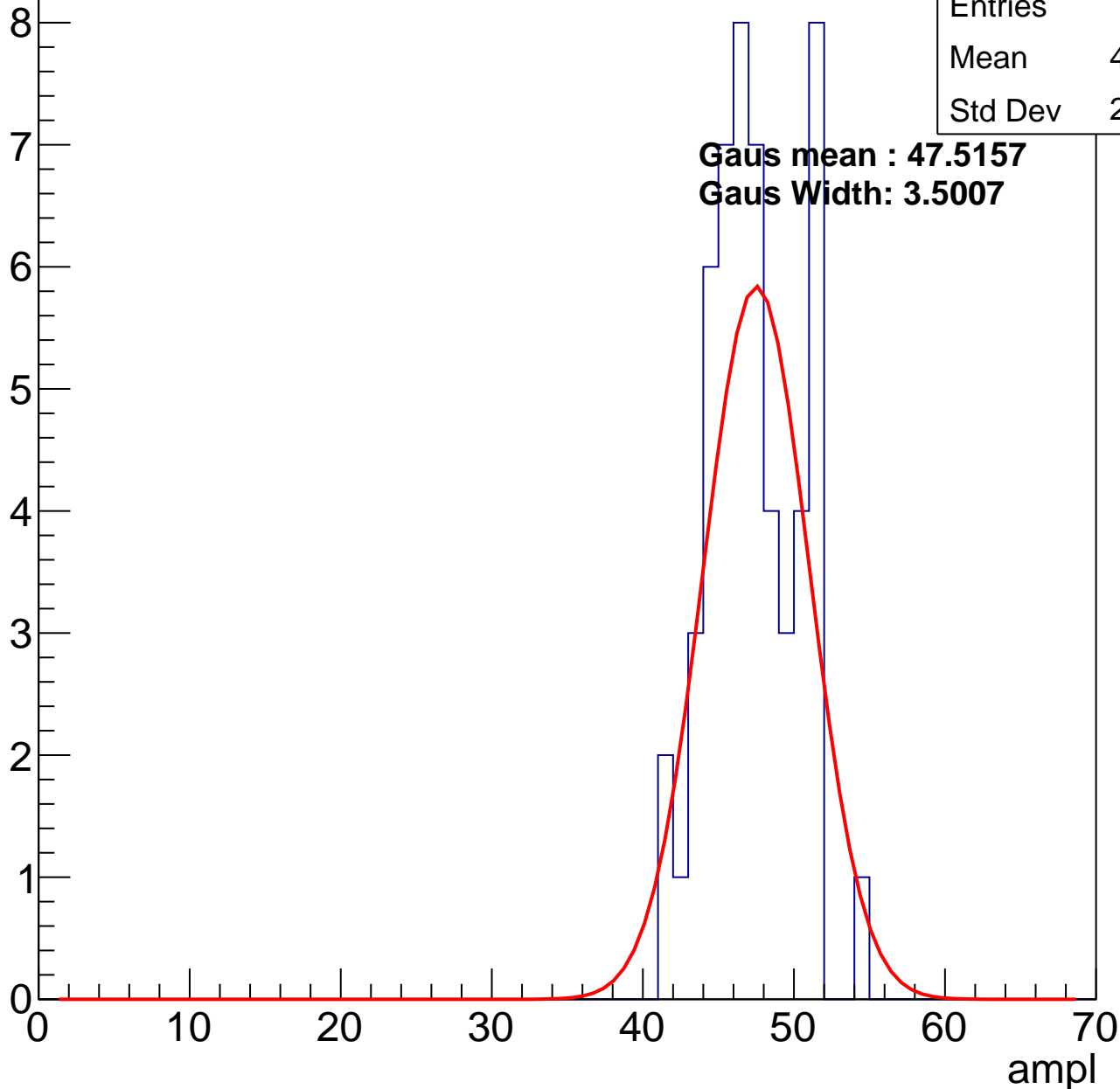
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	46.85
Std Dev	2.928

Gaus mean : 47.5157

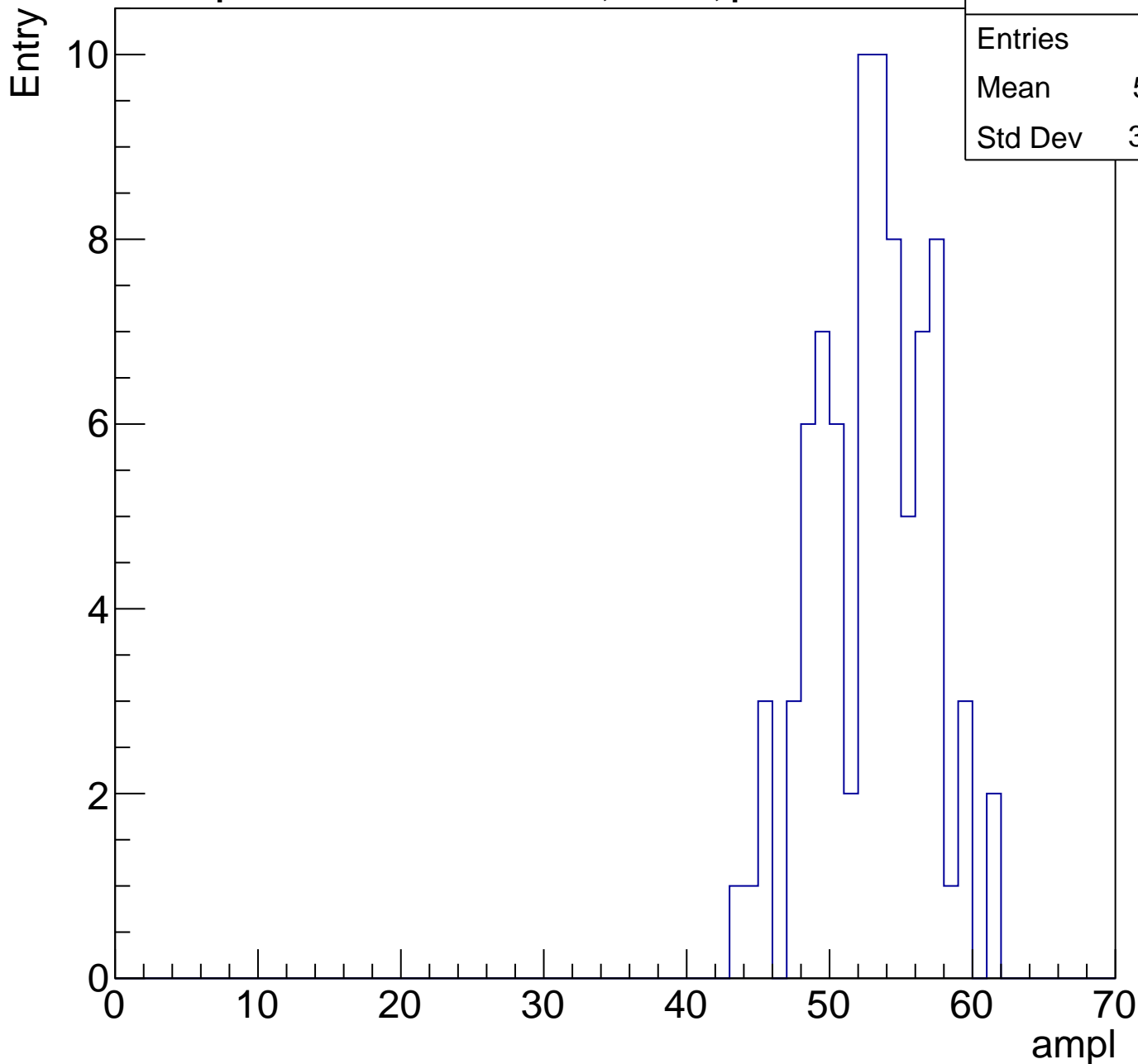
Gaus Width: 3.5007



B1L003S, U13-ch51, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	52.51
Std Dev	3.919

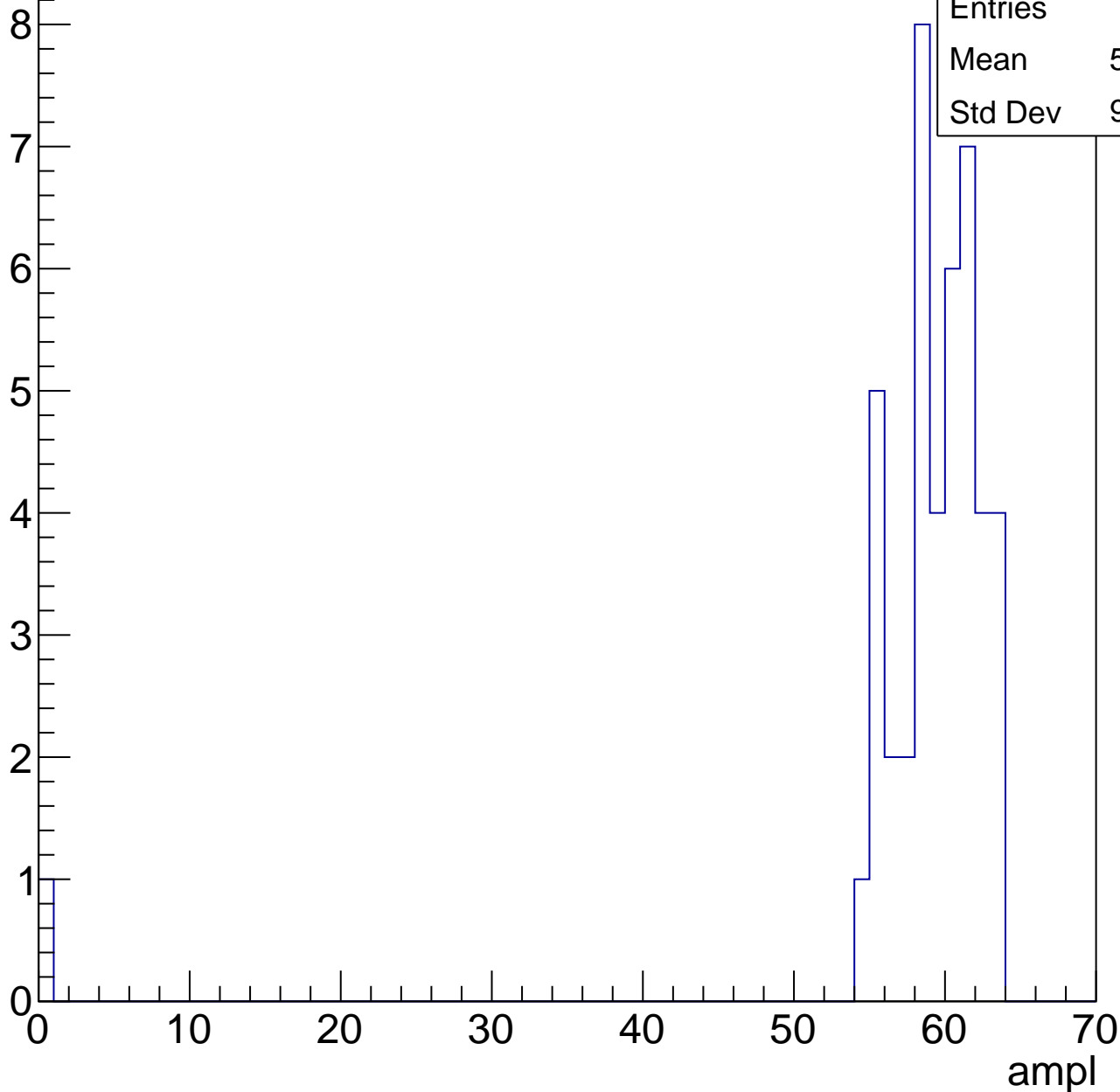


B1L003S, U13-ch51, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

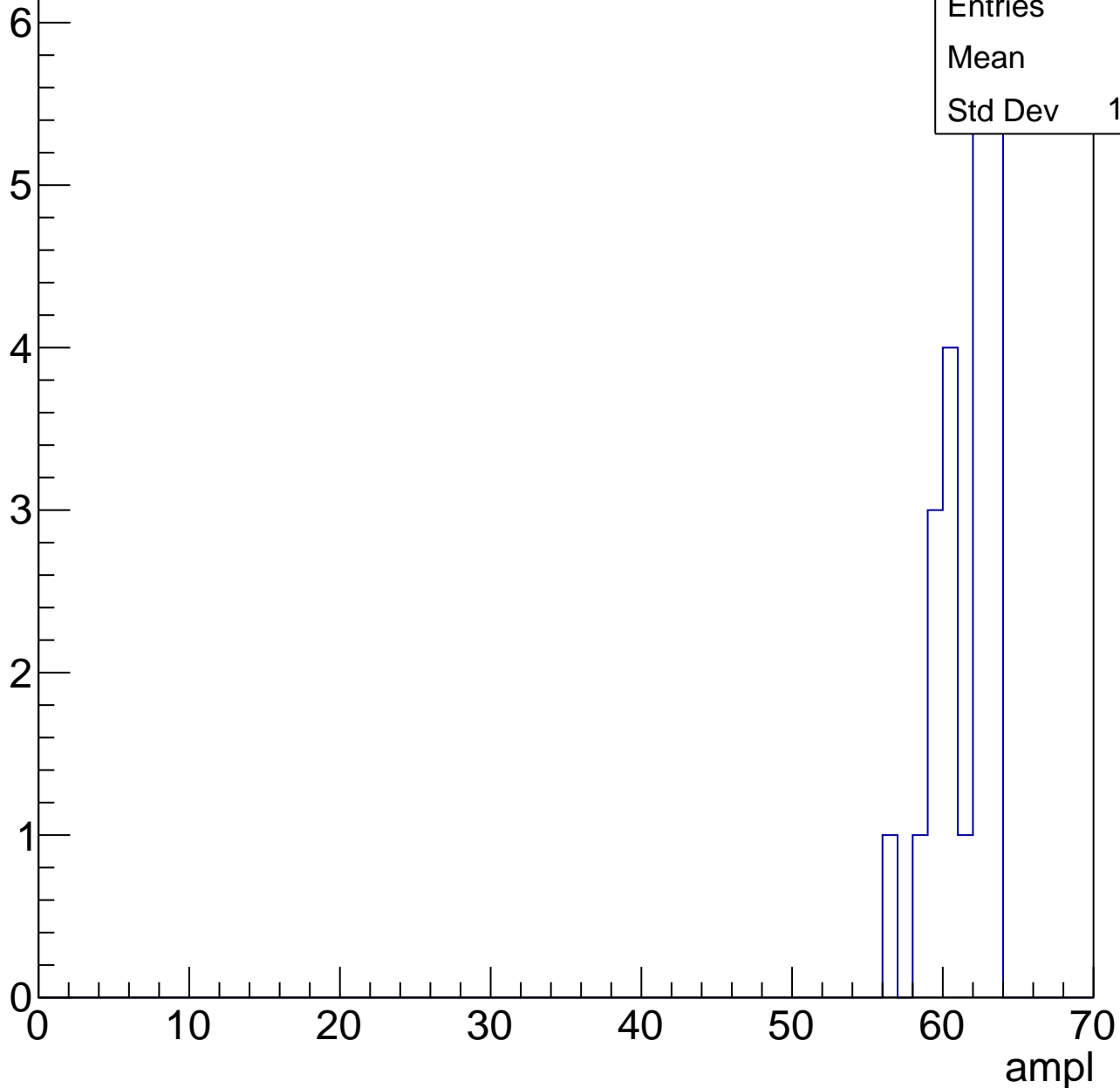
Entries	44
Mean	57.77
Std Dev	9.155



B1L003S, U13-ch51, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch51, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch51, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch52, adc0

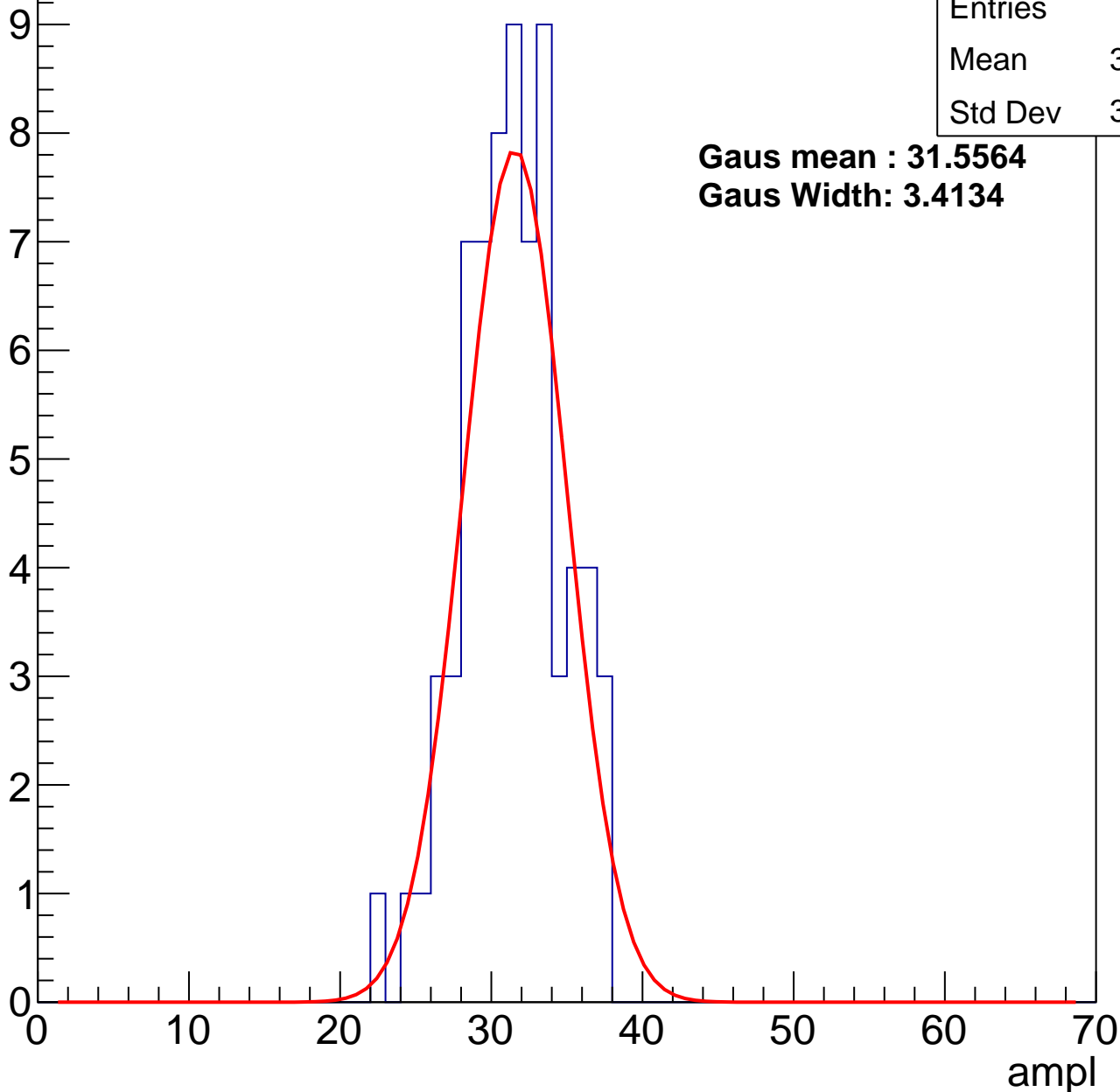
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.94
Std Dev	3.229

Gaus mean : 31.5564

Gaus Width: 3.4134



B1L003S, U13-ch52, adc1

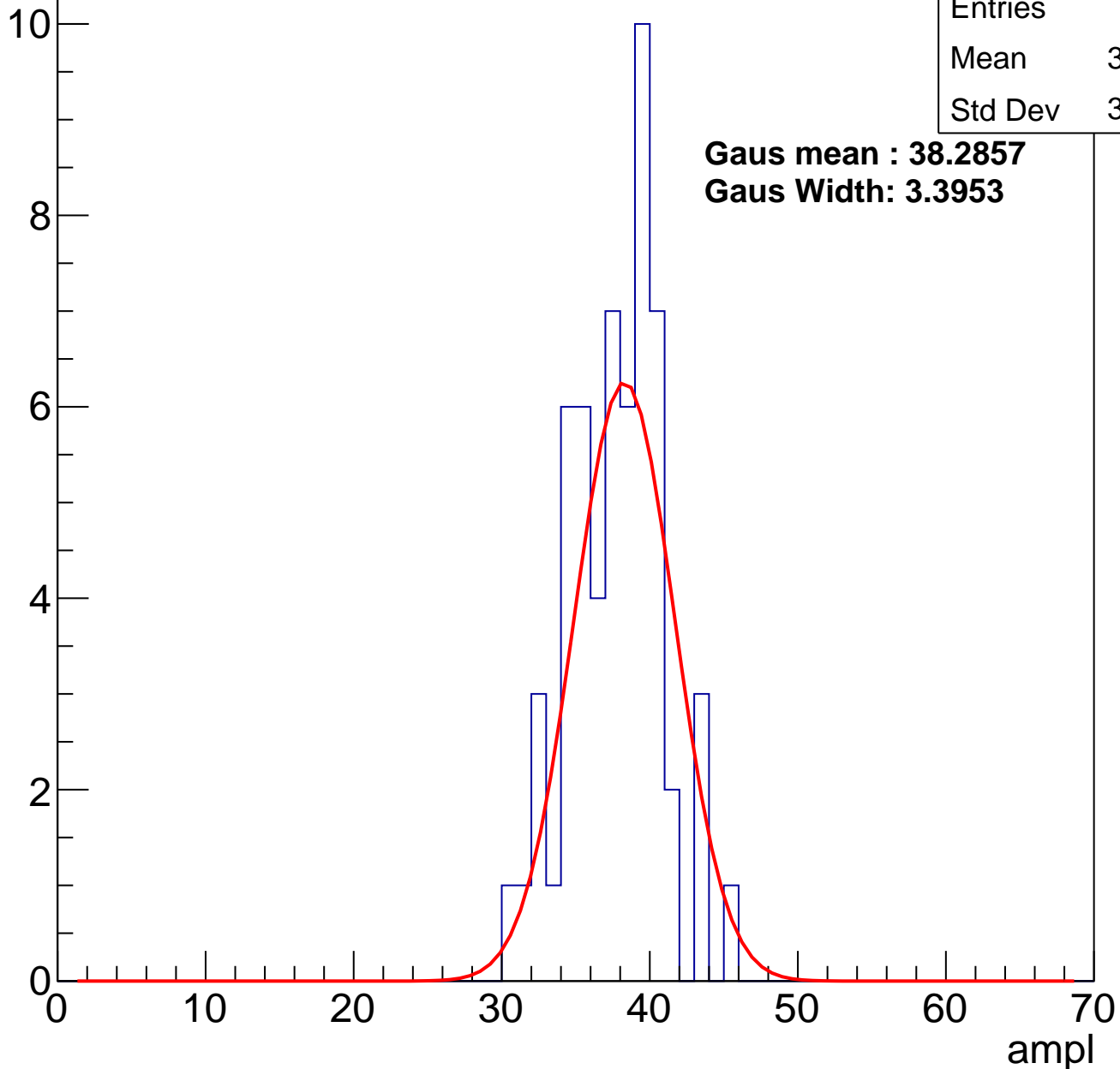
calib_packv5_042523_0143.root, FC#13, port D2

Entries	58
Mean	37.26
Std Dev	3.127

Gaus mean : 38.2857

Gaus Width: 3.3953

Entry



B1L003S, U13-ch52, adc2

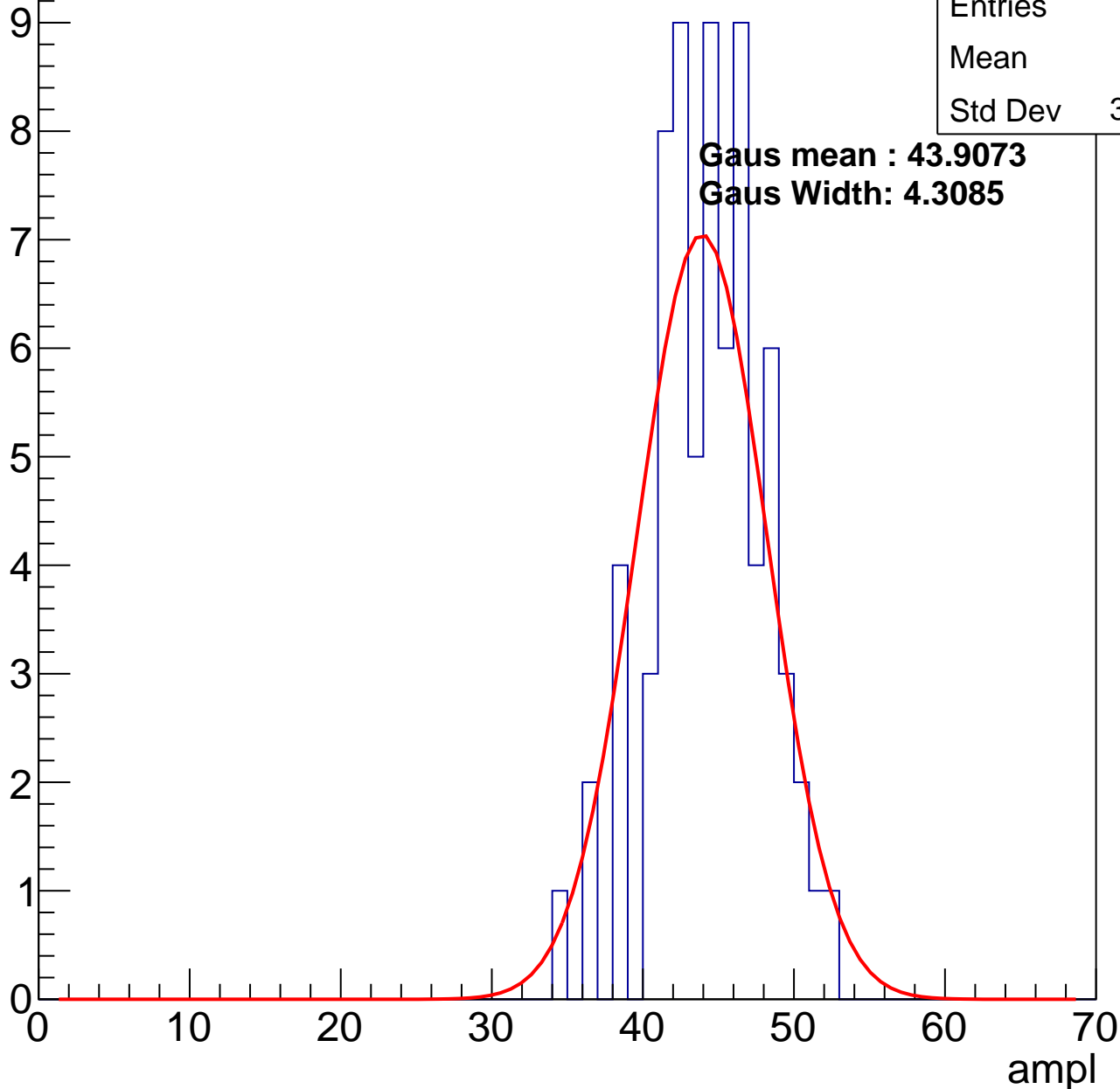
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.9
Std Dev	3.638

Gaus mean : 43.9073

Gaus Width: 4.3085

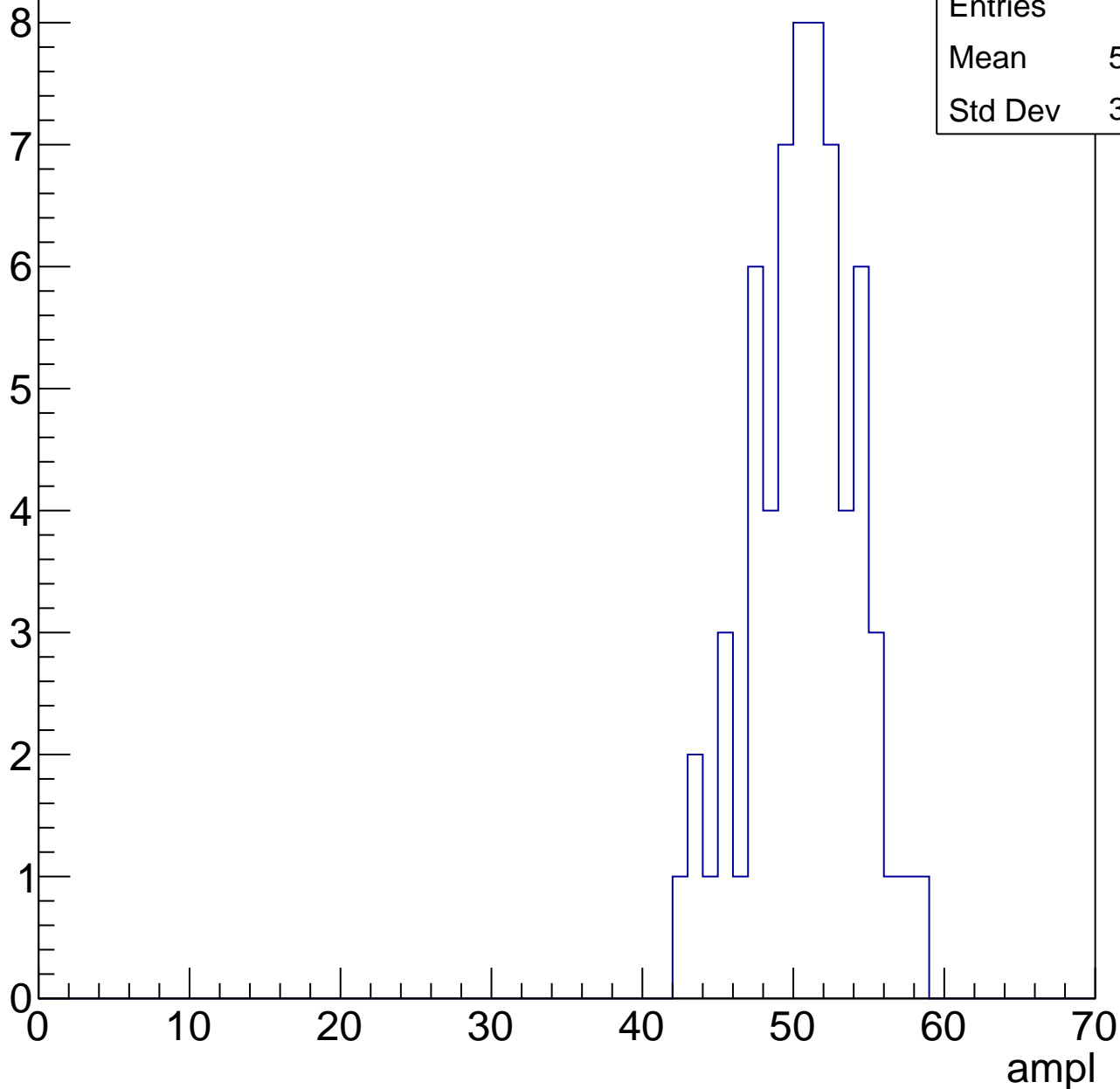


B1L003S, U13-ch52, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	50.22
Std Dev	3.435

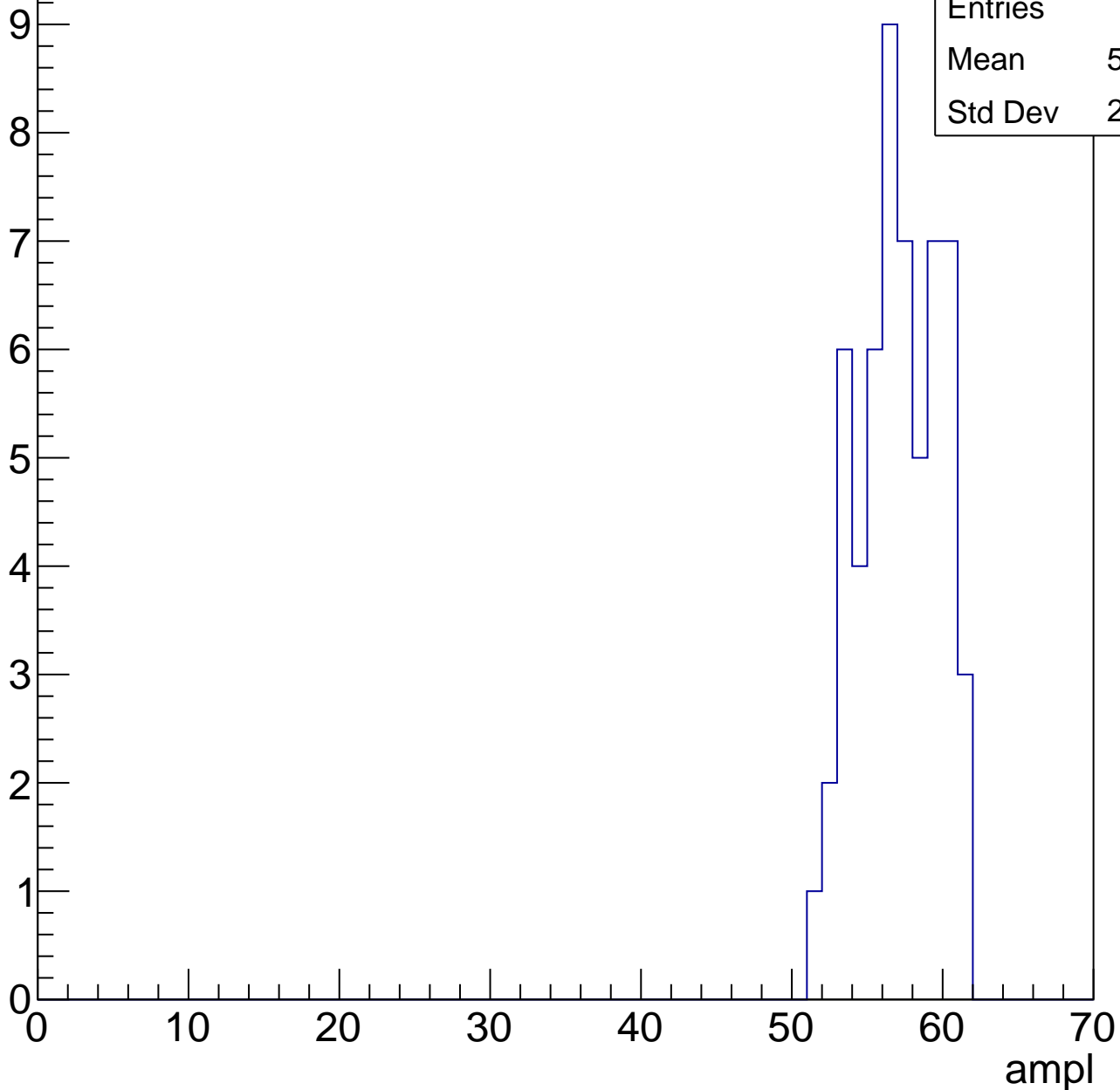


B1L003S, U13-ch52, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.63
Std Dev	2.607

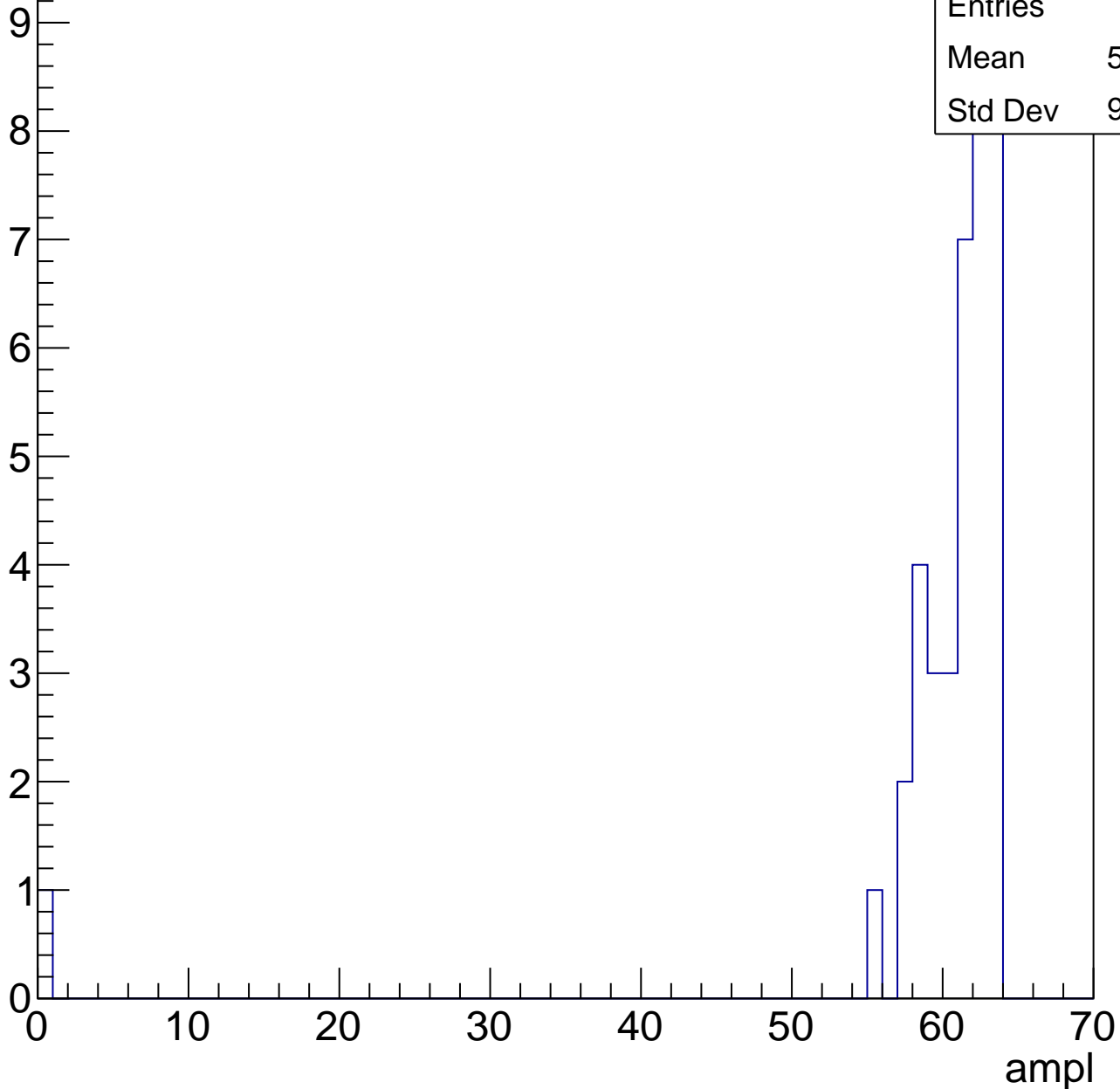


B1L003S, U13-ch52, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59.13
Std Dev	9.932



B1L003S, U13-ch52, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

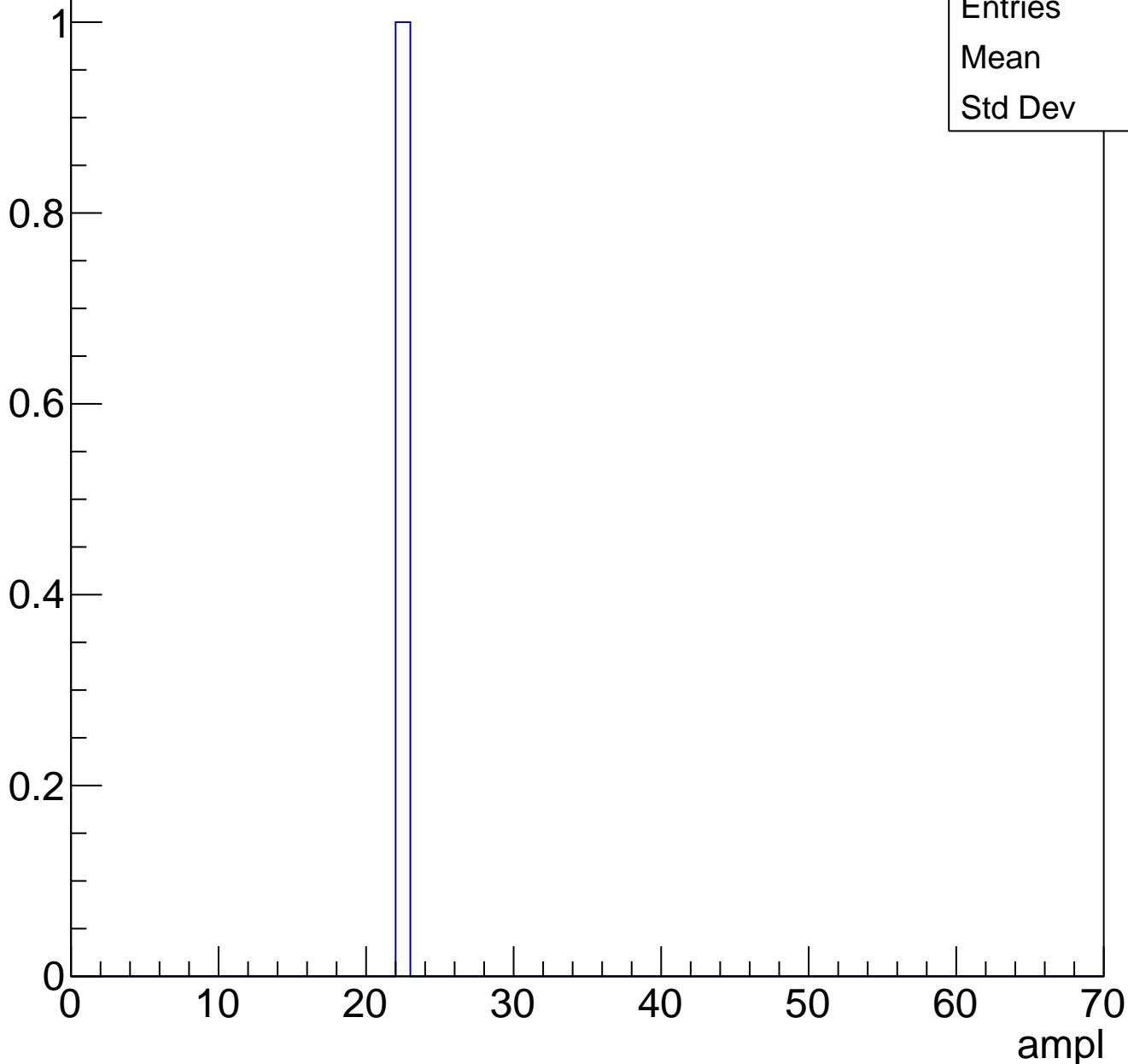
ampl

0 10 20 30 40 50 60 70

B1L003S, U13-ch52, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch53, adc0

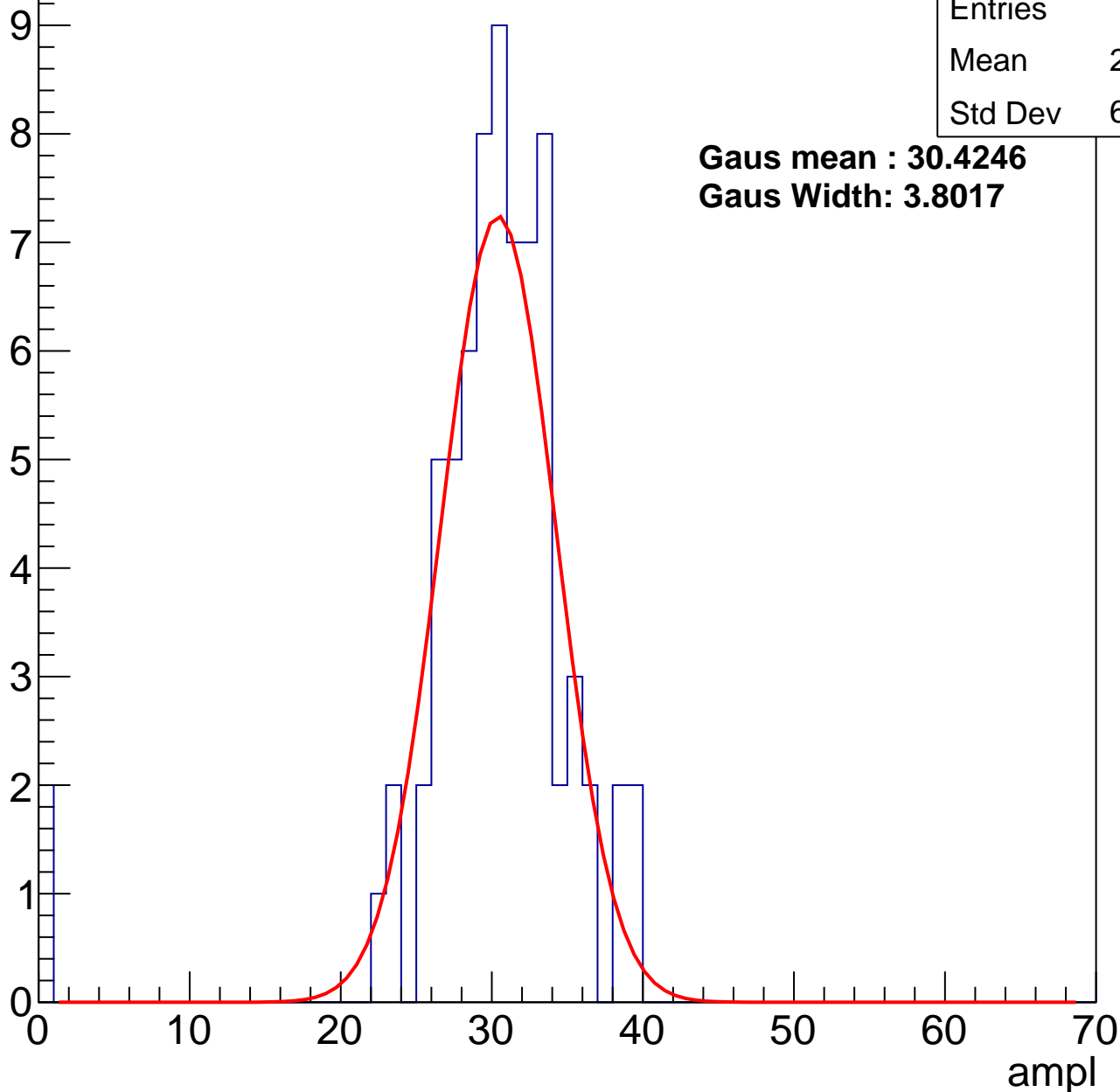
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.55
Std Dev	6.115

Gaus mean : 30.4246

Gaus Width: 3.8017



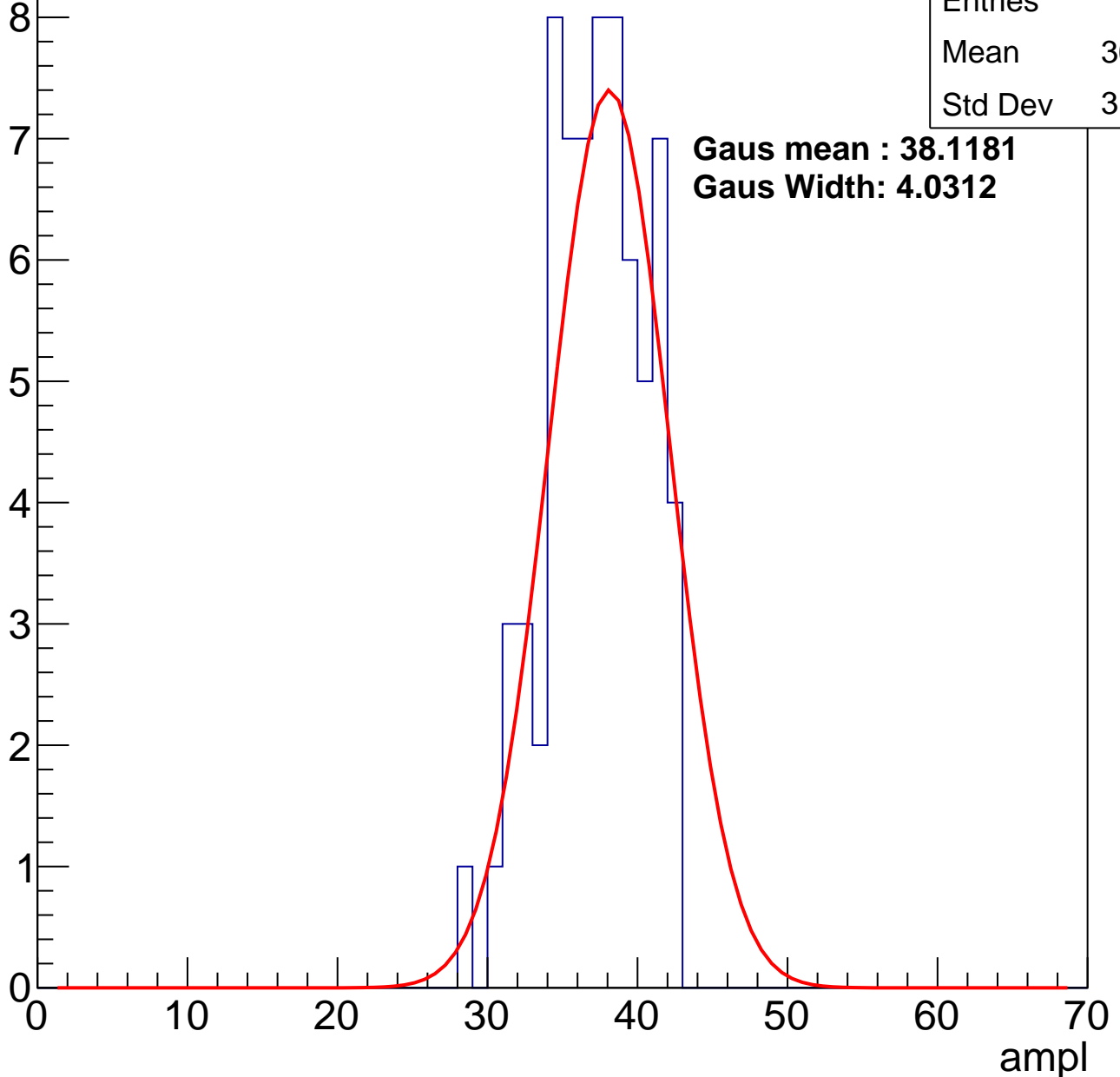
B1L003S, U13-ch53, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.73
Std Dev	3.242

Gaus mean : 38.1181
Gaus Width: 4.0312



B1L003S, U13-ch53, adc2

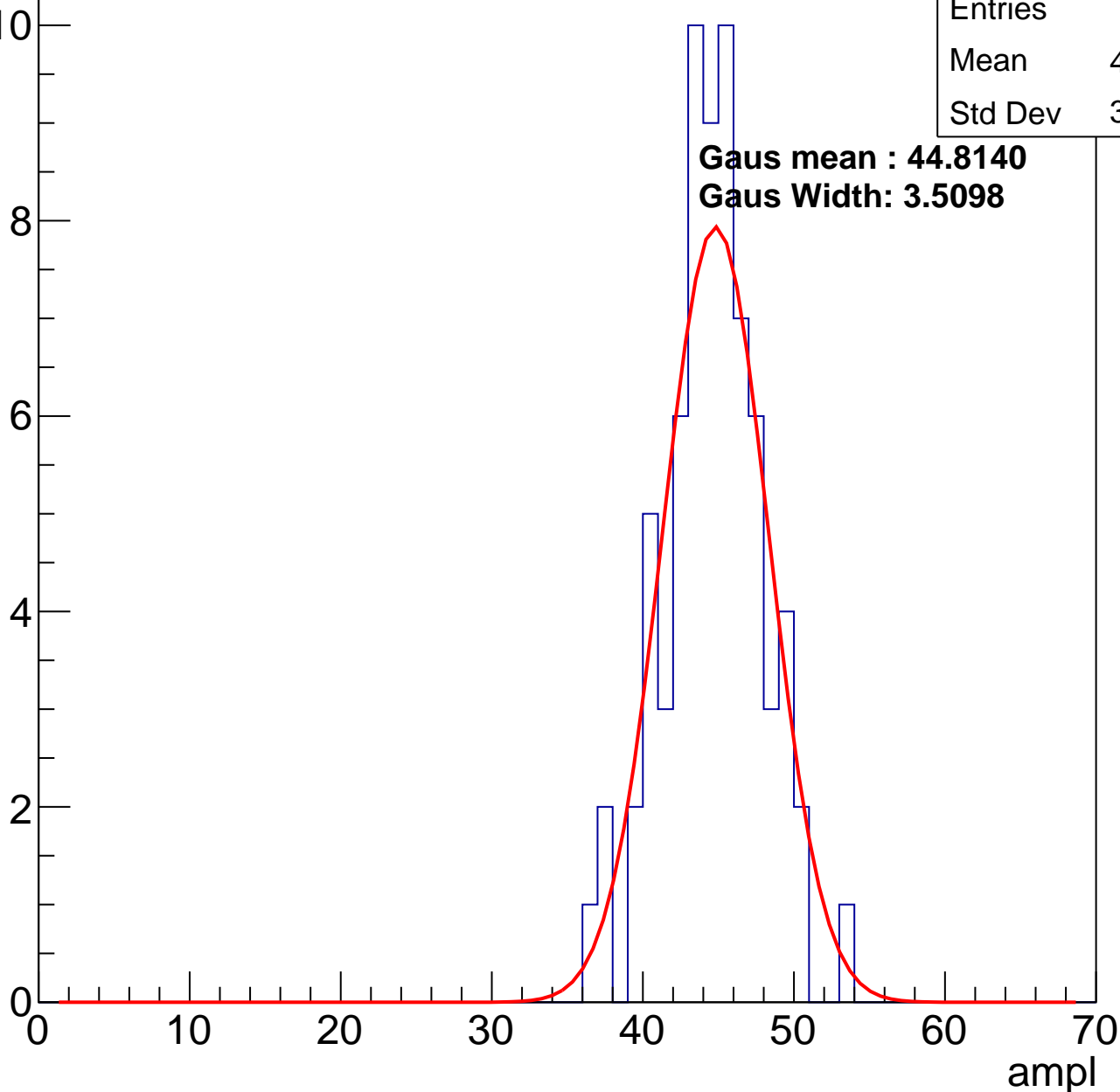
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	44.17
Std Dev	3.237

Gaus mean : 44.8140

Gaus Width: 3.5098

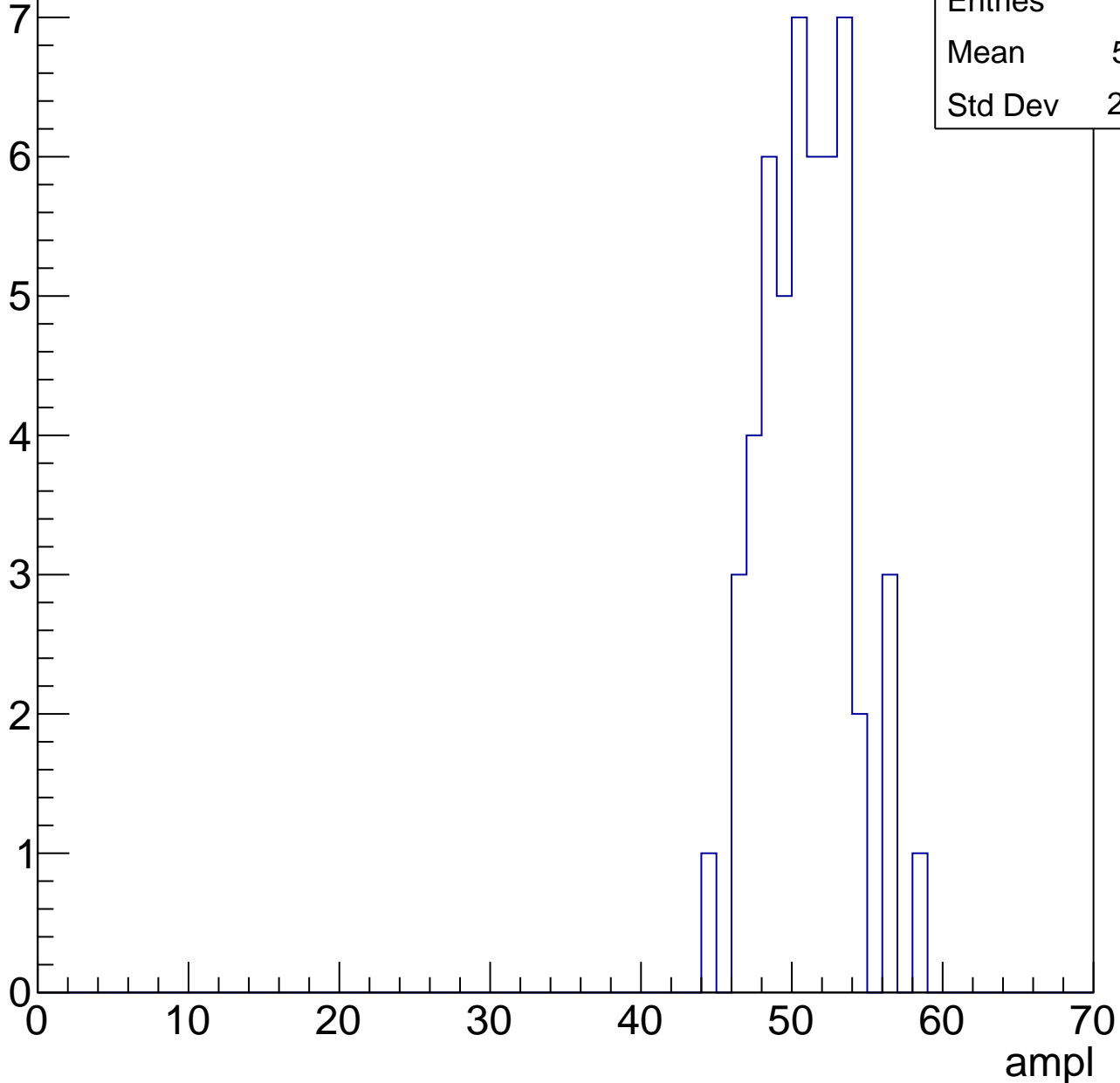


B1L003S, U13-ch53, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	50.51
Std Dev	2.913

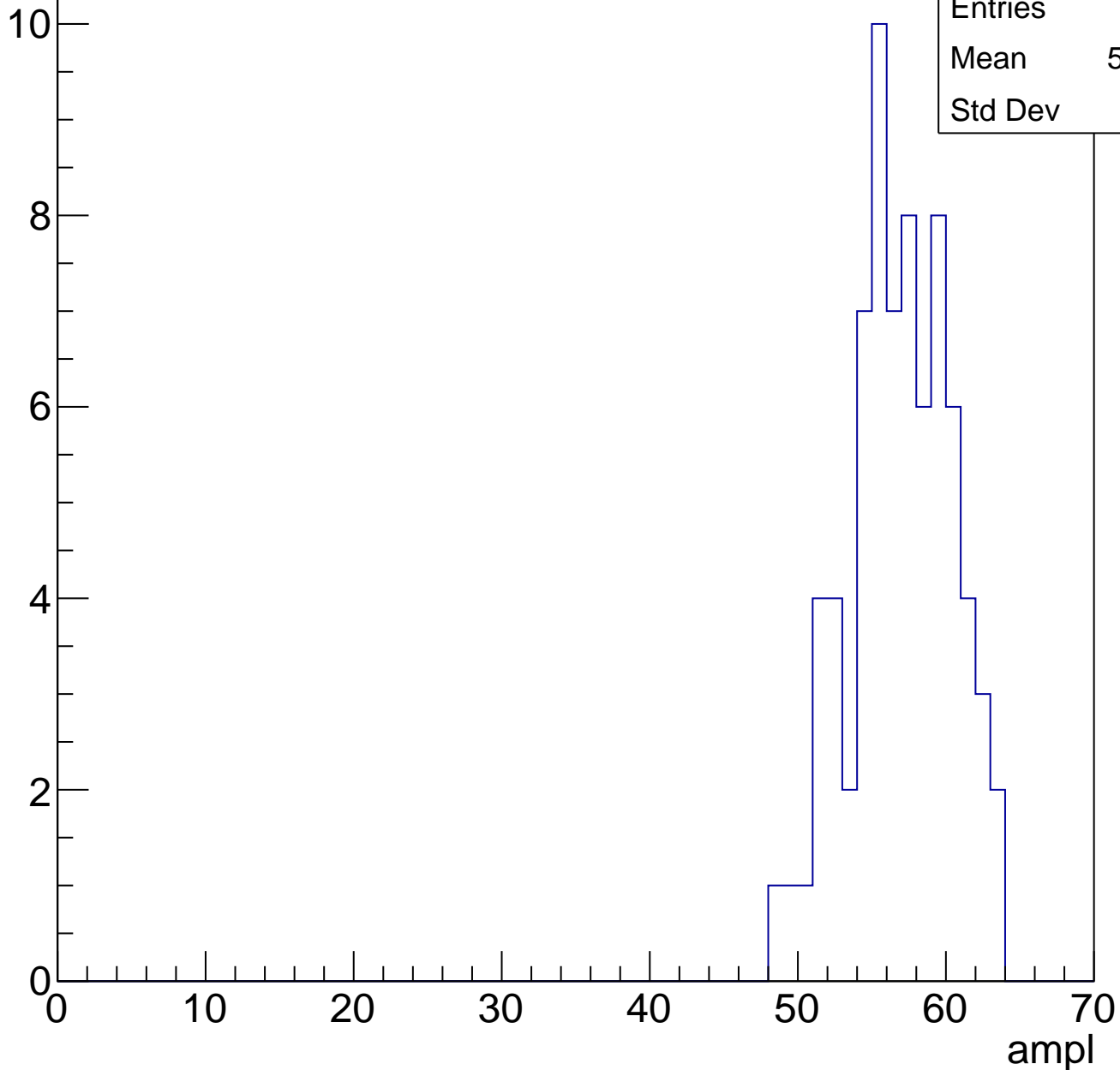


B1L003S, U13-ch53, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	56.45
Std Dev	3.41

Entry

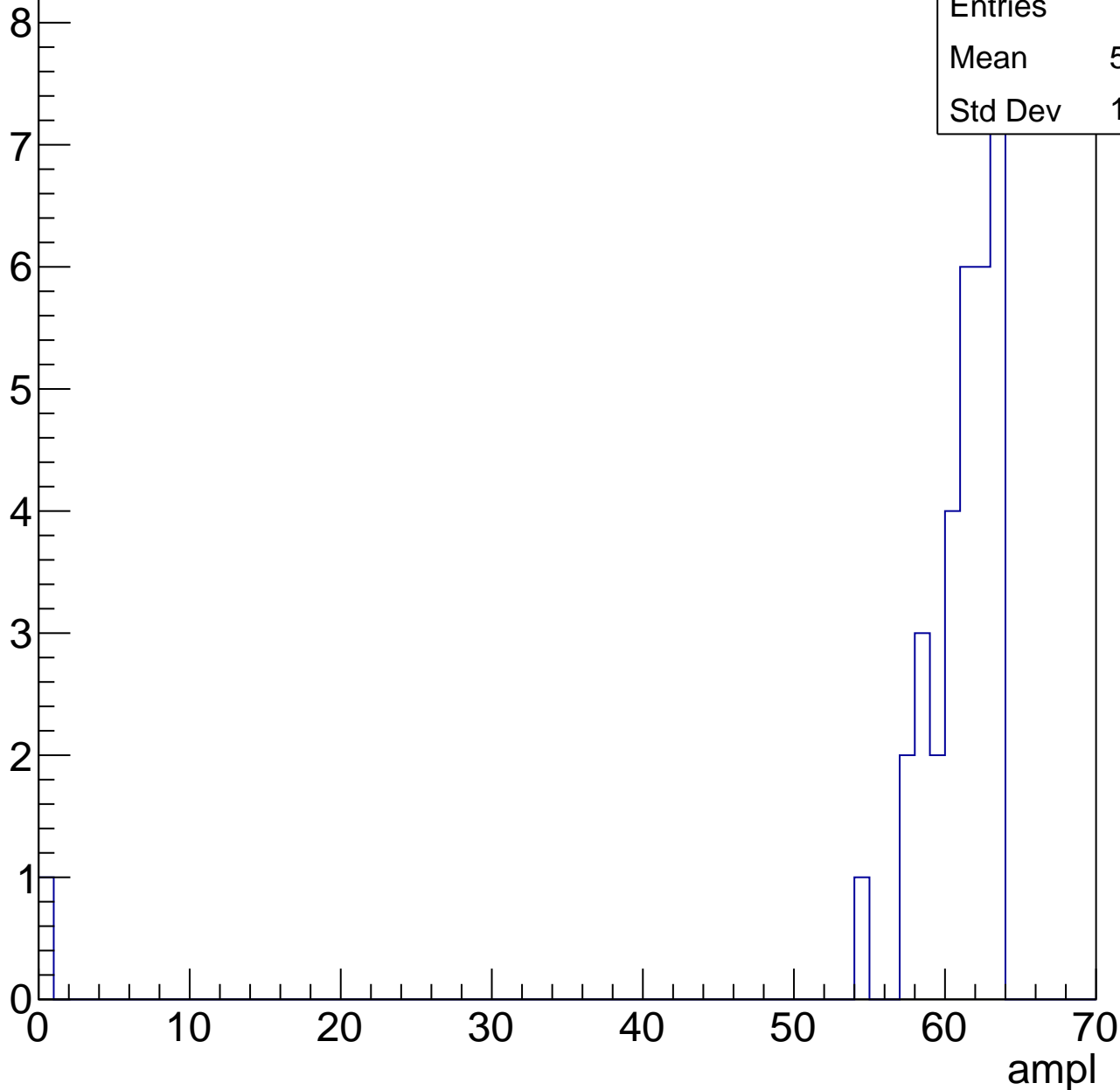


B1L003S, U13-ch53, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.85
Std Dev	10.63



B1L003S, U13-ch53, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch53, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch54, adc0

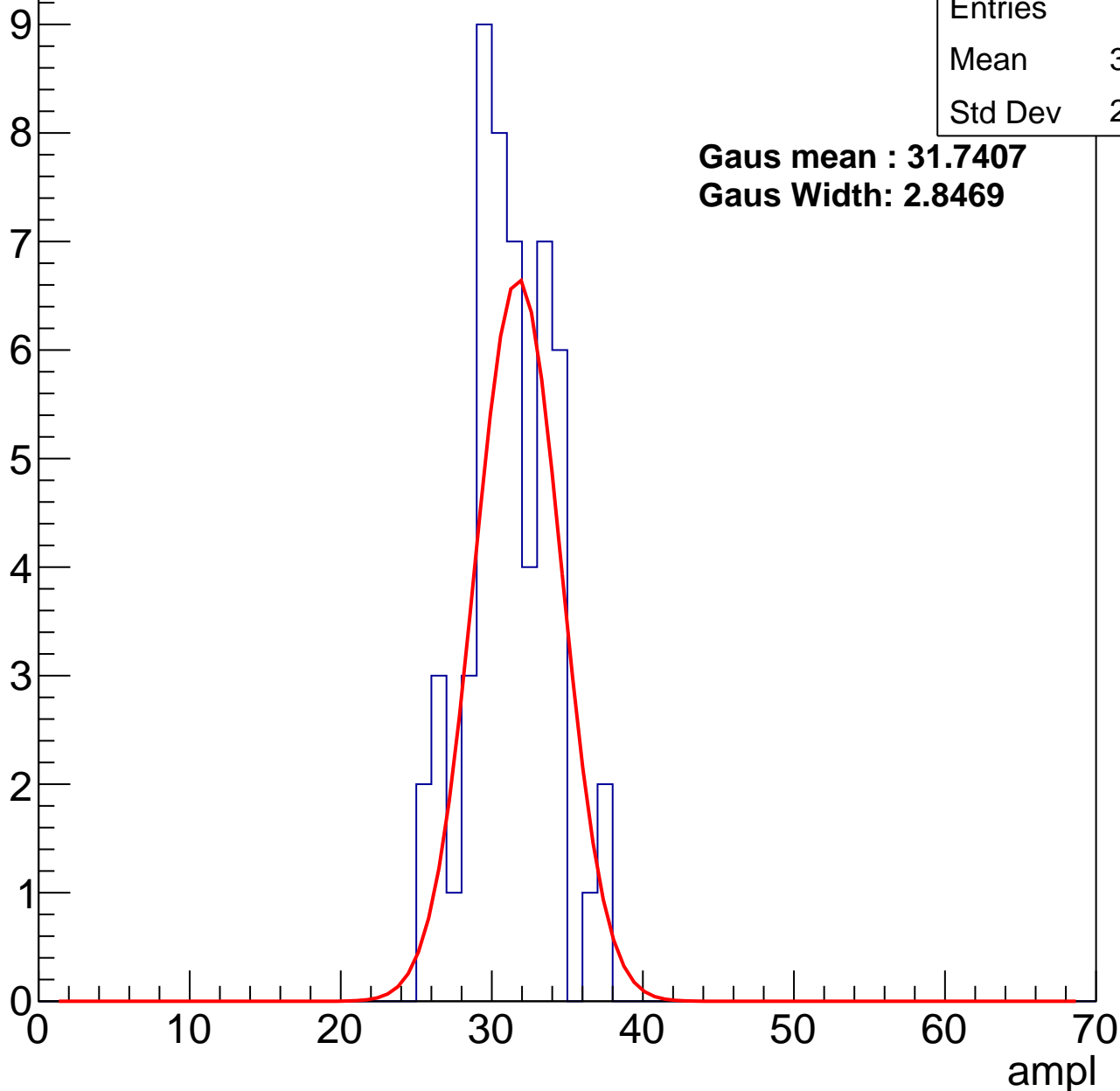
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	30.75
Std Dev	2.794

Gaus mean : 31.7407

Gaus Width: 2.8469

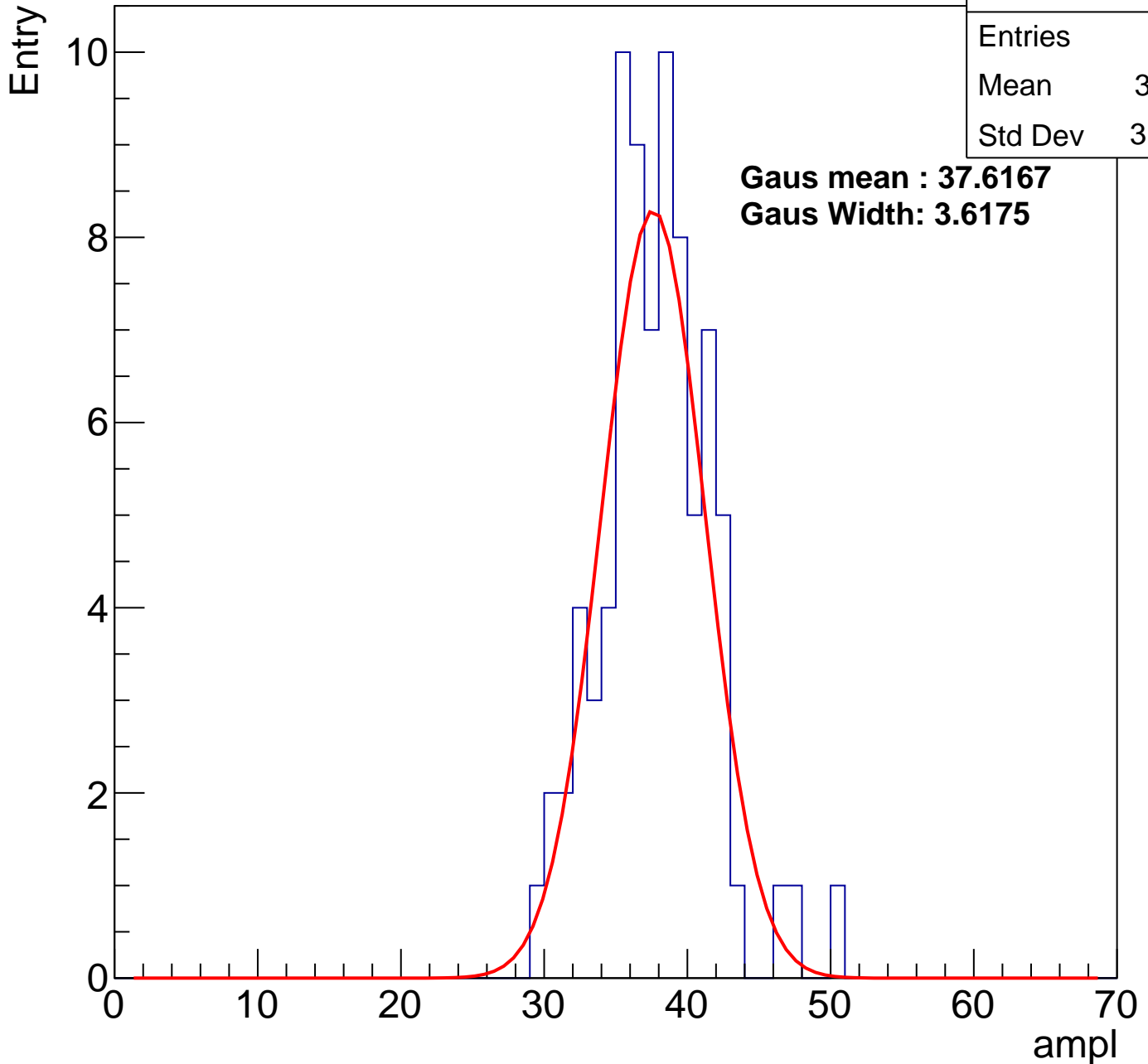


B1L003S, U13-ch54, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	37.31
Std Dev	3.799

Gaus mean : 37.6167
Gaus Width: 3.6175



B1L003S, U13-ch54, adc2

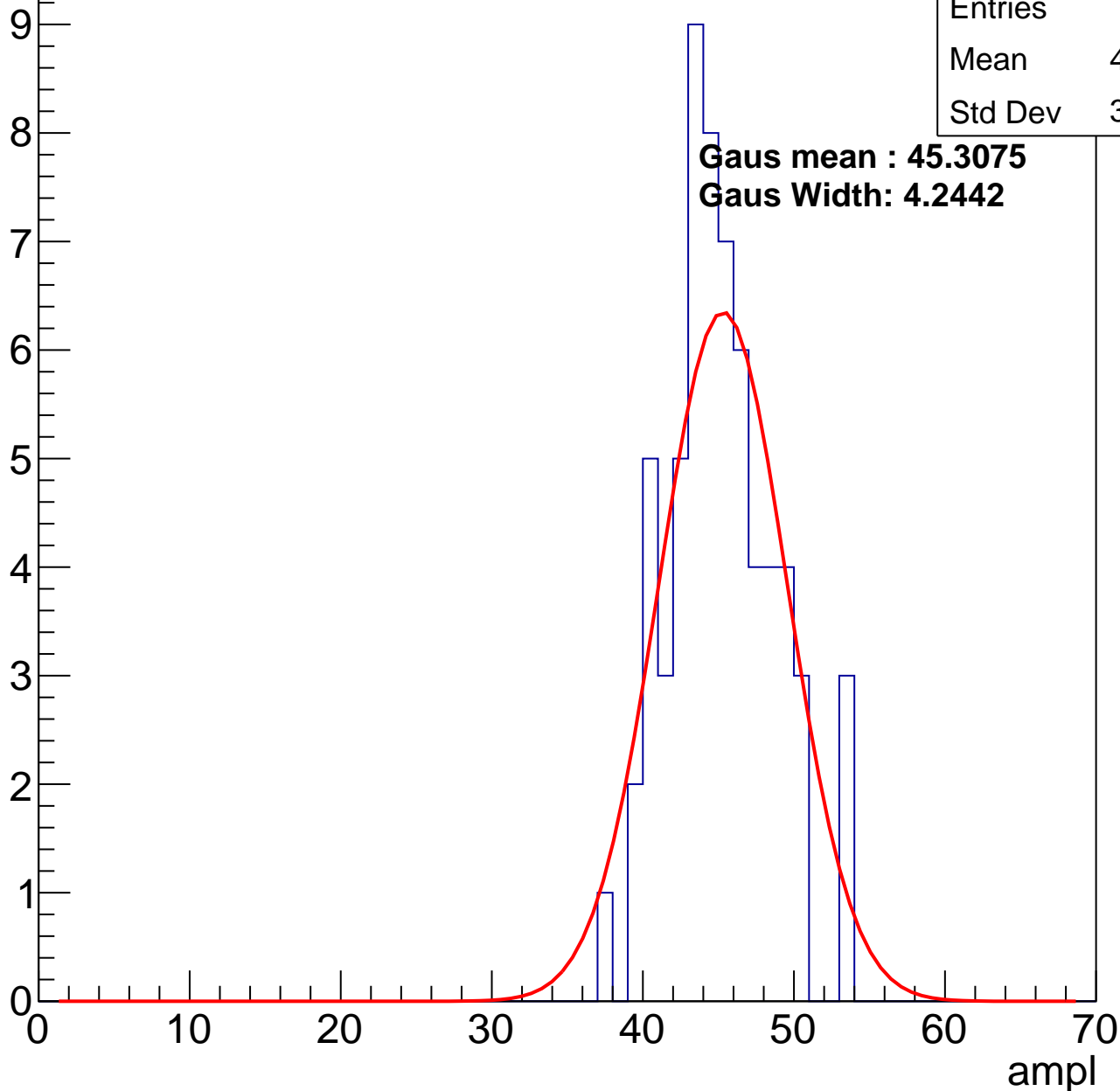
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	44.73
Std Dev	3.497

Gaus mean : 45.3075

Gaus Width: 4.2442

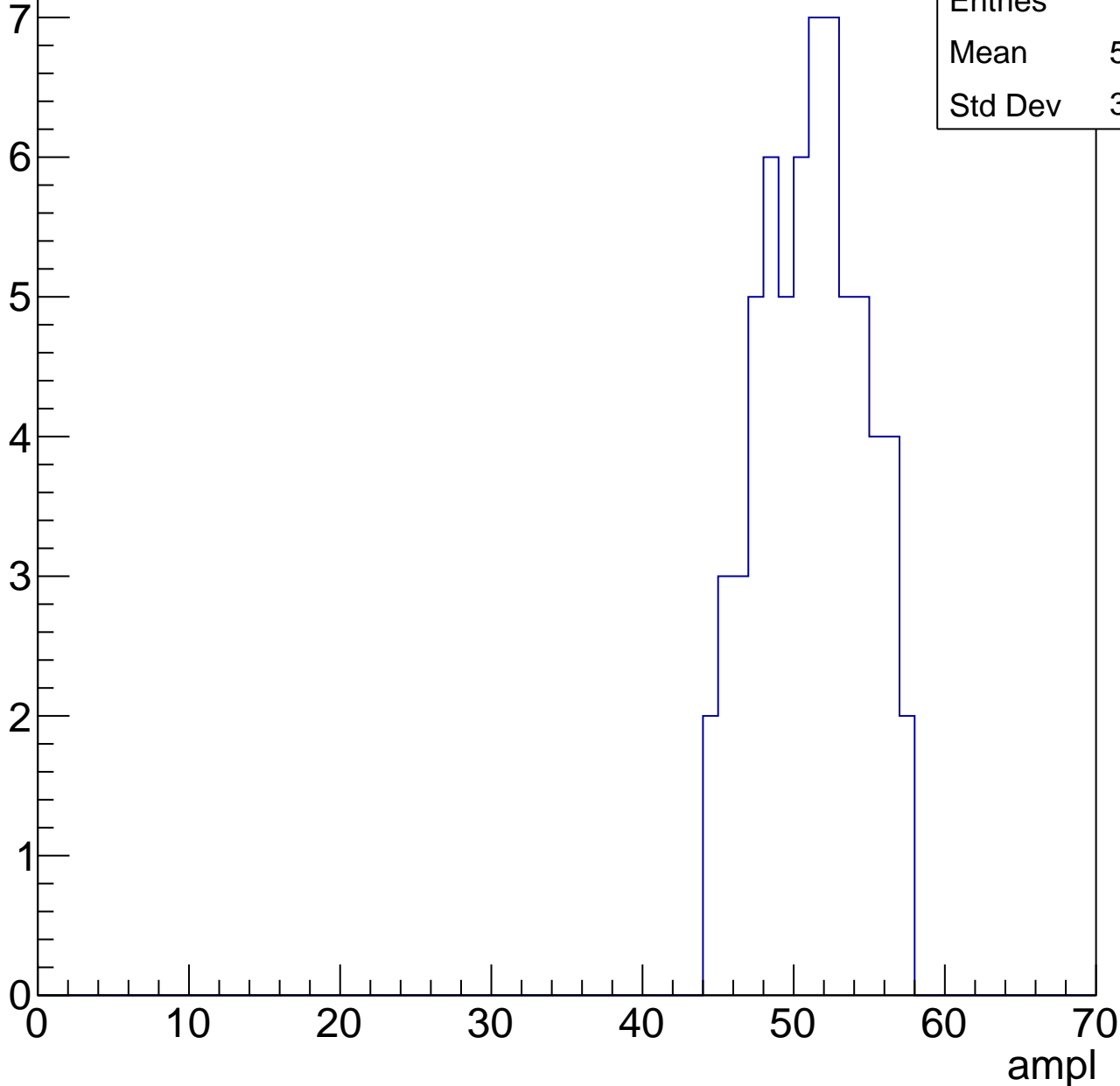


B1L003S, U13-ch54, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	50.67
Std Dev	3.405

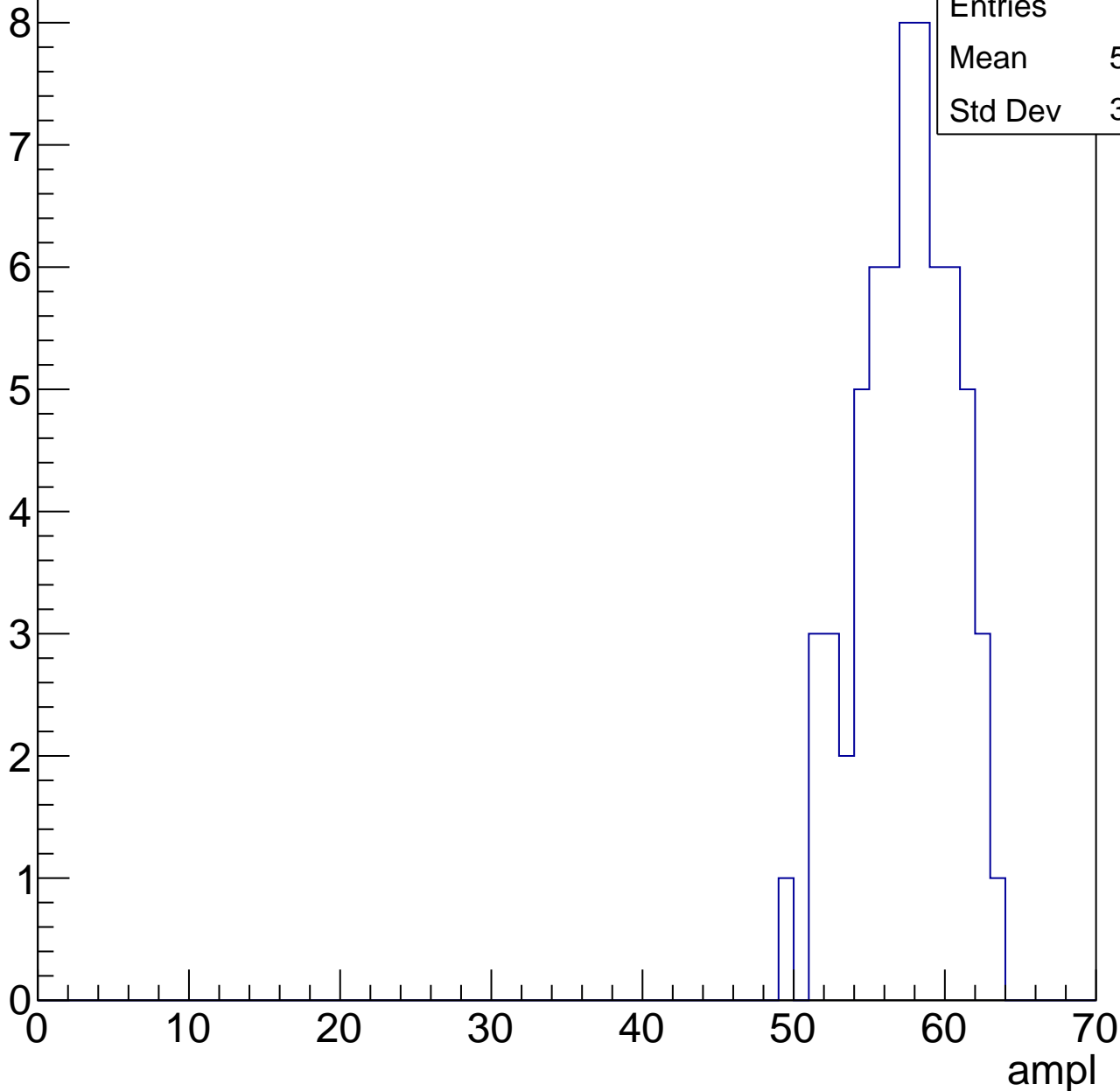


B1L003S, U13-ch54, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.95
Std Dev	3.164



B1L003S, U13-ch54, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	29
Mean	58.93
Std Dev	11.24

ampl

B1L003S, U13-ch54, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch54, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch55, adc0

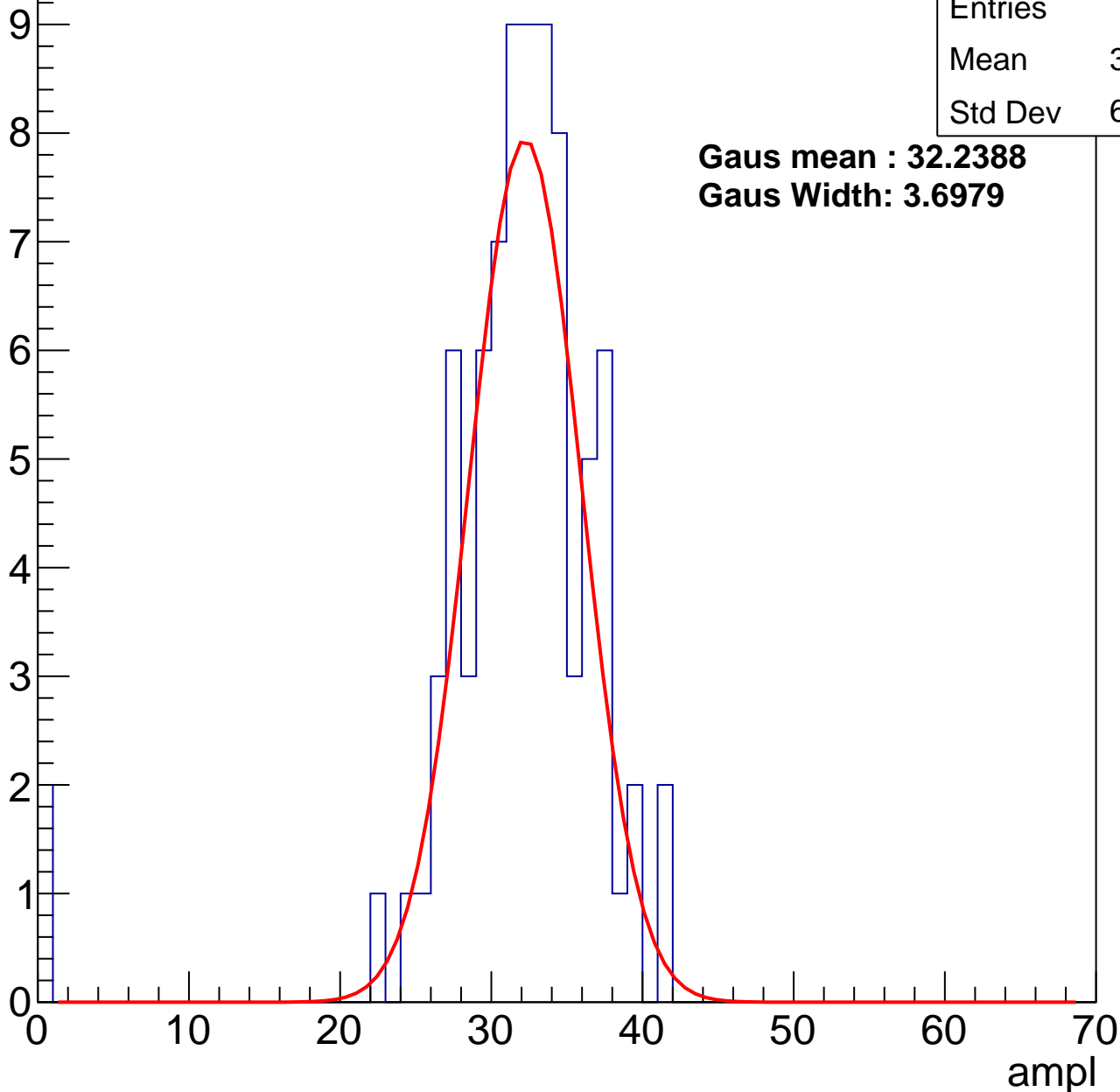
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	31.19
Std Dev	6.169

Gaus mean : 32.2388

Gaus Width: 3.6979



B1L003S, U13-ch55, adc1

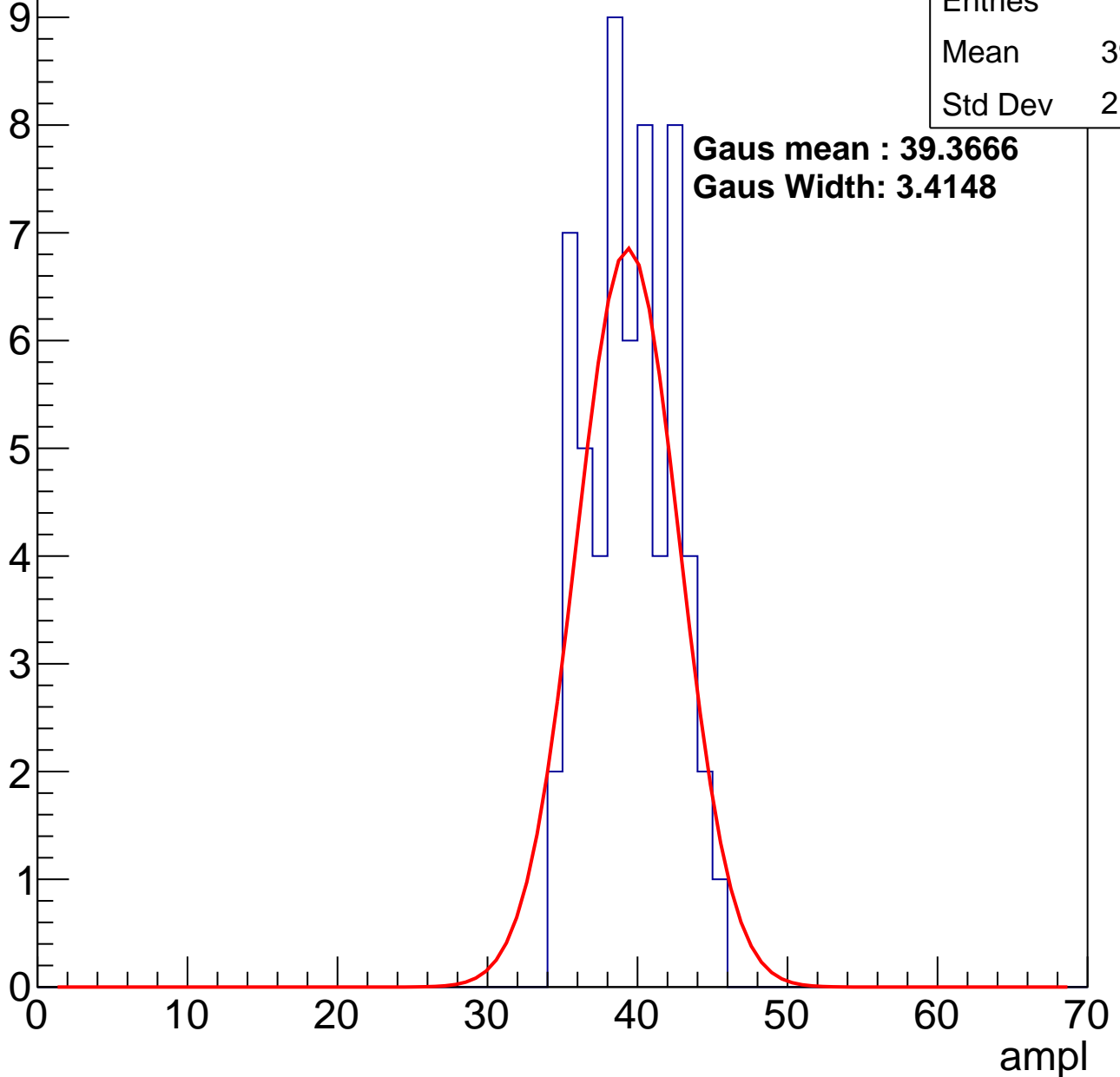
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	39.03
Std Dev	2.822

Gaus mean : 39.3666

Gaus Width: 3.4148



B1L003S, U13-ch55, adc2

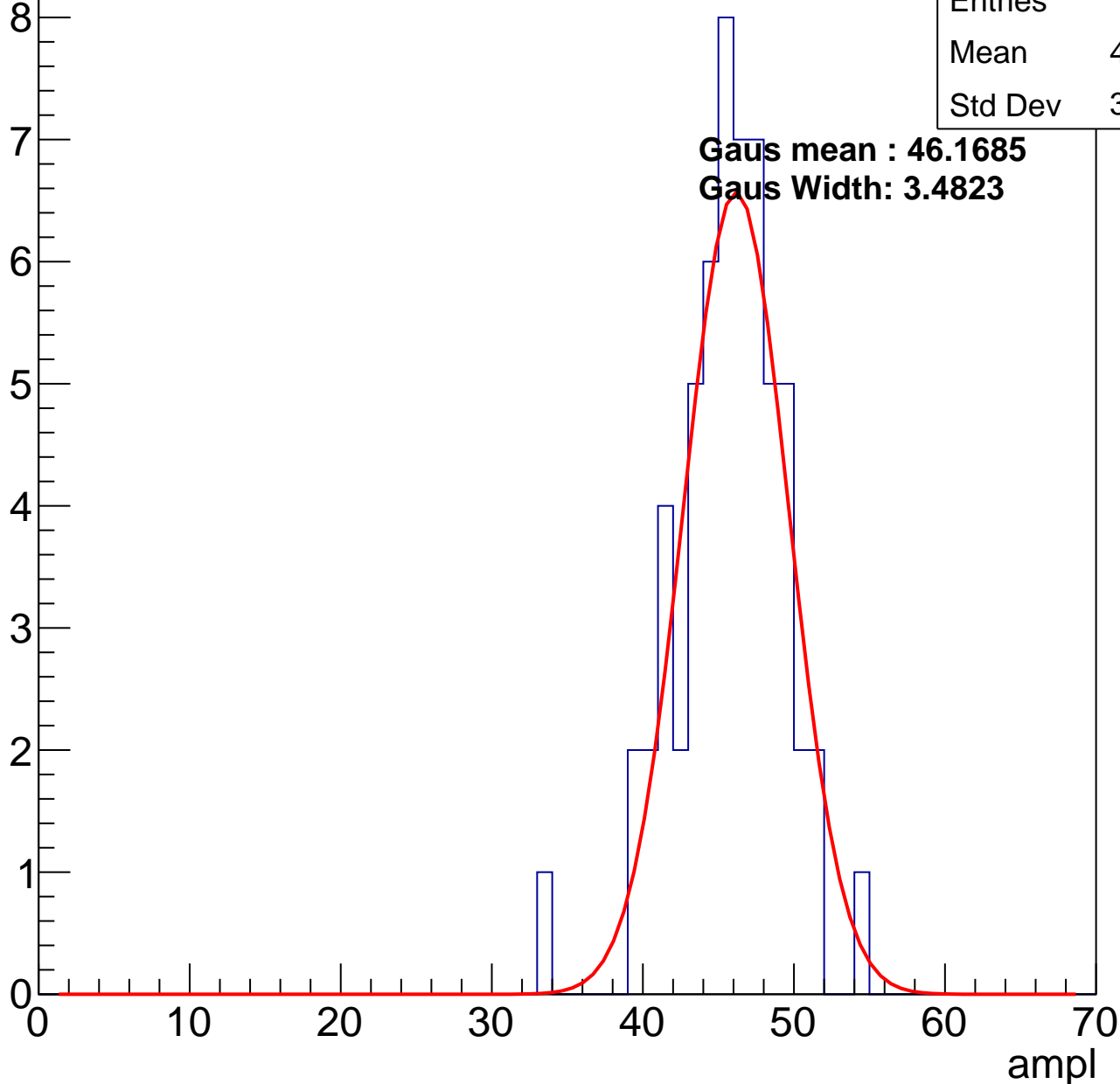
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	45.25
Std Dev	3.525

Gaus mean : 46.1685

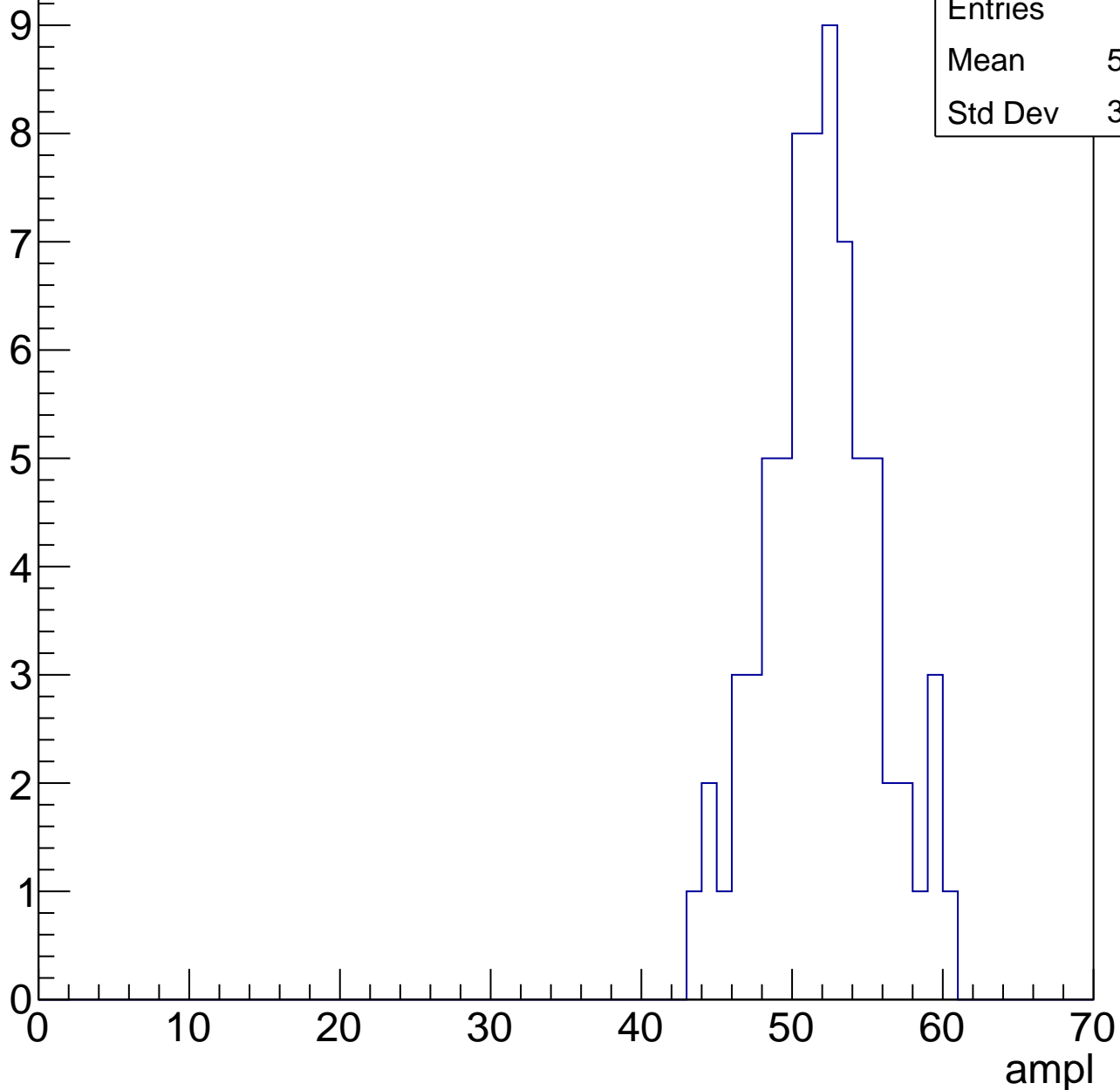
Gaus Width: 3.4823



B1L003S, U13-ch55, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	71
Mean	51.45
Std Dev	3.726

B1L003S, U13-ch55, adc4

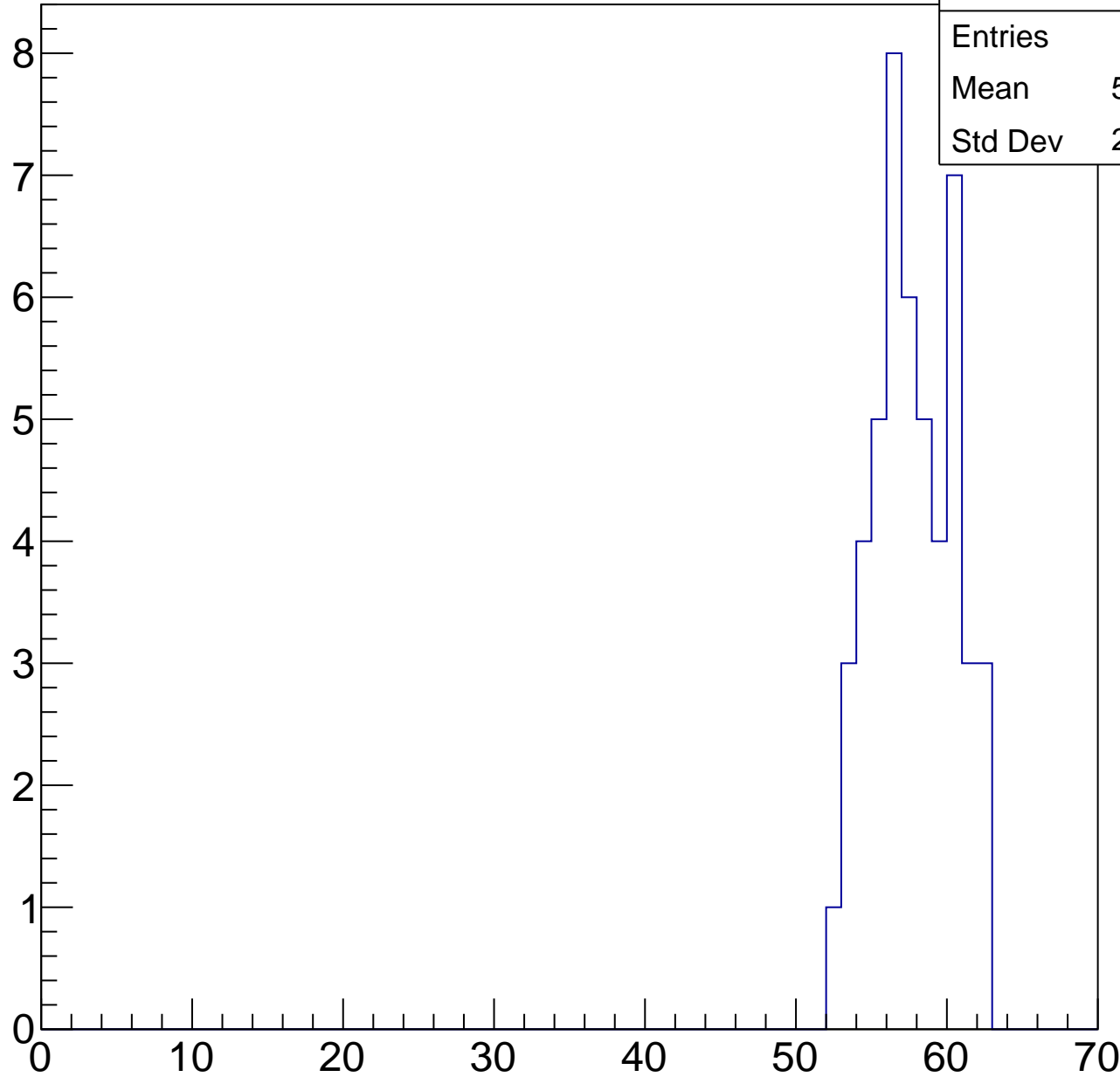
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	57.29
Std Dev	2.634

ampl

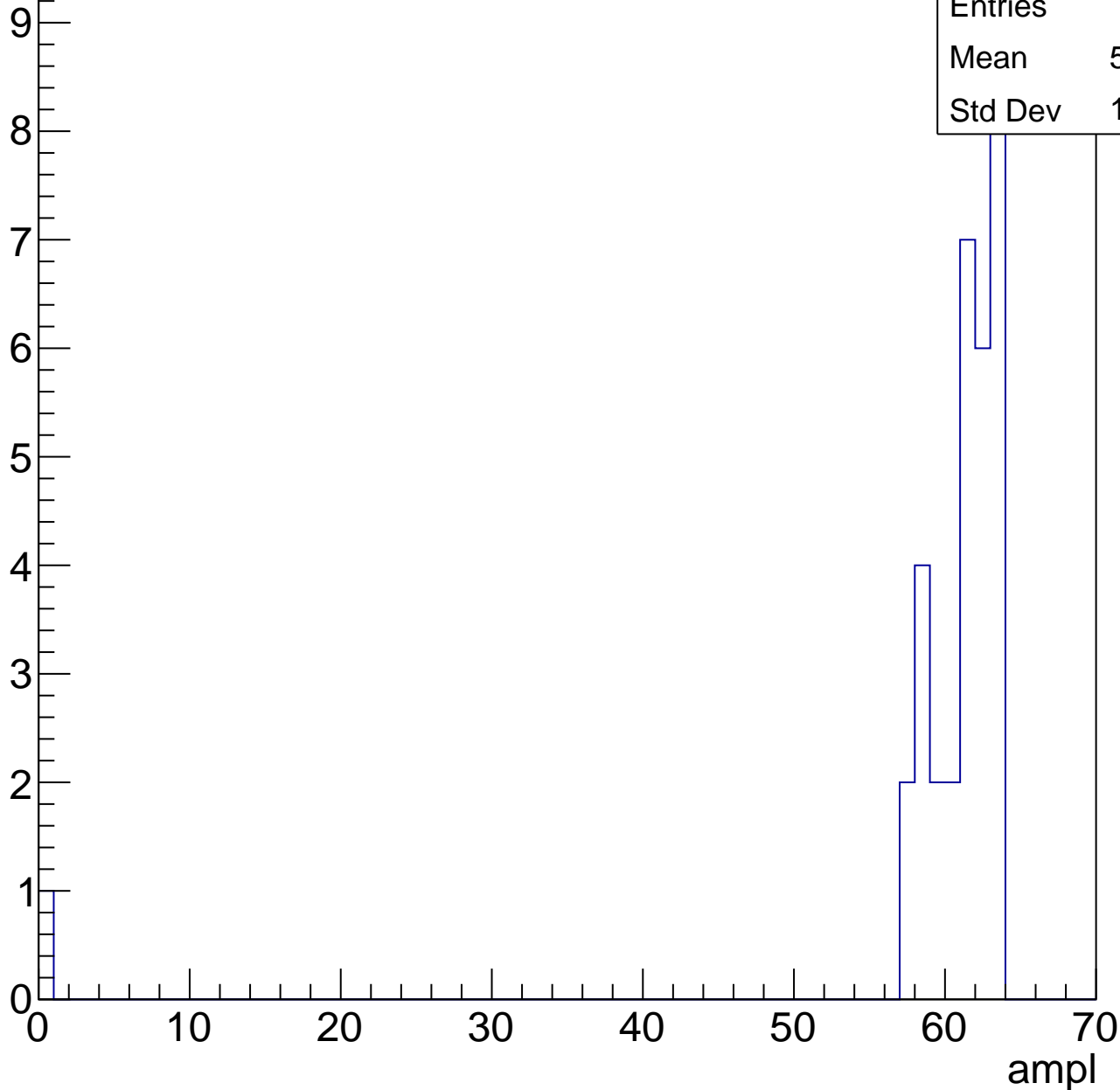


B1L003S, U13-ch55, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	59.09
Std Dev	10.62



B1L003S, U13-ch55, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch55, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch56, adc0

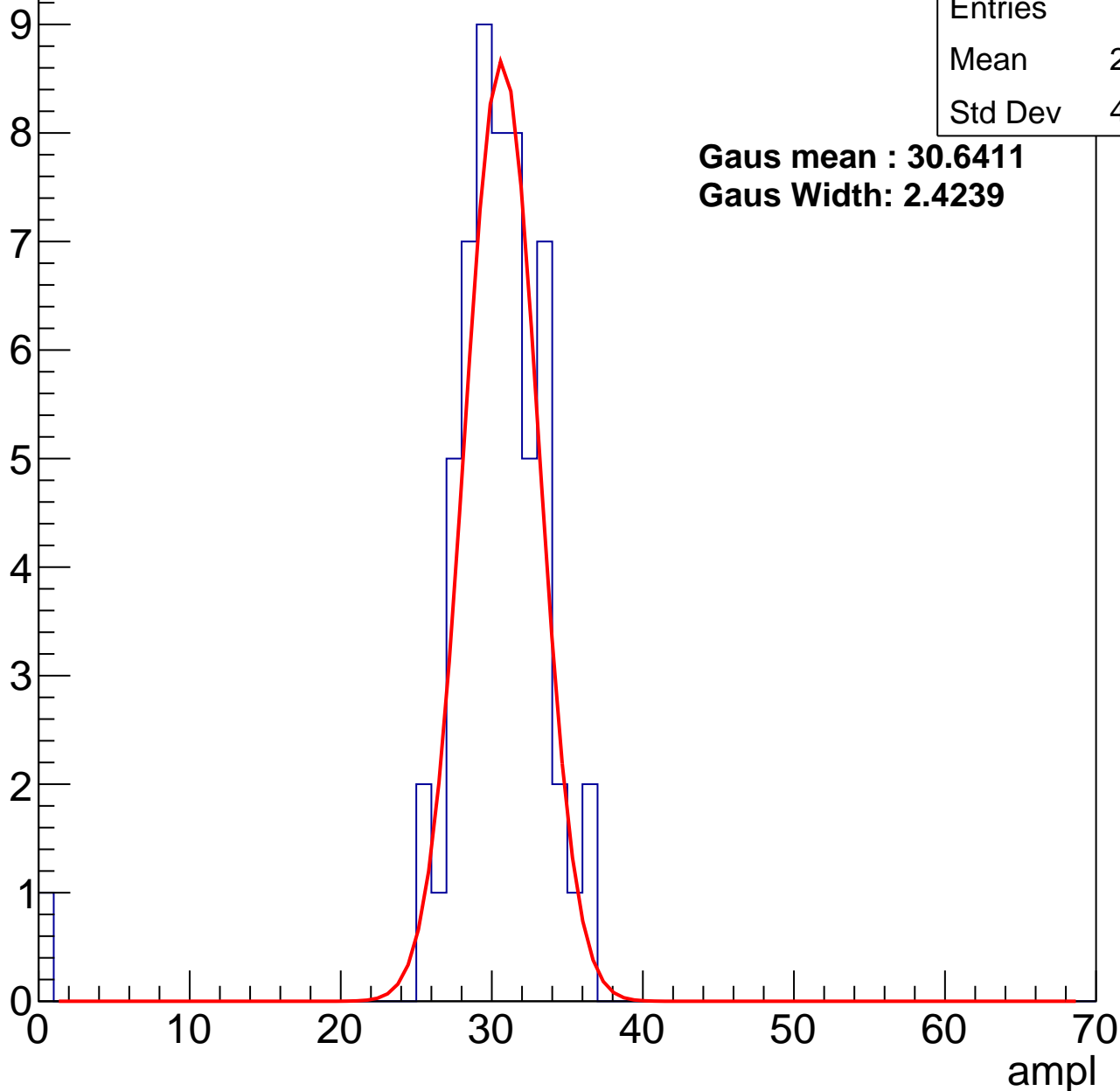
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29.69
Std Dev	4.665

Gaus mean : 30.6411

Gaus Width: 2.4239



B1L003S, U13-ch56, adc1

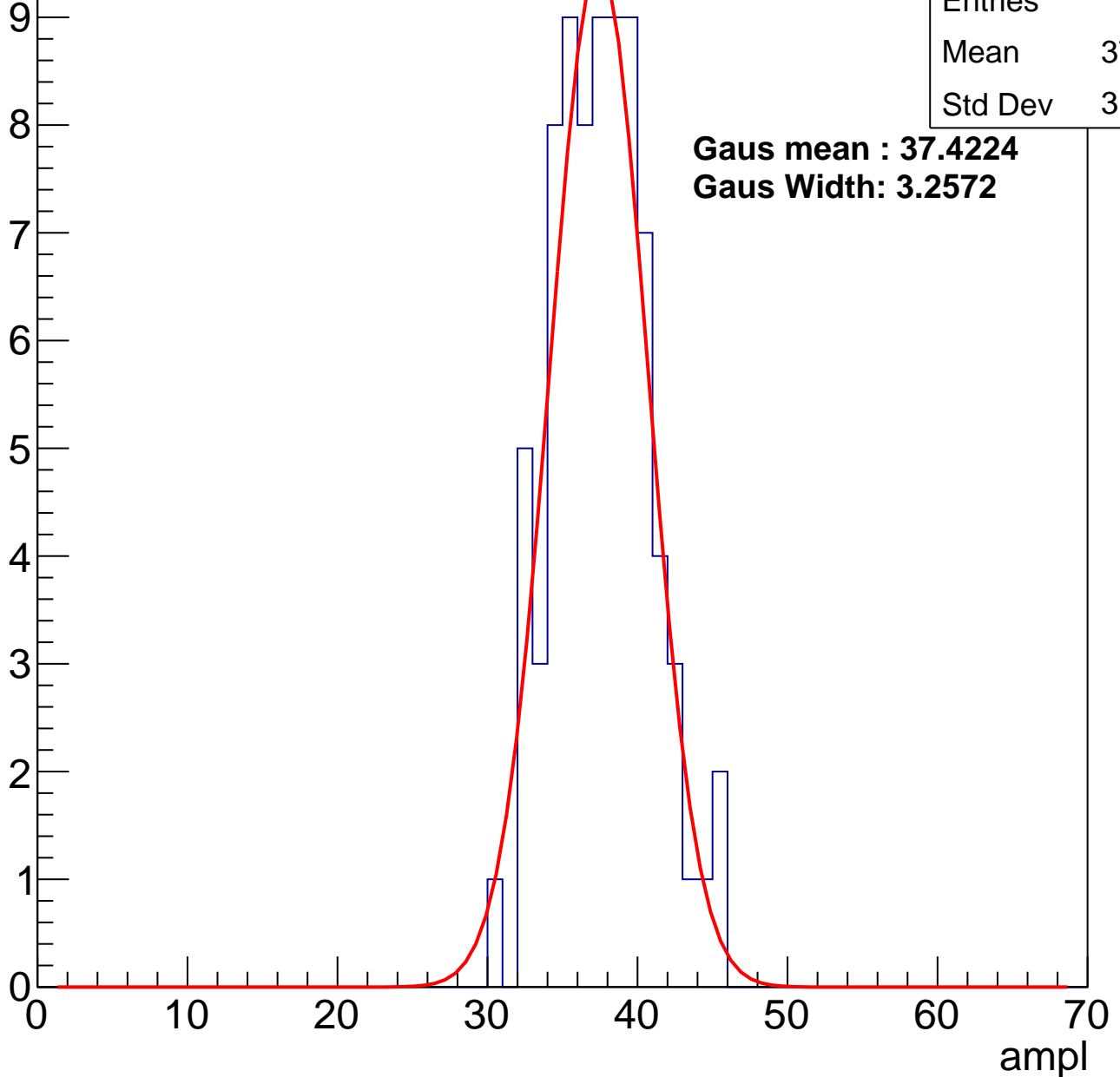
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	37.18
Std Dev	3.173

Gaus mean : 37.4224

Gaus Width: 3.2572



B1L003S, U13-ch56, adc2

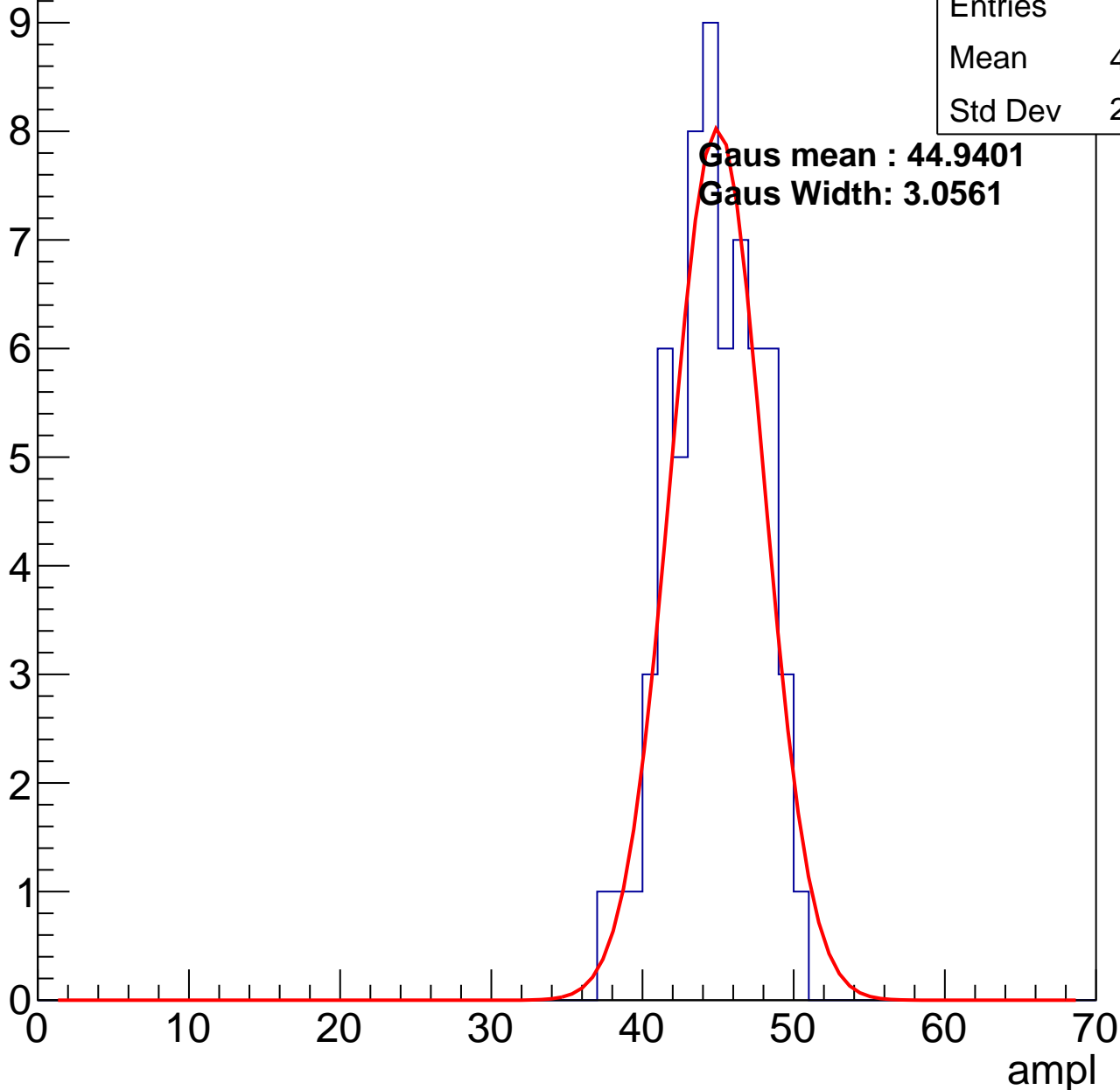
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	44.27
Std Dev	2.902

Gaus mean : 44.9401

Gaus Width: 3.0561

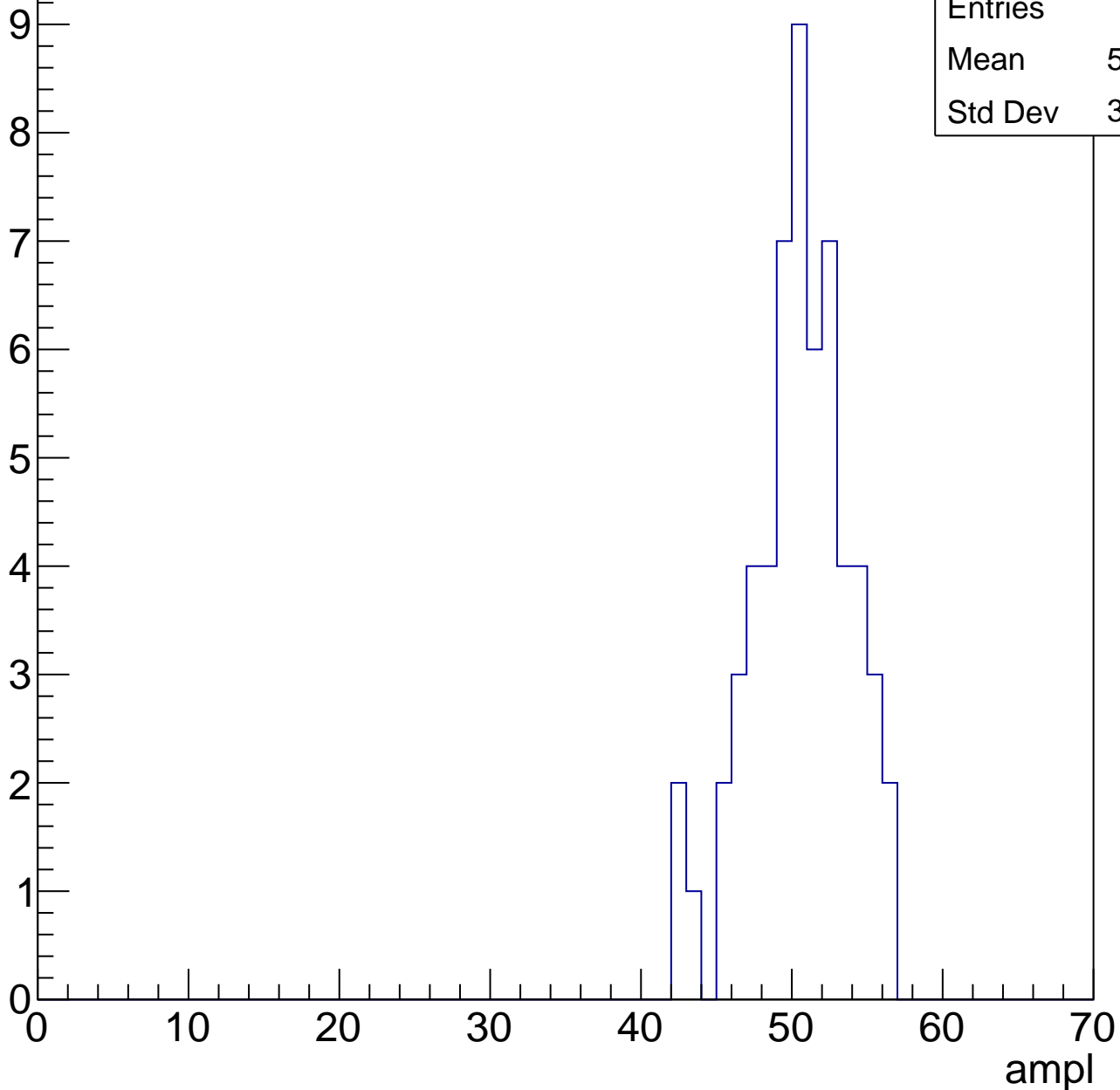


B1L003S, U13-ch56, adc3

calib_packv5_042523_0143.root, FC#13, port D2

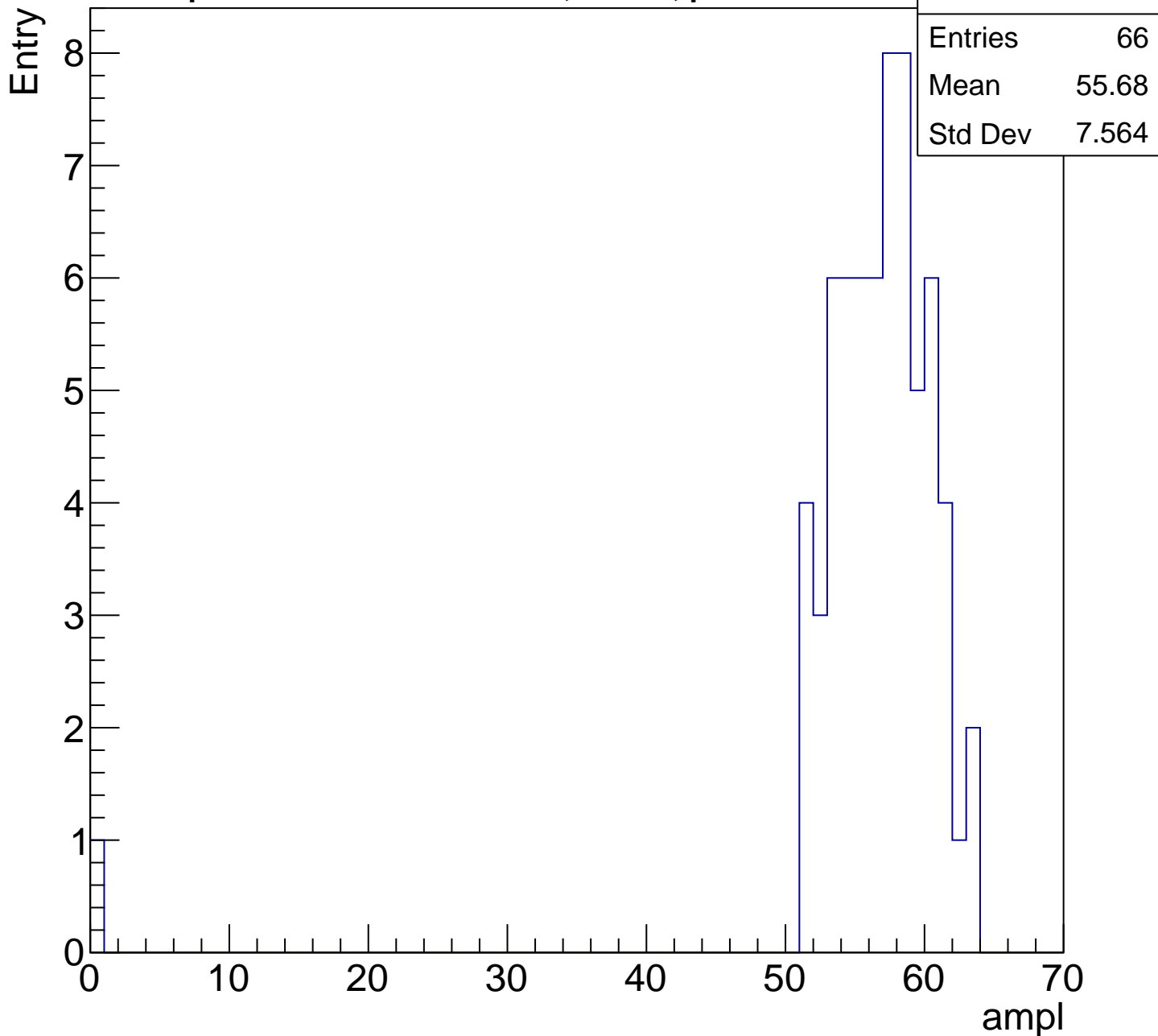
Entry

Entries	58
Mean	50.05
Std Dev	3.256



B1L003S, U13-ch56, adc4

calib_packv5_042523_0143.root, FC#13, port D2

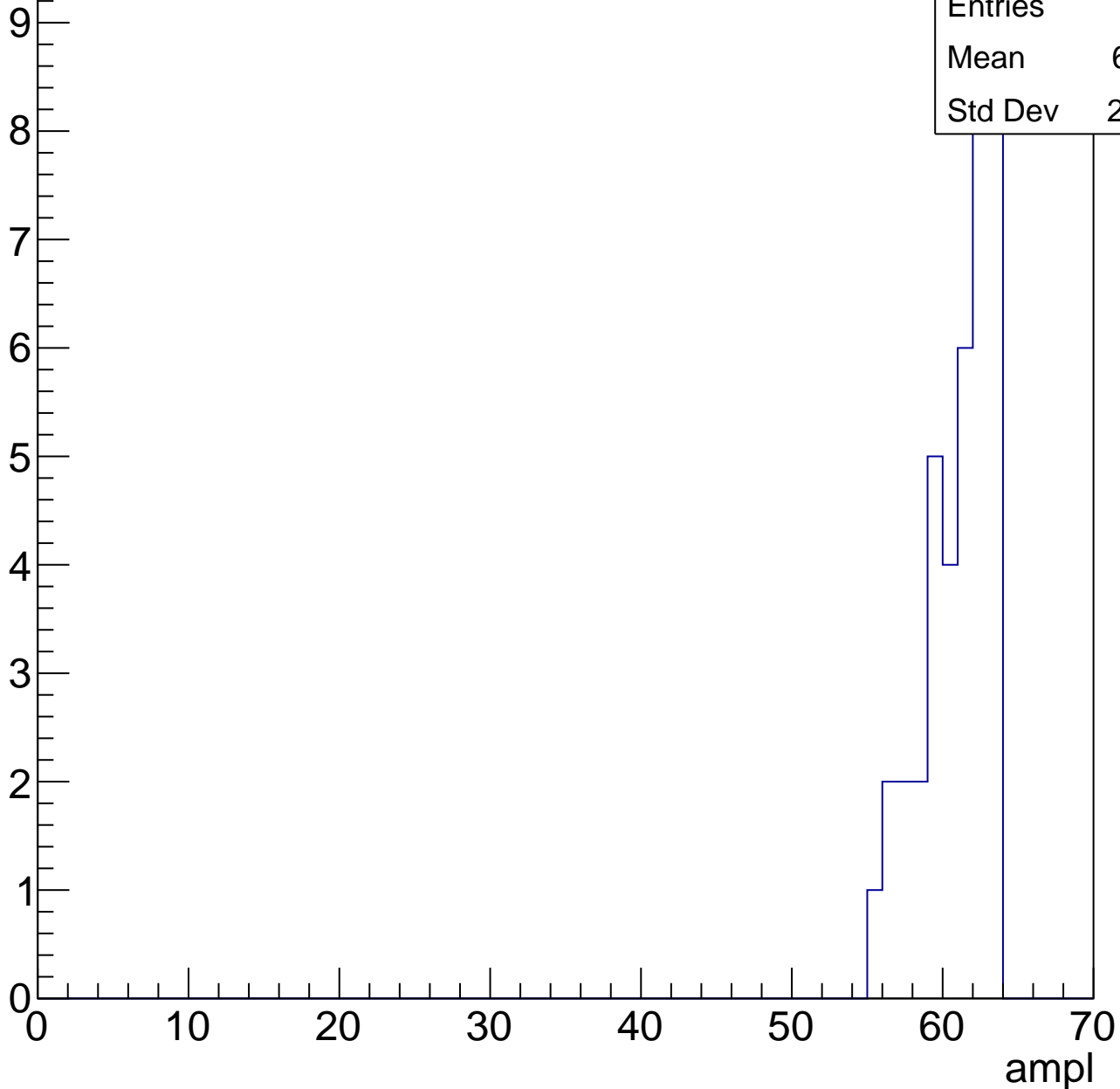


B1L003S, U13-ch56, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	60.51
Std Dev	2.217



B1L003S, U13-ch56, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch56, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L003S, U13-ch57, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	32.4
Std Dev	3.016

Gaus mean : 32.8239

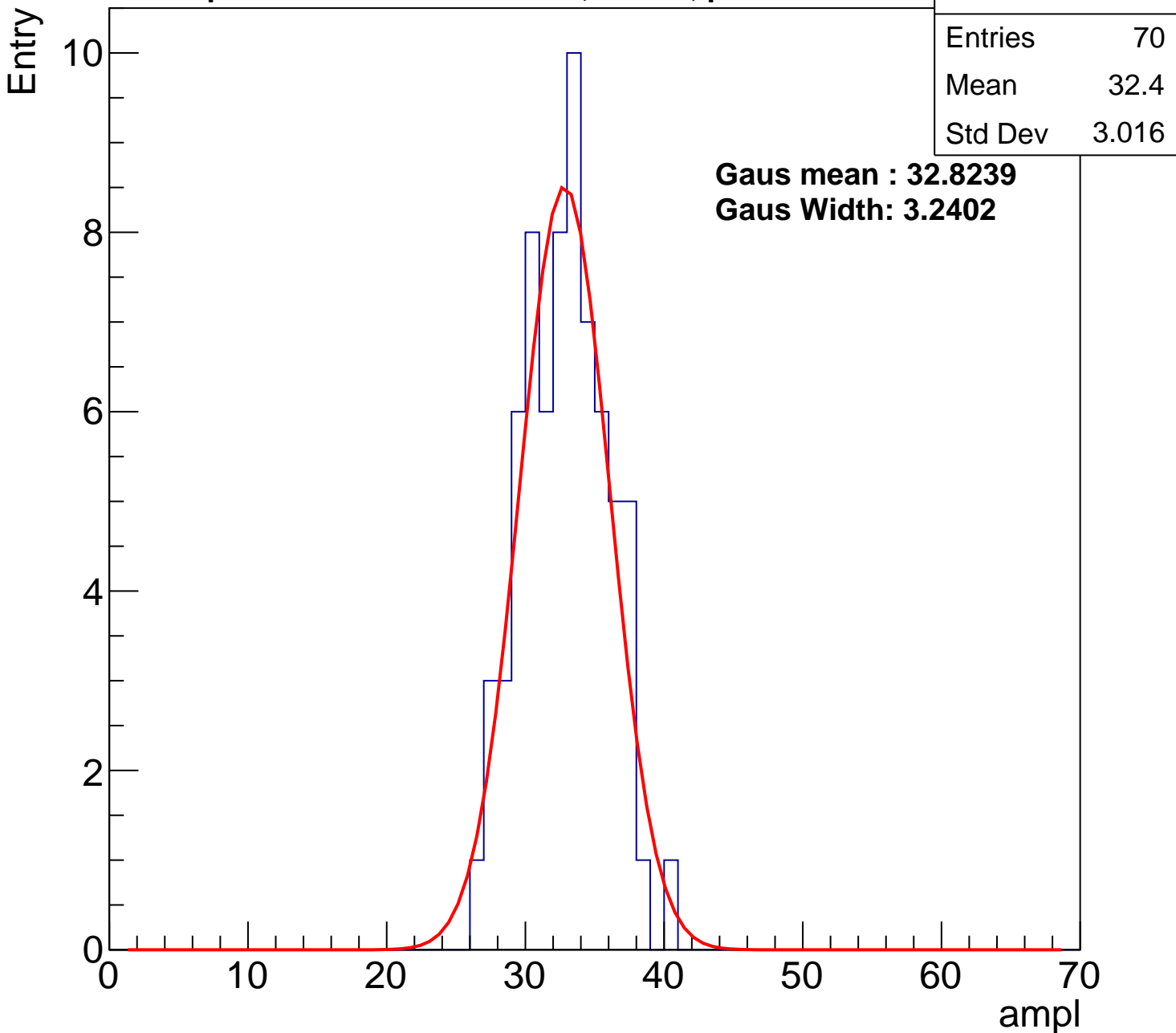
Gaus Width: 3.2402

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch57, adc1

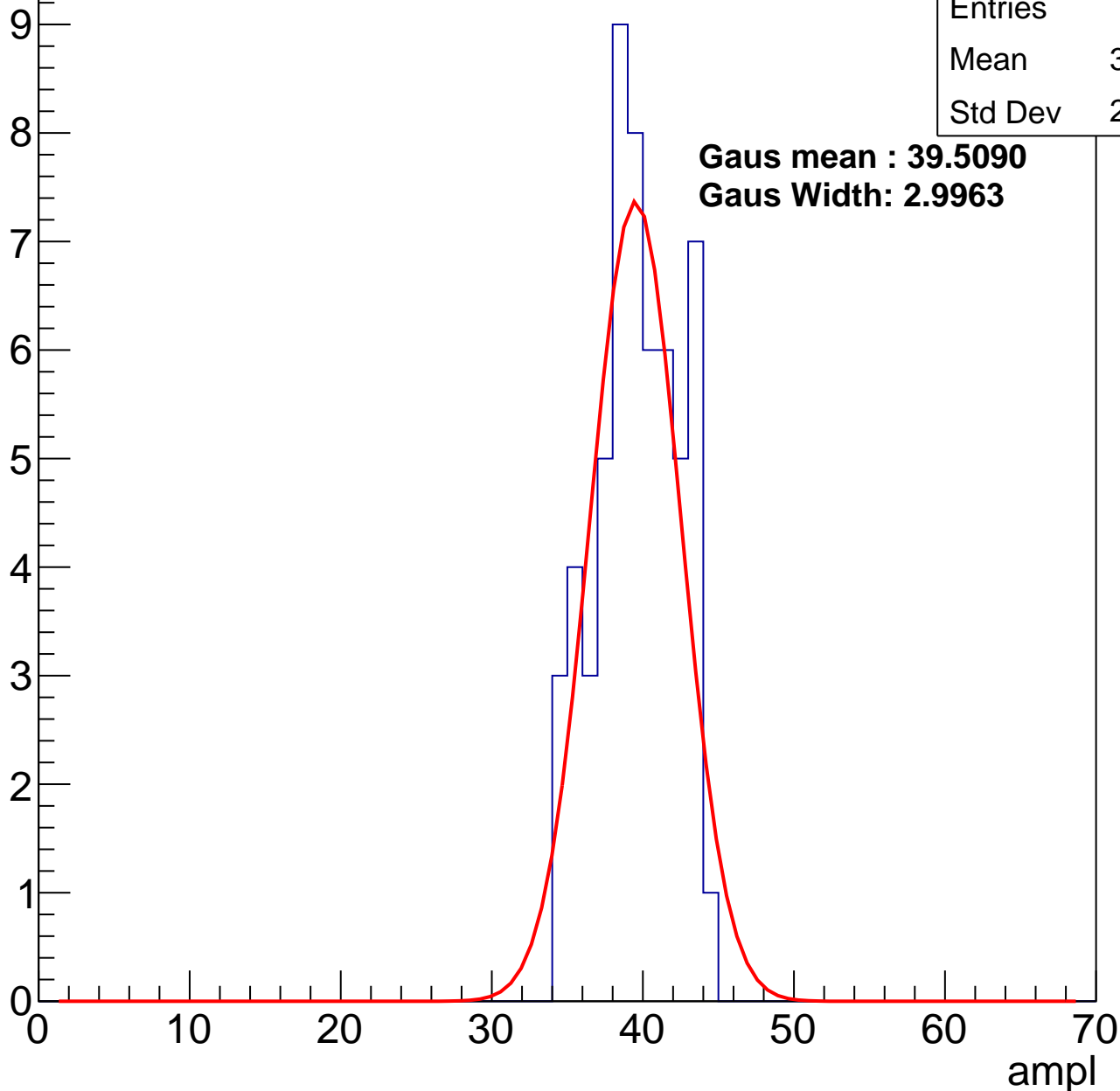
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	39.12
Std Dev	2.669

Gaus mean : 39.5090

Gaus Width: 2.9963



B1L003S, U13-ch57, adc2

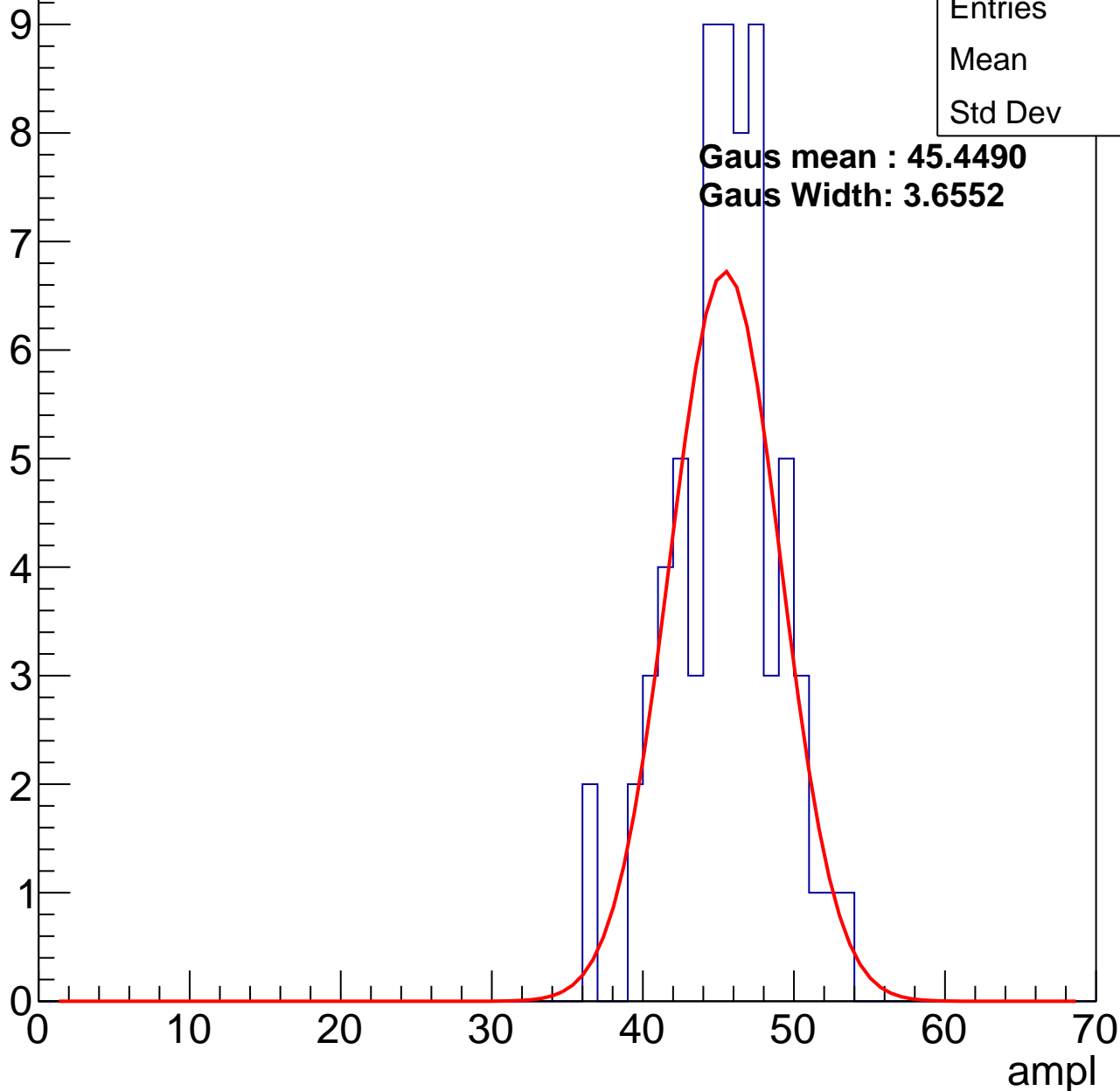
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	45
Std Dev	3.46

Gaus mean : 45.4490

Gaus Width: 3.6552

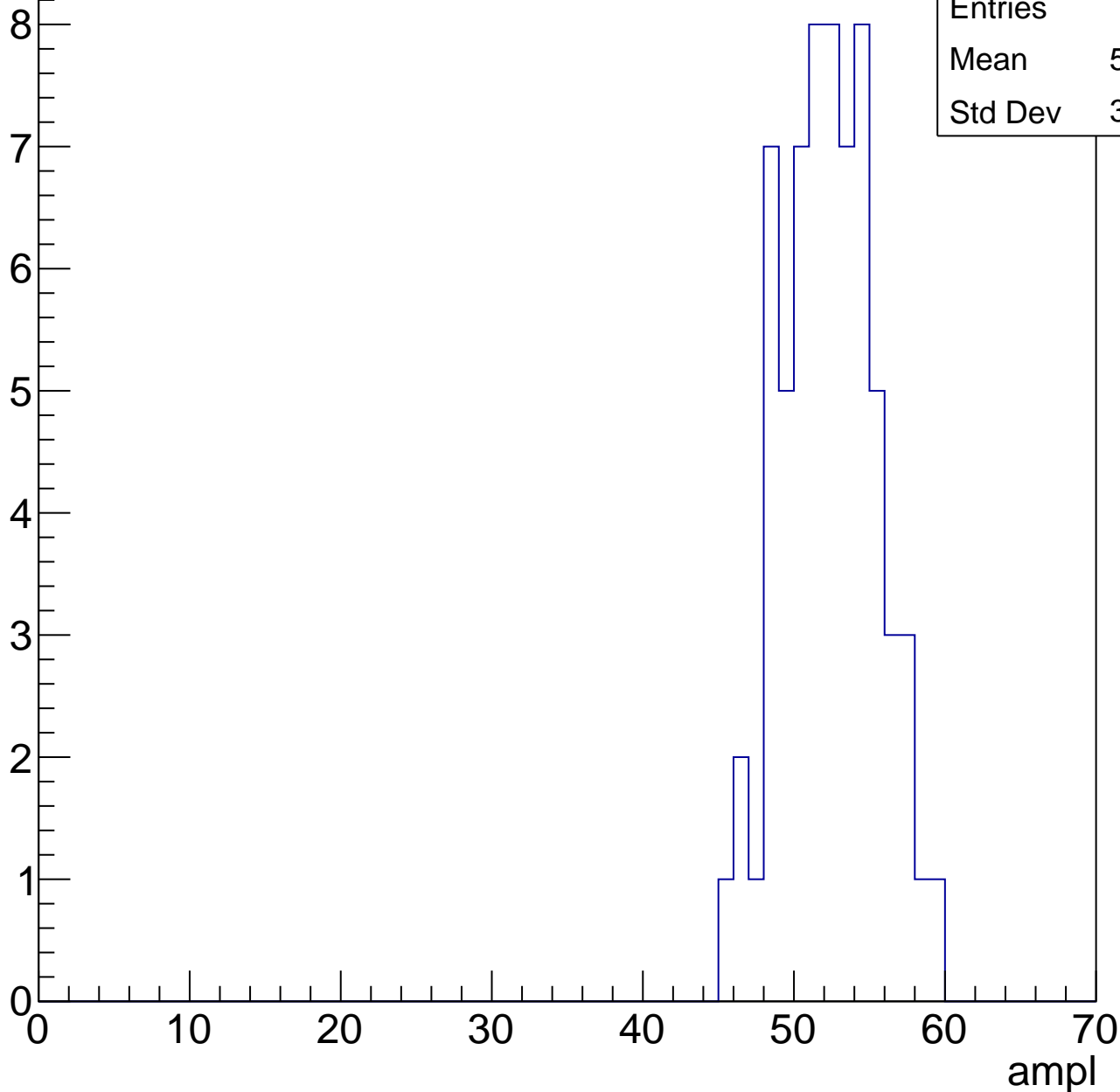


B1L003S, U13-ch57, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	51.84
Std Dev	3.064

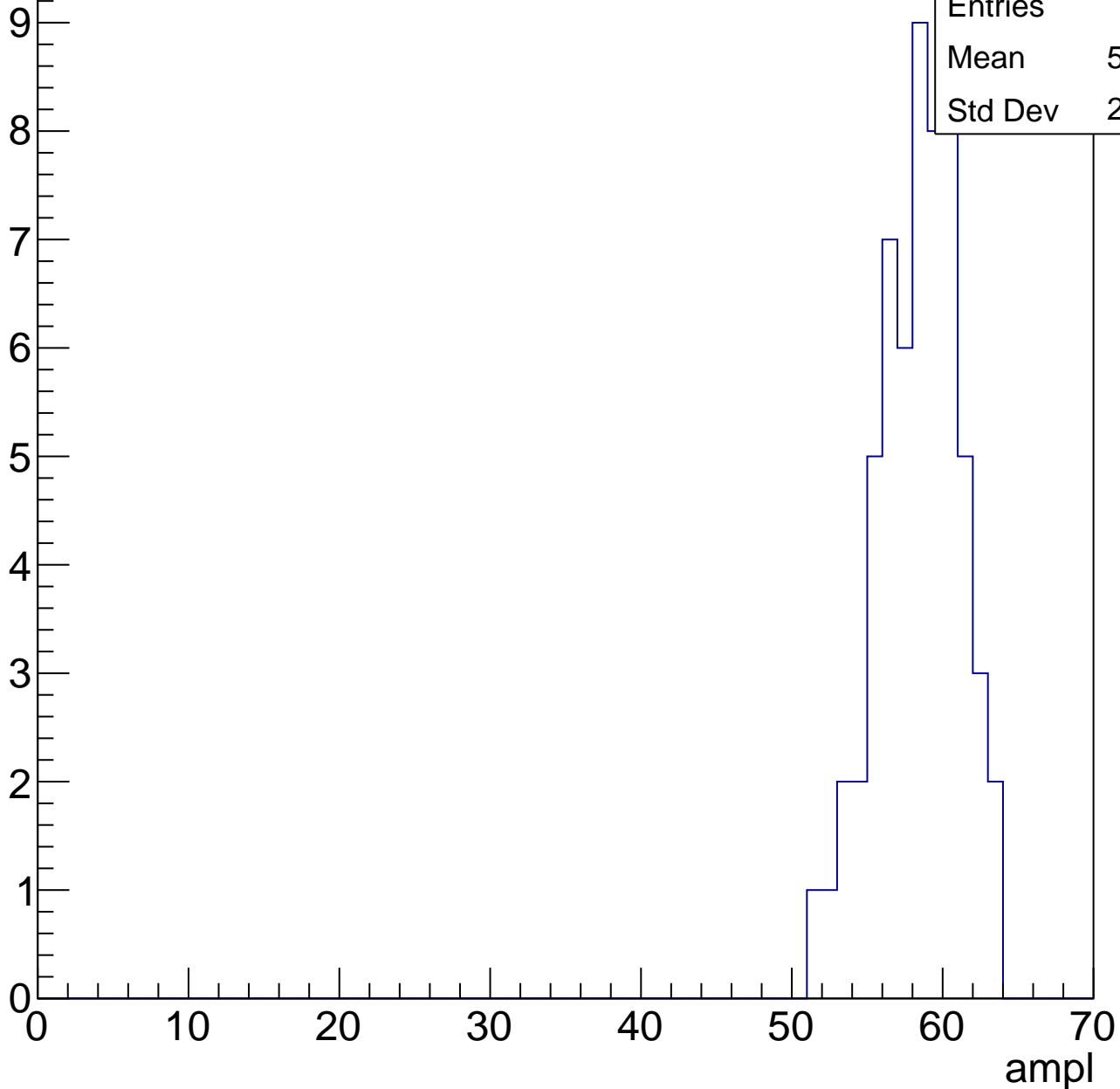


B1L003S, U13-ch57, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.92
Std Dev	2.695

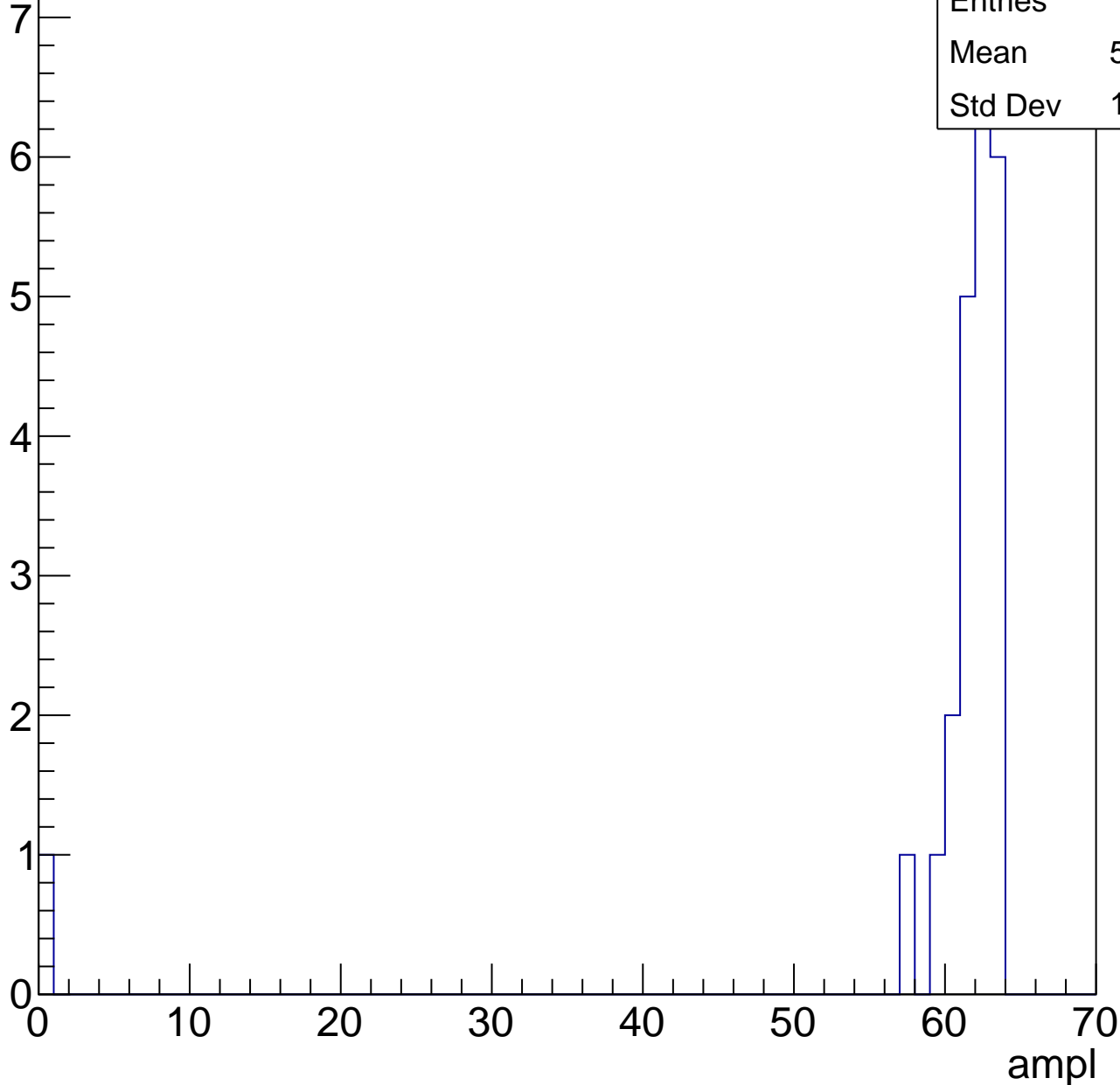


B1L003S, U13-ch57, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.83
Std Dev	12.62



B1L003S, U13-ch57, adc6

calib_packv5_042523_0143.root, FC#13, port D2

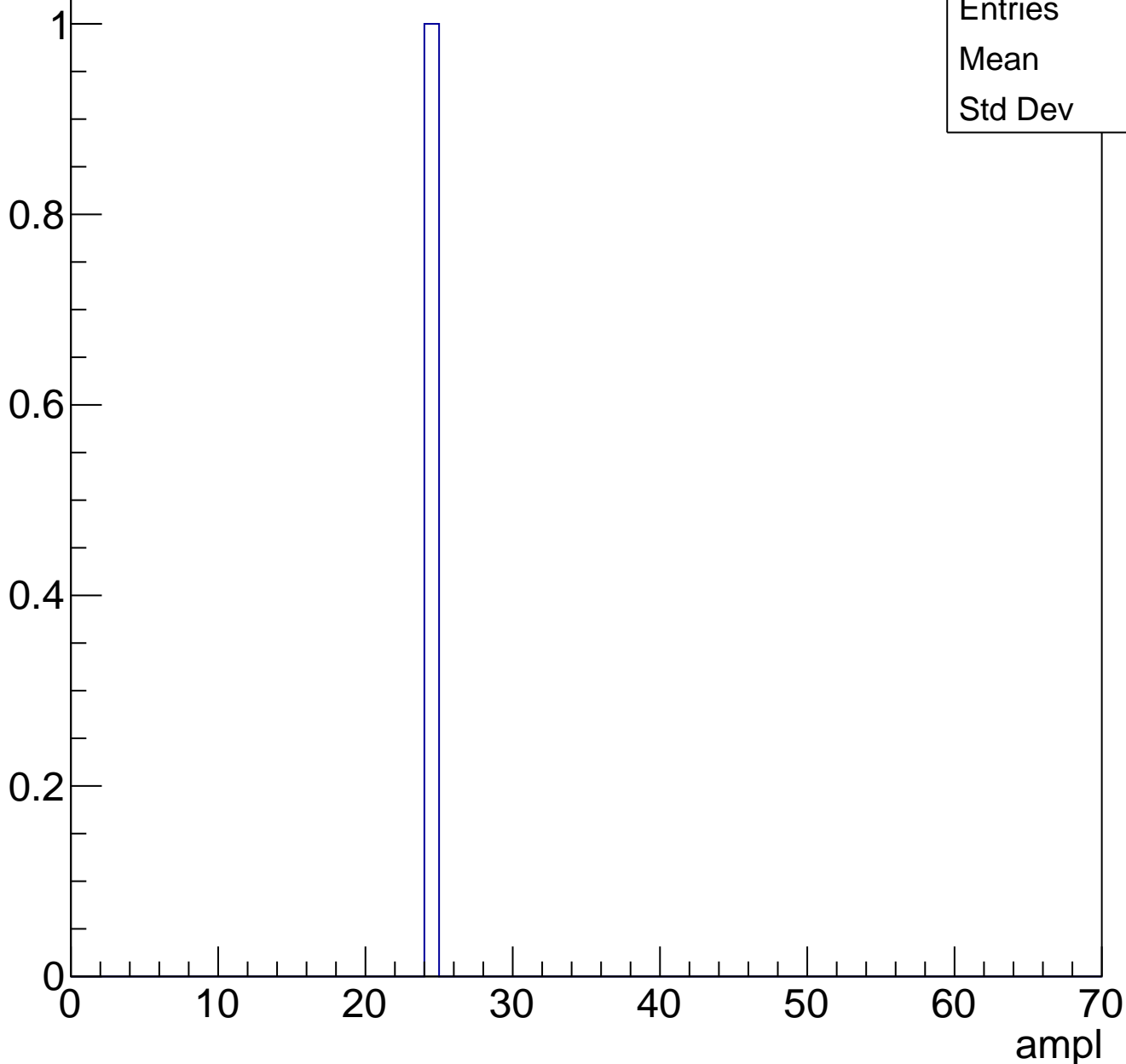
Entry



B1L003S, U13-ch57, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	24
Std Dev	0

B1L003S, U13-ch58, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	31.7
Std Dev	4.974

Gaus mean : 32.5145

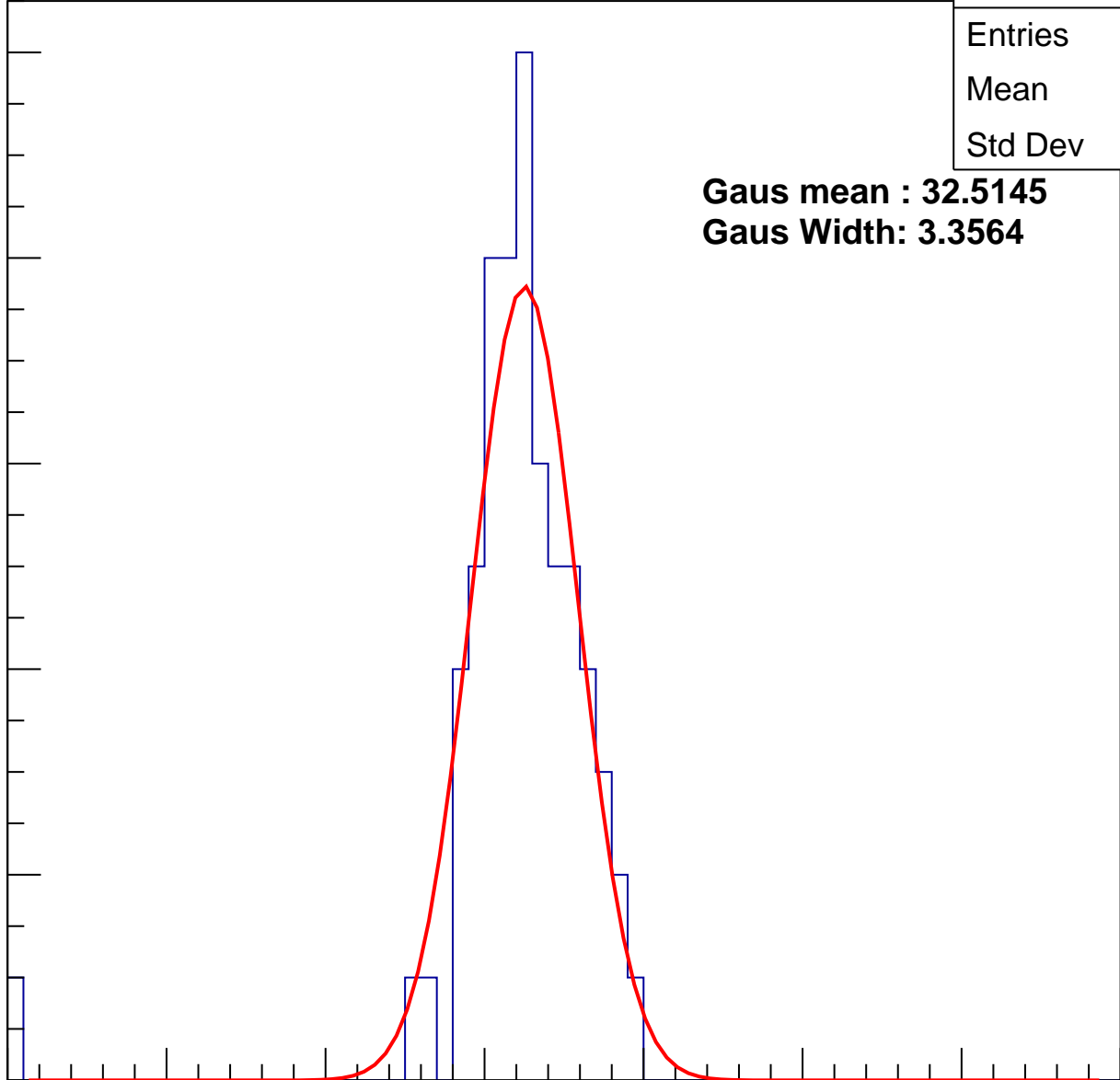
Gaus Width: 3.3564

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch58, adc1

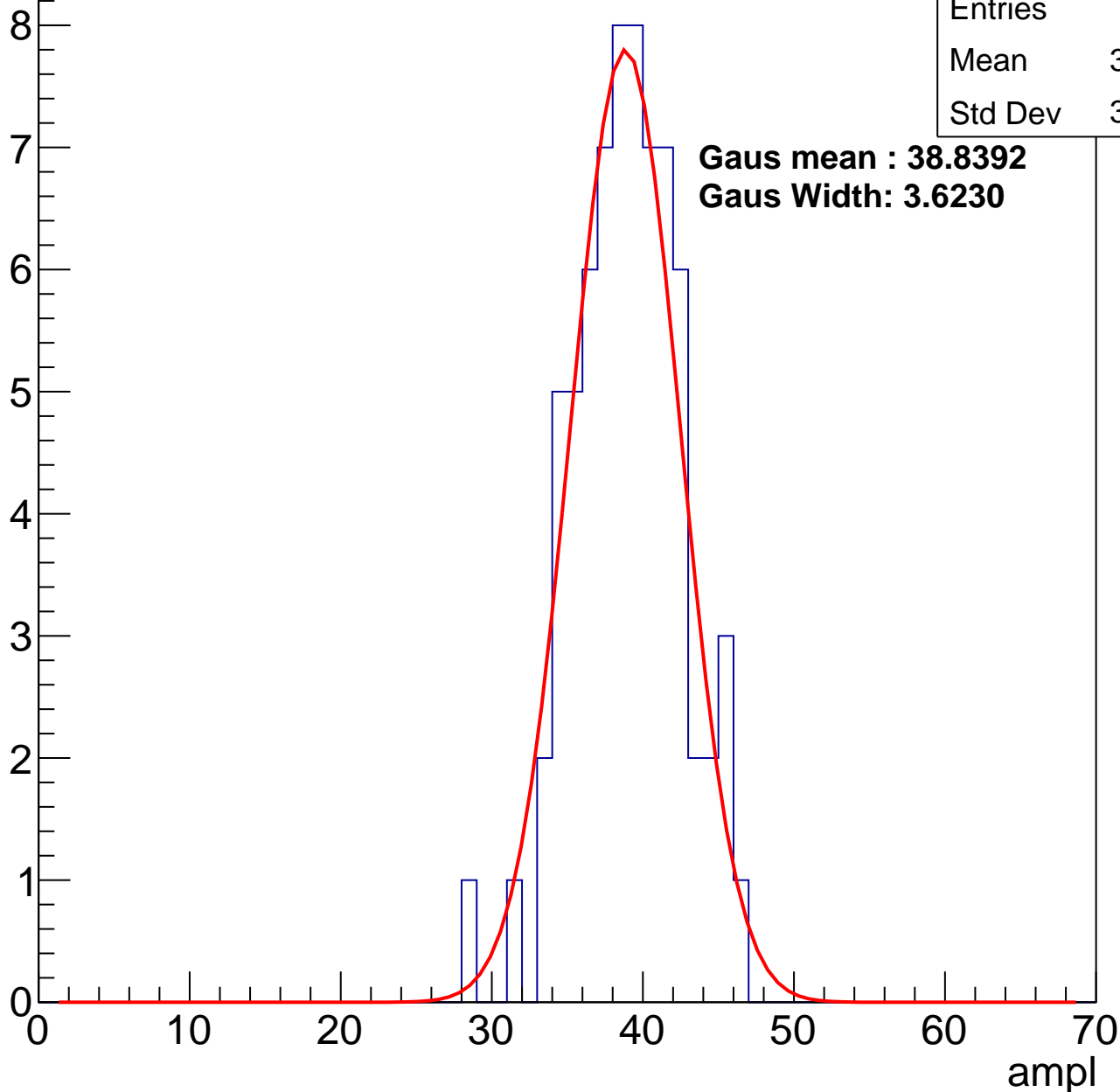
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	38.52
Std Dev	3.488

Gaus mean : 38.8392

Gaus Width: 3.6230



B1L003S, U13-ch58, adc2

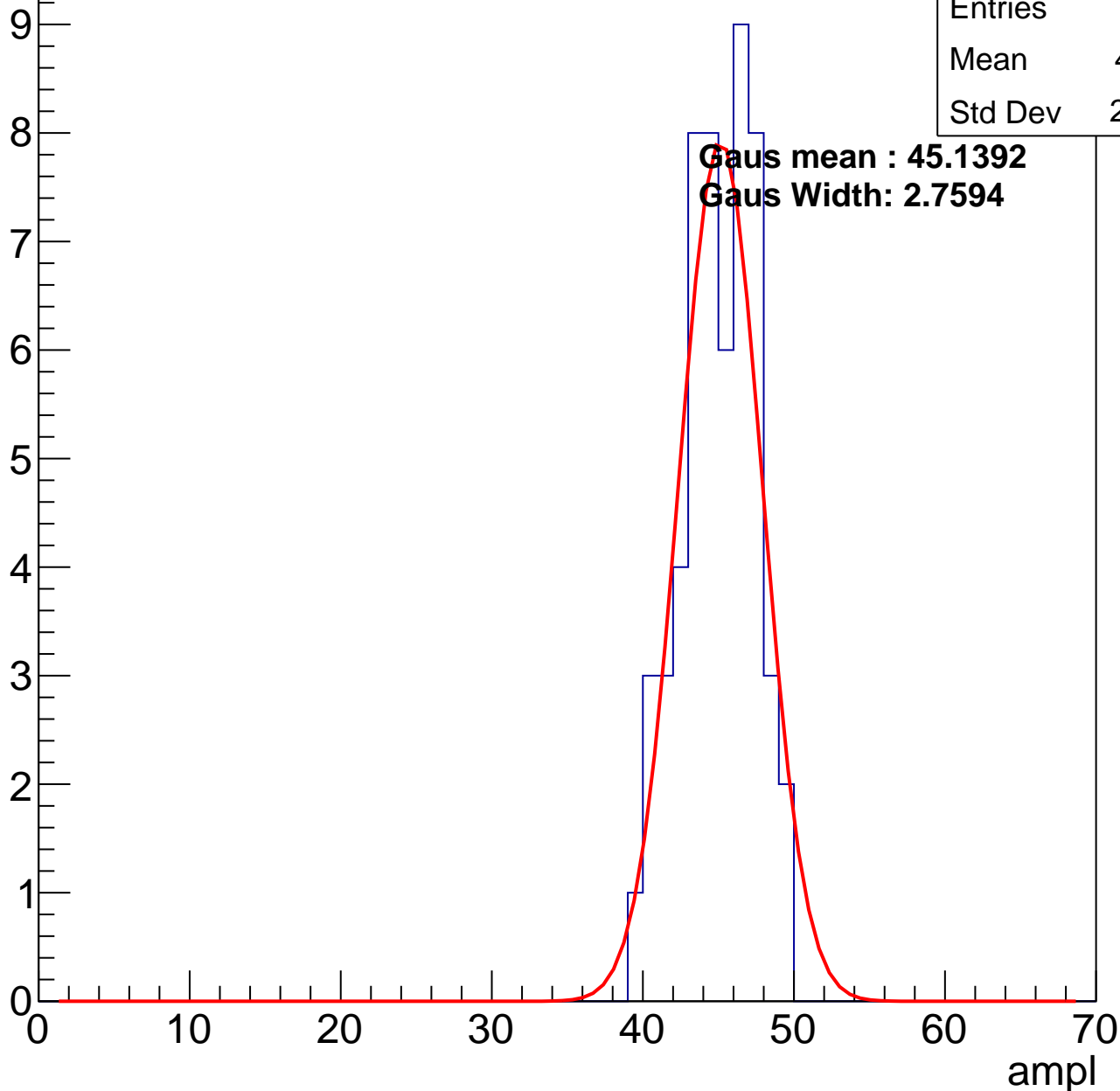
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	44.51
Std Dev	2.419

Gaus mean : 45.1392

Gaus Width: 2.7594

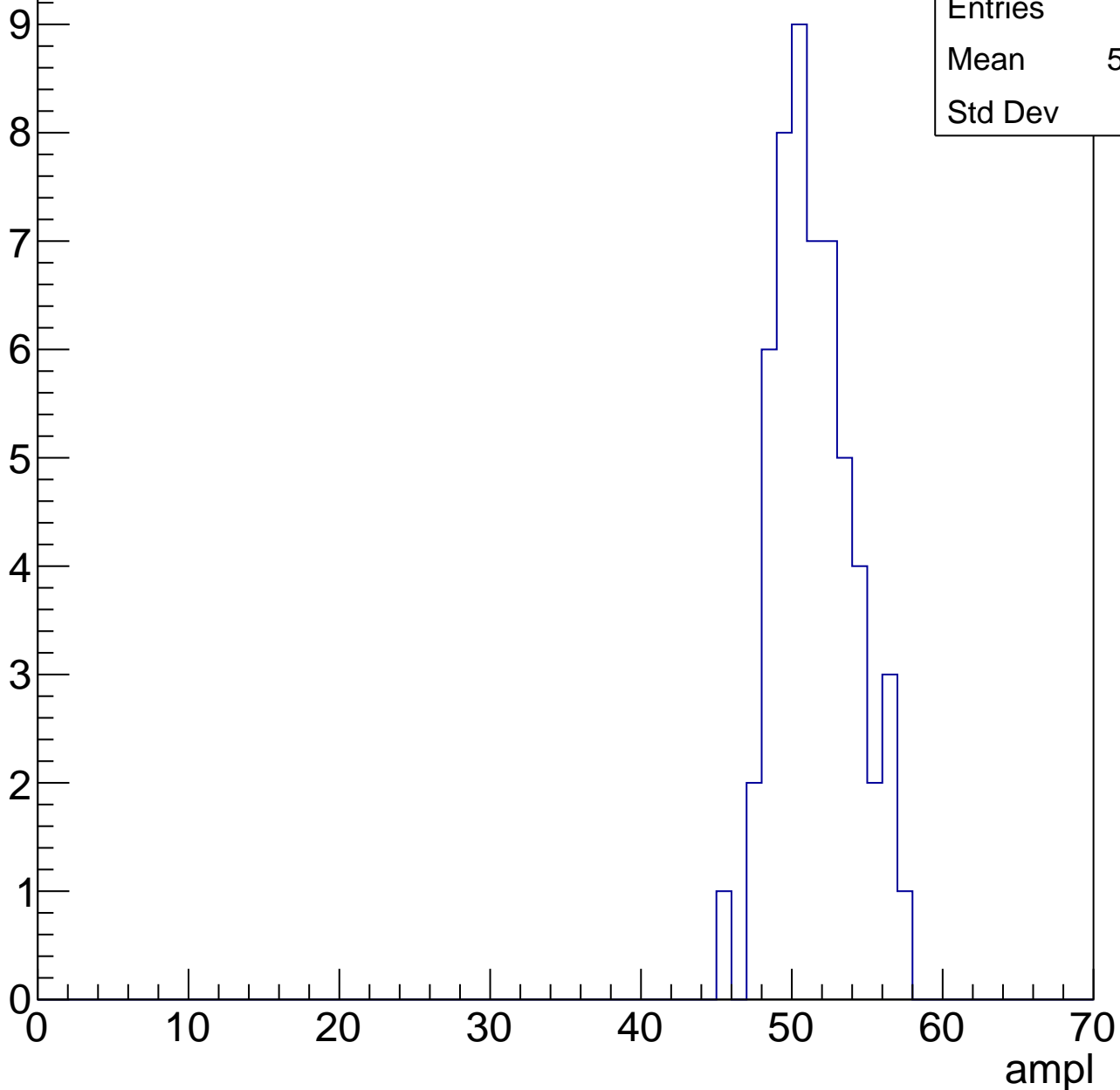


B1L003S, U13-ch58, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	51.02
Std Dev	2.59

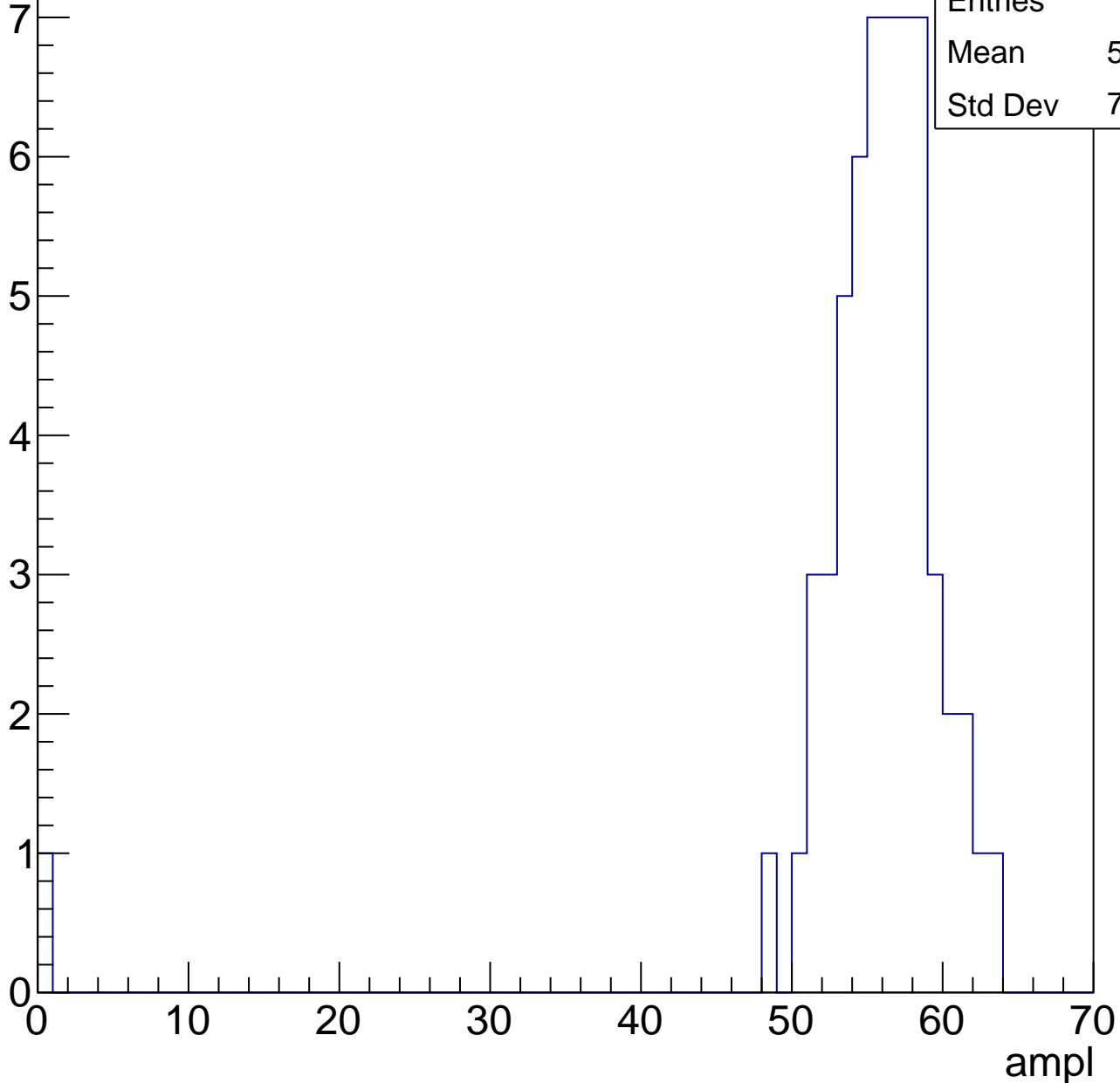


B1L003S, U13-ch58, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.77
Std Dev	7.923

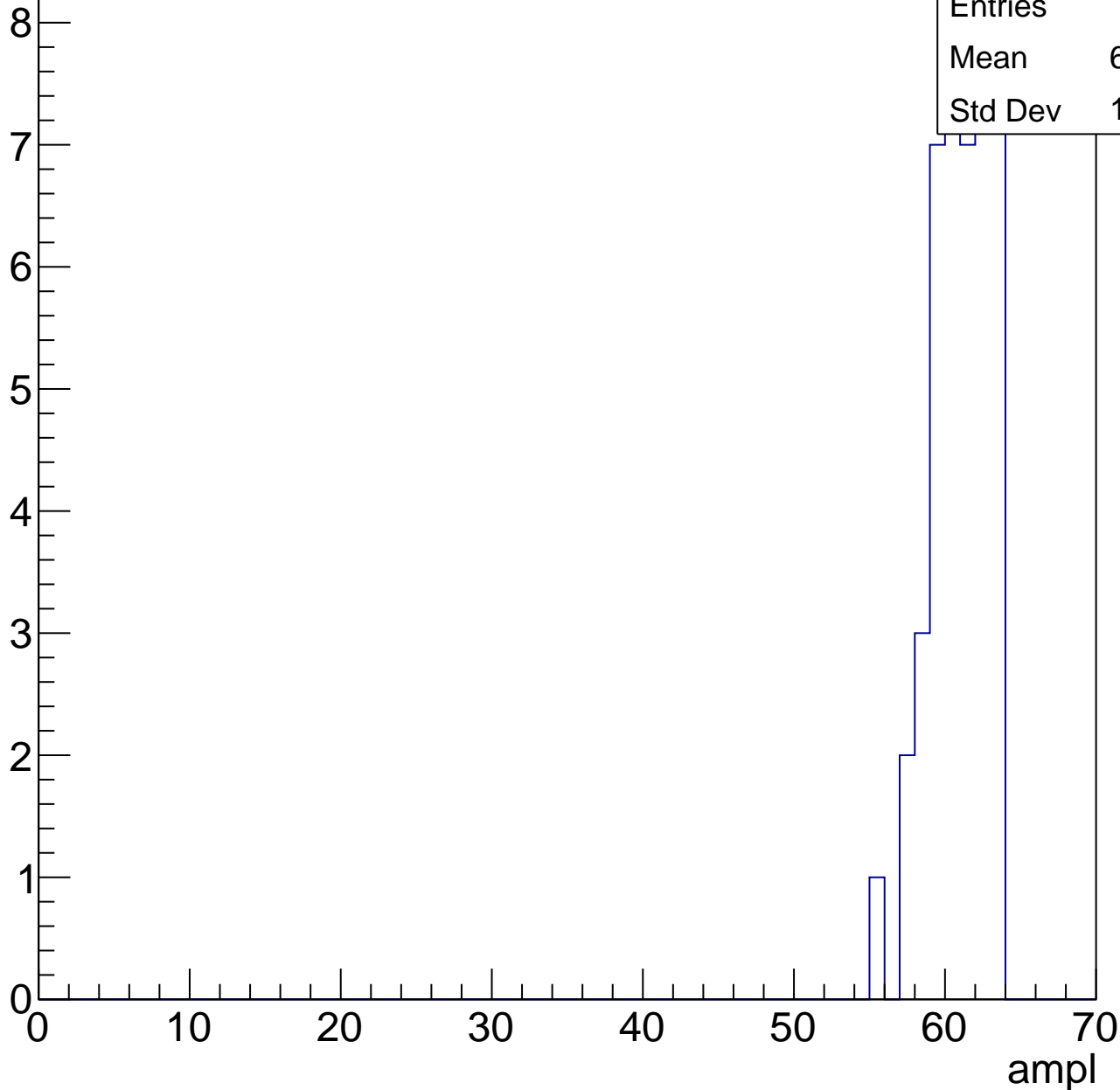


B1L003S, U13-ch58, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	60.52
Std Dev	1.913



B1L003S, U13-ch58, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch58, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch59, adc0

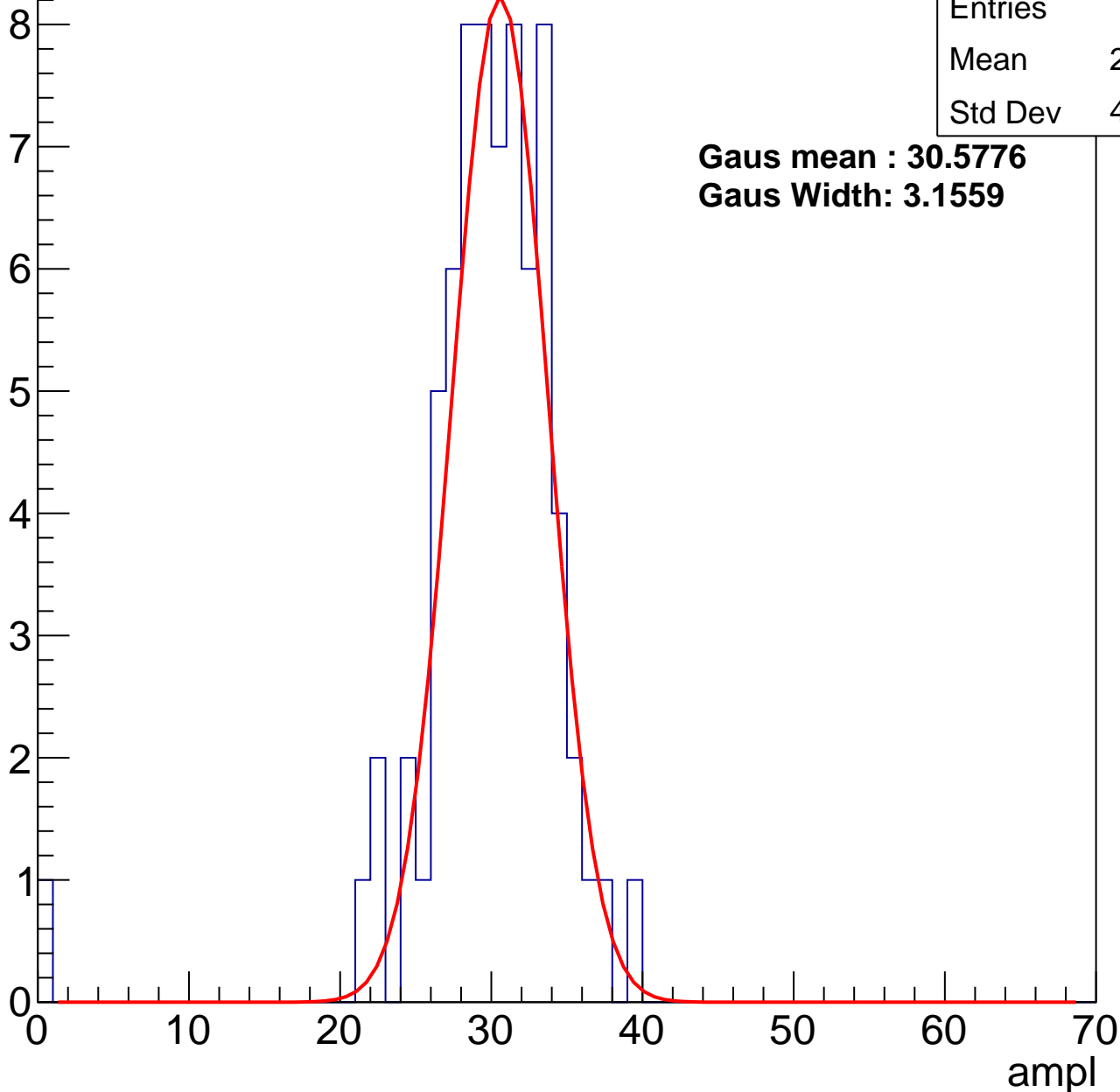
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.42
Std Dev	4.915

Gaus mean : 30.5776

Gaus Width: 3.1559



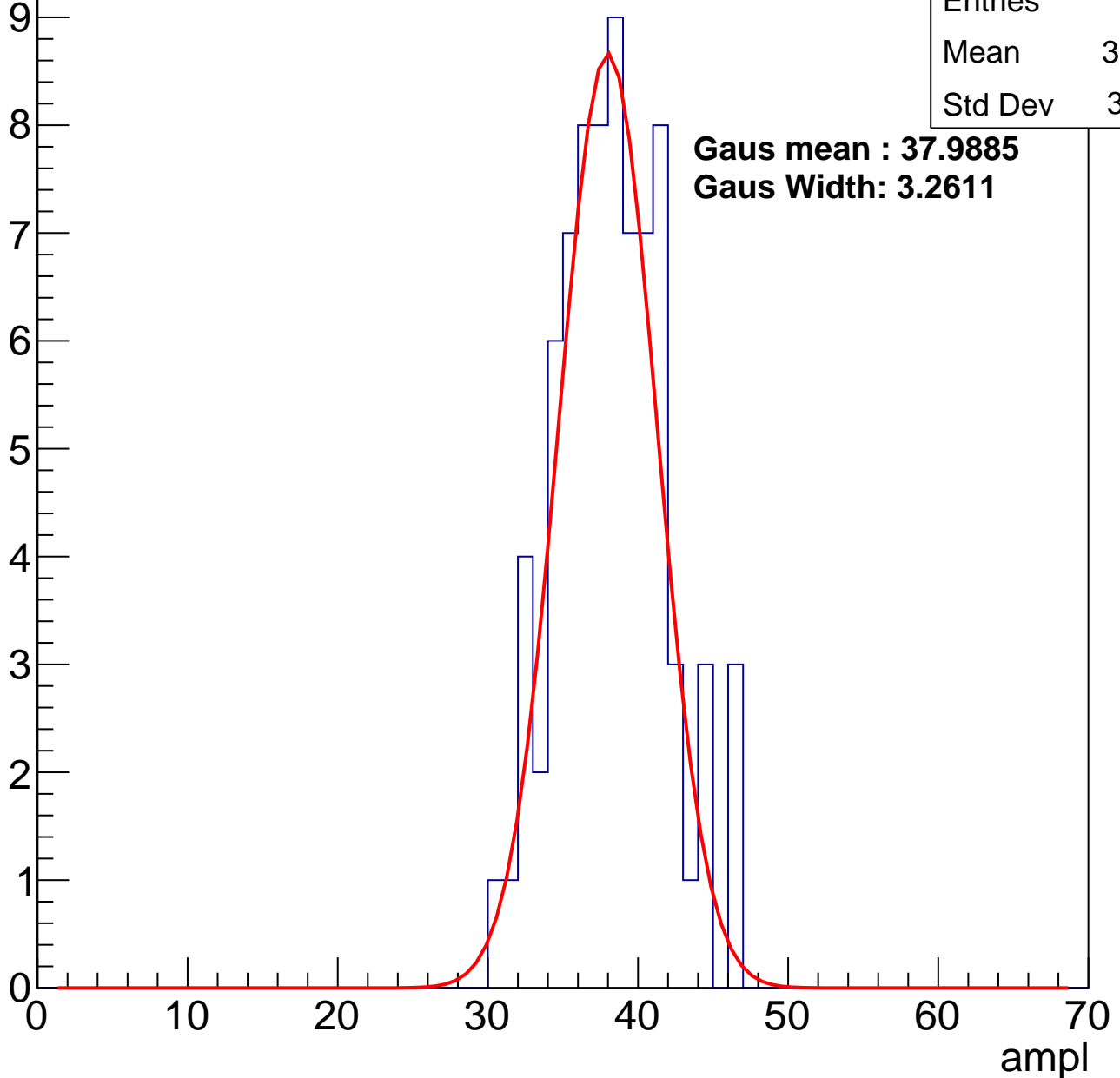
B1L003S, U13-ch59, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	37.82
Std Dev	3.551

Gaus mean : 37.9885
Gaus Width: 3.2611



B1L003S, U13-ch59, adc2

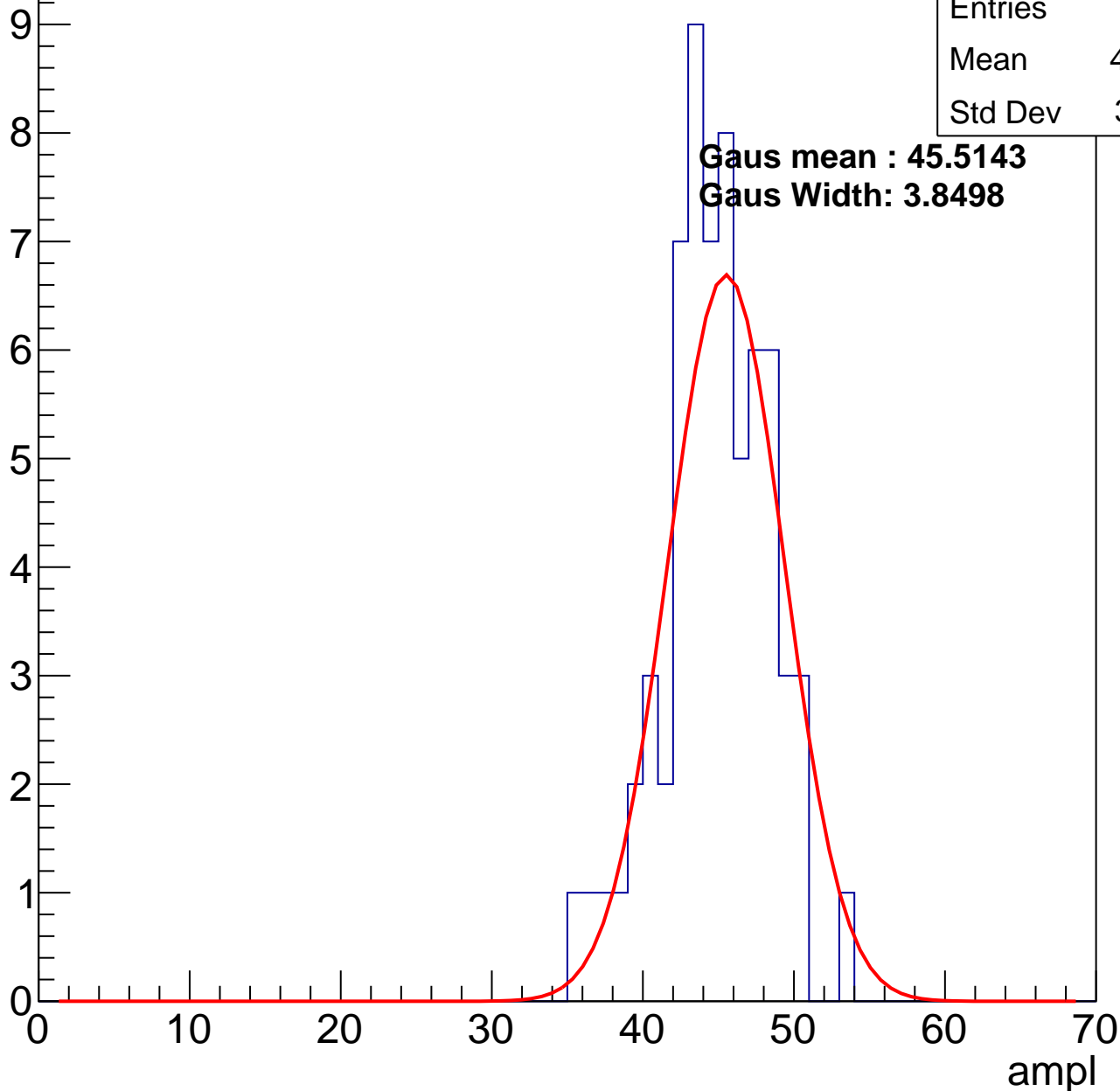
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.32
Std Dev	3.521

Gaus mean : 45.5143

Gaus Width: 3.8498

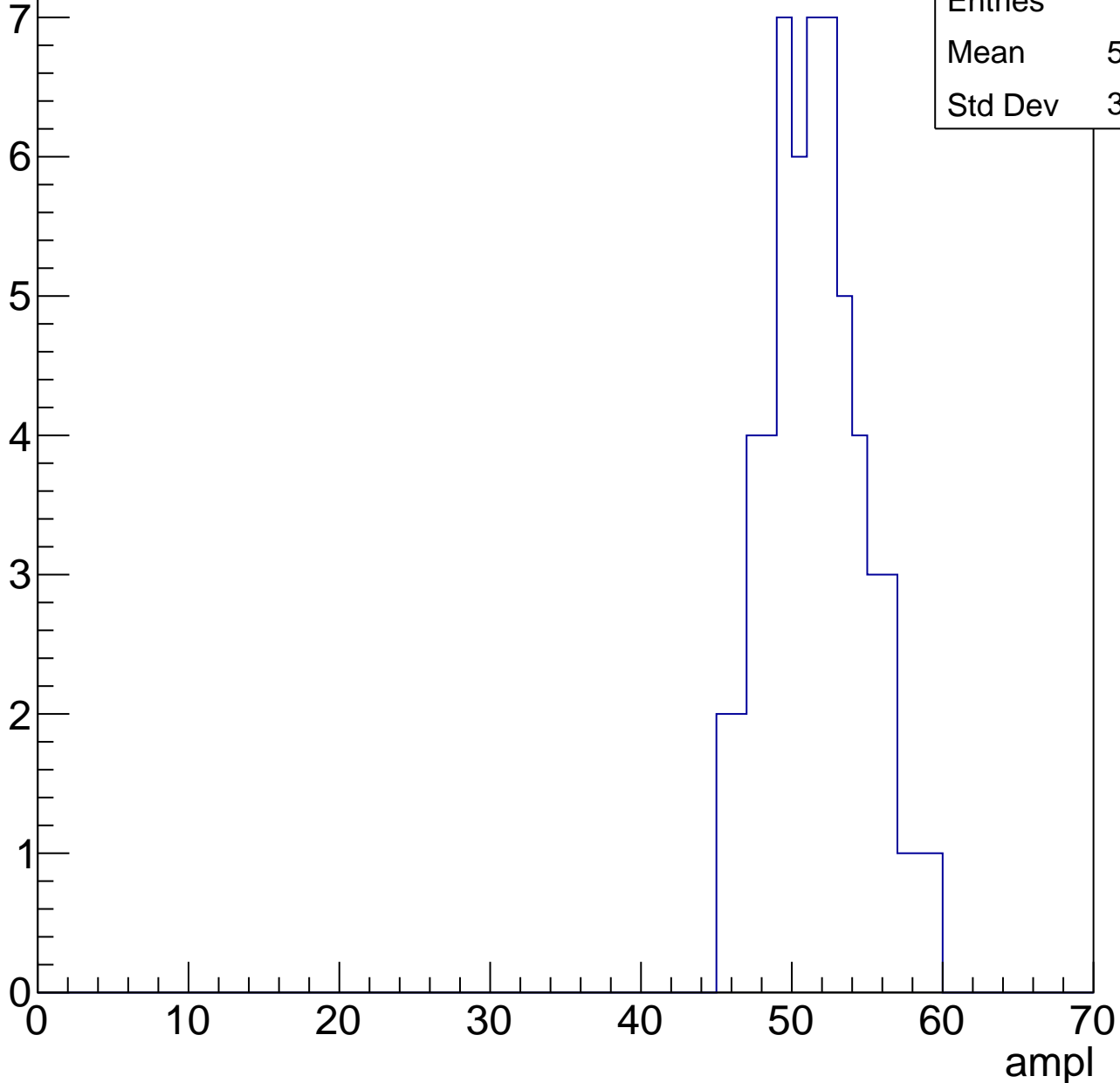


B1L003S, U13-ch59, adc3

calib_packv5_042523_0143.root, FC#13, port D2

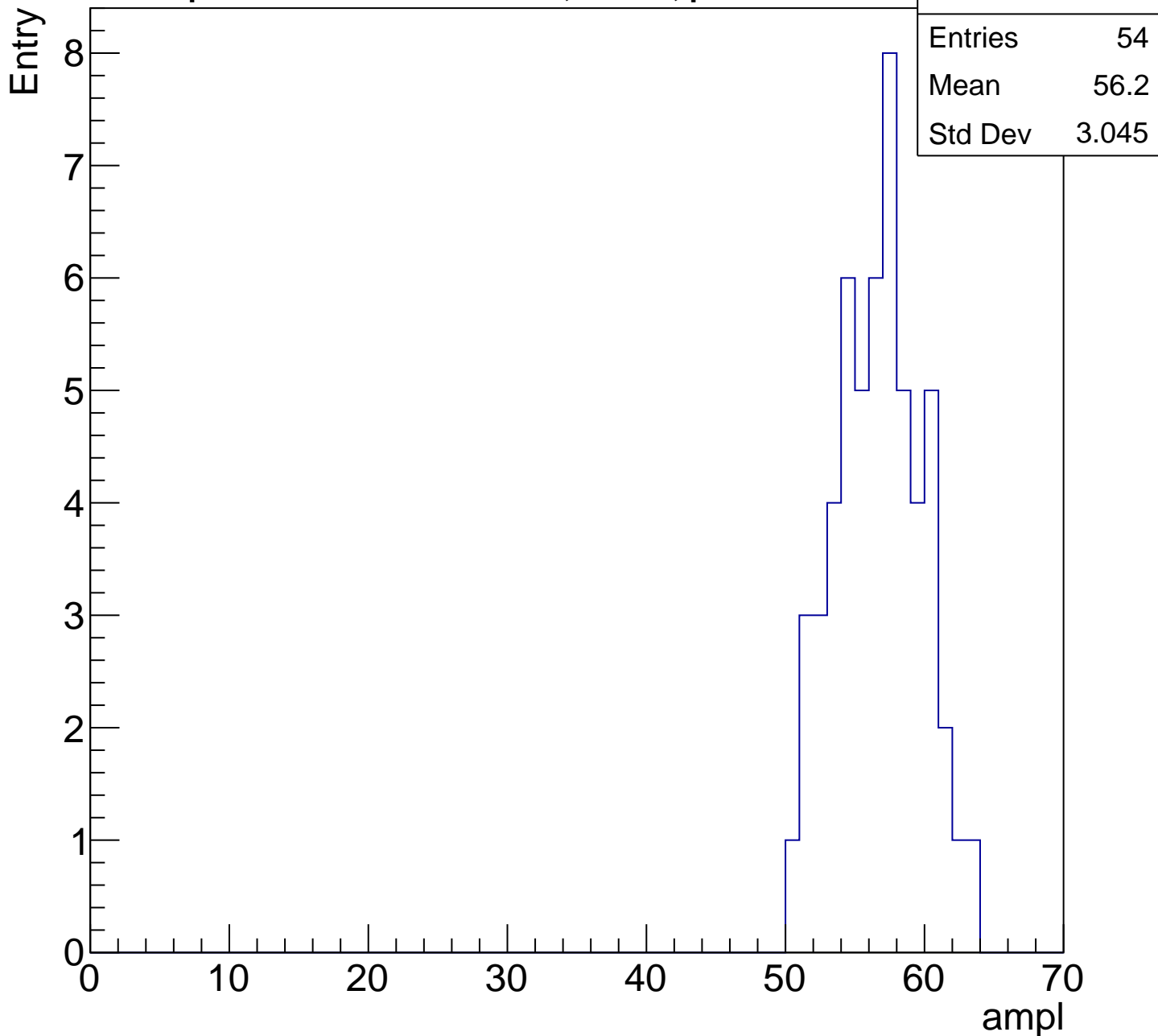
Entry

Entries	57
Mean	51.12
Std Dev	3.218



B1L003S, U13-ch59, adc4

calib_packv5_042523_0143.root, FC#13, port D2

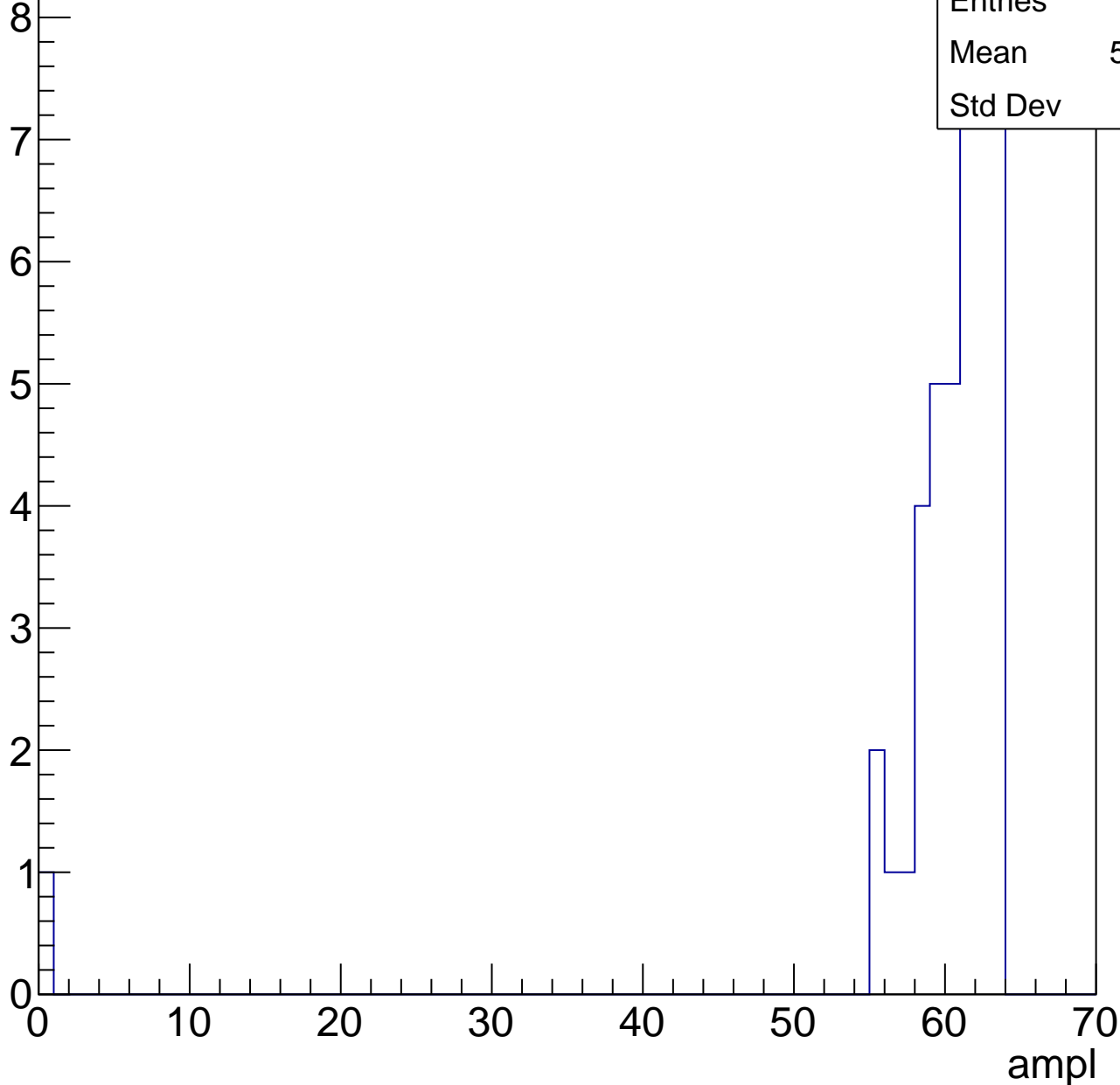


B1L003S, U13-ch59, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	59.02
Std Dev	9.36



B1L003S, U13-ch59, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch59, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch60, adc0

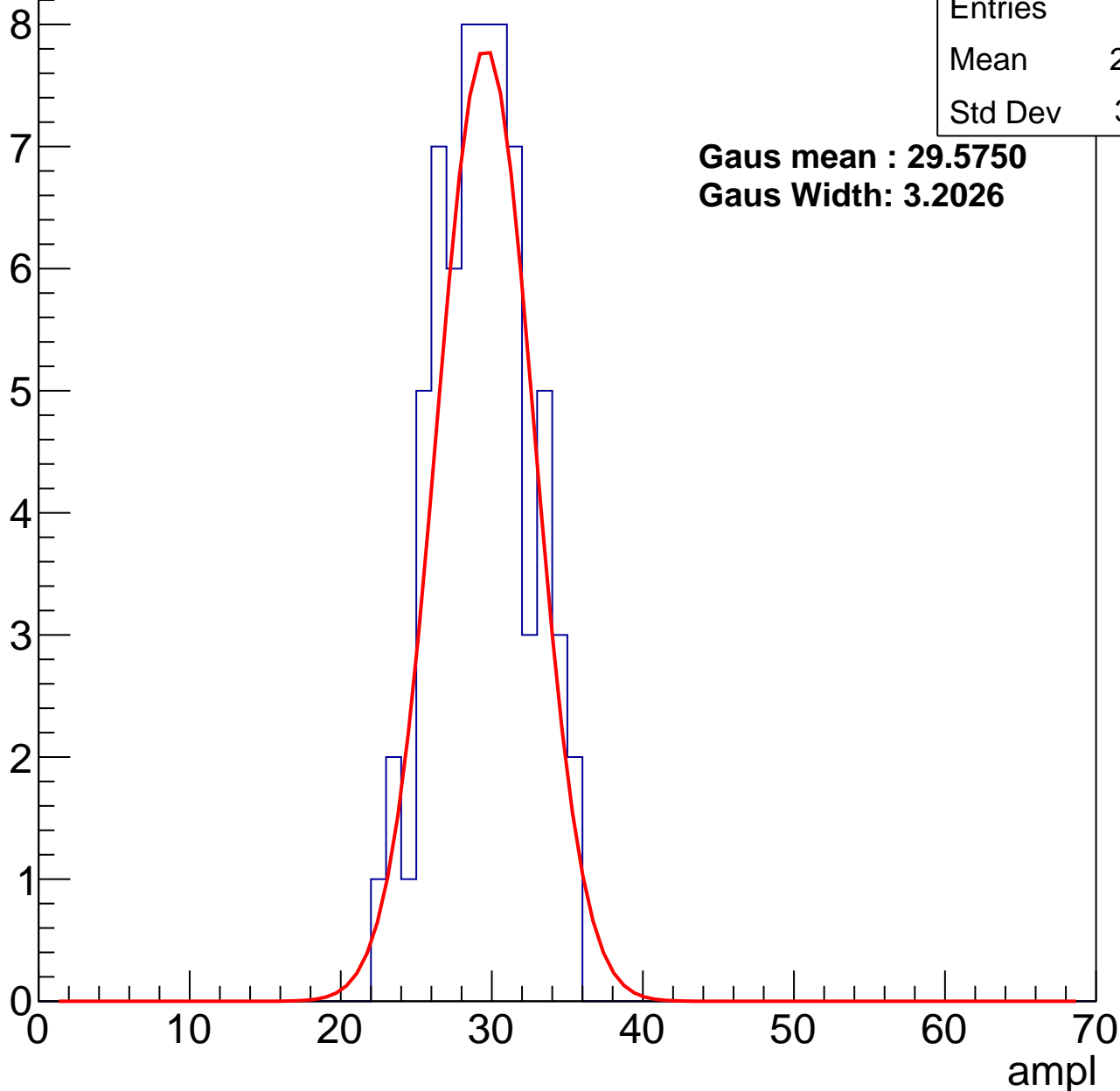
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.89
Std Dev	3.041

Gaus mean : 29.5750

Gaus Width: 3.2026



B1L003S, U13-ch60, adc1

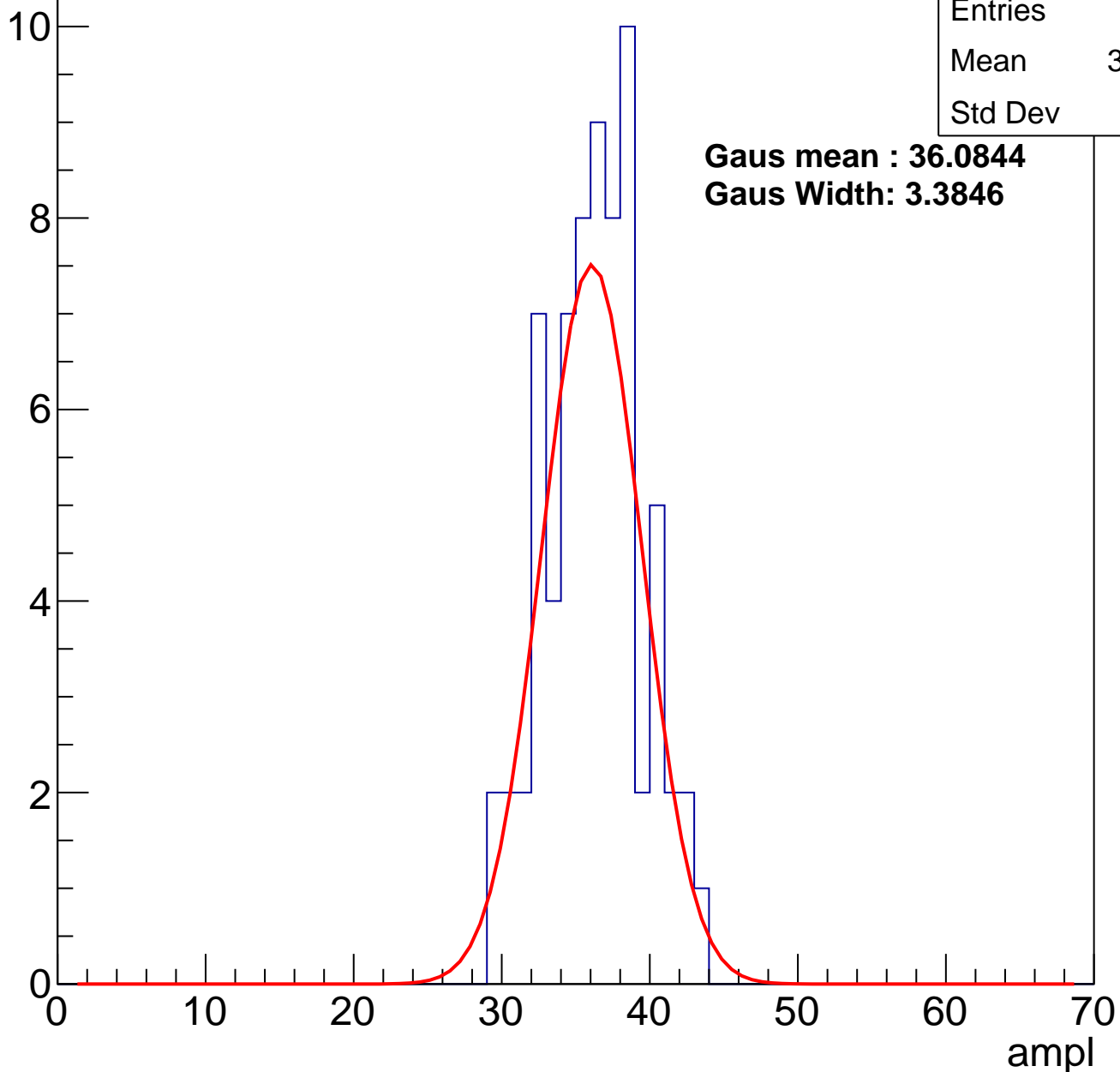
calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	35.79
Std Dev	3.18

Gaus mean : 36.0844

Gaus Width: 3.3846

Entry



B1L003S, U13-ch60, adc2

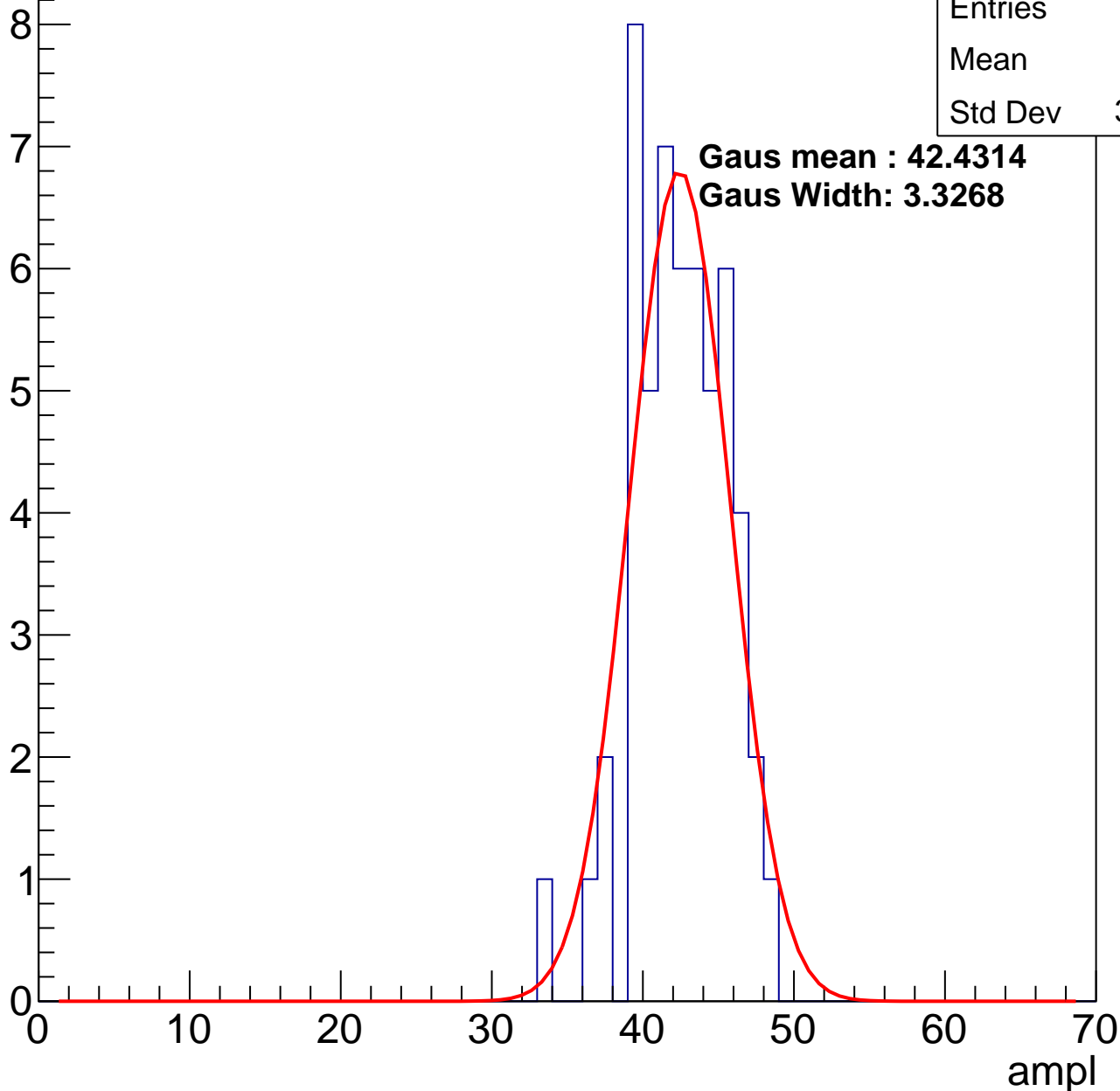
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42
Std Dev	3.031

Gaus mean : 42.4314

Gaus Width: 3.3268

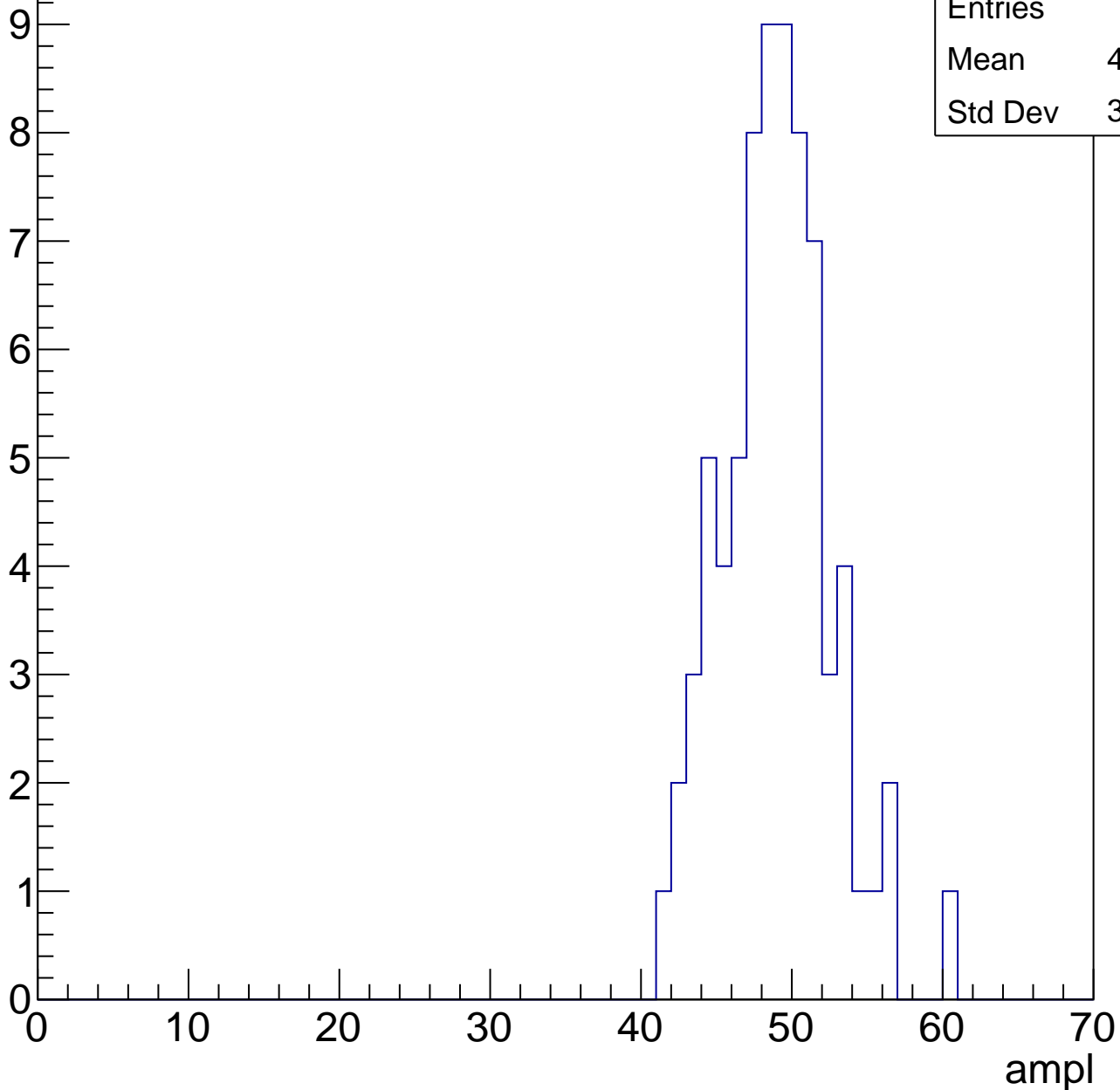


B1L003S, U13-ch60, adc3

calib_packv5_042523_0143.root, FC#13, port D2

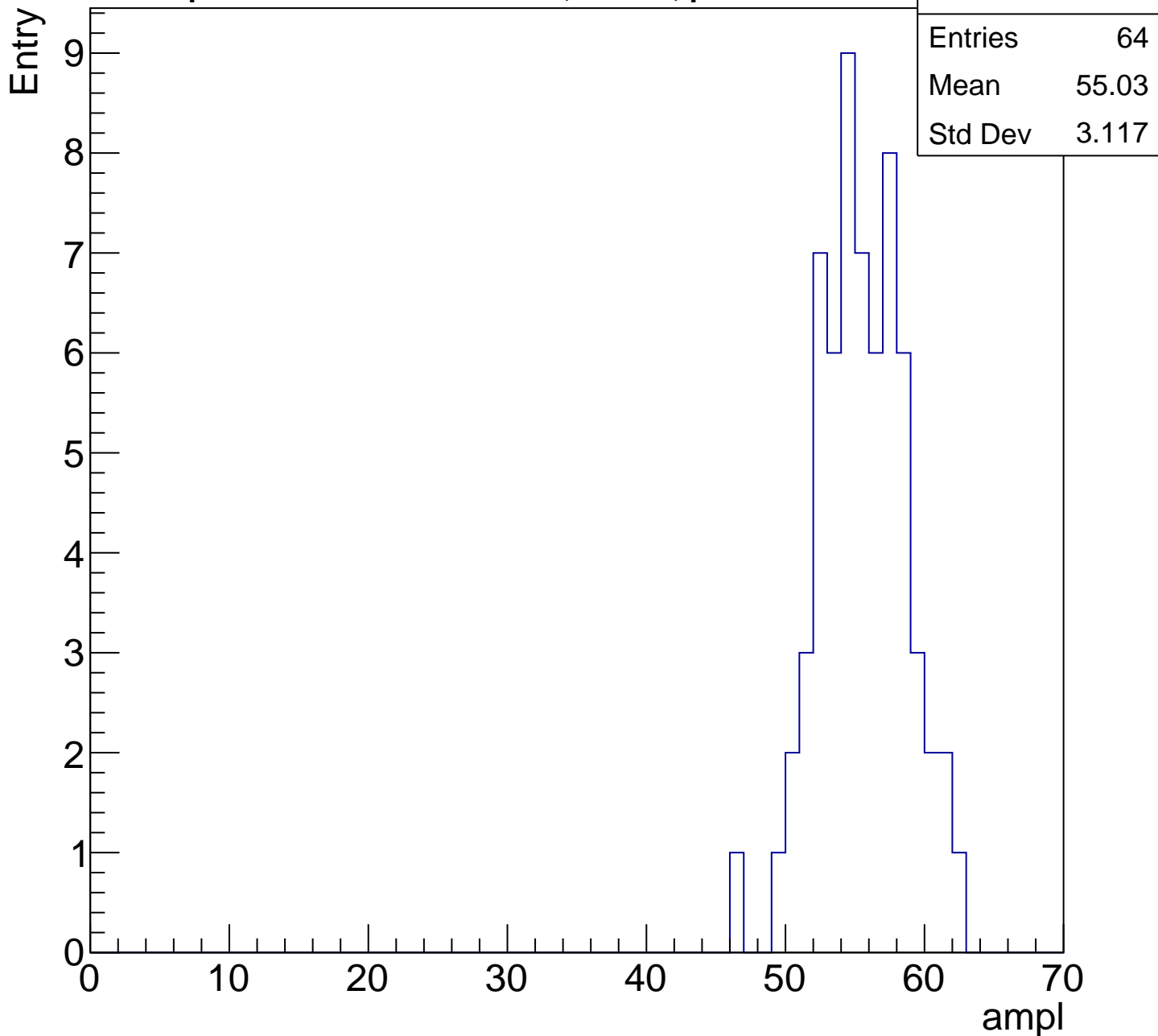
Entry

Entries	73
Mean	48.48
Std Dev	3.574



B1L003S, U13-ch60, adc4

calib_packv5_042523_0143.root, FC#13, port D2

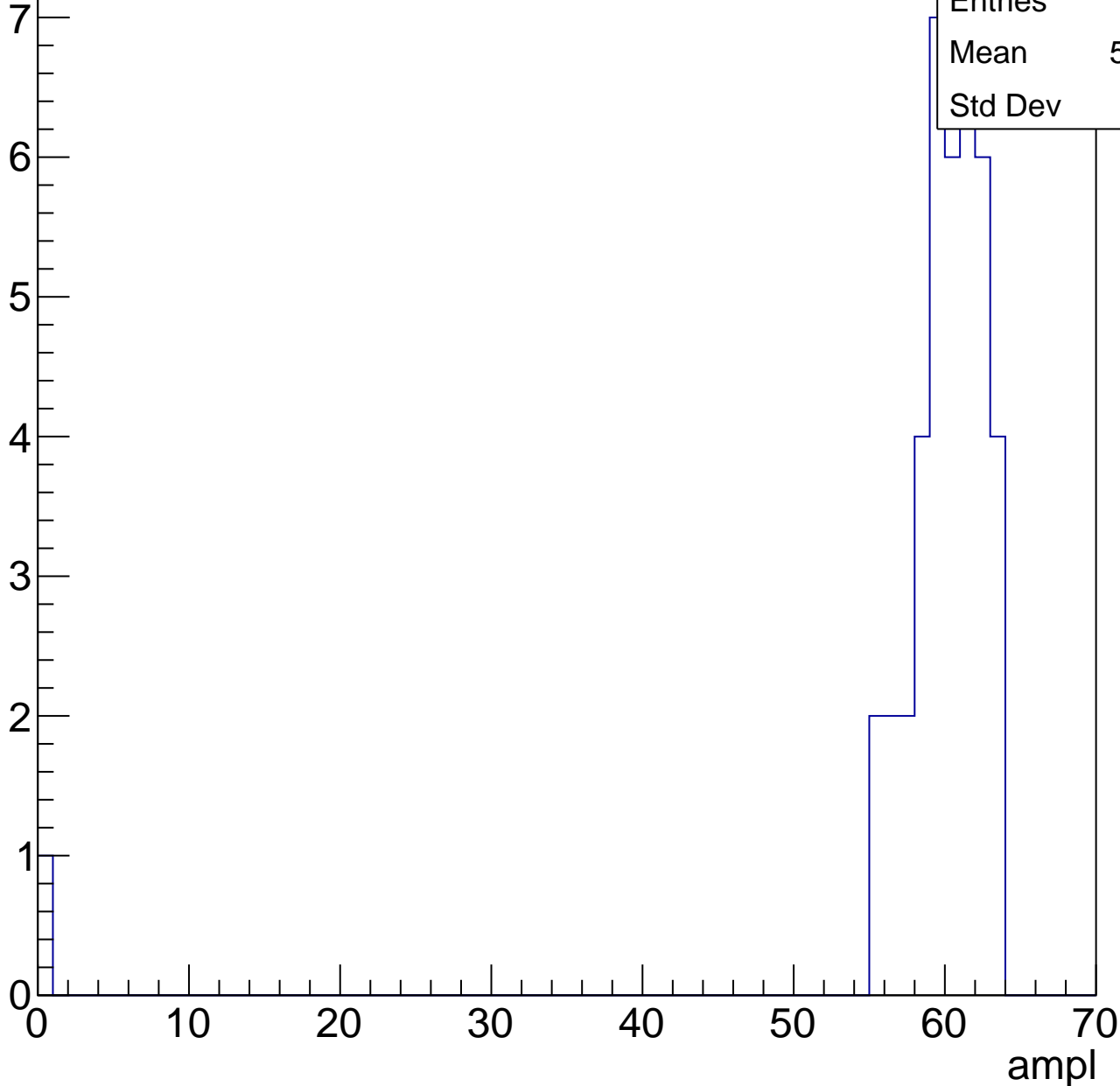


B1L003S, U13-ch60, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.34
Std Dev	9.47

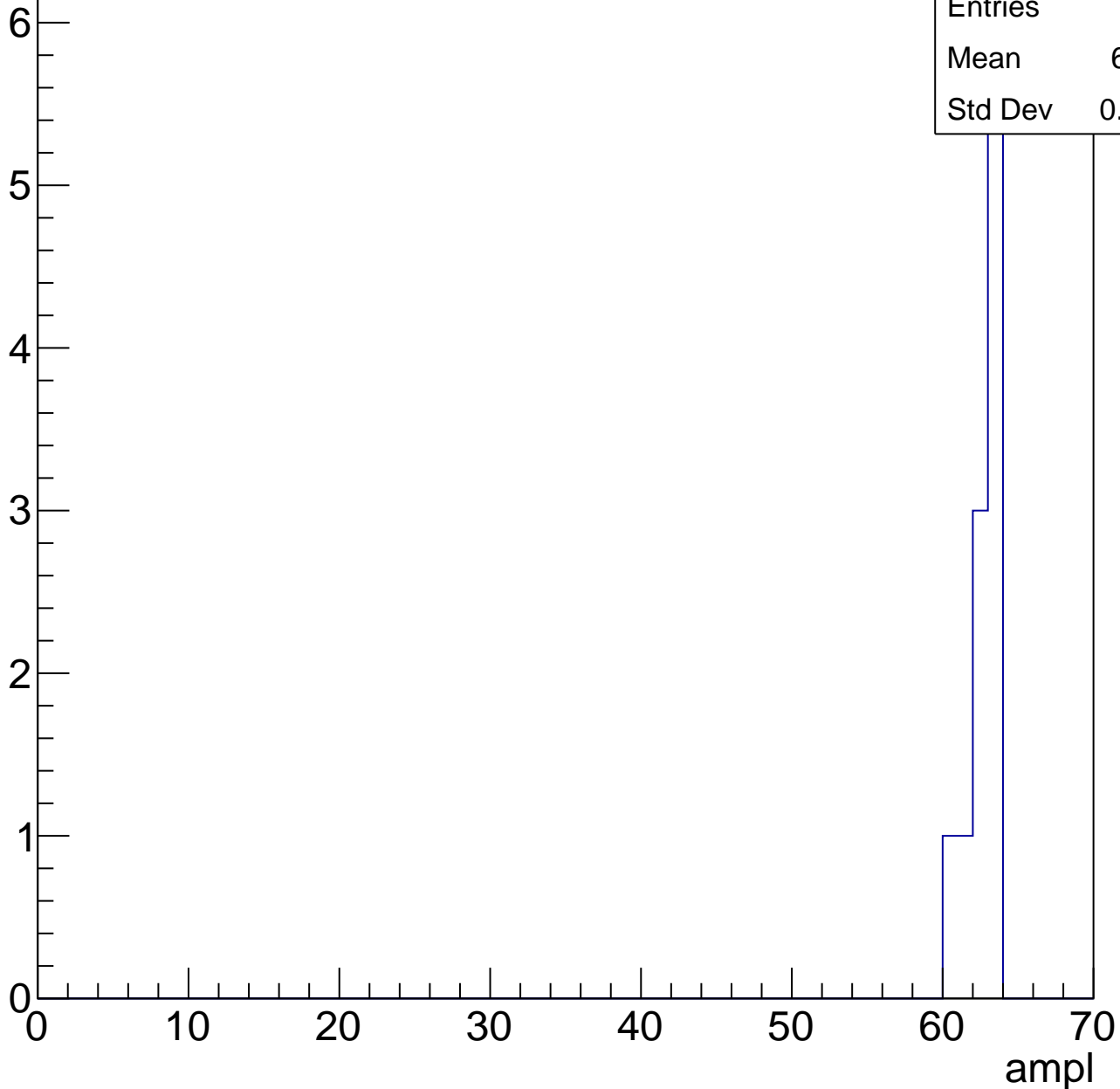


B1L003S, U13-ch60, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.27
Std Dev	0.9621



B1L003S, U13-ch60, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L003S, U13-ch61, adc0

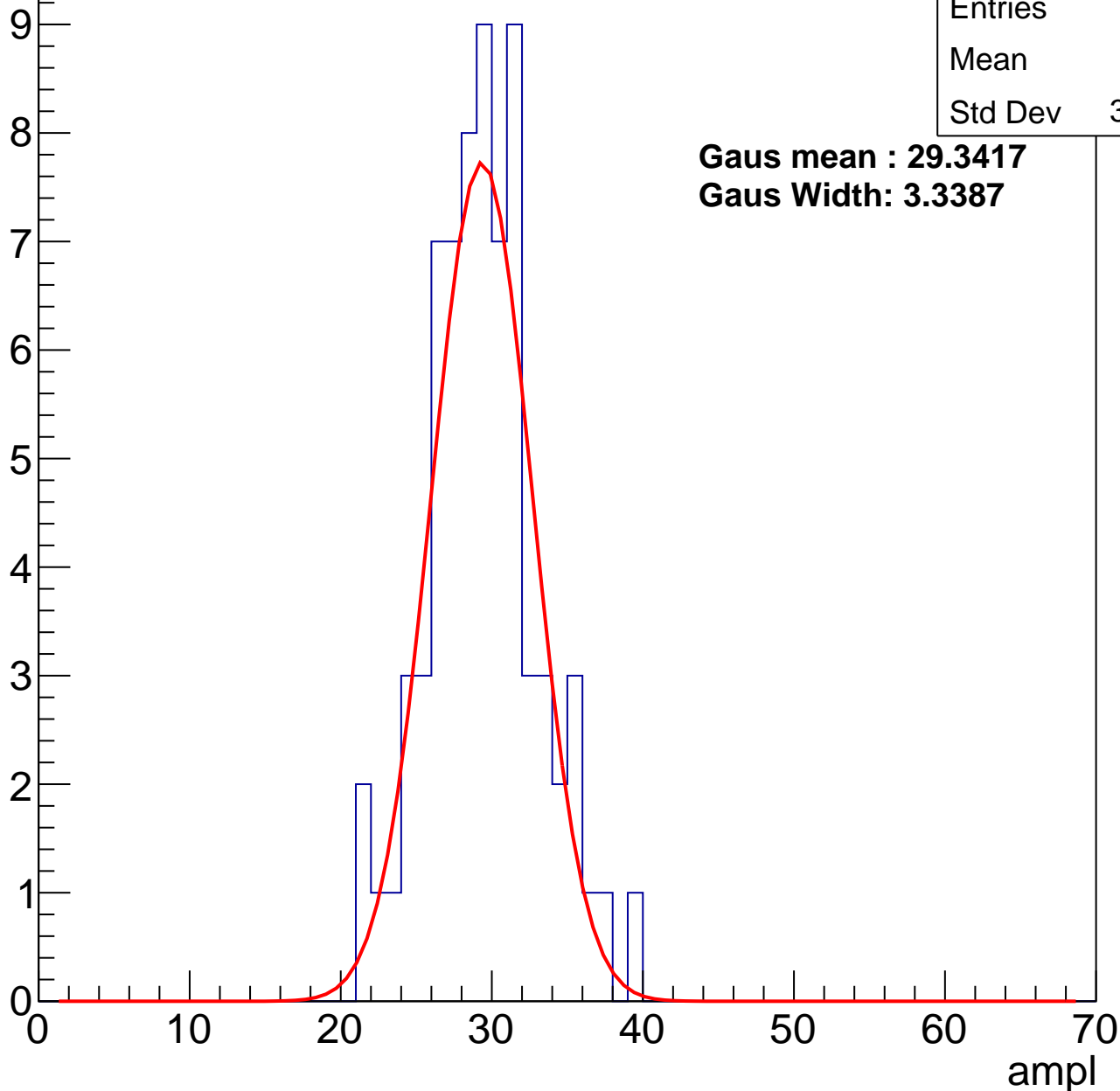
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29
Std Dev	3.608

Gaus mean : 29.3417

Gaus Width: 3.3387

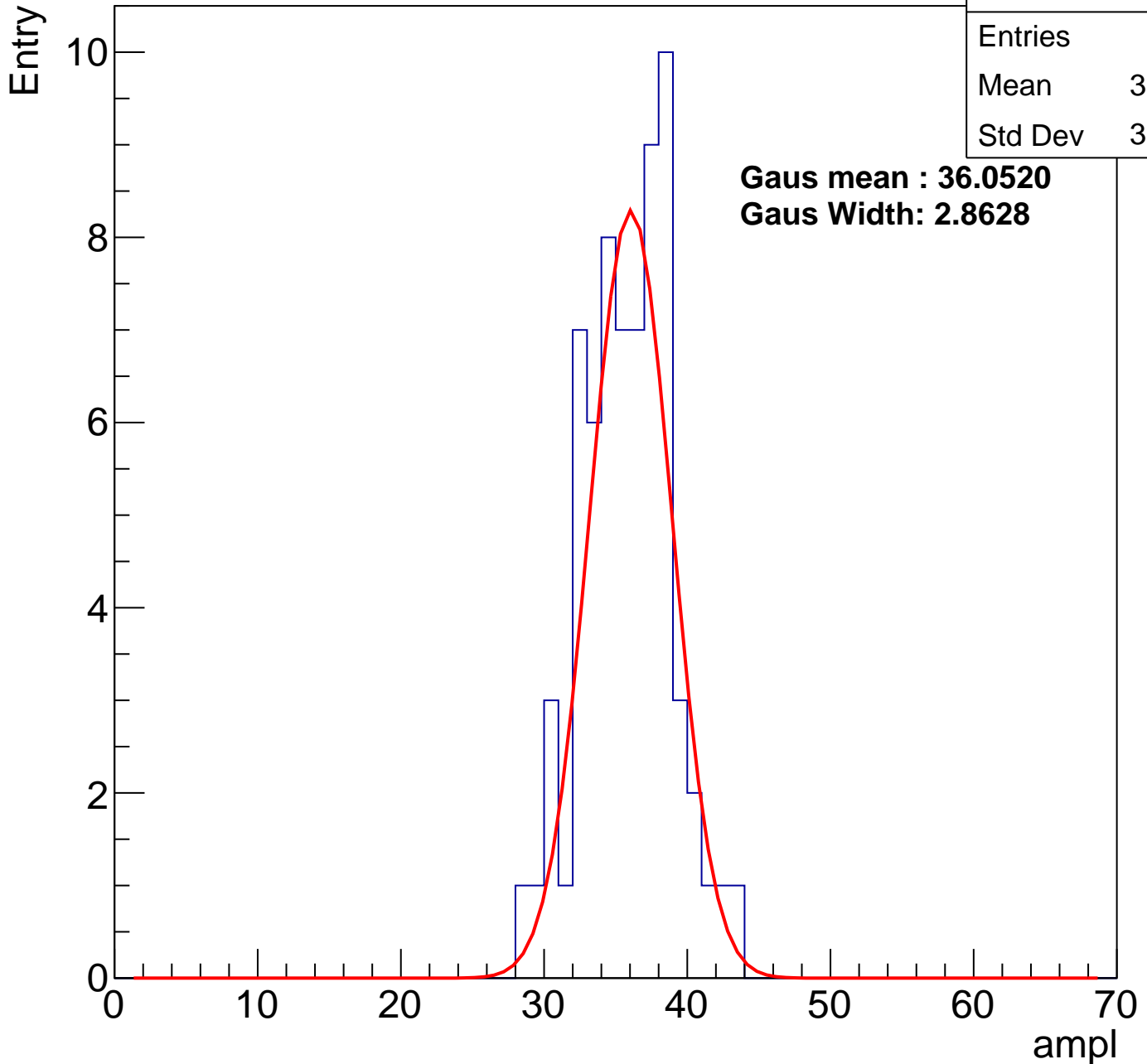


B1L003S, U13-ch61, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	35.37
Std Dev	3.072

Gaus mean : 36.0520
Gaus Width: 2.8628



B1L003S, U13-ch61, adc2

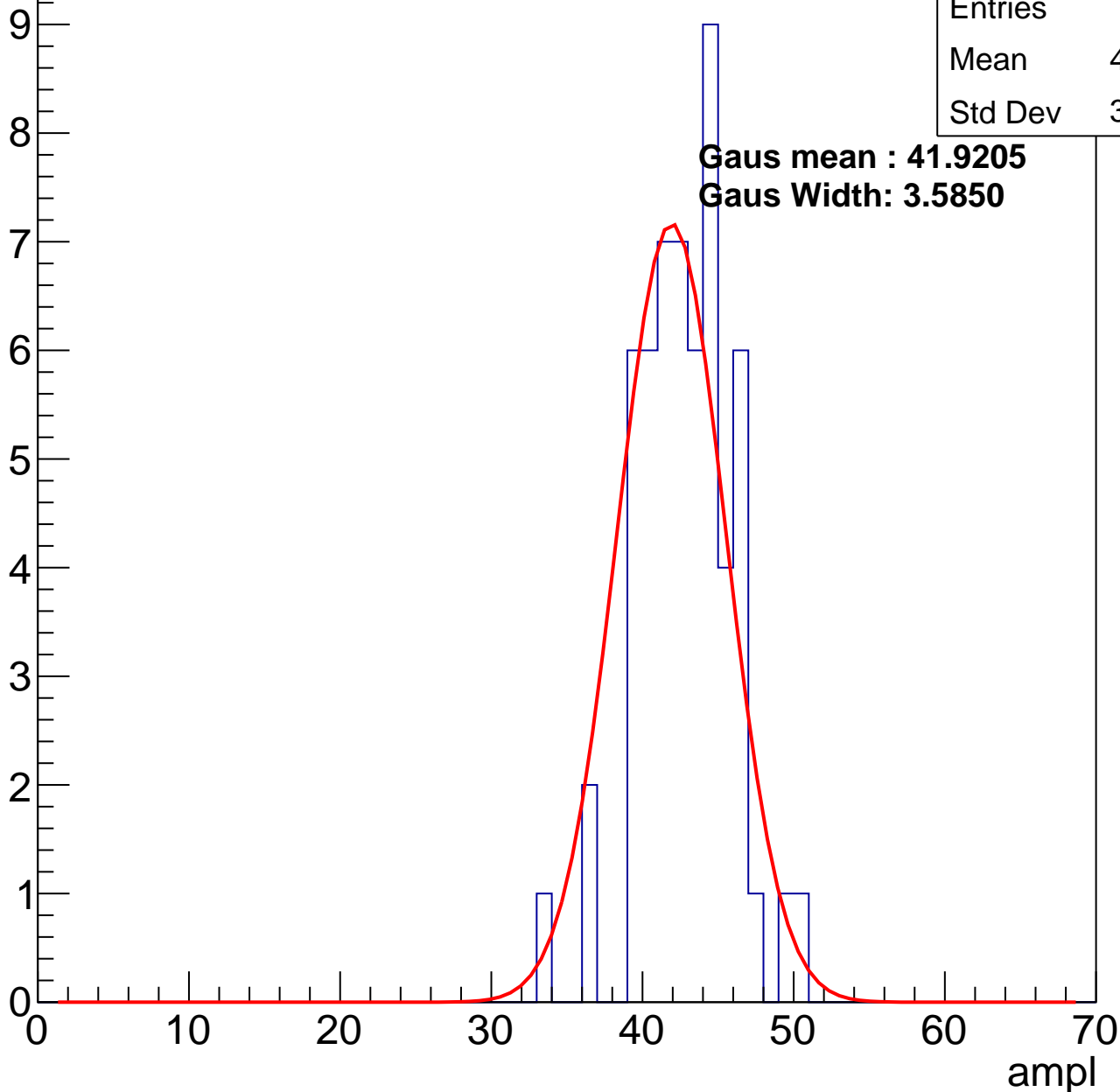
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.39
Std Dev	3.082

Gaus mean : 41.9205

Gaus Width: 3.5850



B1L003S, U13-ch61, adc3

calib_packv5_042523_0143.root, FC#13, port D2

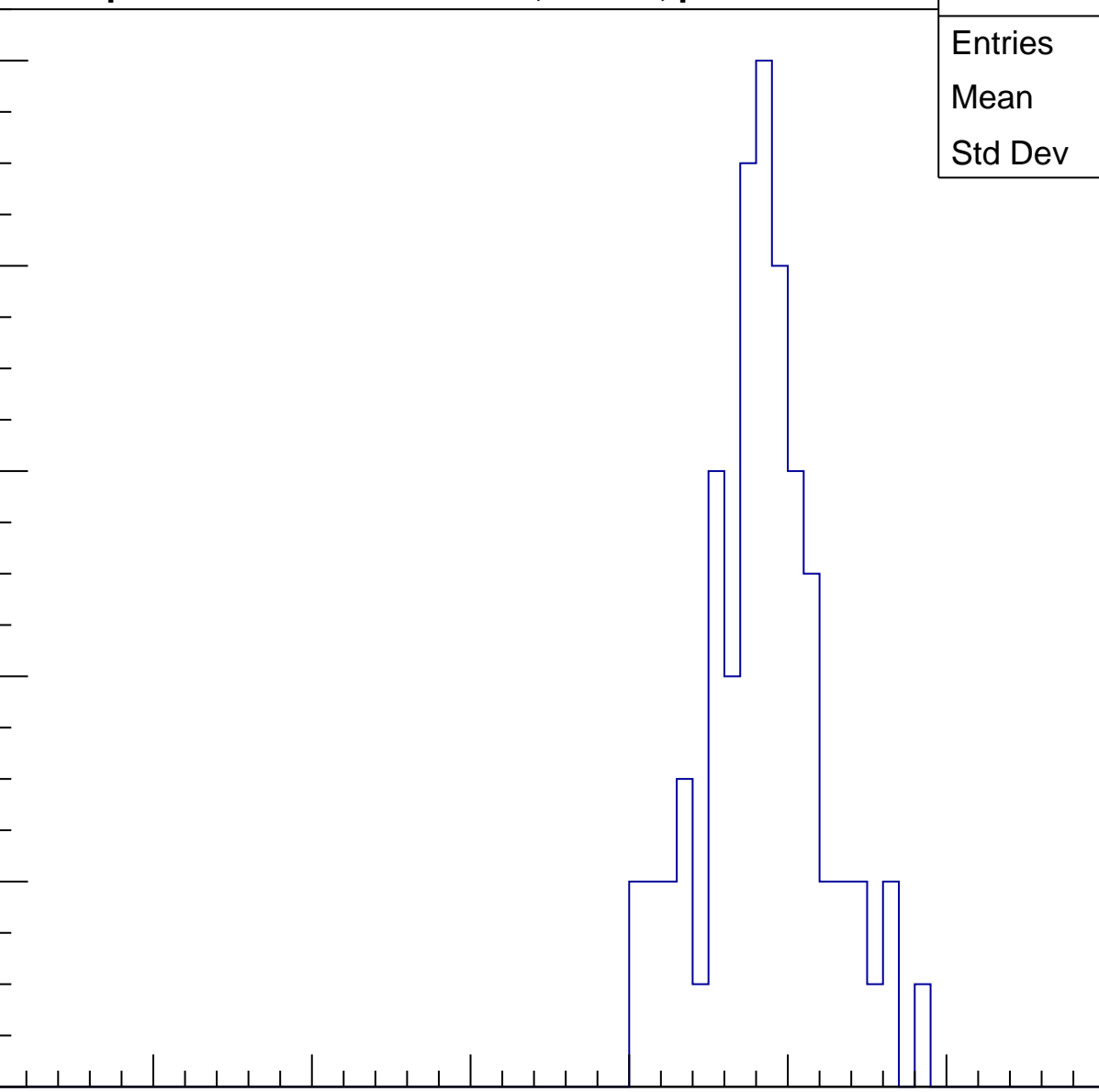
Entries	68
Mean	48.03
Std Dev	3.788

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

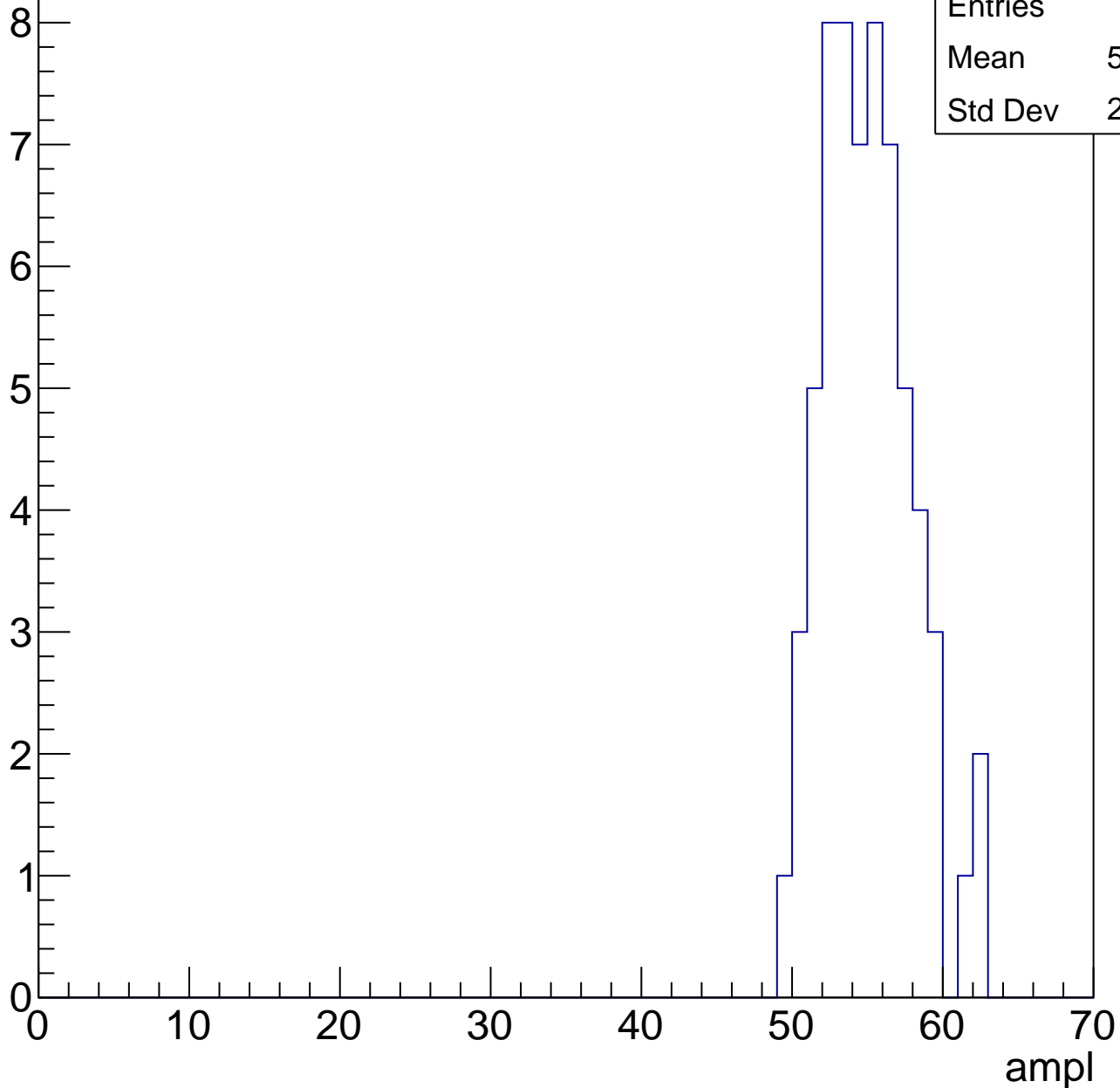
ampl



B1L003S, U13-ch61, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

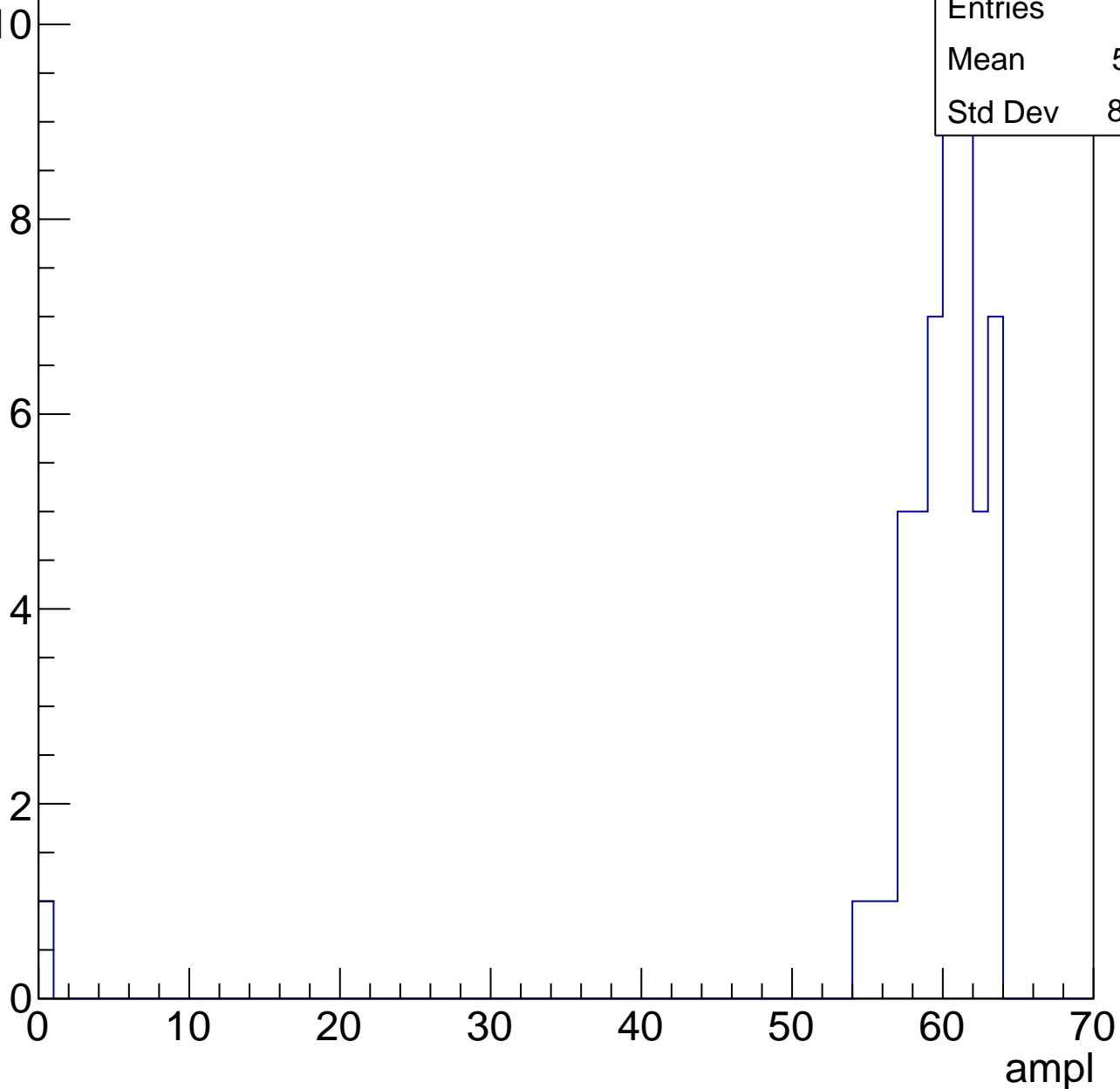


B1L003S, U13-ch61, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	58.71
Std Dev	8.497



B1L003S, U13-ch61, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch61, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch62, adc0

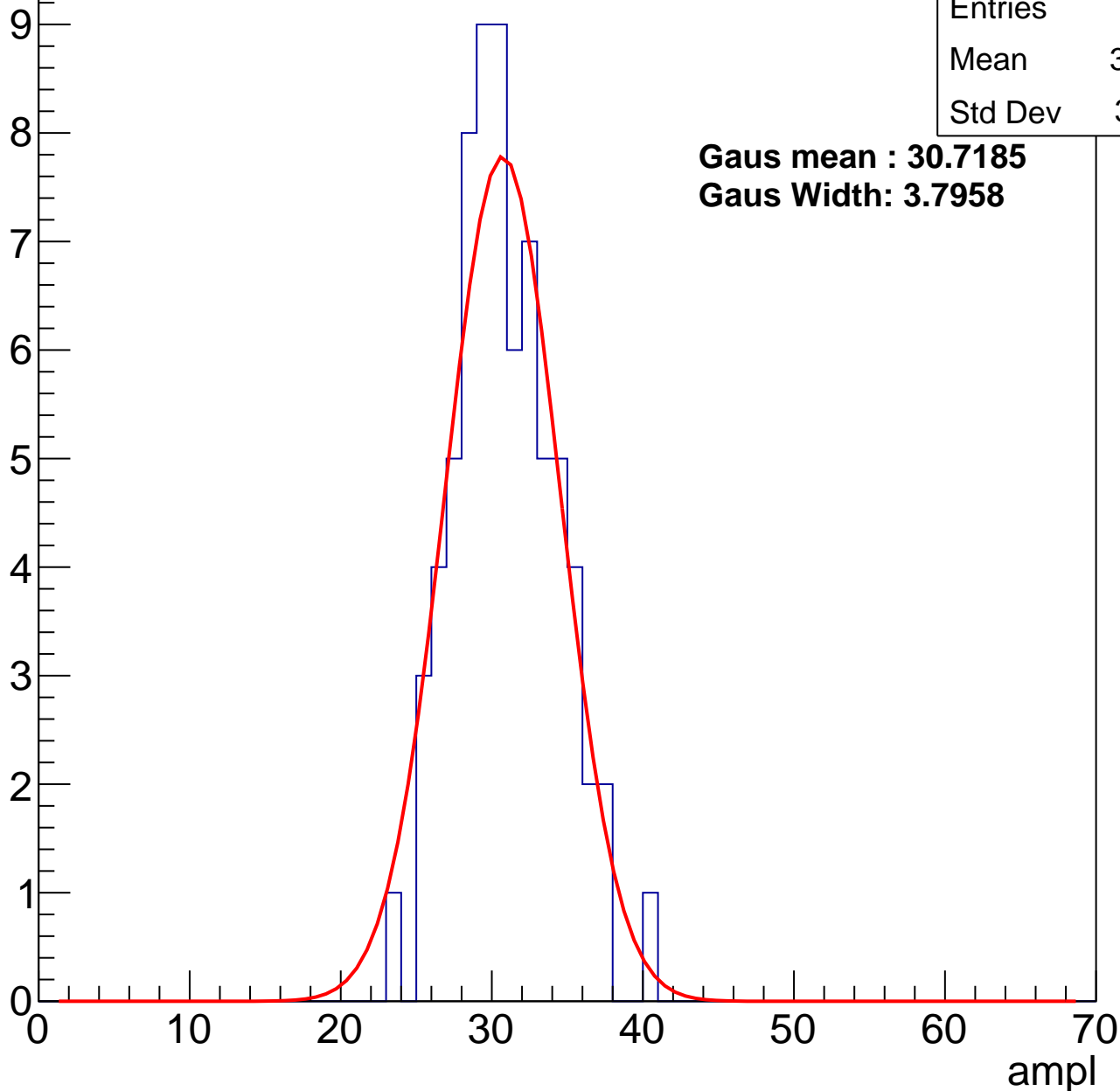
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.46
Std Dev	3.331

Gaus mean : 30.7185

Gaus Width: 3.7958



B1L003S, U13-ch62, adc1

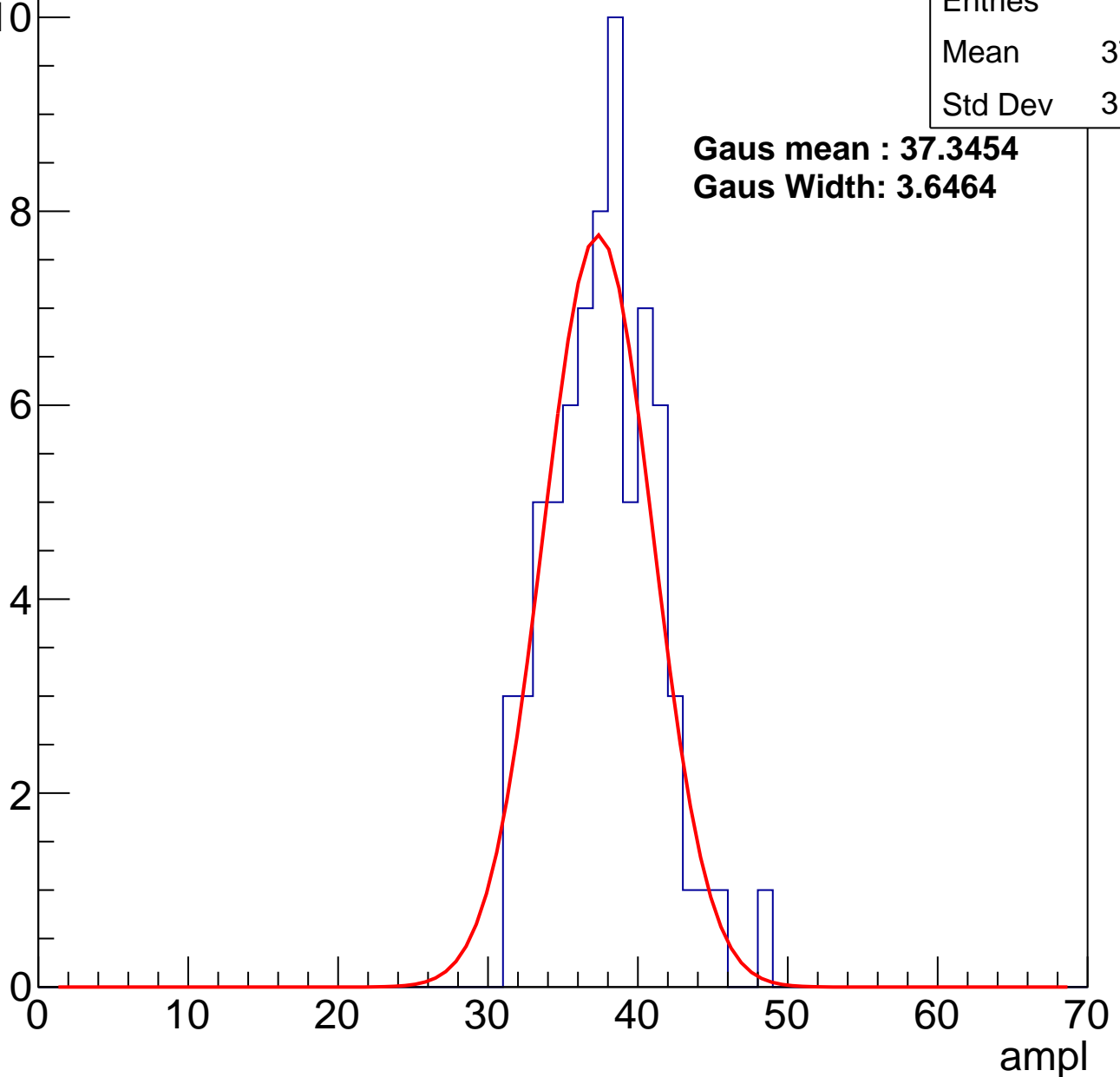
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.35
Std Dev	3.465

Gaus mean : 37.3454

Gaus Width: 3.6464



B1L003S, U13-ch62, adc2

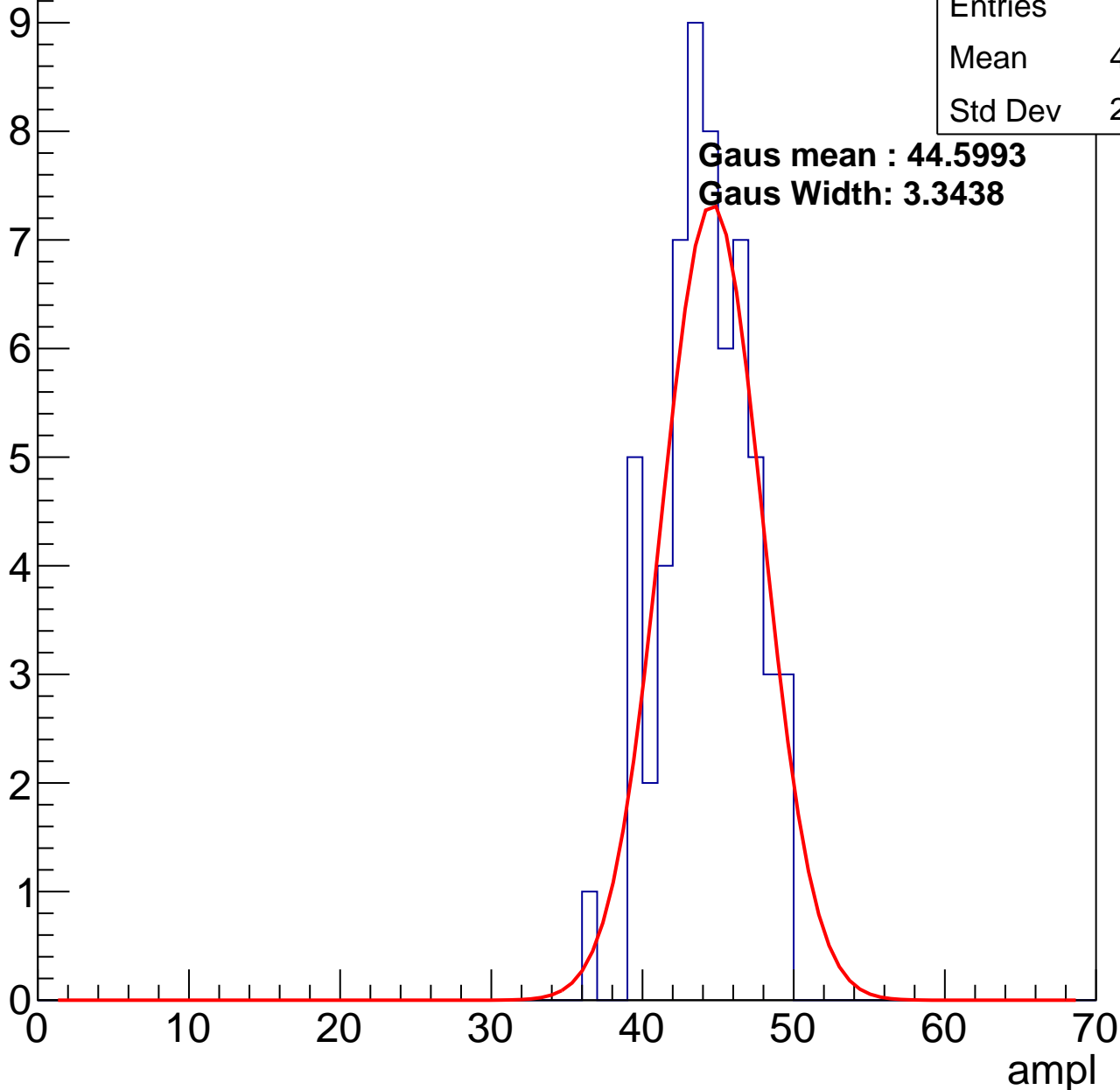
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.77
Std Dev	2.866

Gaus mean : 44.5993

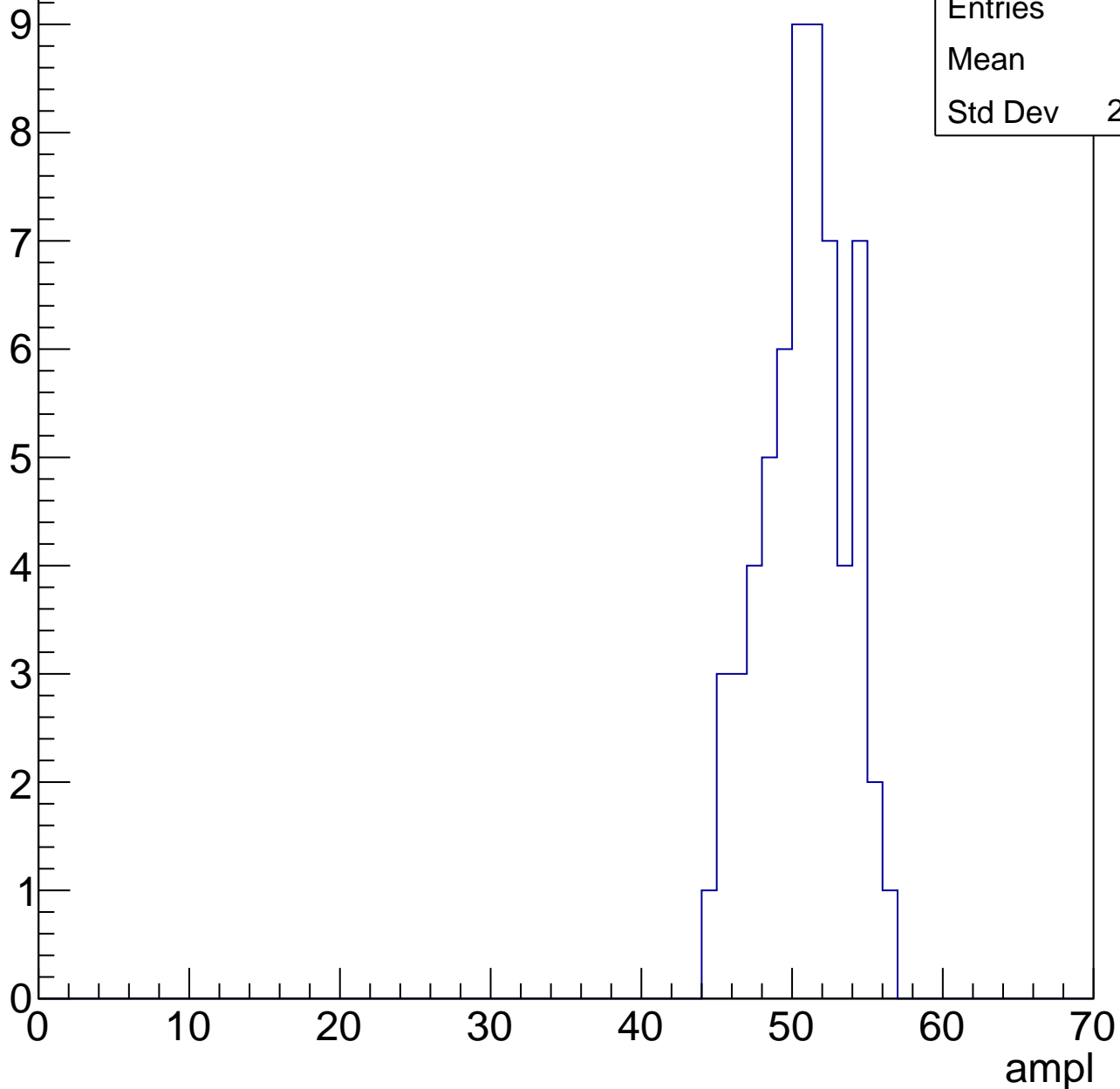
Gaus Width: 3.3438



B1L003S, U13-ch62, adc3

calib_packv5_042523_0143.root, FC#13, port D2

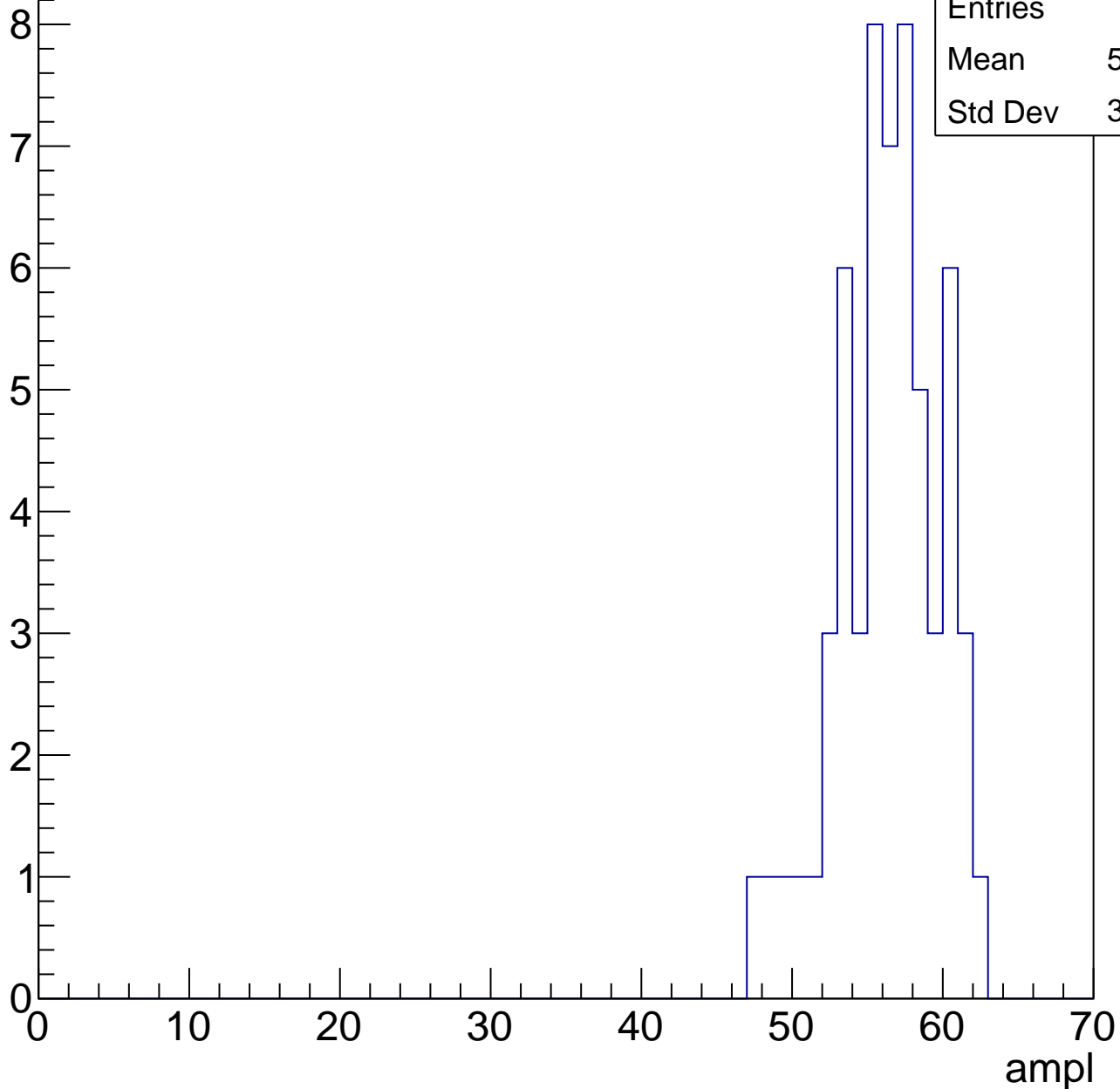
Entry



B1L003S, U13-ch62, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

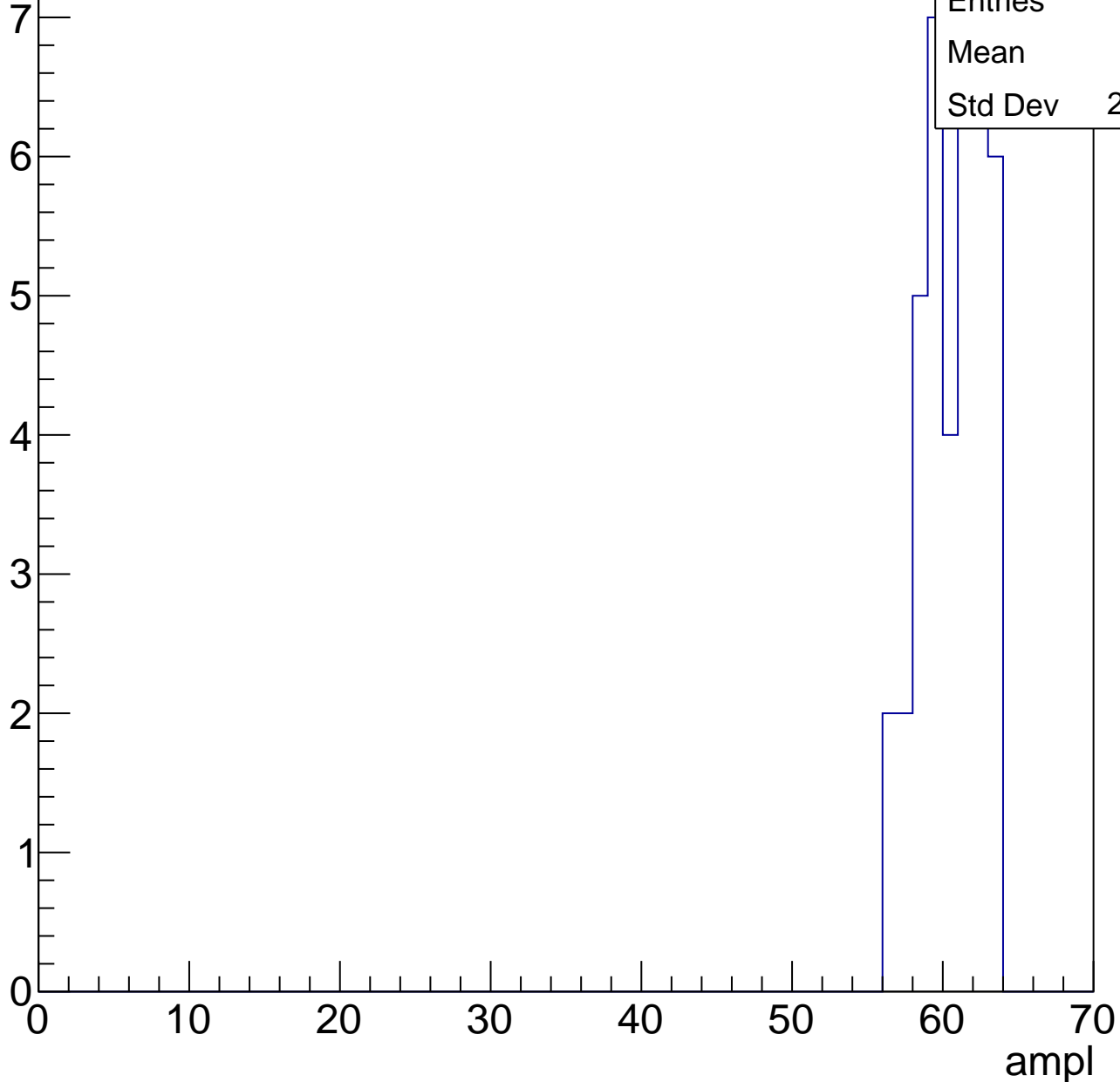


Entries	58
Mean	55.88
Std Dev	3.317

B1L003S, U13-ch62, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

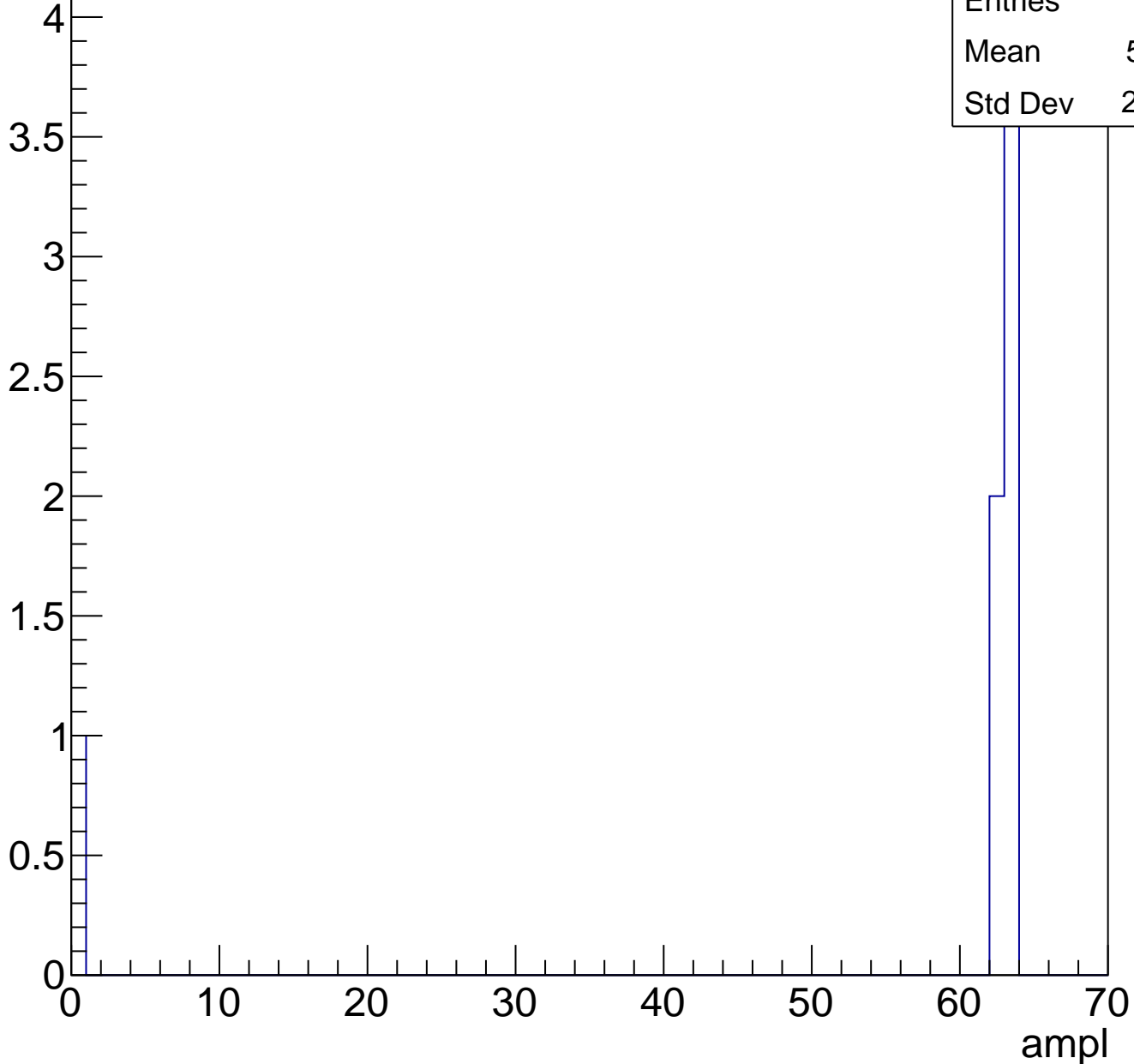


Entries	40
Mean	60.2
Std Dev	2.027

B1L003S, U13-ch62, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch62, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch63, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	28.64
Std Dev	6.897

Gaus mean : 30.6134

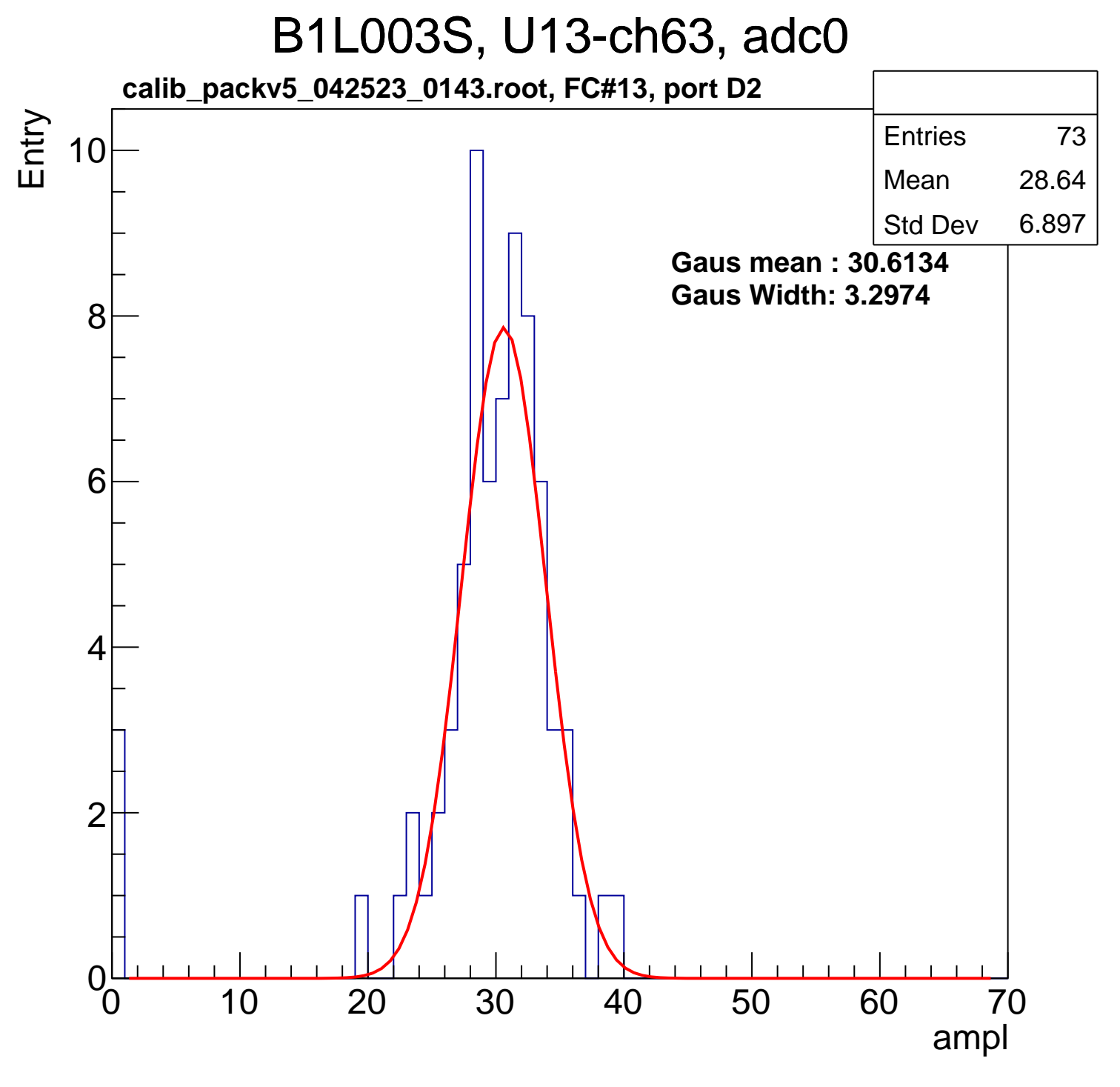
Gaus Width: 3.2974

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch63, adc1

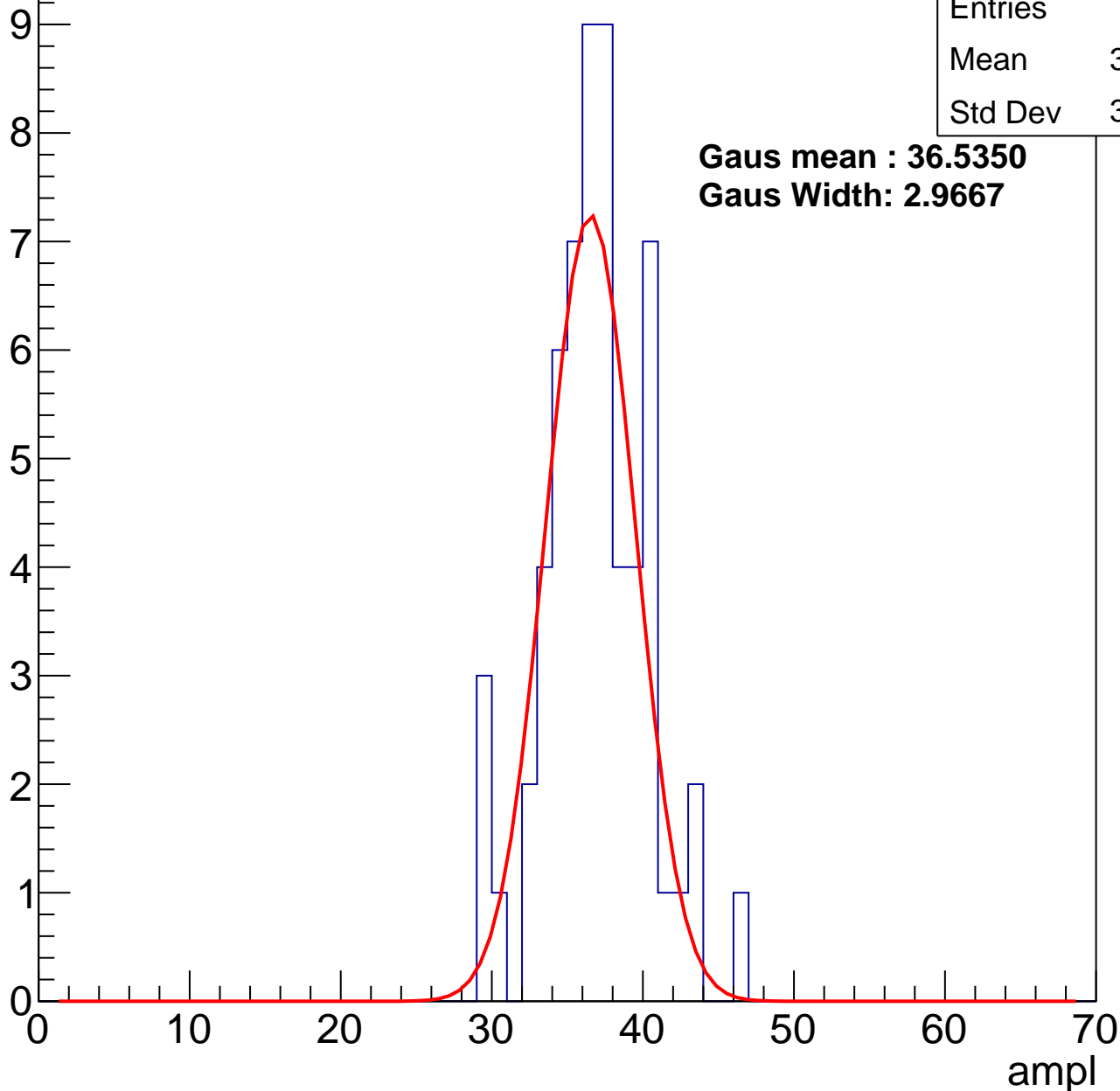
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.43
Std Dev	3.395

Gaus mean : 36.5350

Gaus Width: 2.9667



B1L003S, U13-ch63, adc2

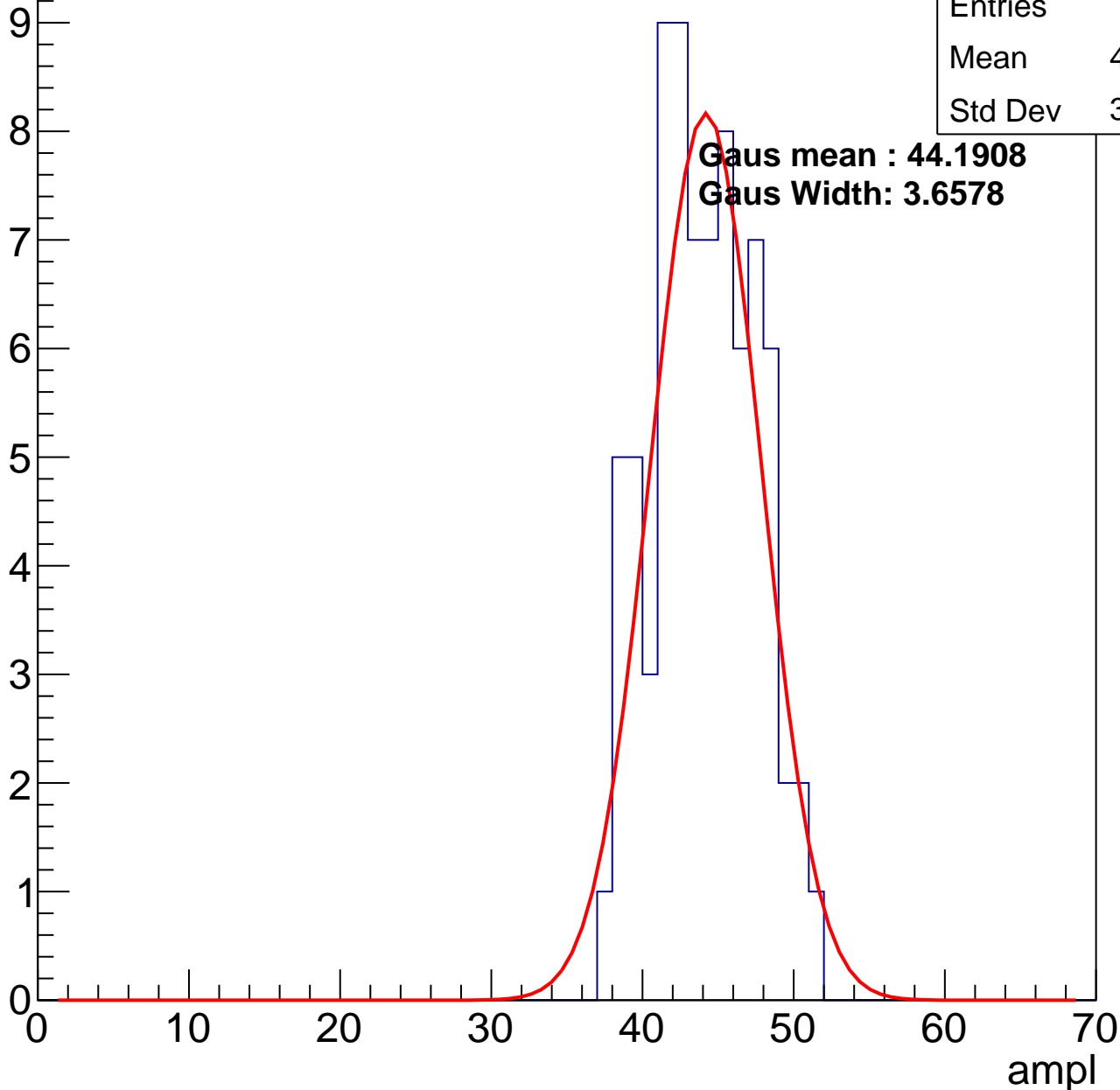
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	43.59
Std Dev	3.349

Gaus mean : 44.1908

Gaus Width: 3.6578

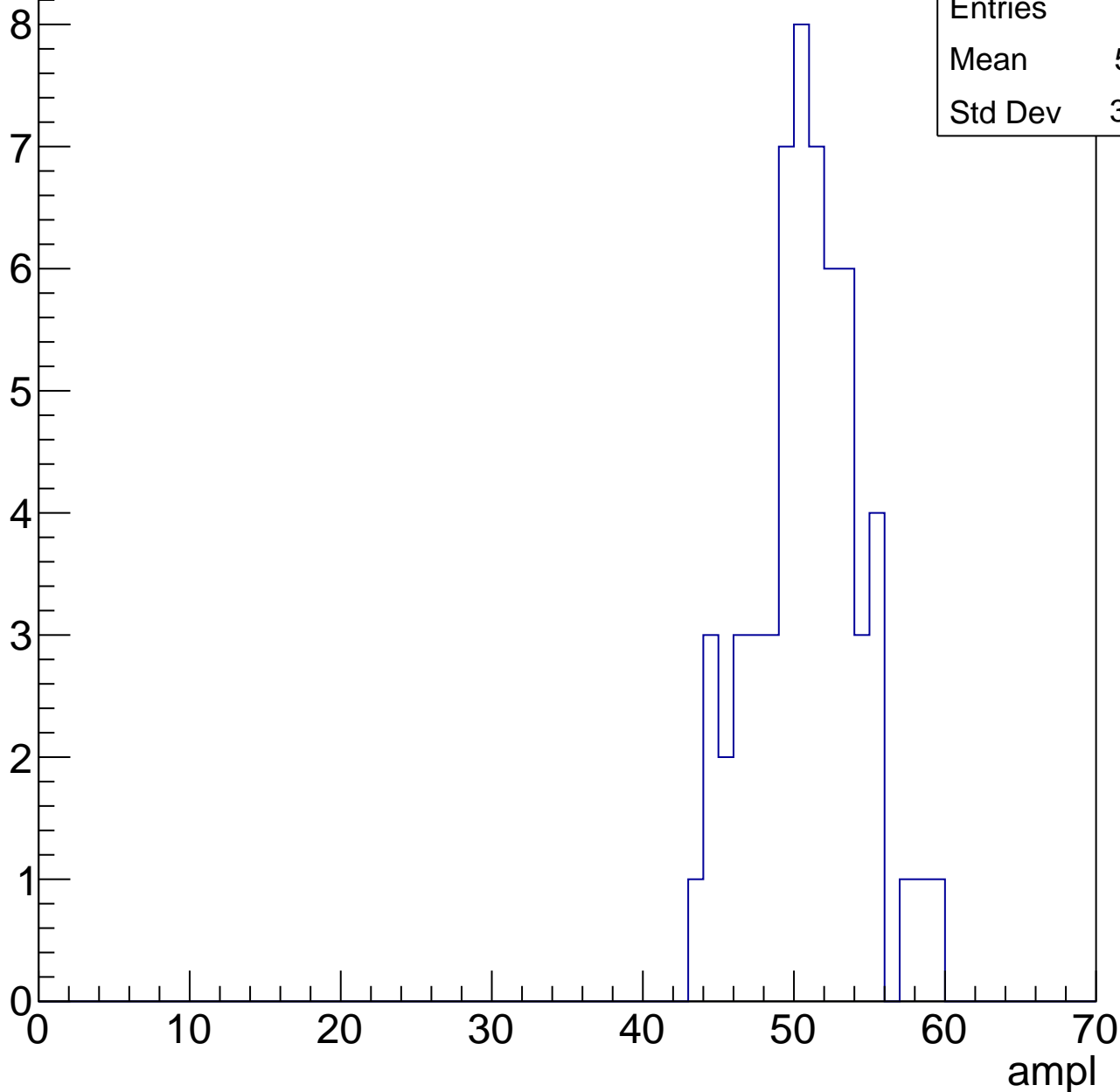


B1L003S, U13-ch63, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	50.41
Std Dev	3.489

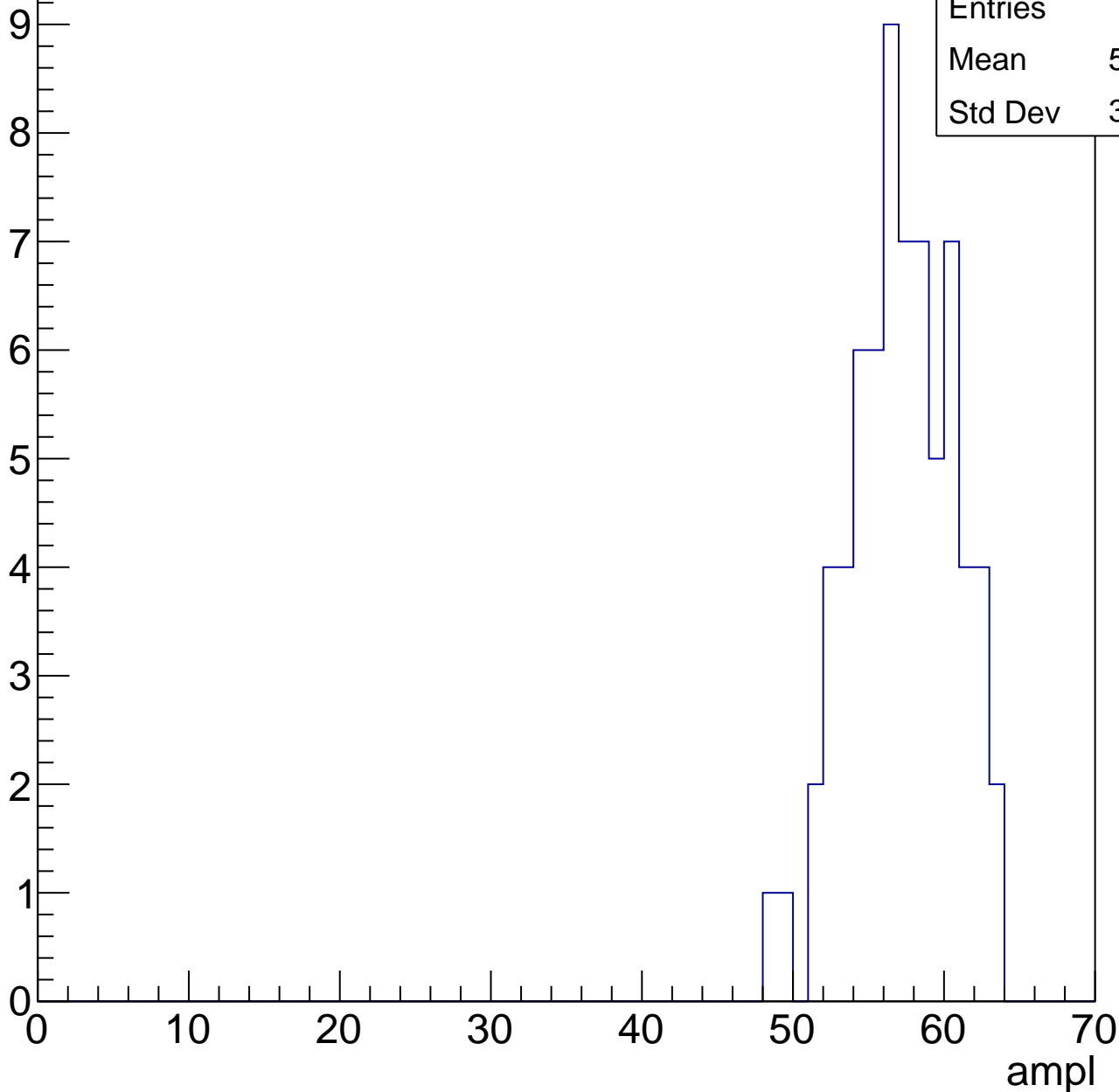


B1L003S, U13-ch63, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	56.74
Std Dev	3.382

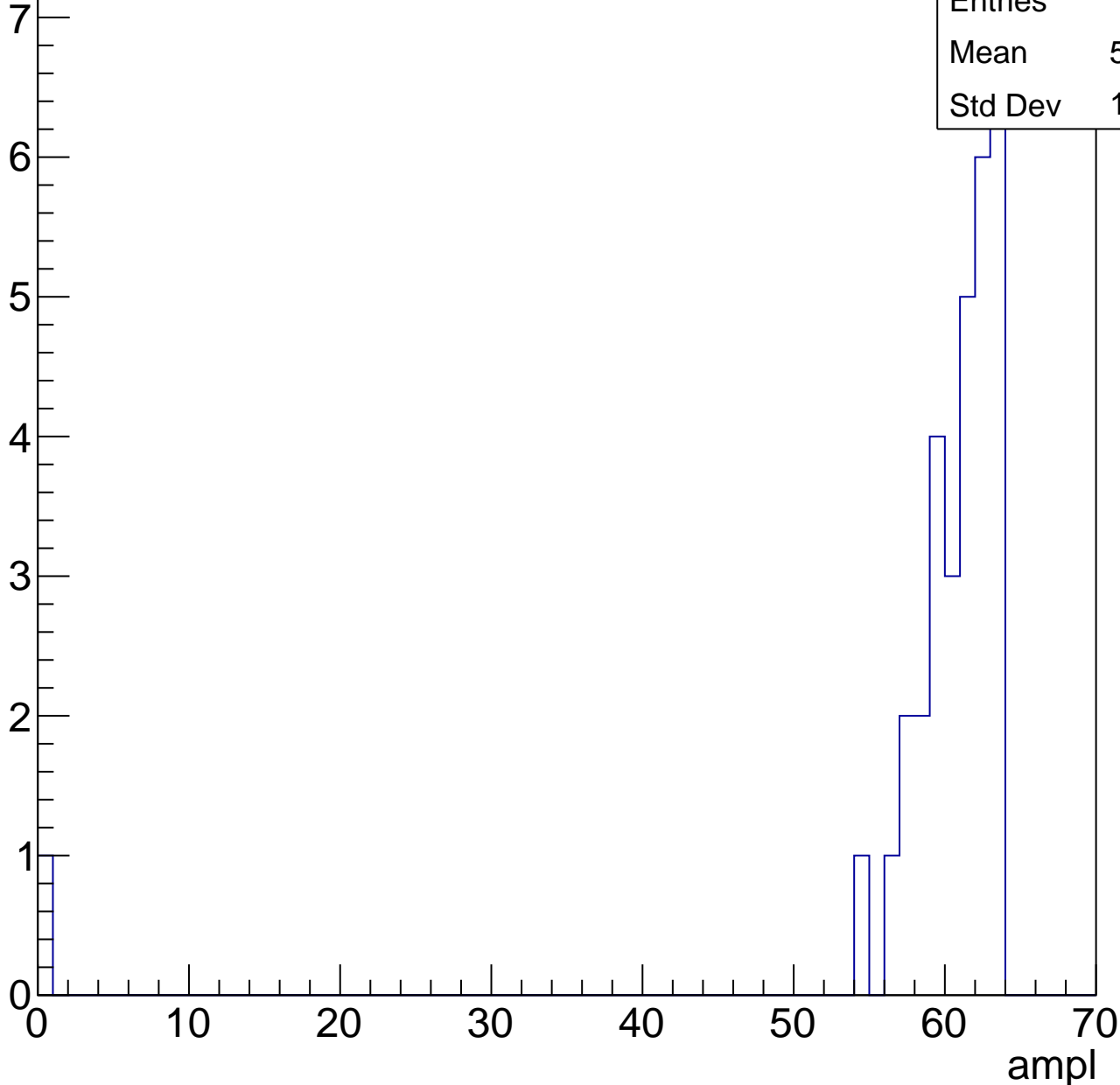


B1L003S, U13-ch63, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	32
Mean	58.56
Std Dev	10.76



B1L003S, U13-ch63, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch63, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch64, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	92
Mean	29.68
Std Dev	7.284

Gaus mean : 32.0054

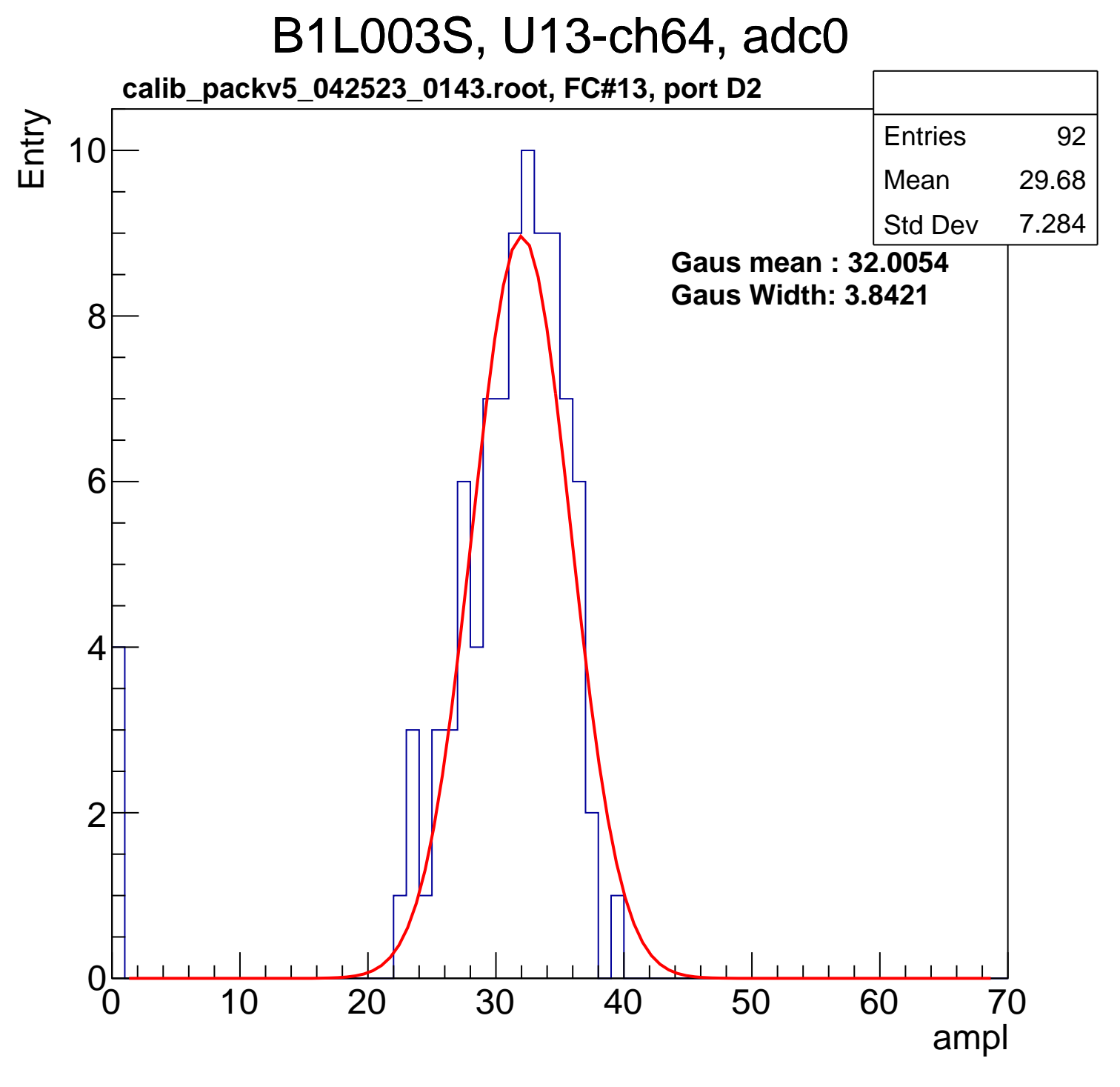
Gaus Width: 3.8421

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch64, adc1

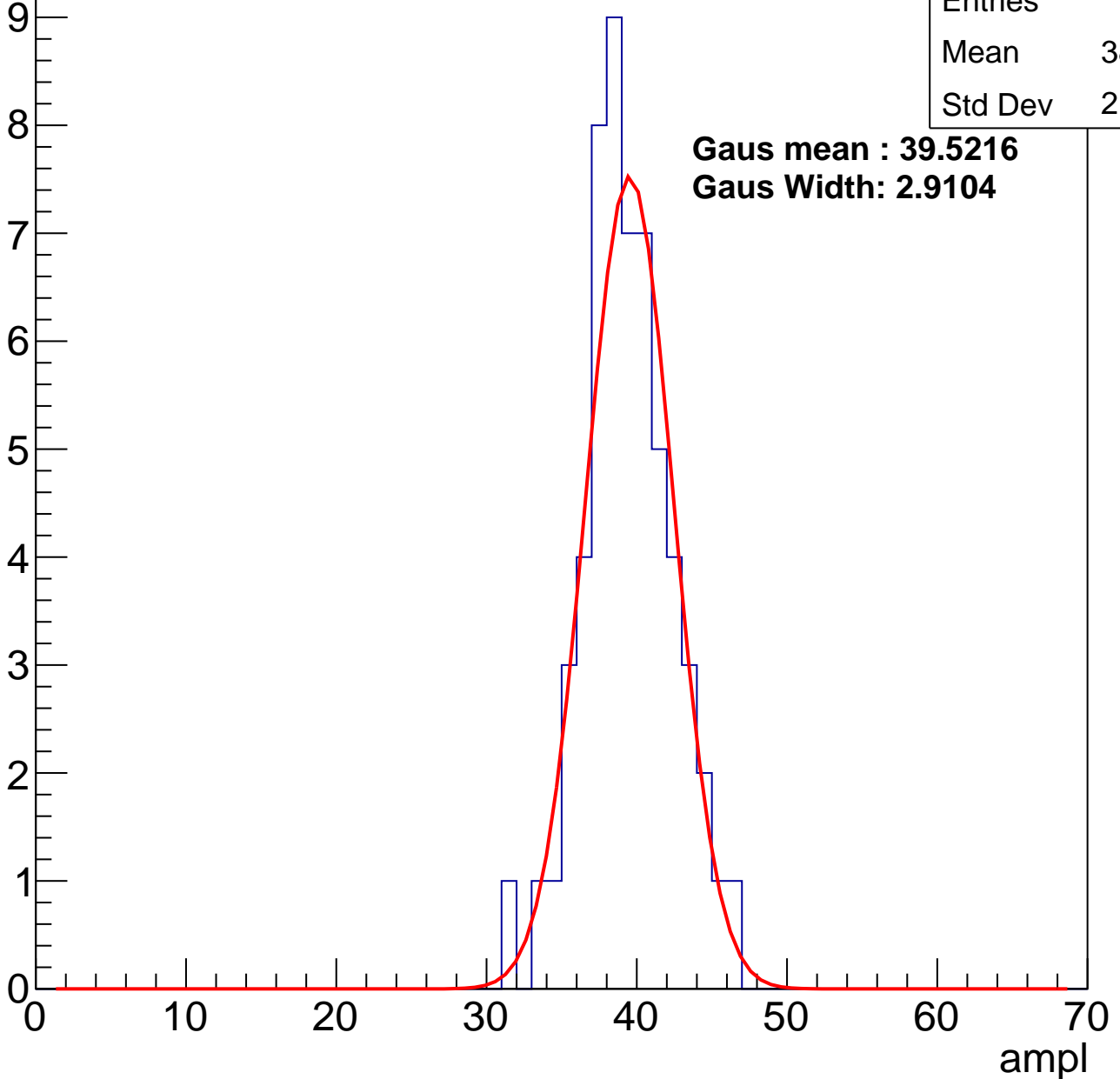
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	38.93
Std Dev	2.949

Gaus mean : 39.5216

Gaus Width: 2.9104



B1L003S, U13-ch64, adc2

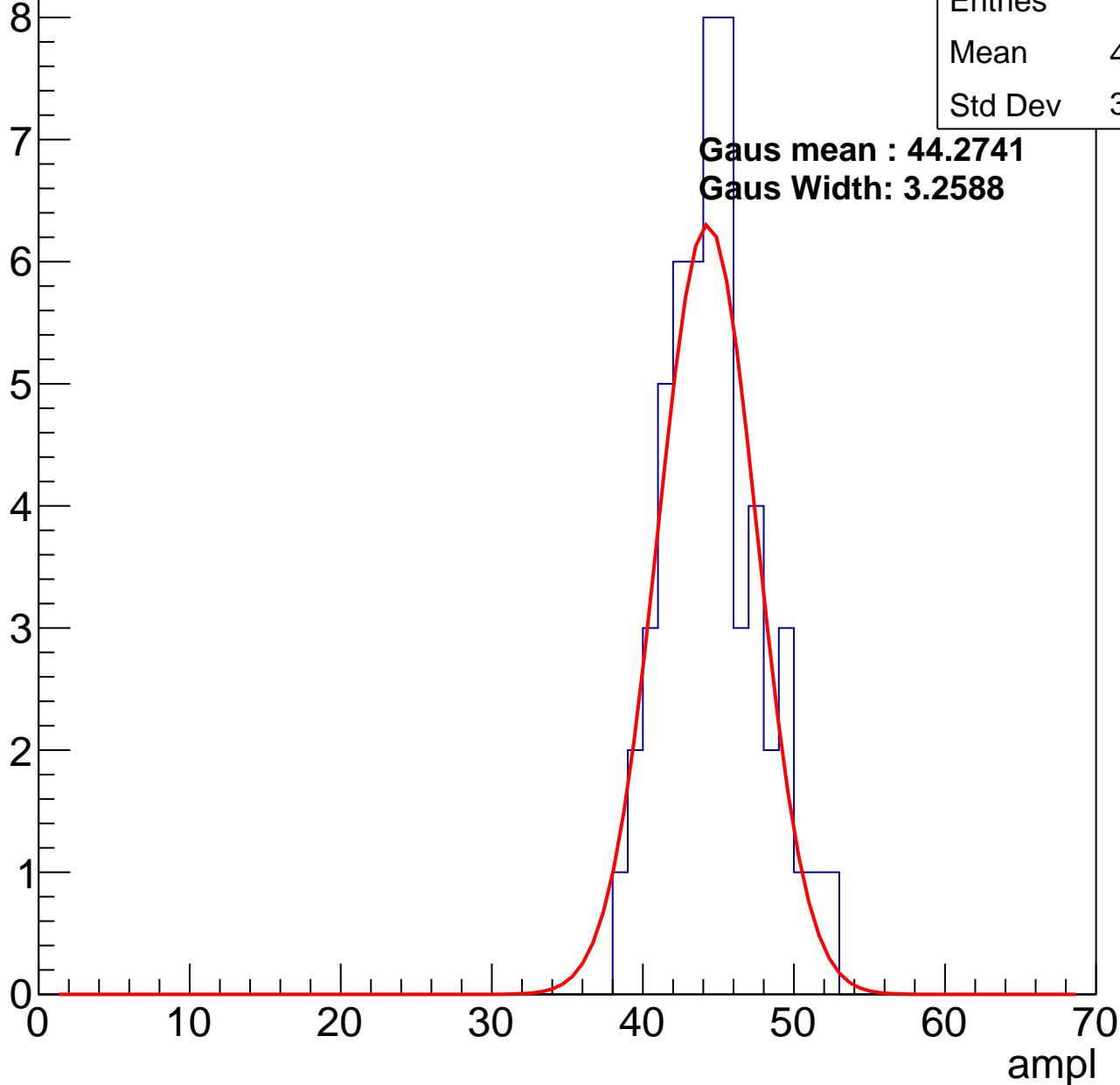
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	44.17
Std Dev	3.102

Gaus mean : 44.2741

Gaus Width: 3.2588

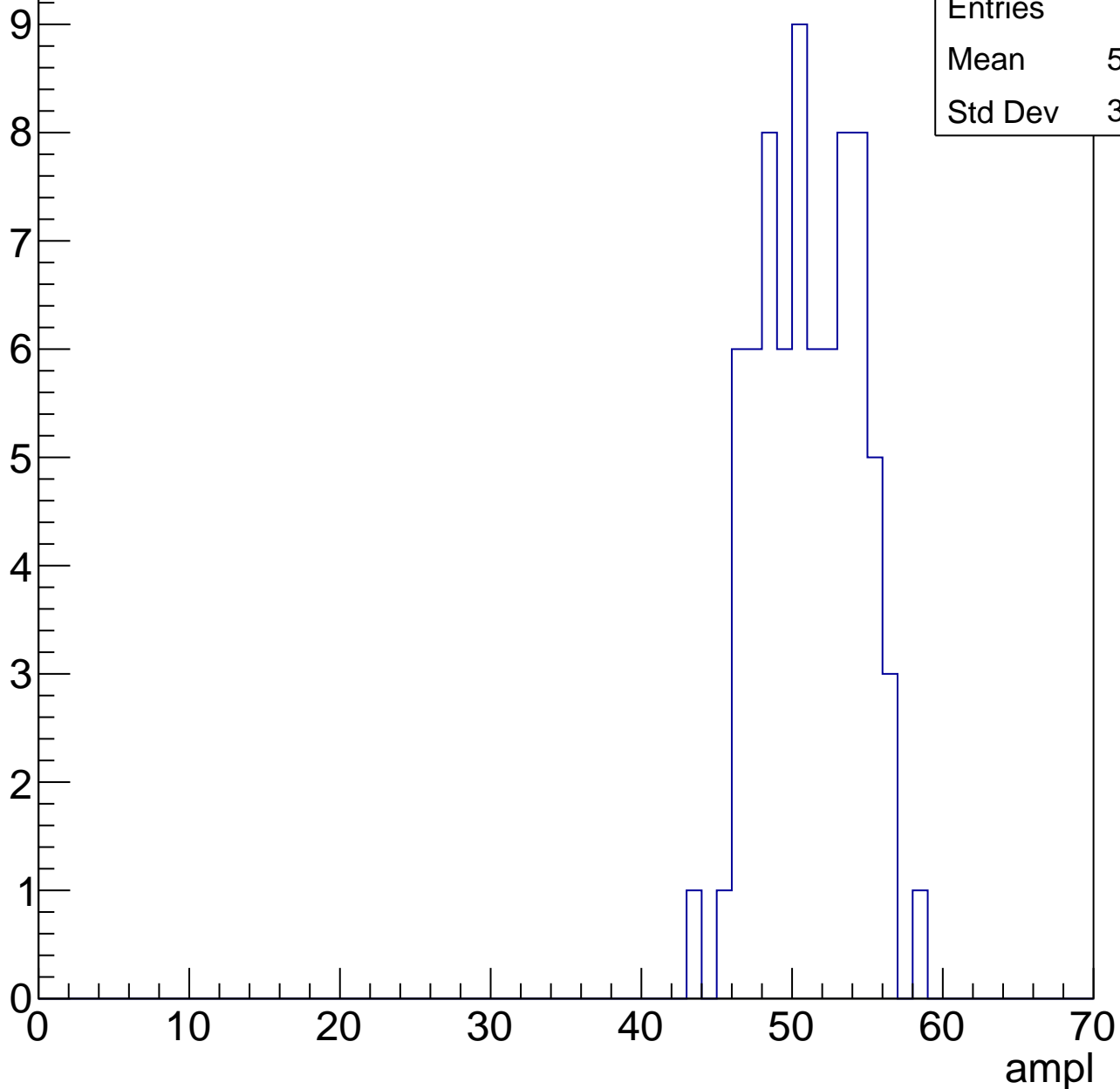


B1L003S, U13-ch64, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	50.66
Std Dev	3.197

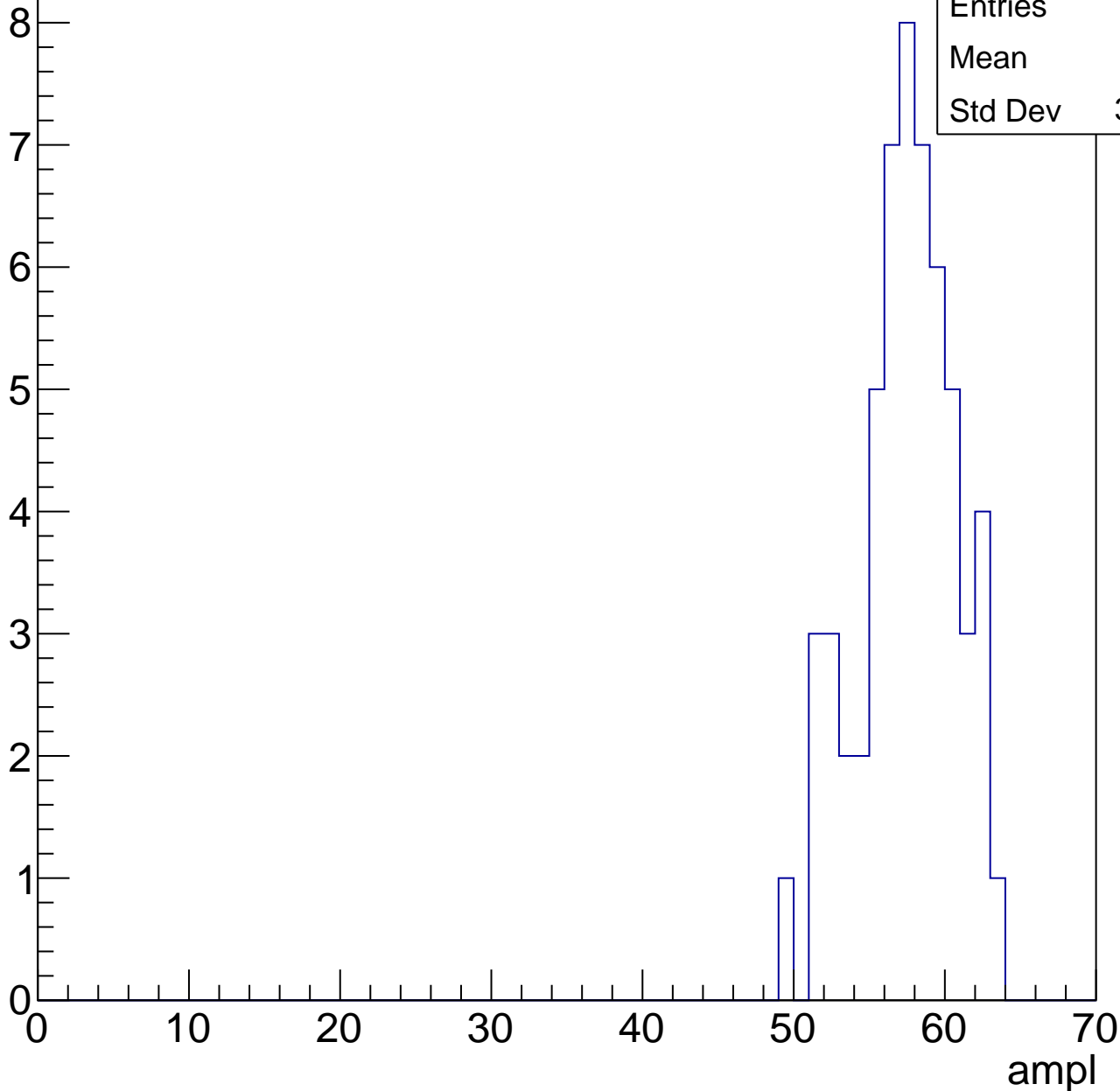


B1L003S, U13-ch64, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57
Std Dev	3.201

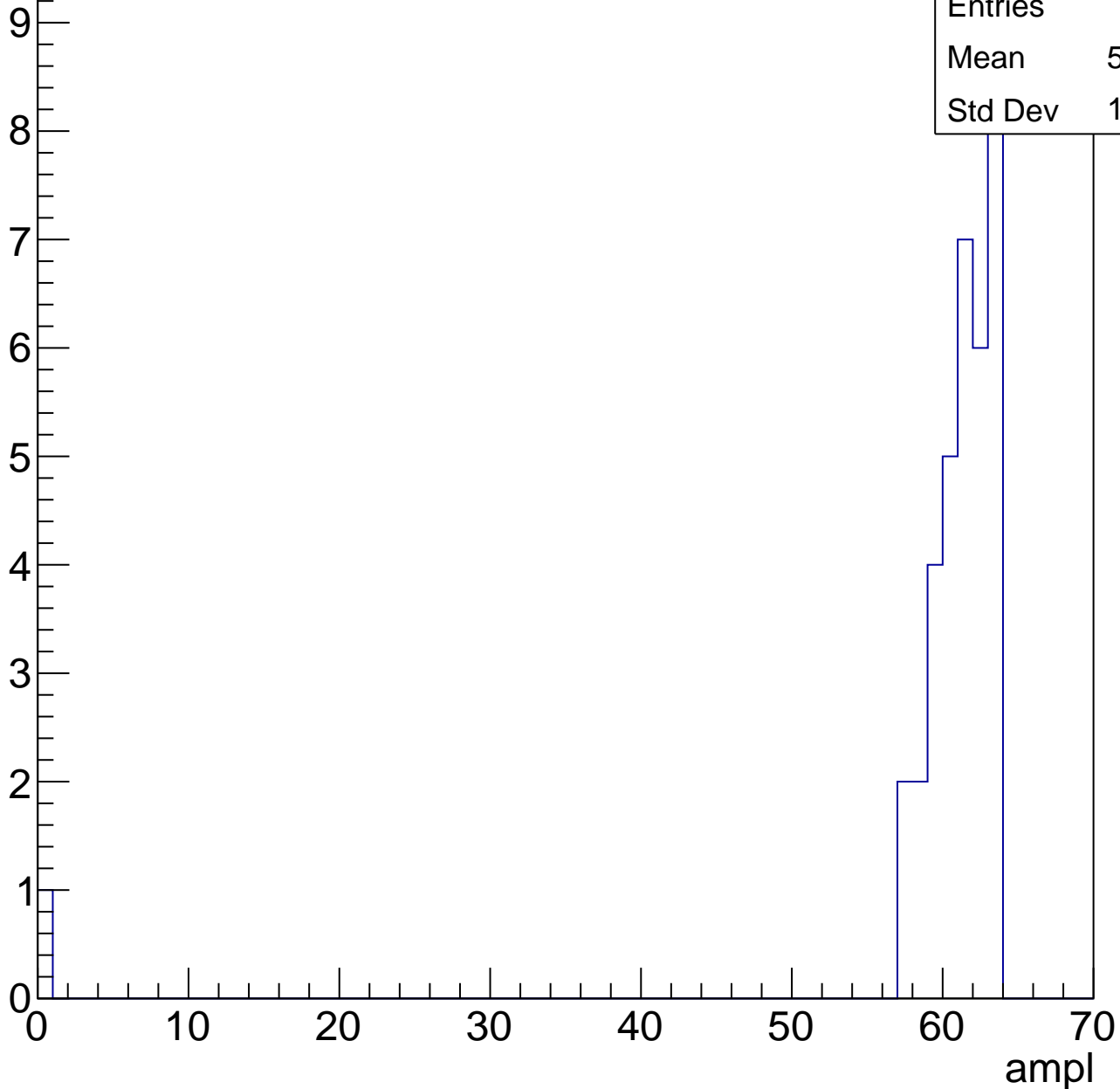


B1L003S, U13-ch64, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.22
Std Dev	10.17



B1L003S, U13-ch64, adc6

calib_packv5_042523_0143.root, FC#13, port D2

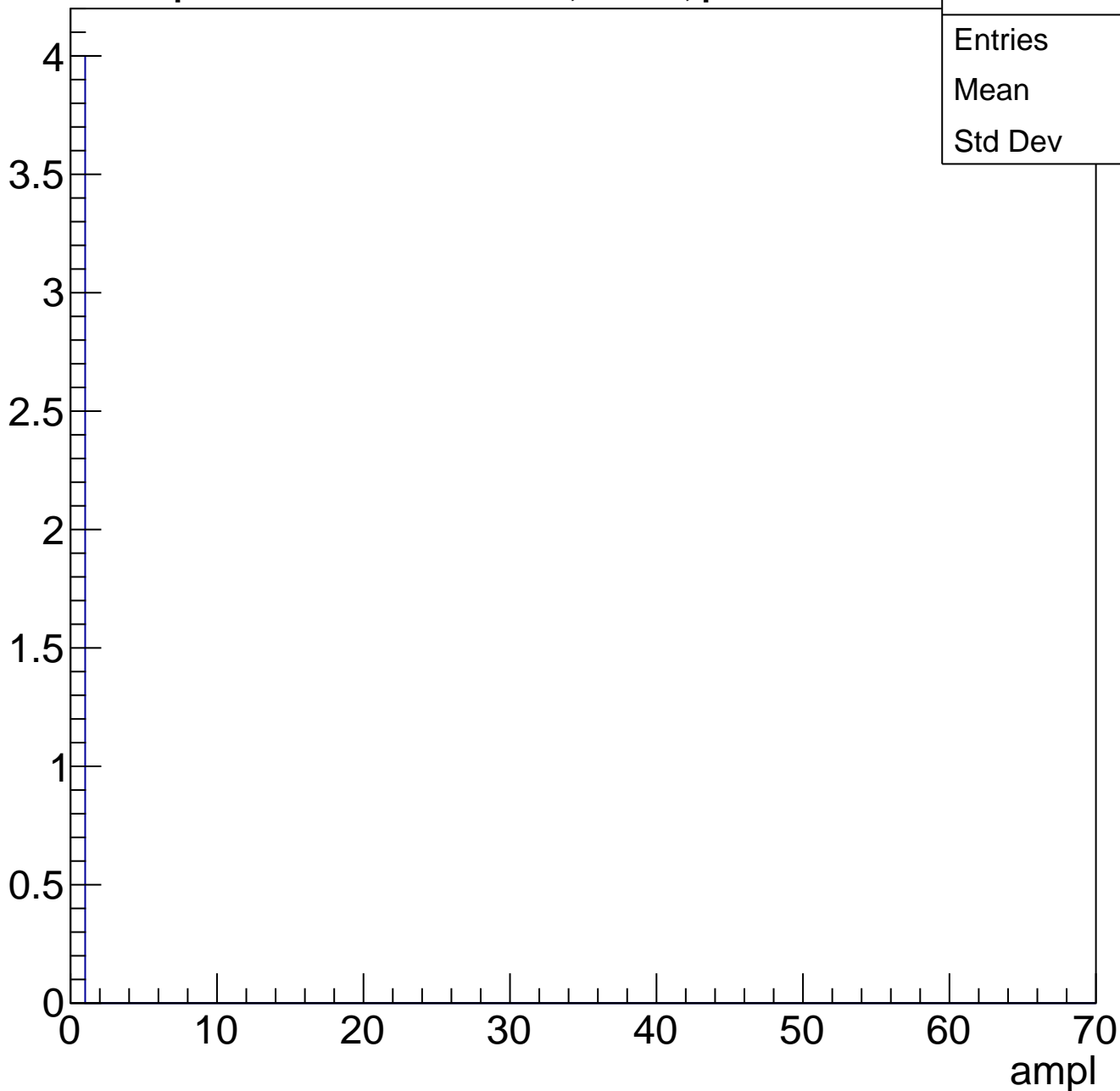
Entry



B1L003S, U13-ch64, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L003S, U13-ch65, adc0

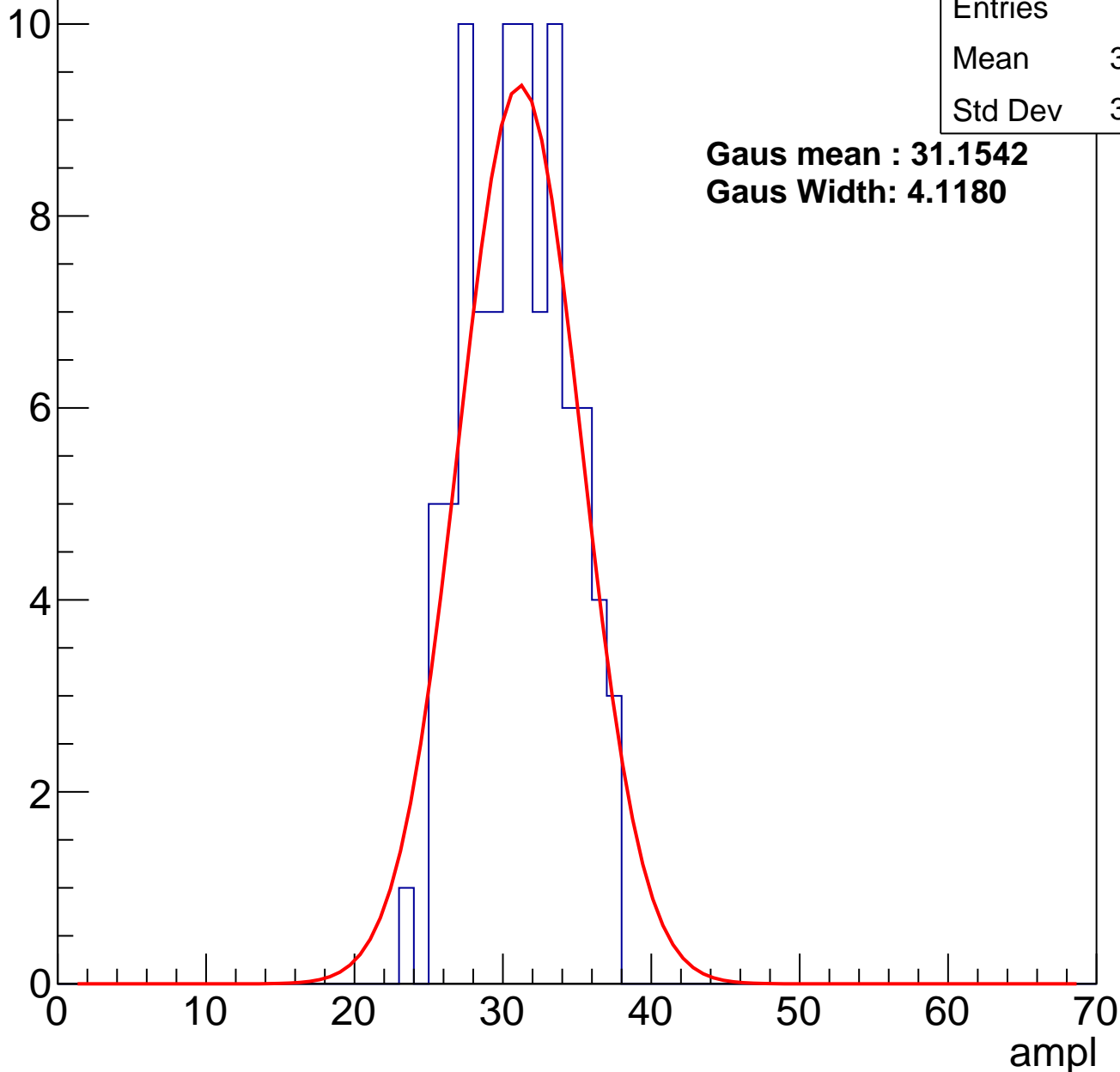
calib_packv5_042523_0143.root, FC#13, port D2

Entries	91
Mean	30.55
Std Dev	3.342

Gaus mean : 31.1542

Gaus Width: 4.1180

Entry



B1L003S, U13-ch65, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	38.43
Std Dev	3.201

Gaus mean : 38.3446

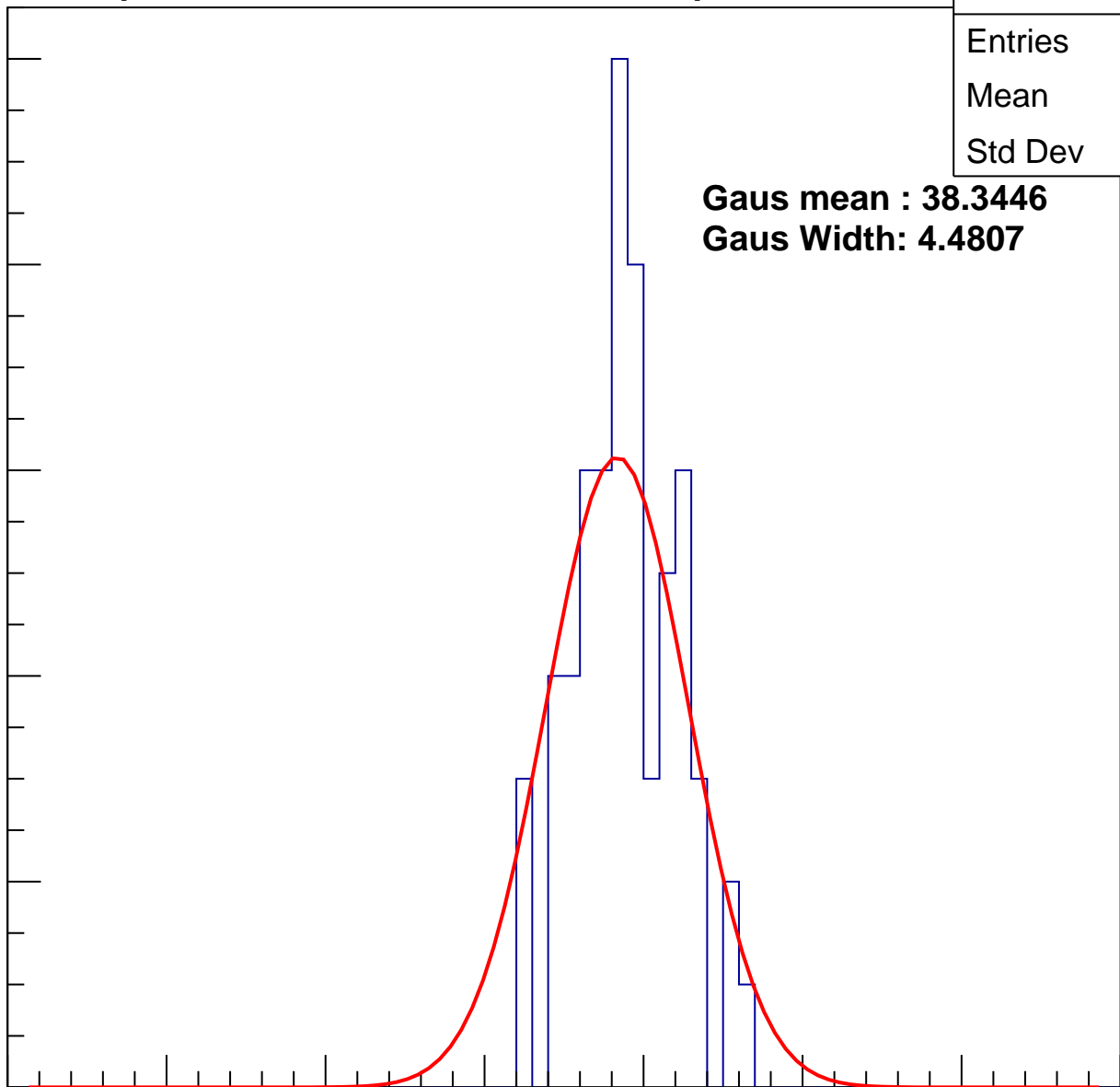
Gaus Width: 4.4807

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch65, adc2

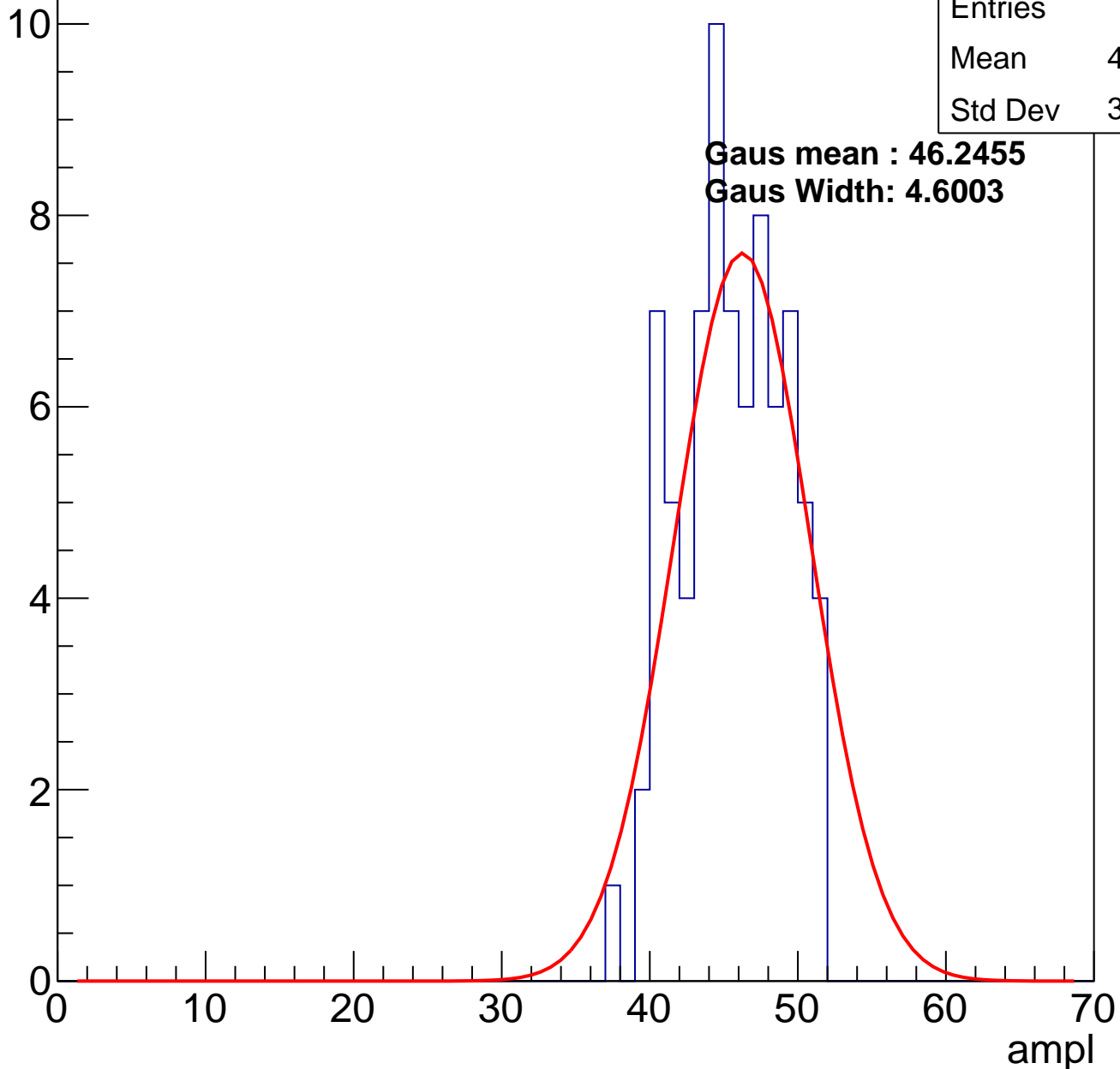
calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	45.08
Std Dev	3.445

Gaus mean : 46.2455

Gaus Width: 4.6003

Entry

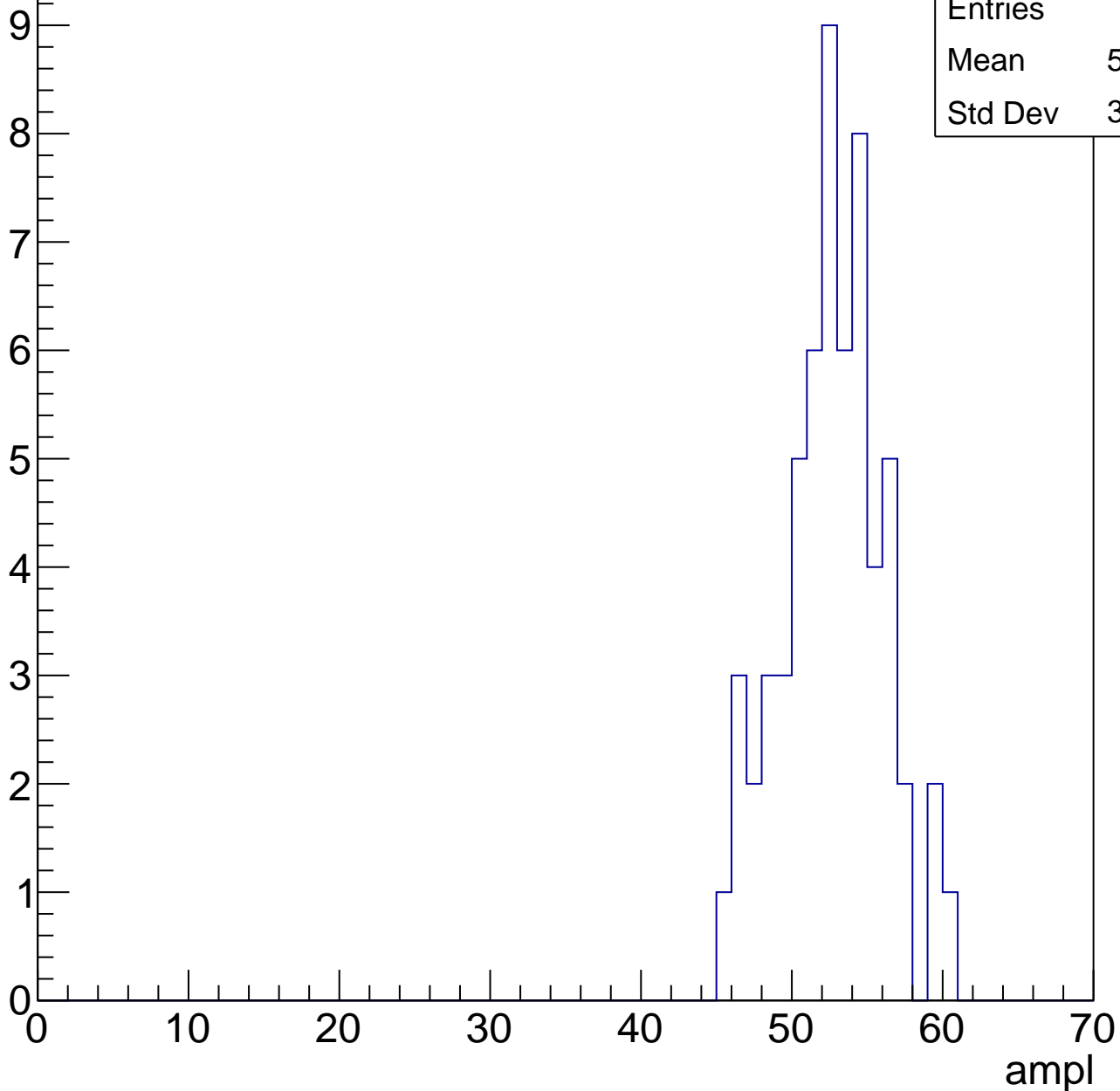


B1L003S, U13-ch65, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	52.23
Std Dev	3.343

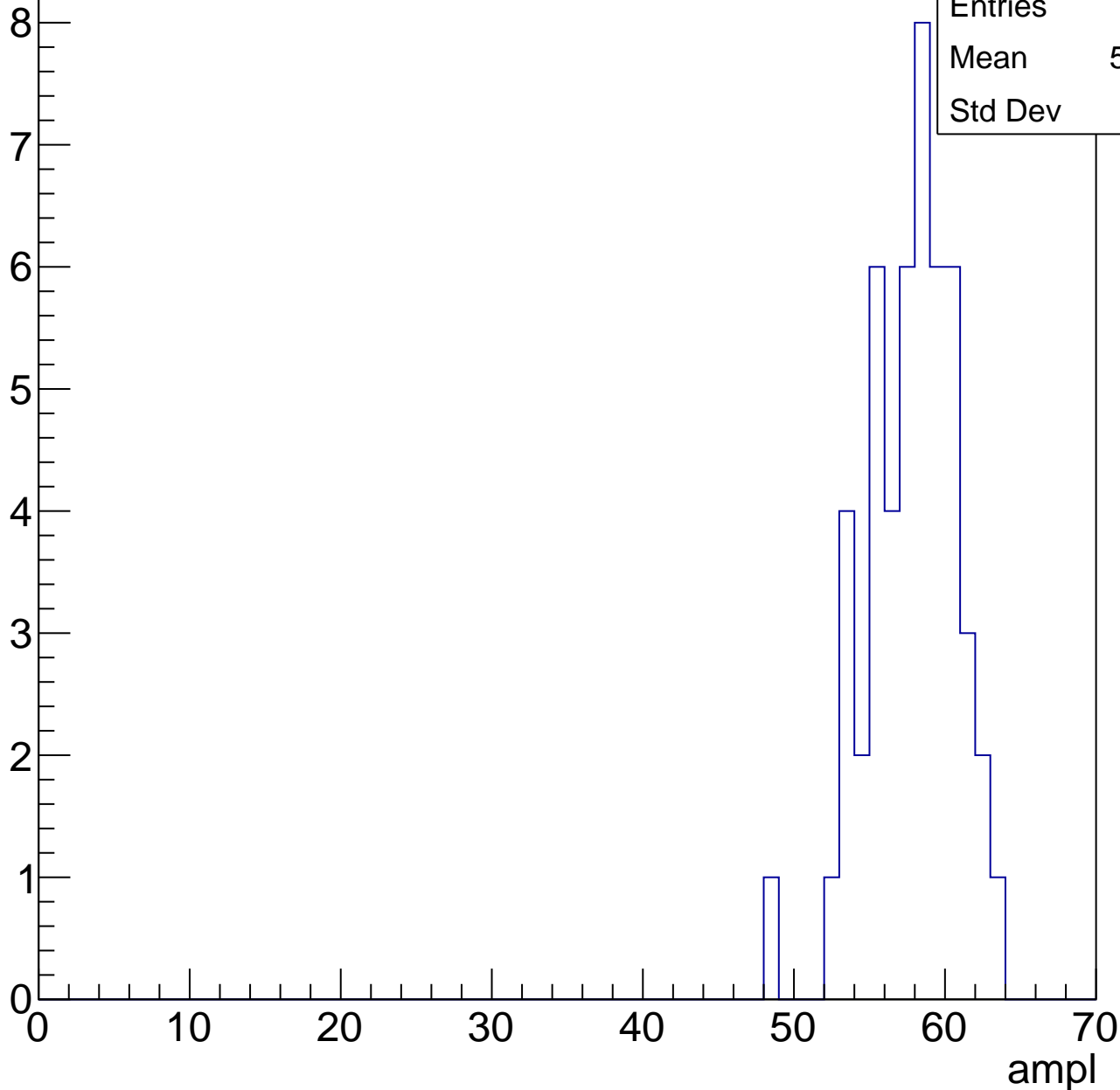


B1L003S, U13-ch65, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

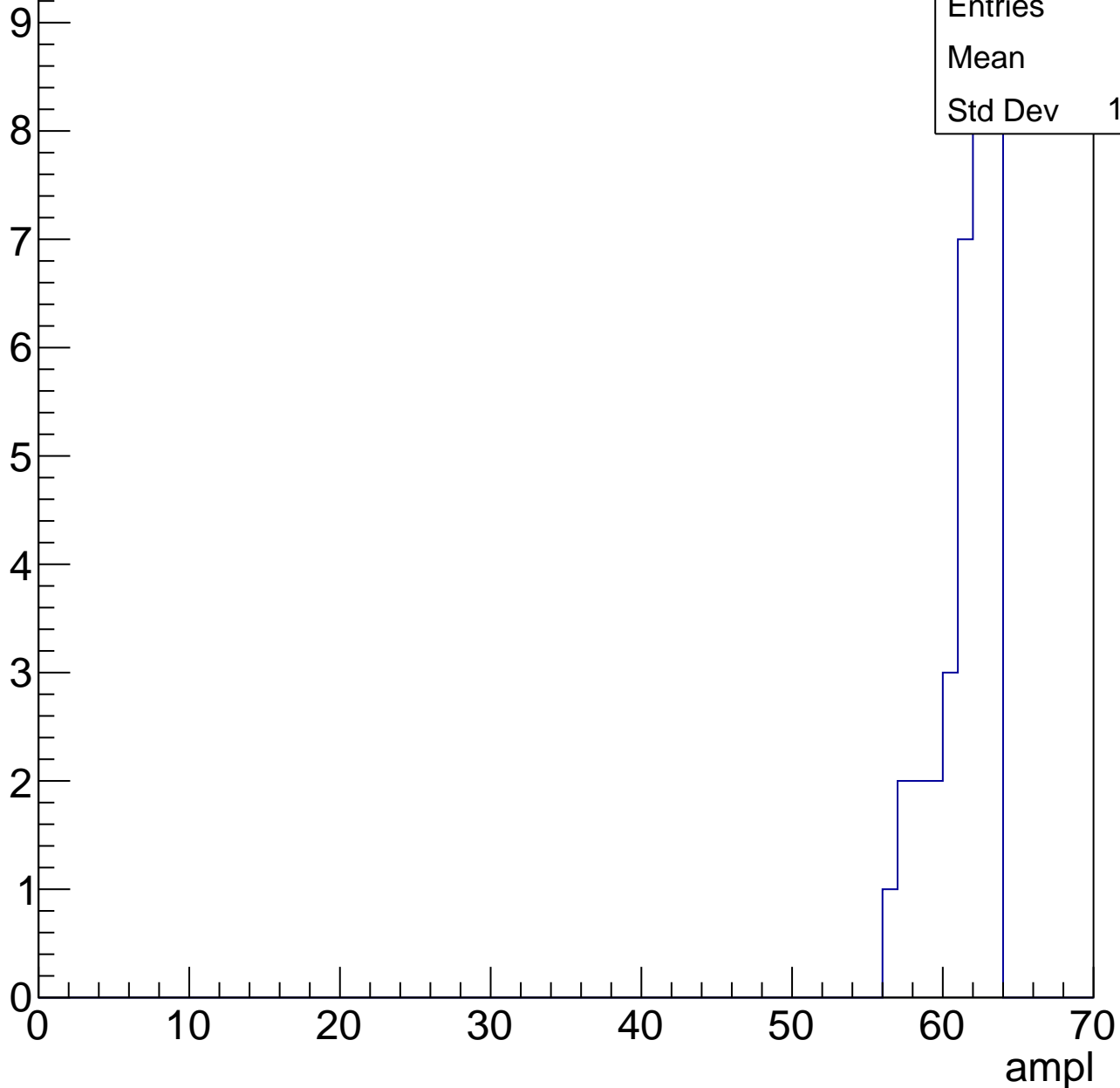
Entries	50
Mean	57.28
Std Dev	2.94



B1L003S, U13-ch65, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch65, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch65, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch66, adc0

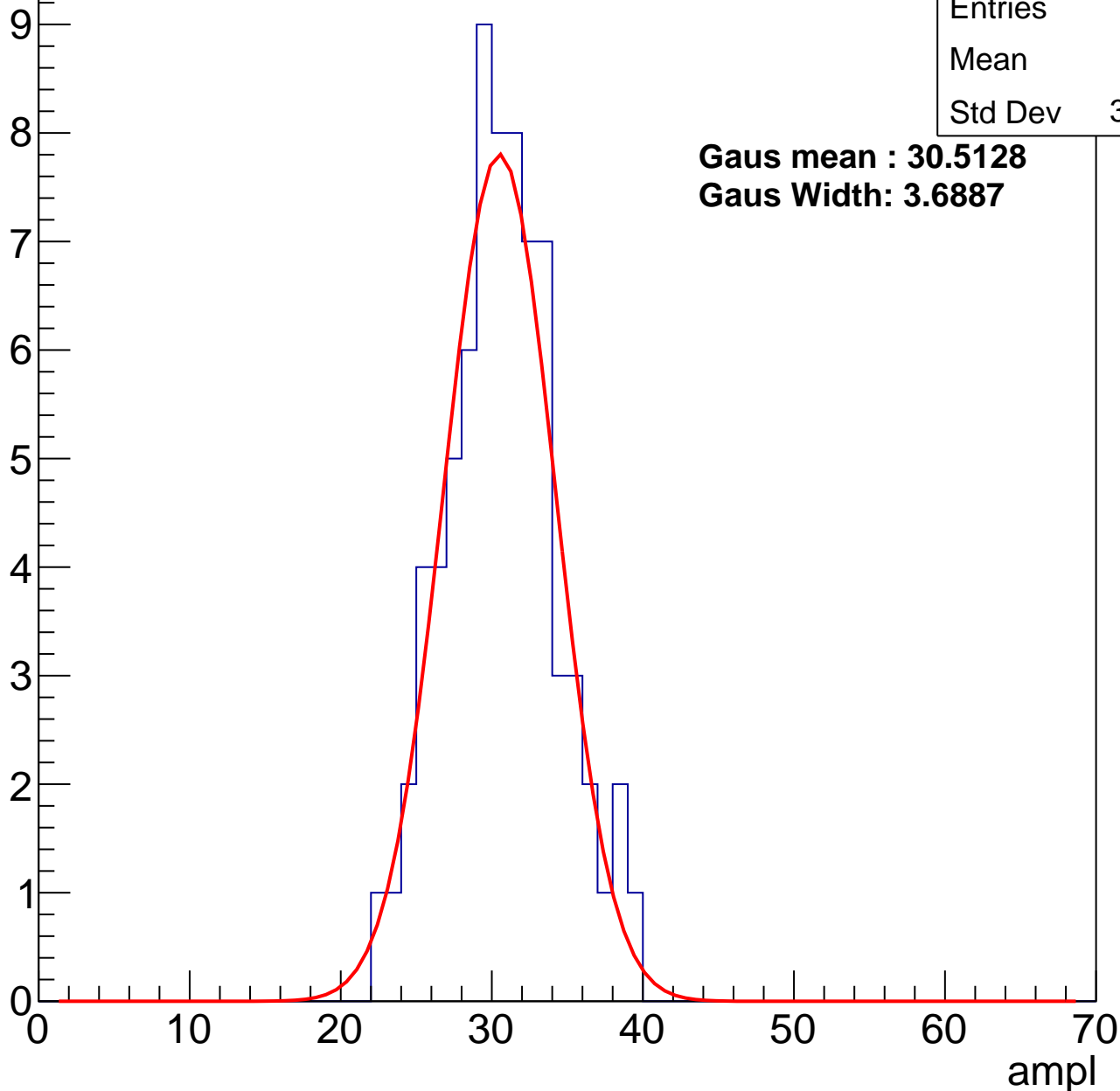
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.2
Std Dev	3.632

Gaus mean : 30.5128

Gaus Width: 3.6887



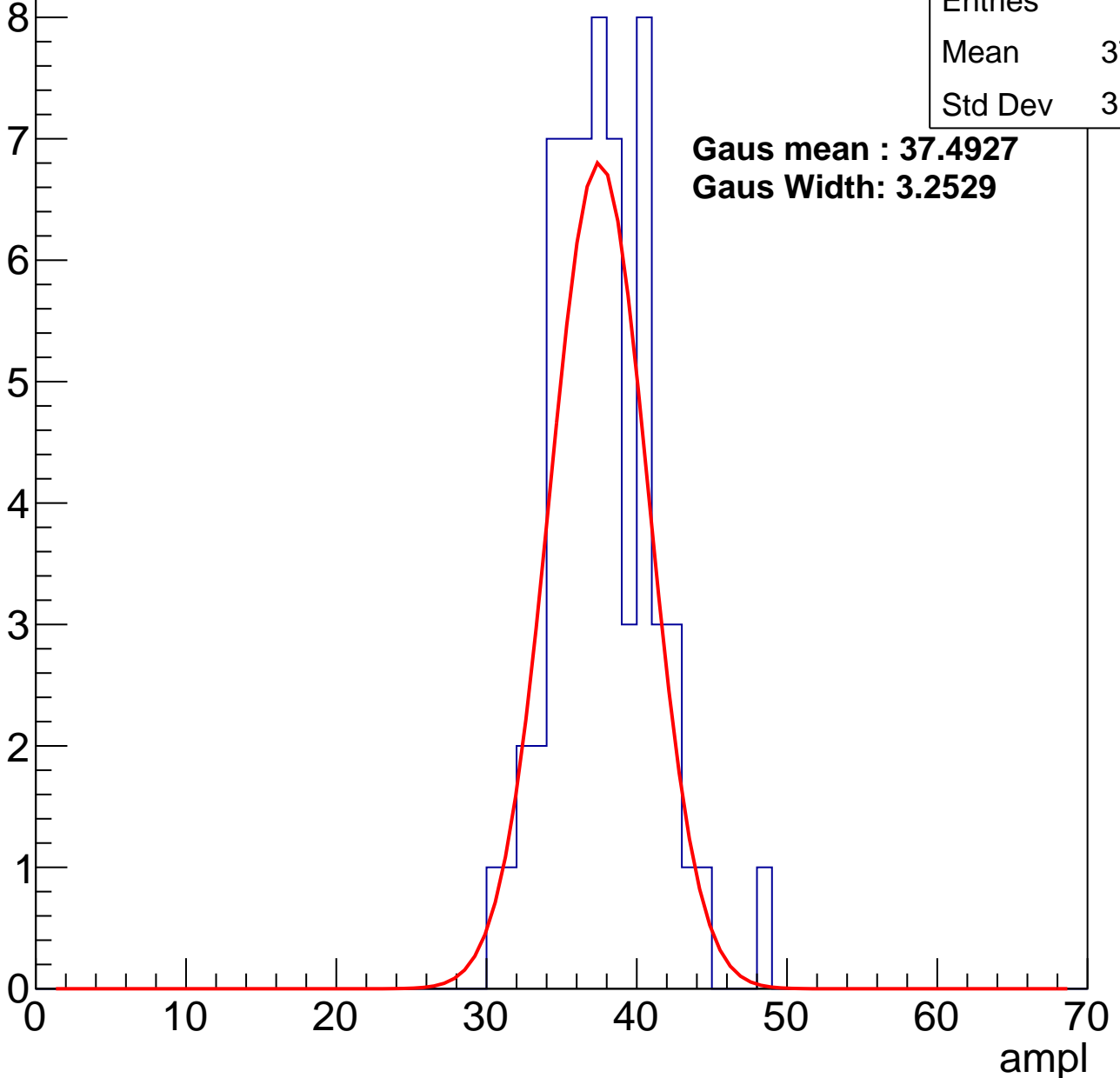
B1L003S, U13-ch66, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.24
Std Dev	3.315

Gaus mean : 37.4927
Gaus Width: 3.2529

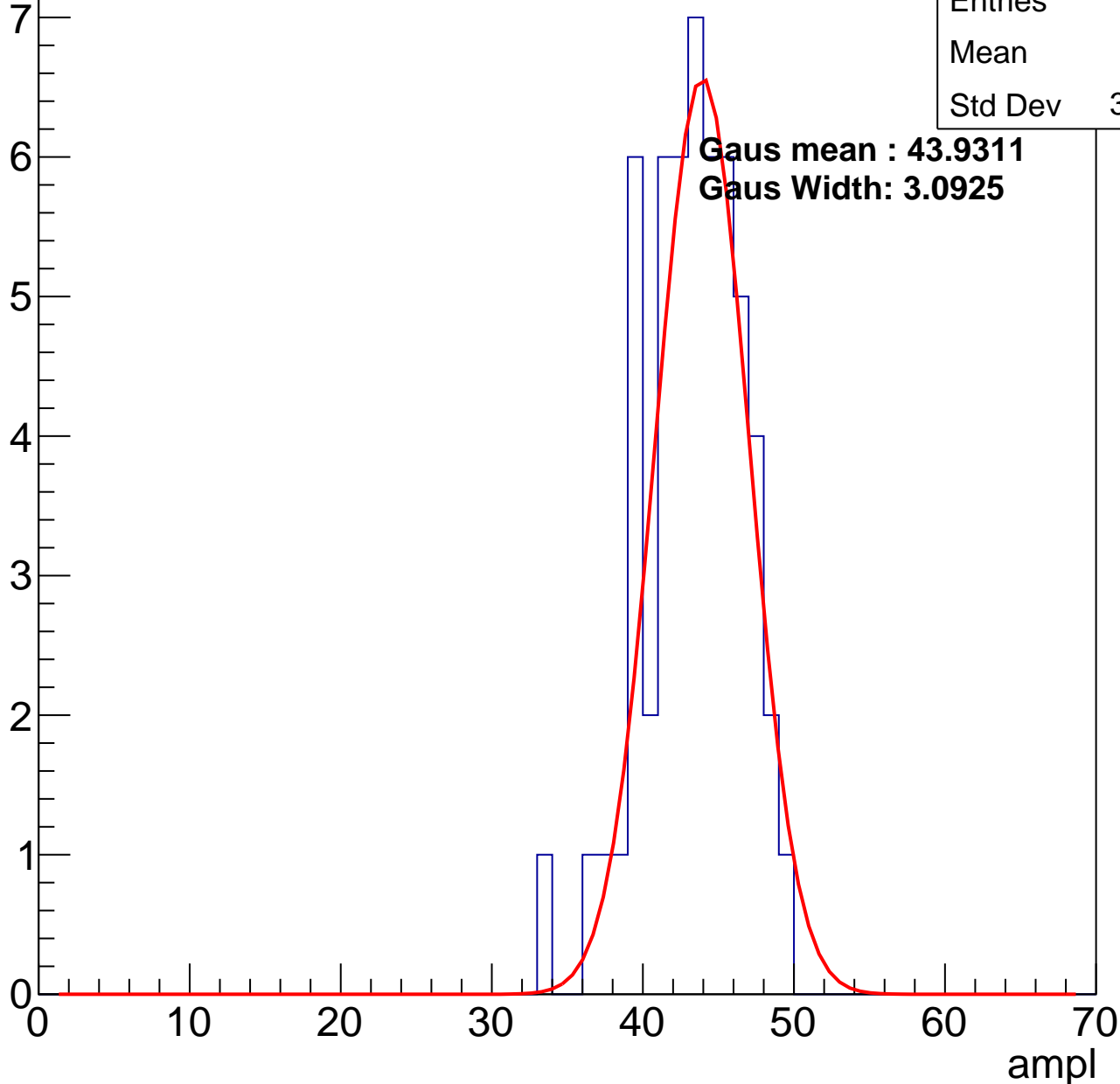


B1L003S, U13-ch66, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.8
Std Dev	3.238

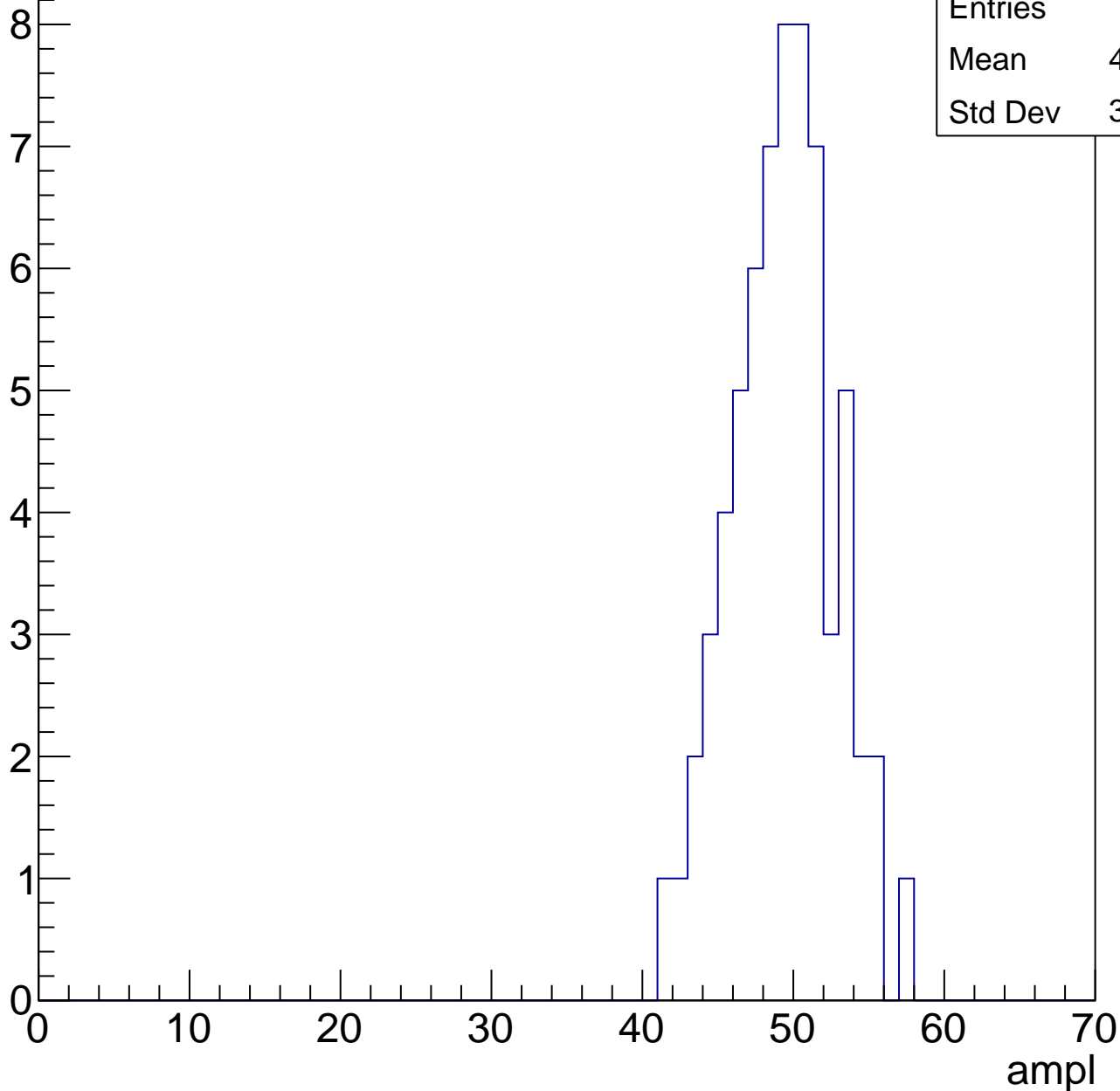


B1L003S, U13-ch66, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	48.83
Std Dev	3.345

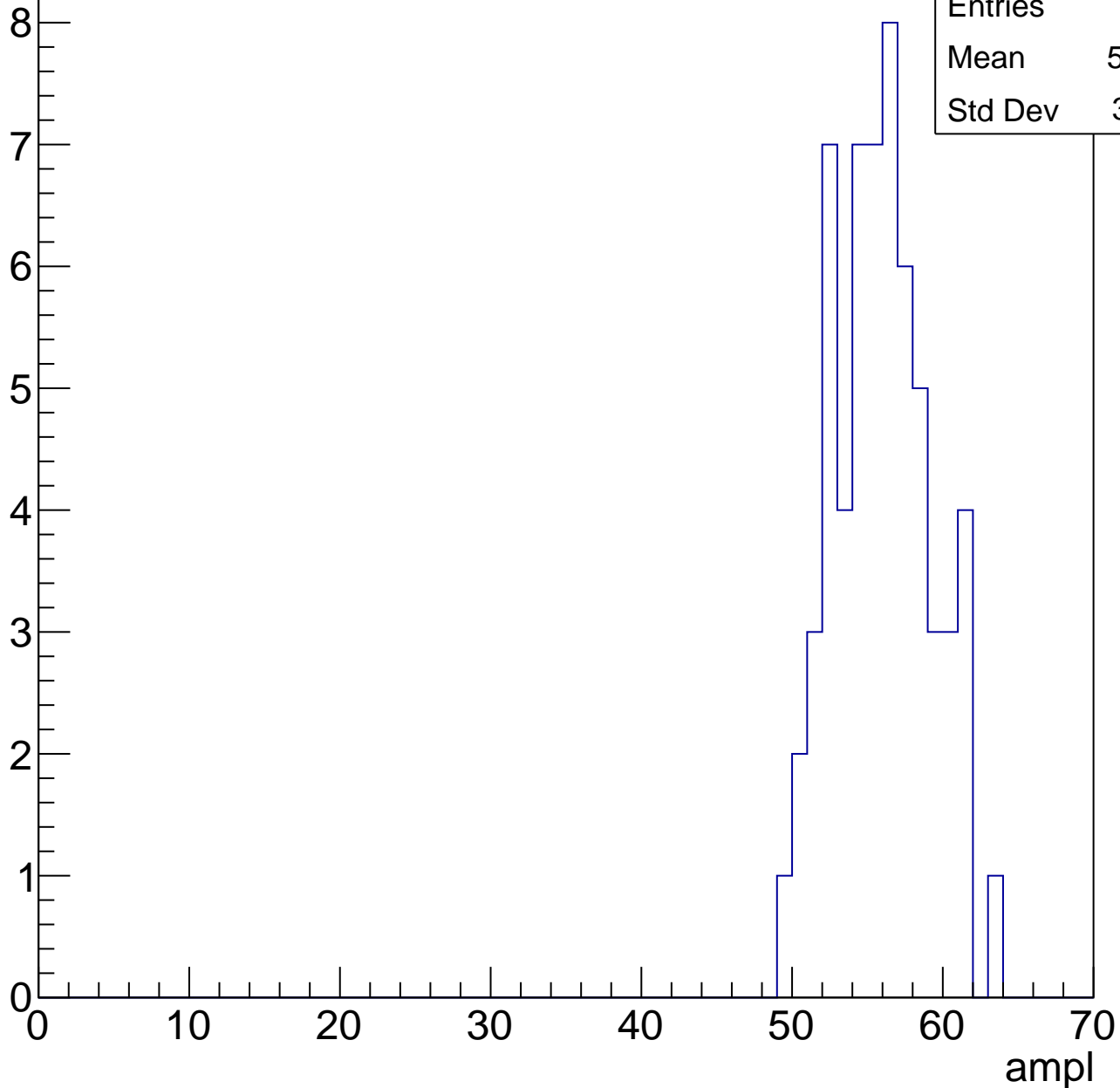


B1L003S, U13-ch66, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.49
Std Dev	3.171

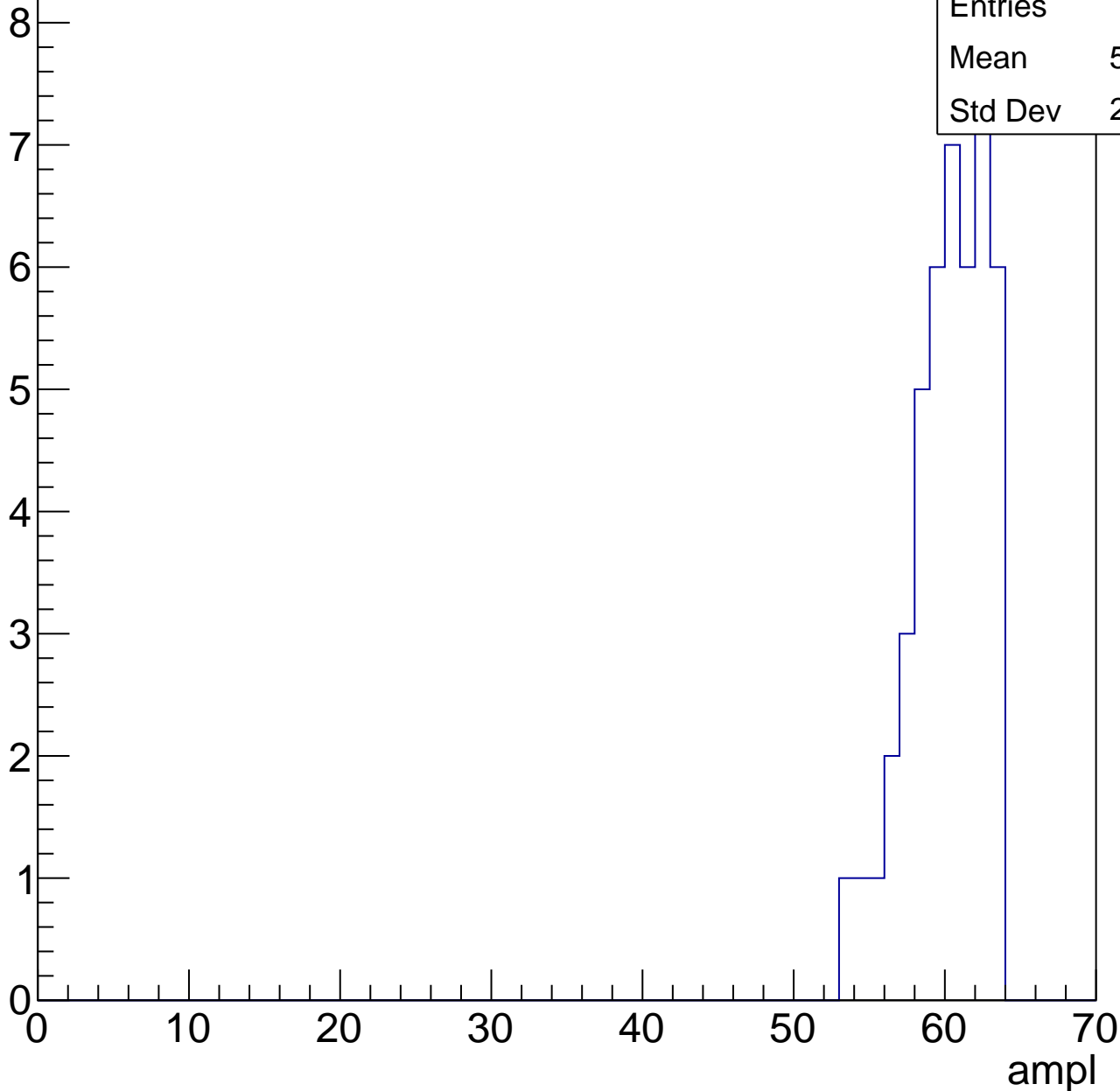


B1L003S, U13-ch66, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

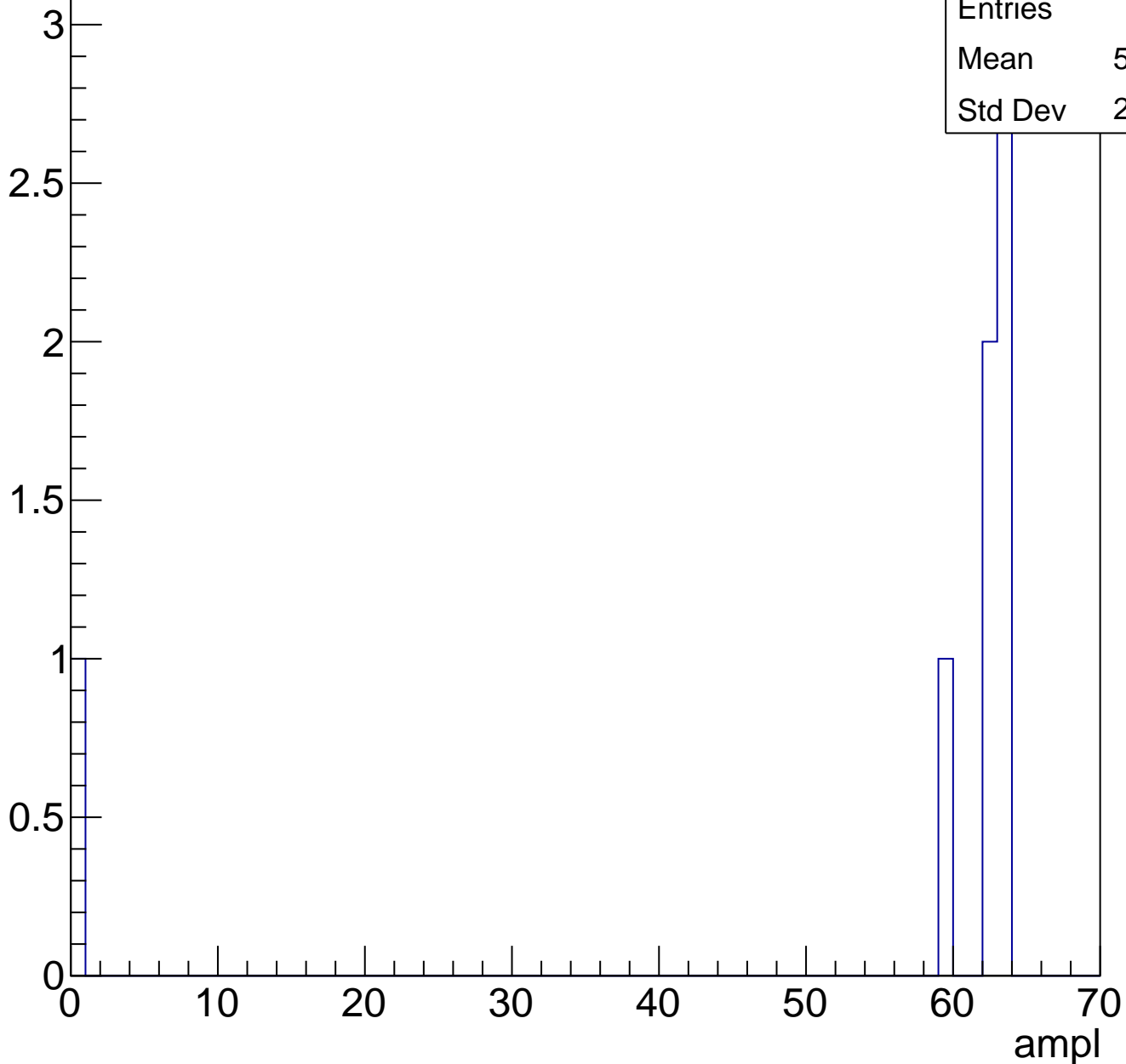
Entries	46
Mean	59.76
Std Dev	2.486



B1L003S, U13-ch66, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch66, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch67, adc0

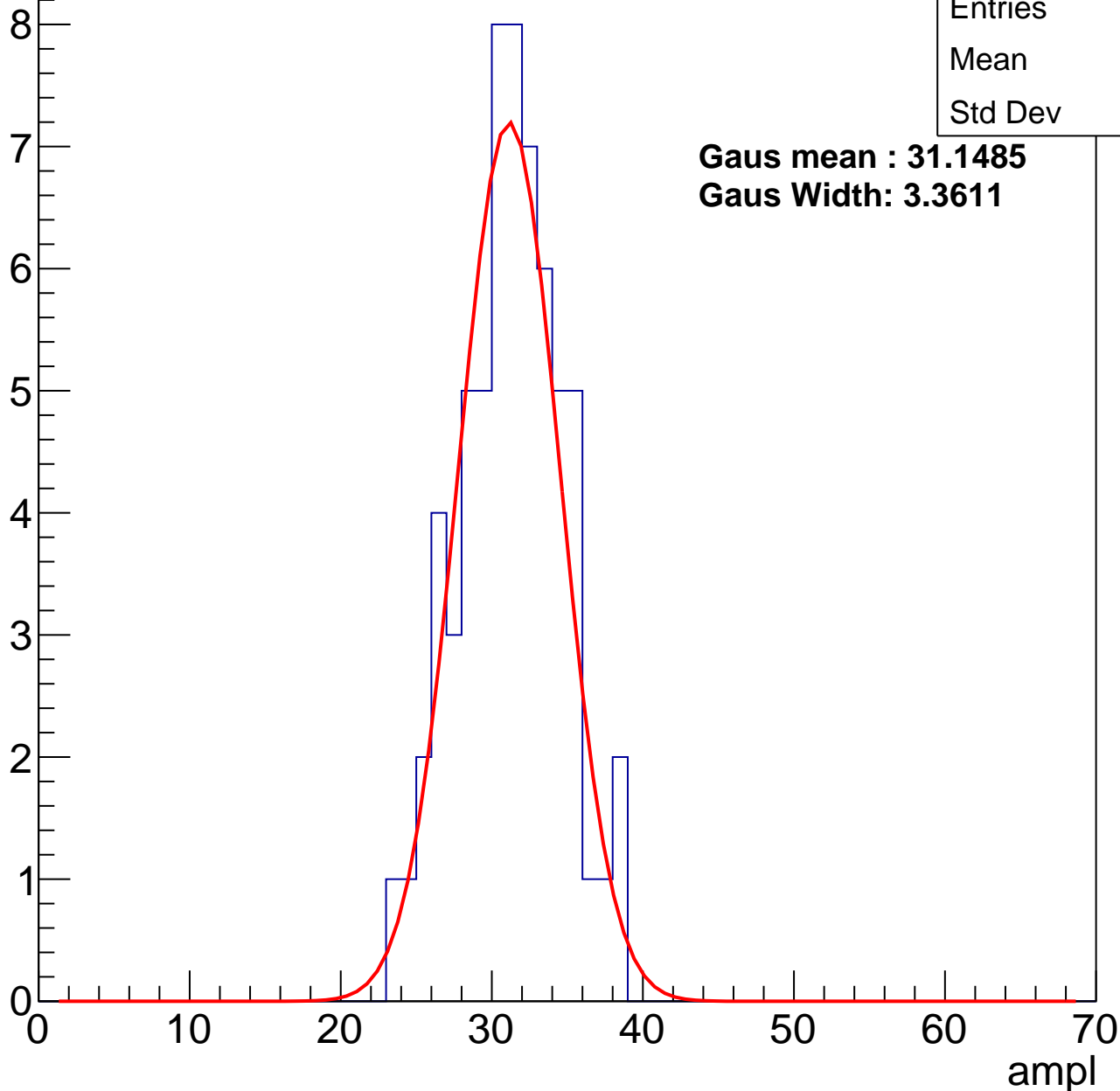
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.8
Std Dev	3.35

Gaus mean : 31.1485

Gaus Width: 3.3611



B1L003S, U13-ch67, adc1

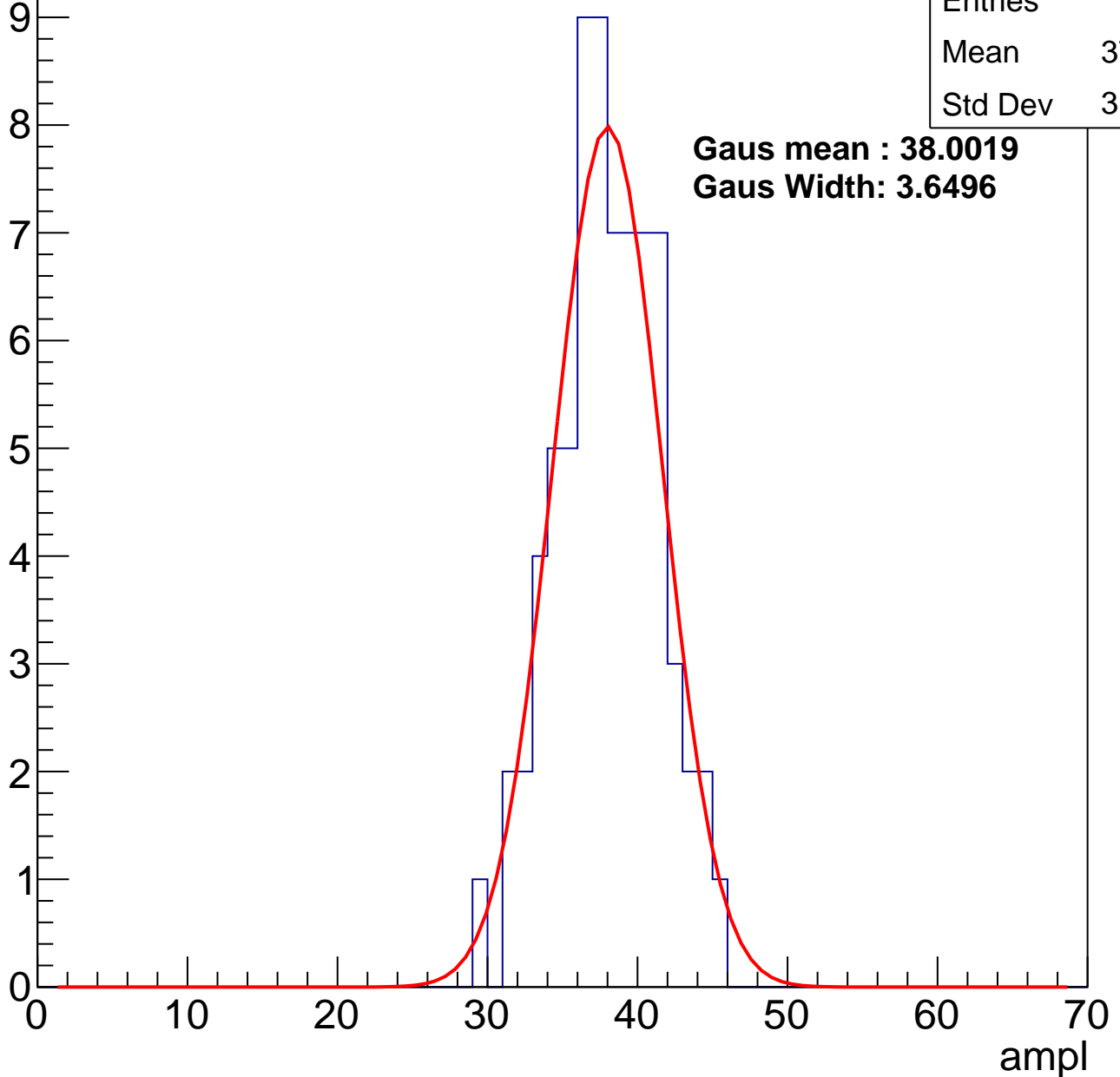
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.53
Std Dev	3.356

Gaus mean : 38.0019

Gaus Width: 3.6496



B1L003S, U13-ch67, adc2

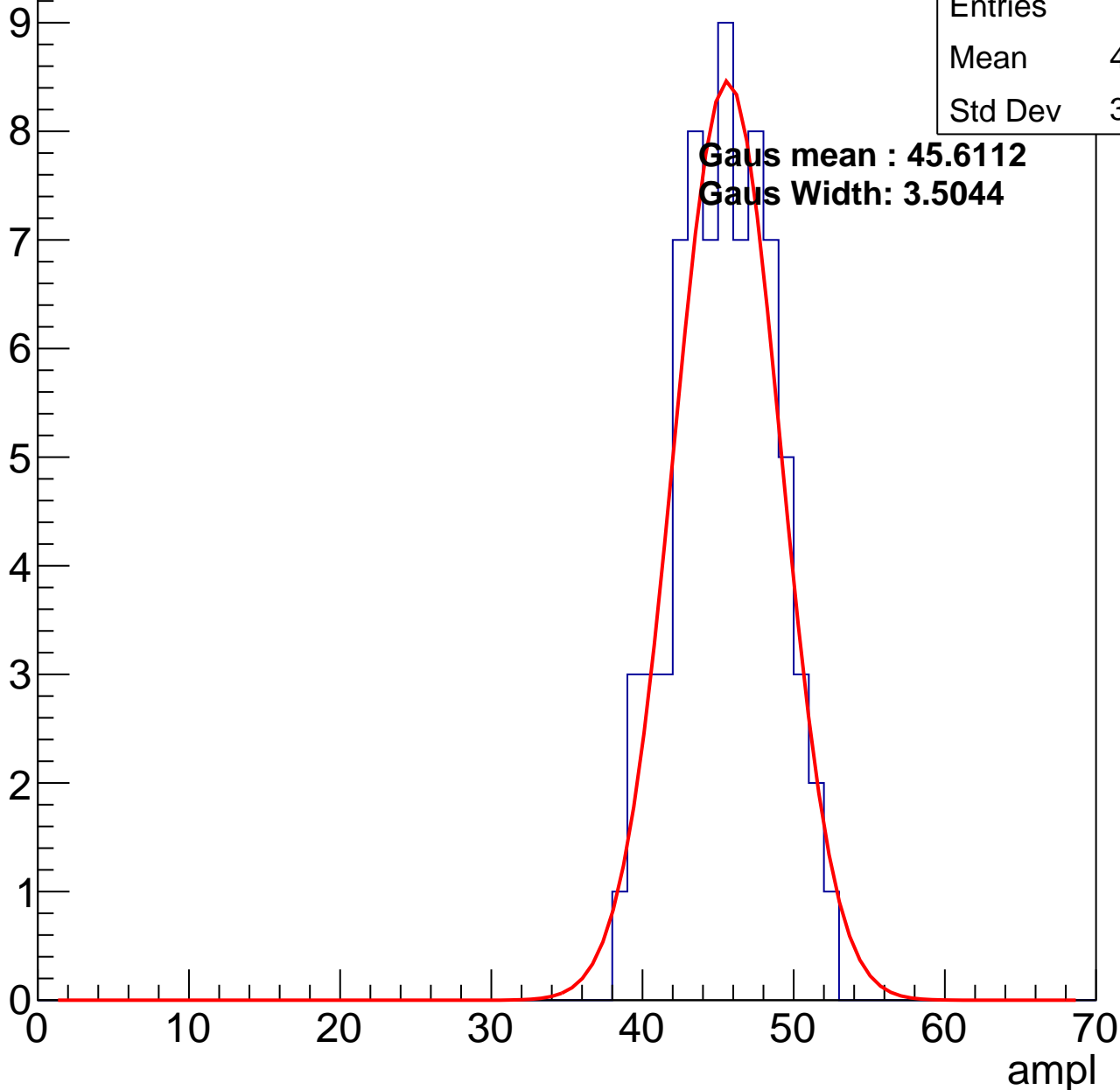
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	45.03
Std Dev	3.205

Gaus mean : 45.6112

Gaus Width: 3.5044

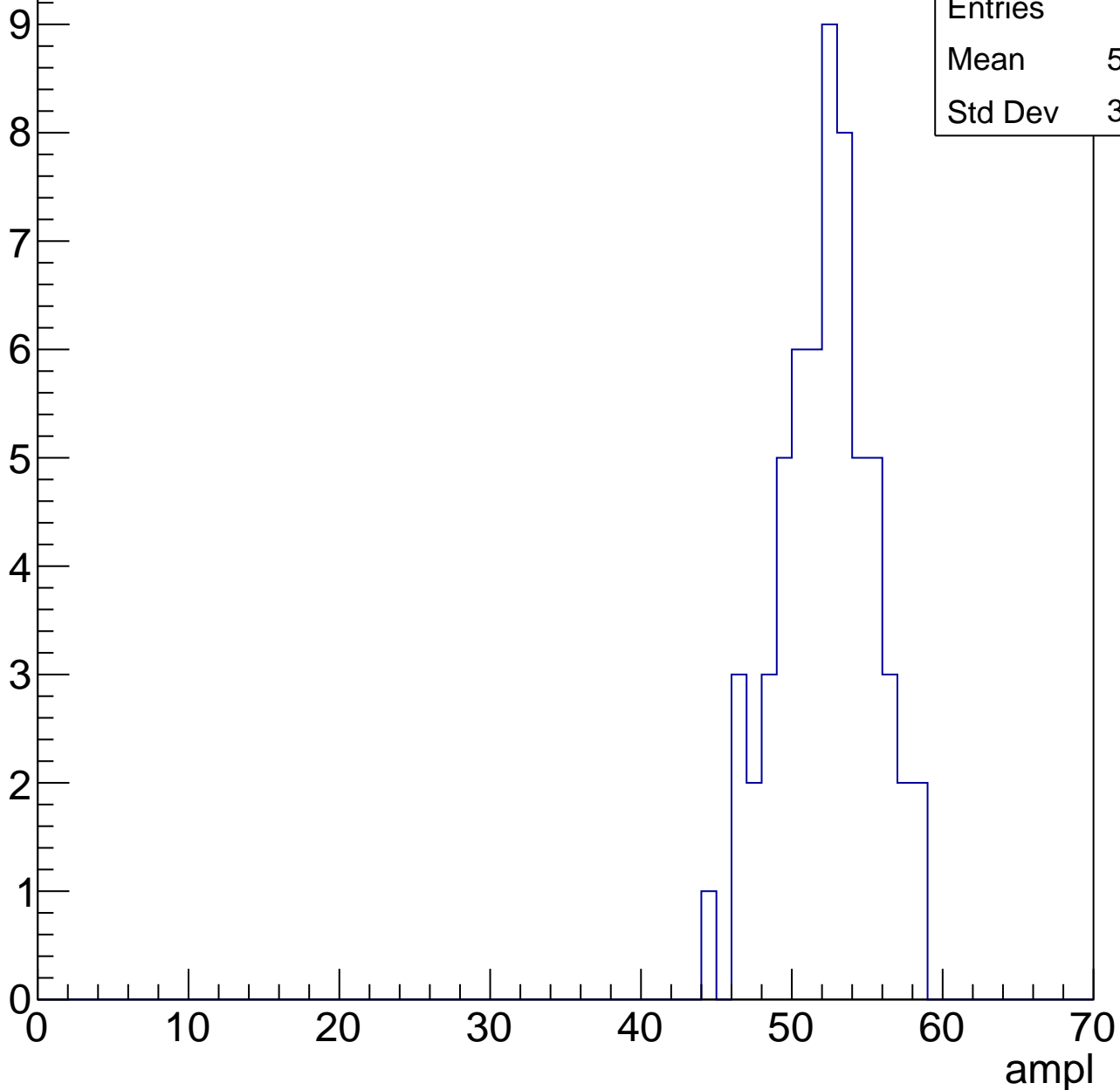


B1L003S, U13-ch67, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

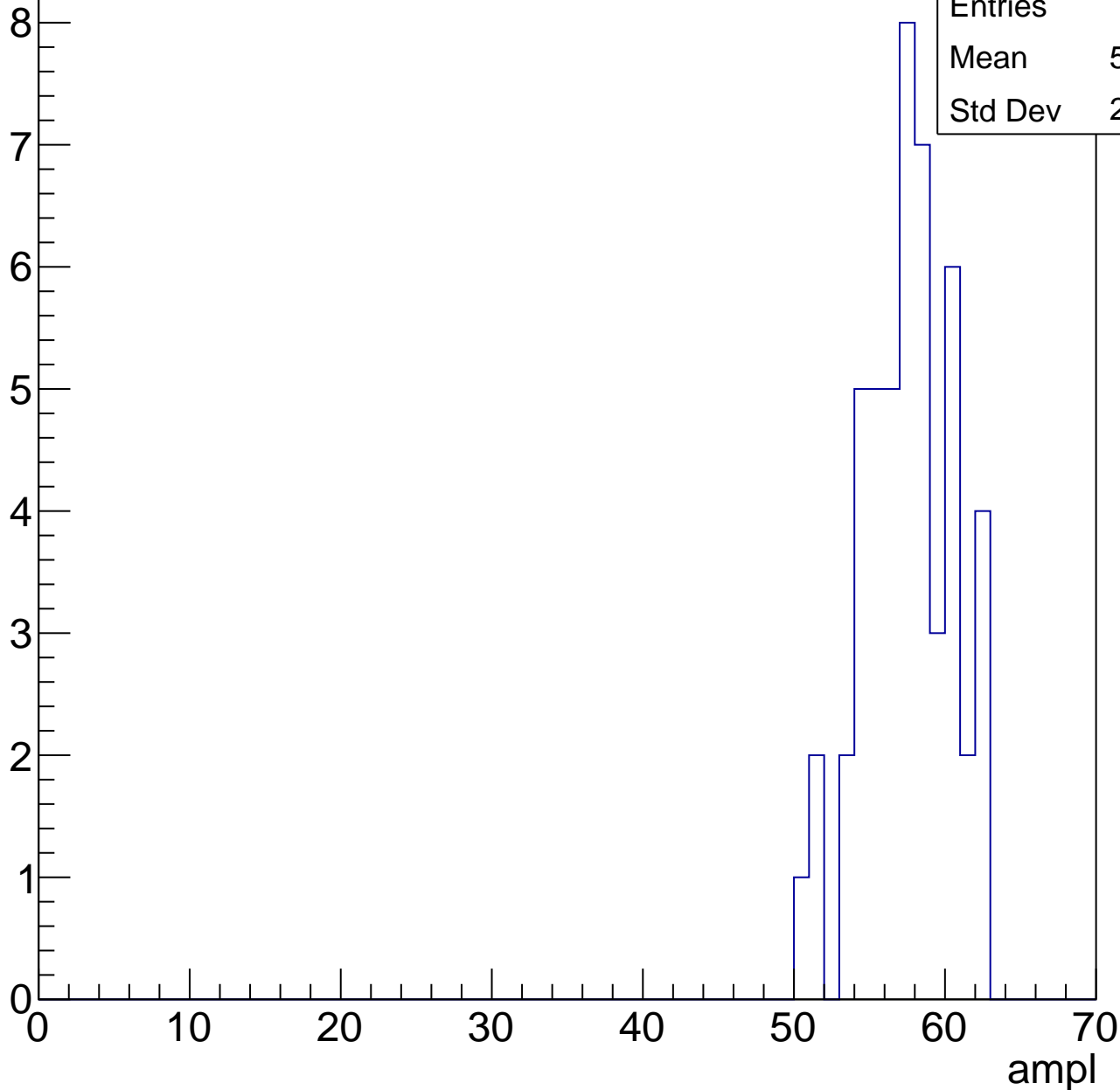
Entries	60
Mean	51.77
Std Dev	3.122



B1L003S, U13-ch67, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



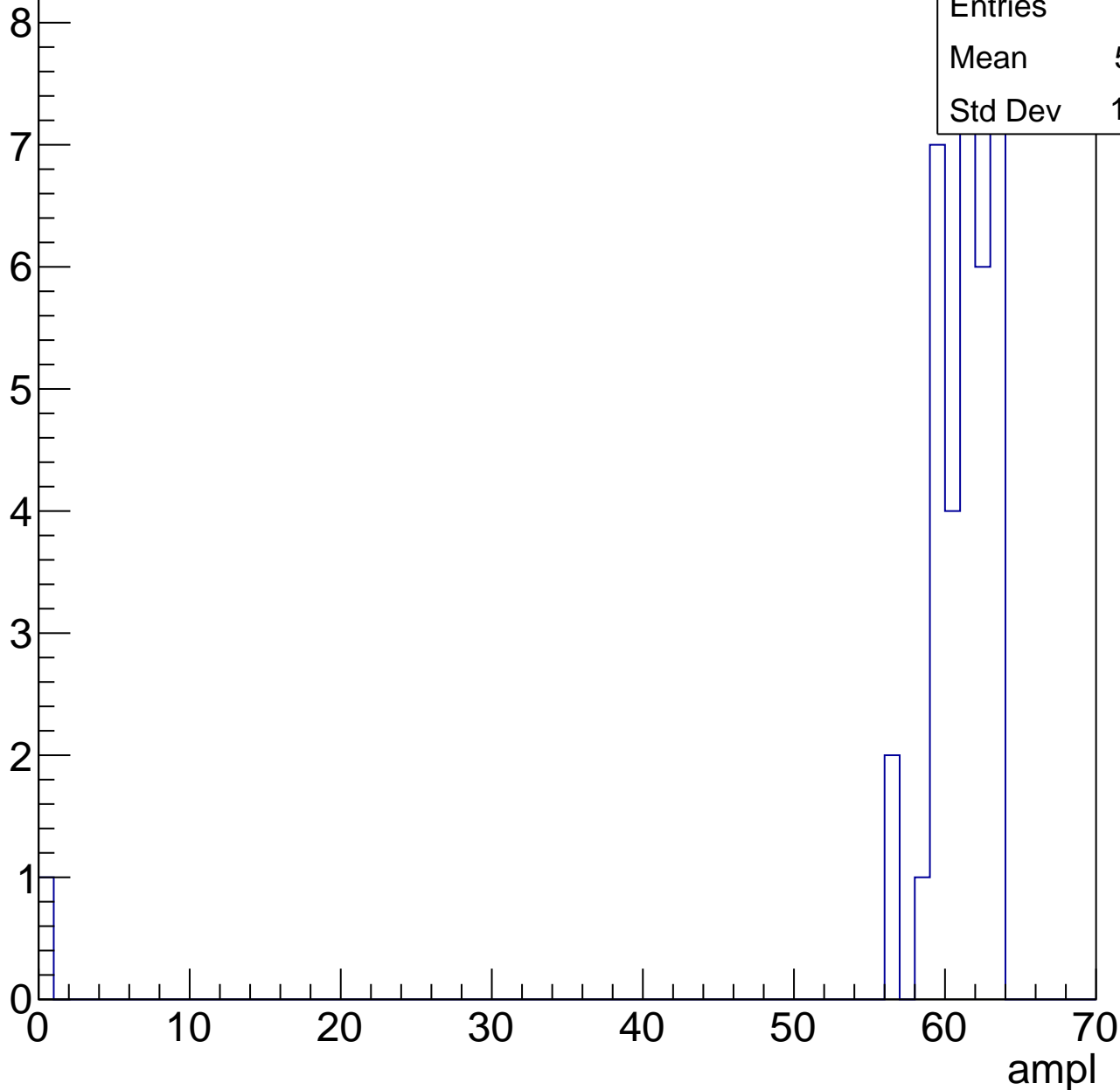
Entries	50
Mean	57.04
Std Dev	2.925

B1L003S, U13-ch67, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	59.11
Std Dev	10.02



B1L003S, U13-ch67, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch67, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch68, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	30.42
Std Dev	4.929

Gaus mean : 31.1524

Gaus Width: 3.6900

10

8

6

4

2

0

0

10

20

30

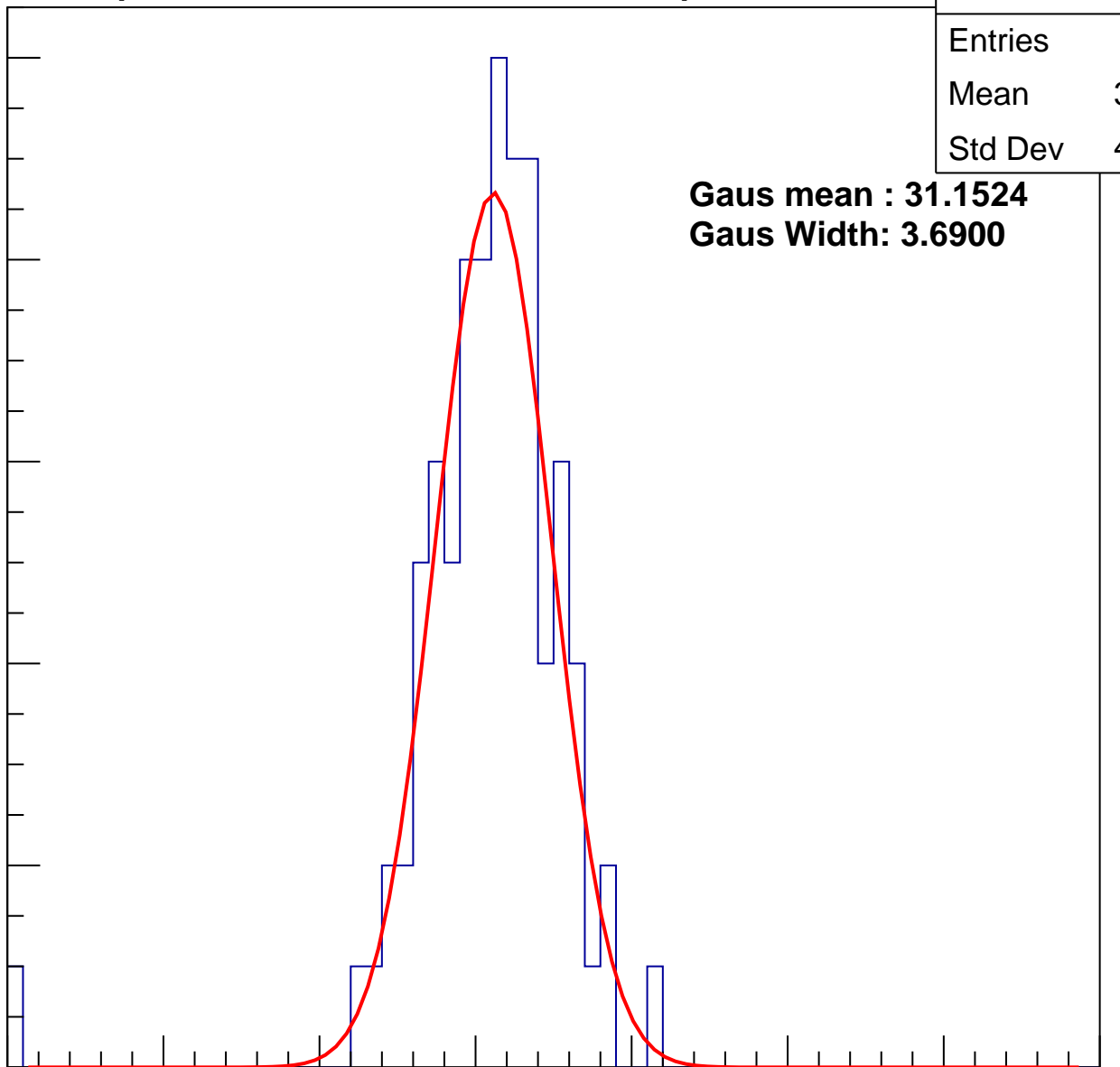
40

50

60

70

ampl



B1L003S, U13-ch68, adc1

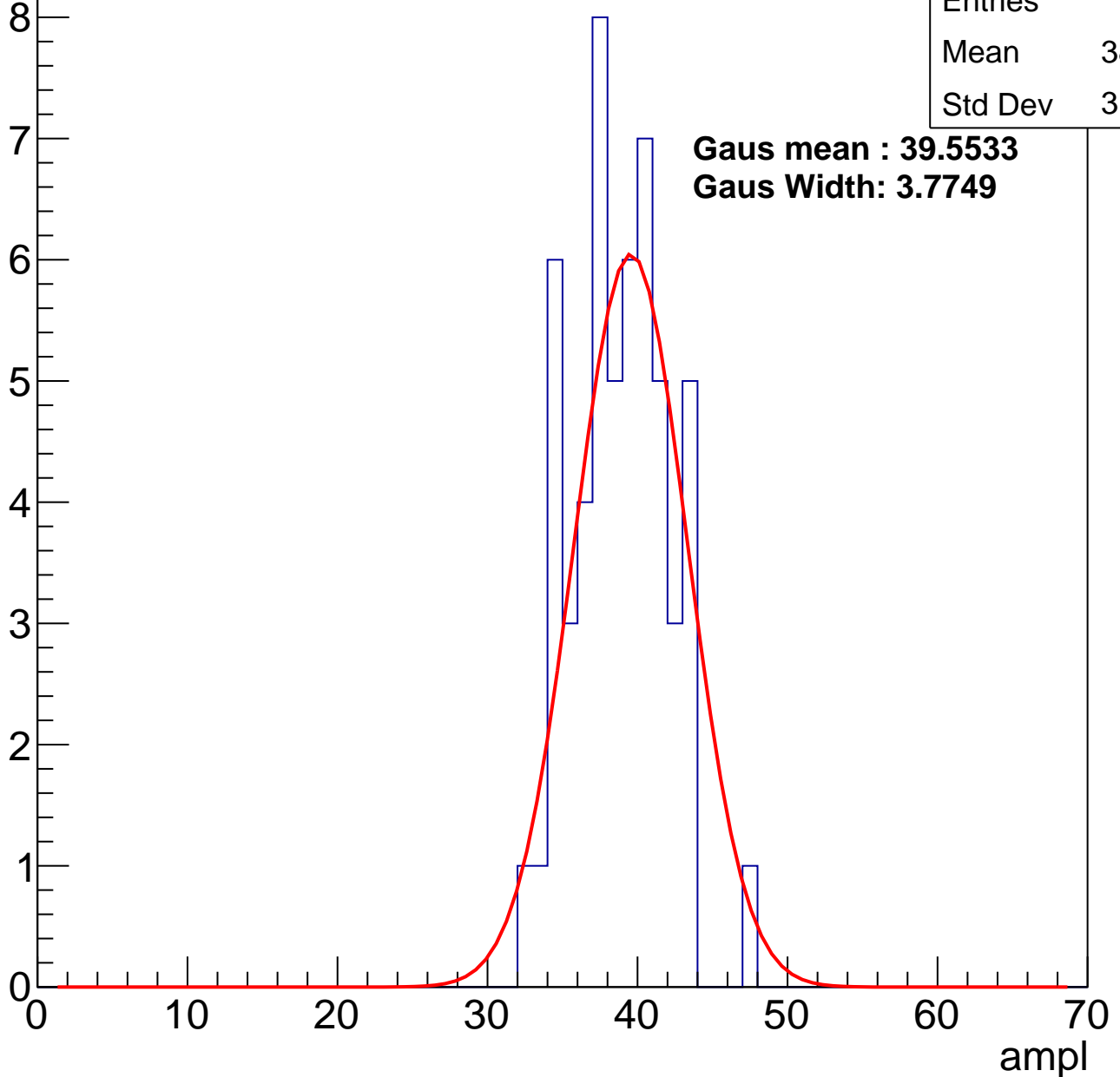
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	38.38
Std Dev	3.113

Gaus mean : 39.5533

Gaus Width: 3.7749



B1L003S, U13-ch68, adc2

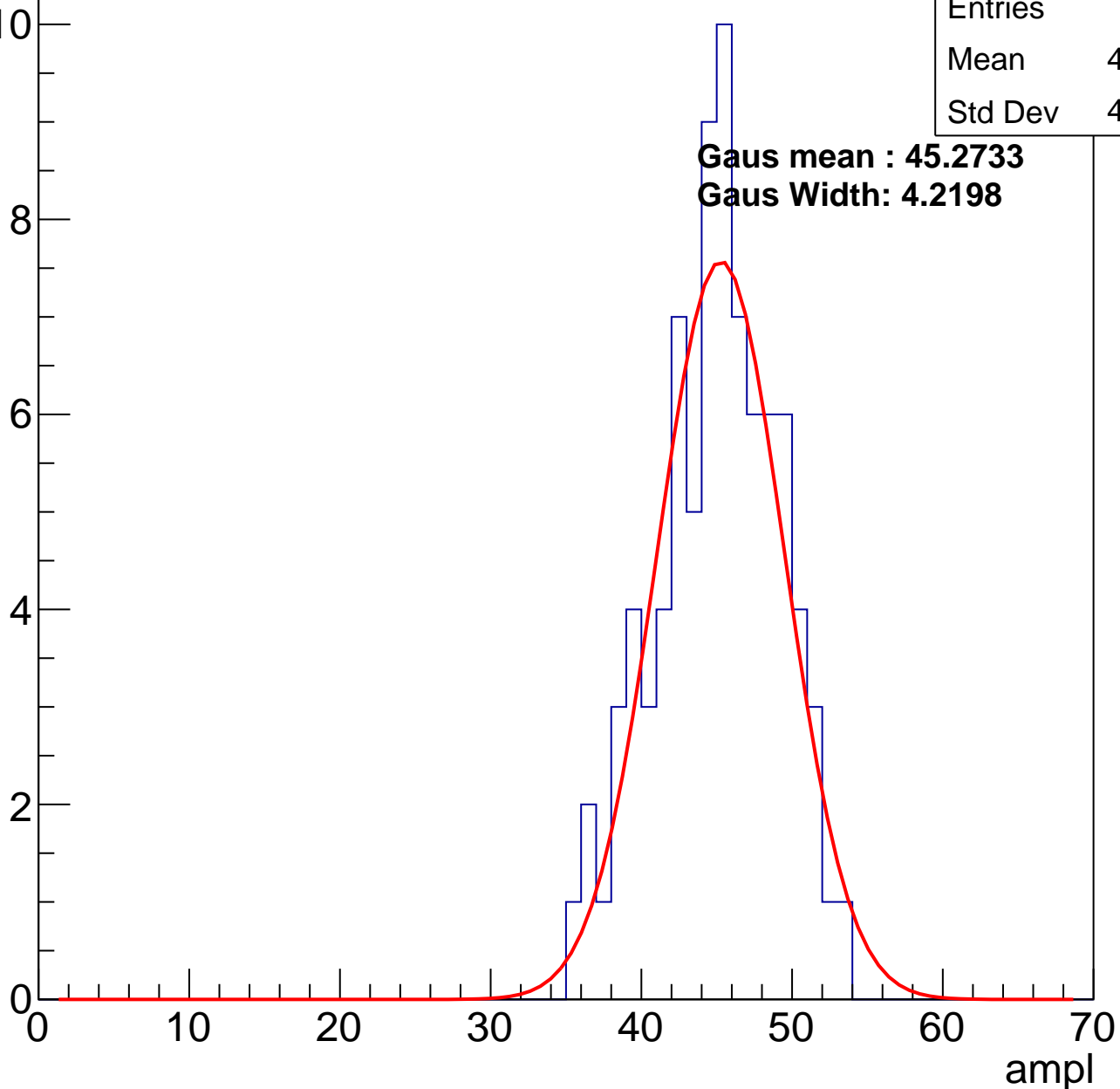
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	44.54
Std Dev	4.007

Gaus mean : 45.2733

Gaus Width: 4.2198

Entry

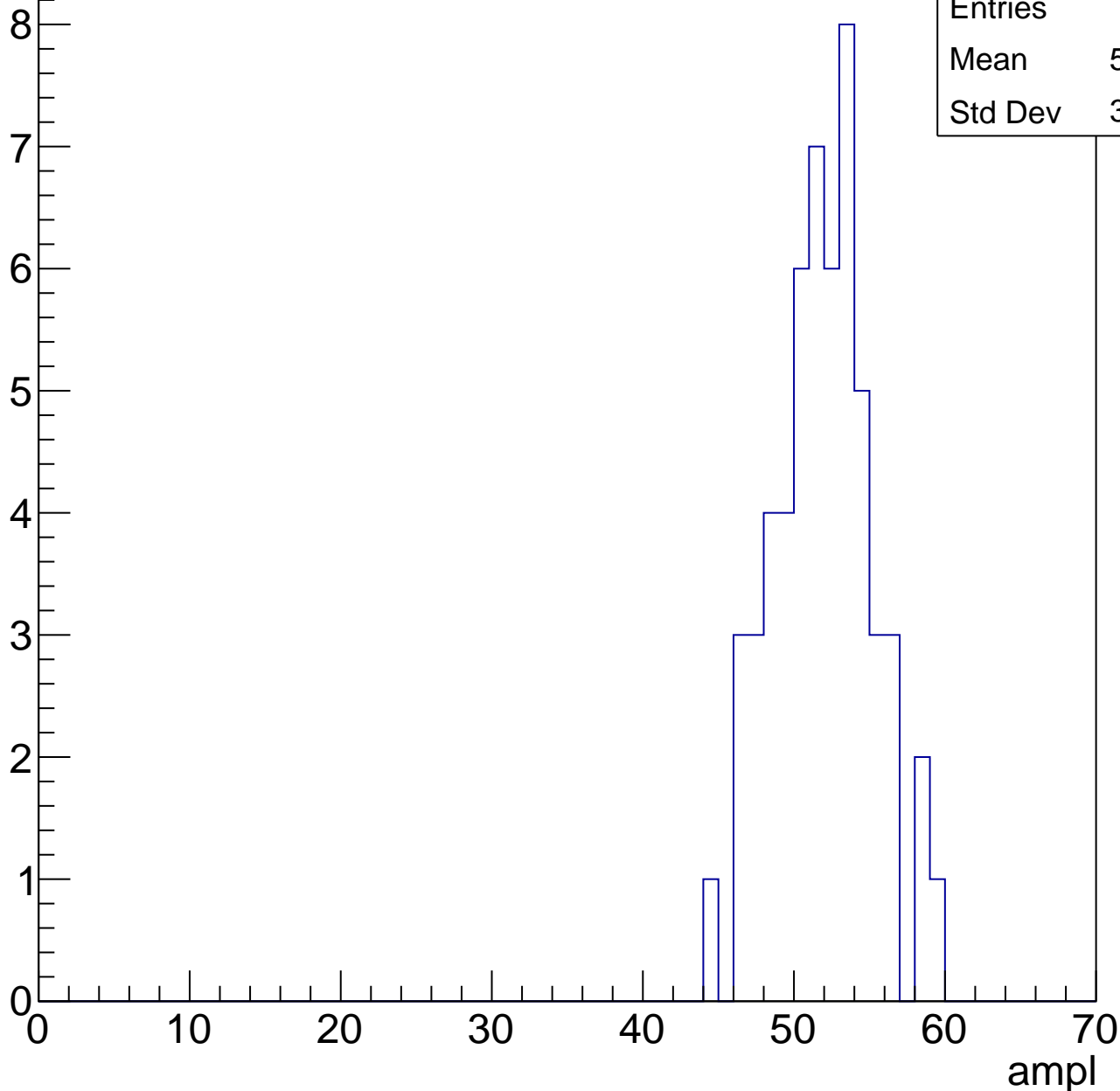


B1L003S, U13-ch68, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

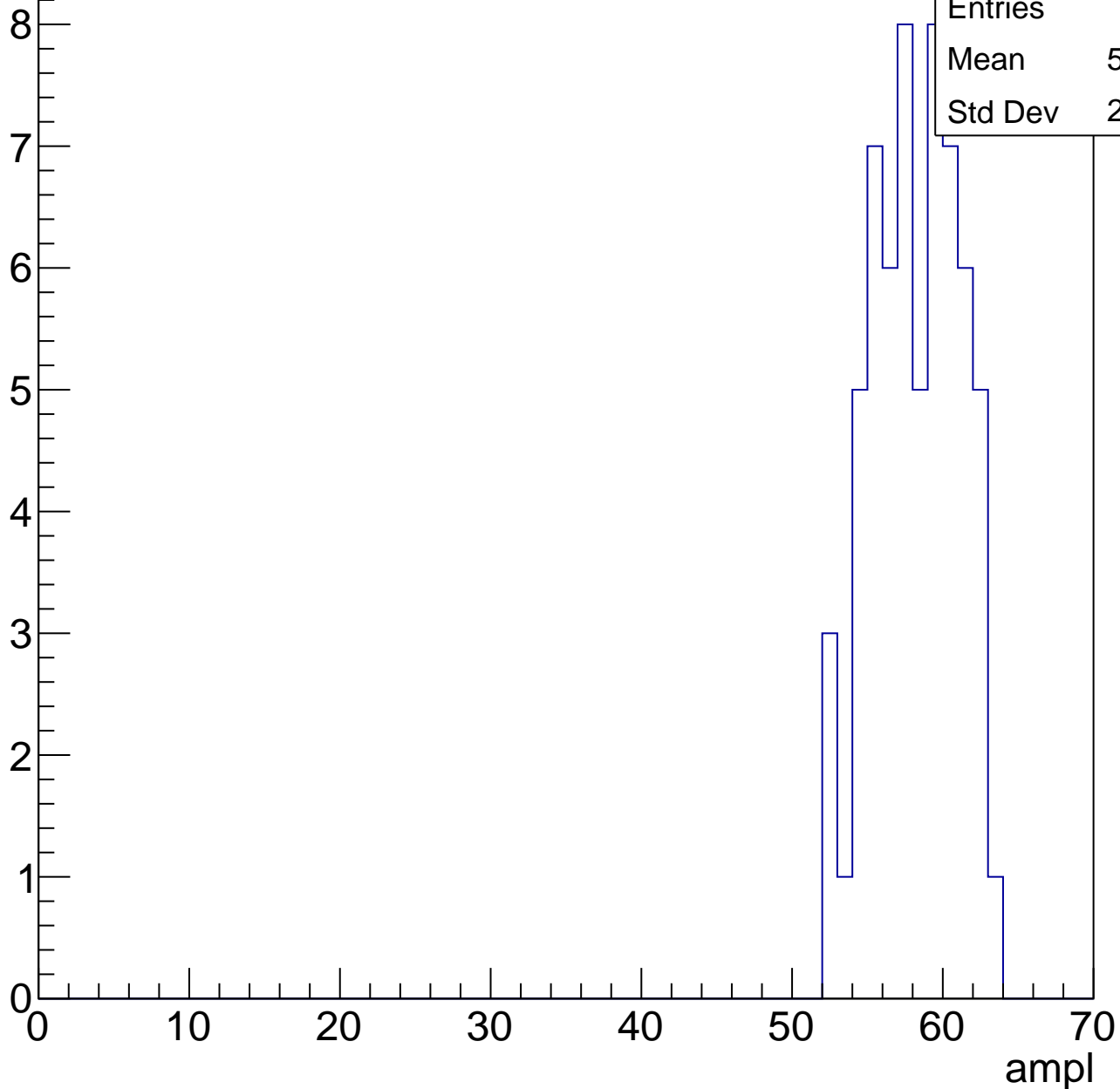
Entries	56
Mean	51.46
Std Dev	3.235



B1L003S, U13-ch68, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

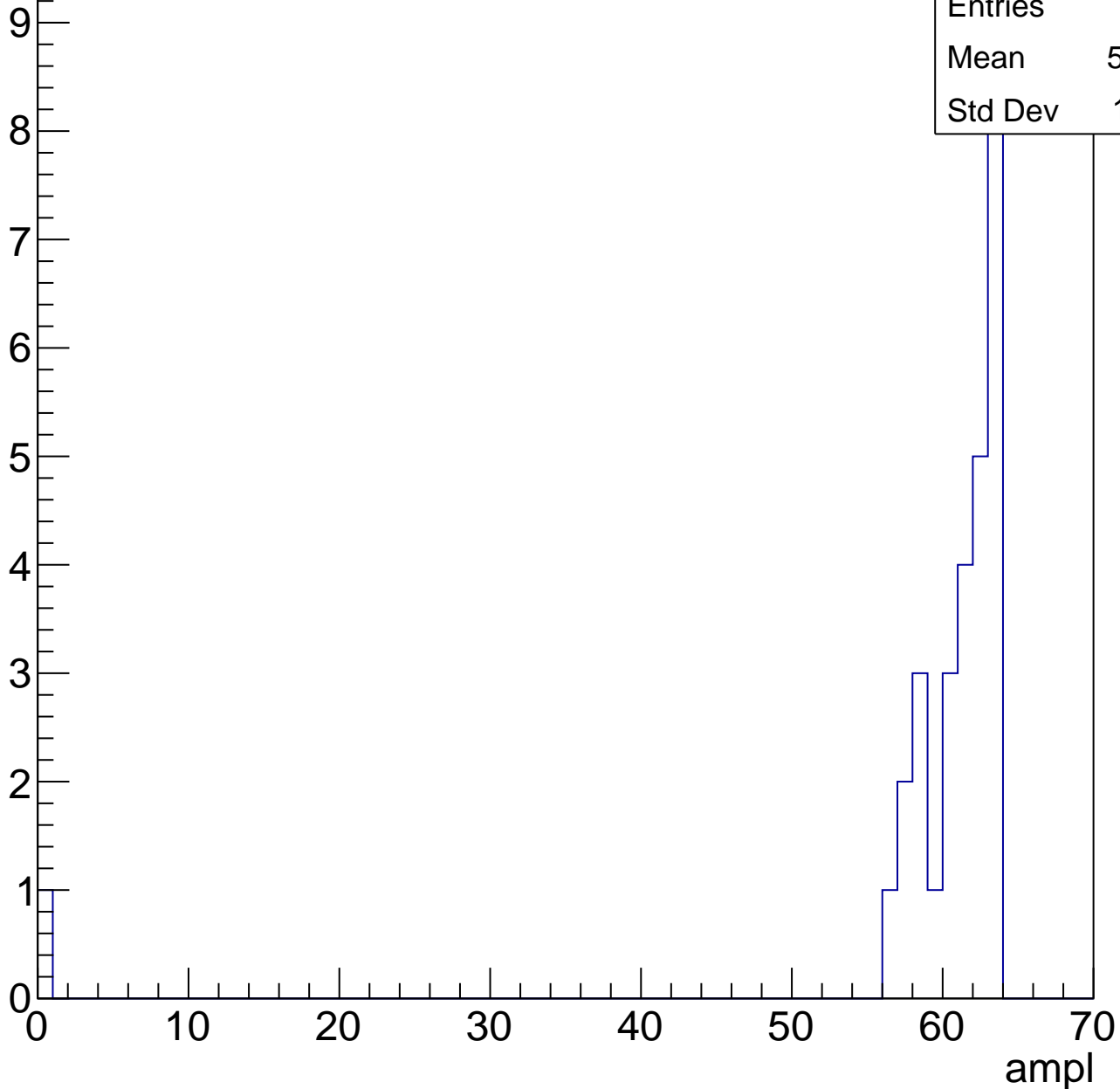


B1L003S, U13-ch68, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	29
Mean	58.76
Std Dev	11.31



B1L003S, U13-ch68, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch68, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch69, adc0

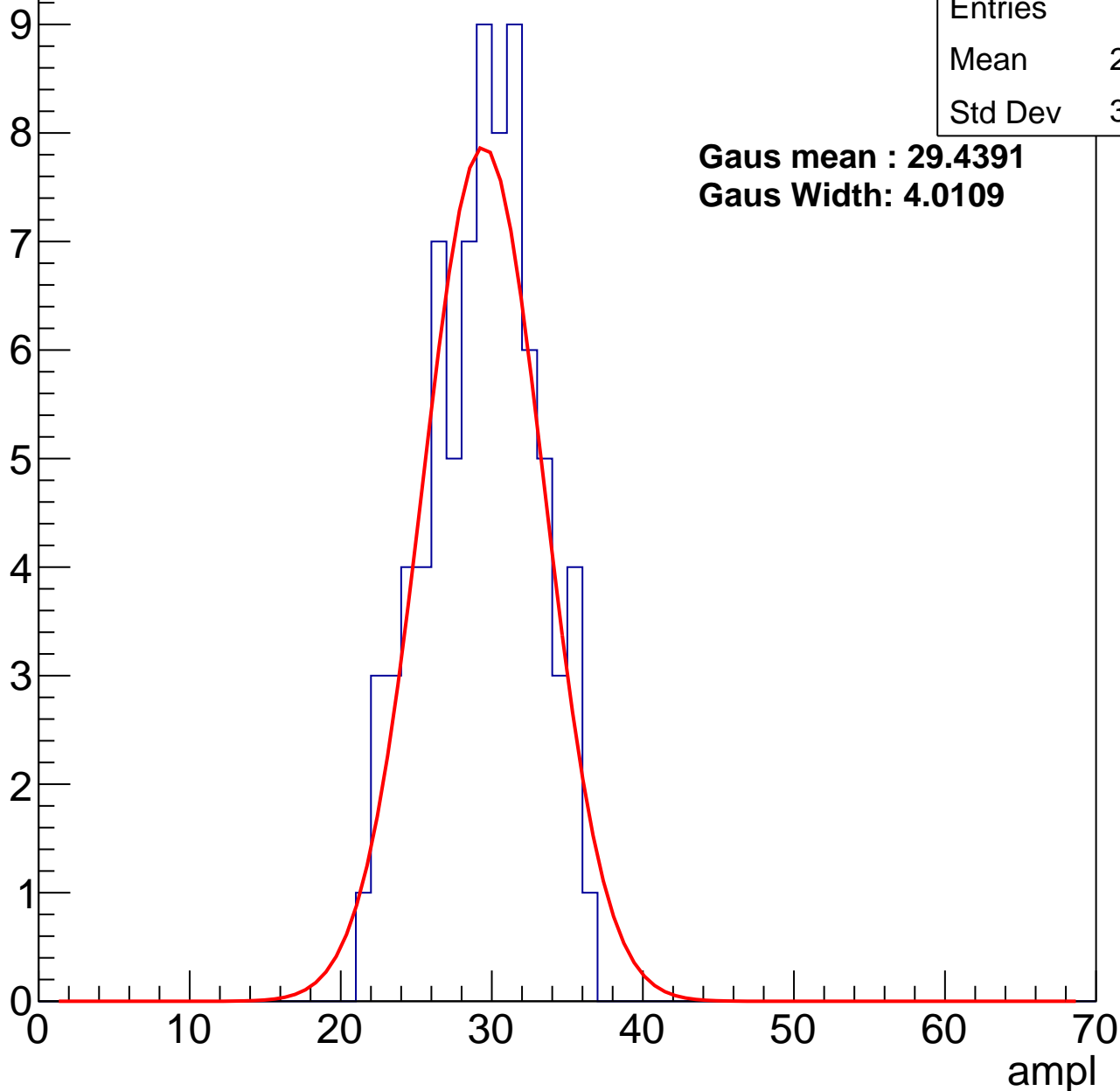
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	28.86
Std Dev	3.589

Gaus mean : 29.4391

Gaus Width: 4.0109



B1L003S, U13-ch69, adc1

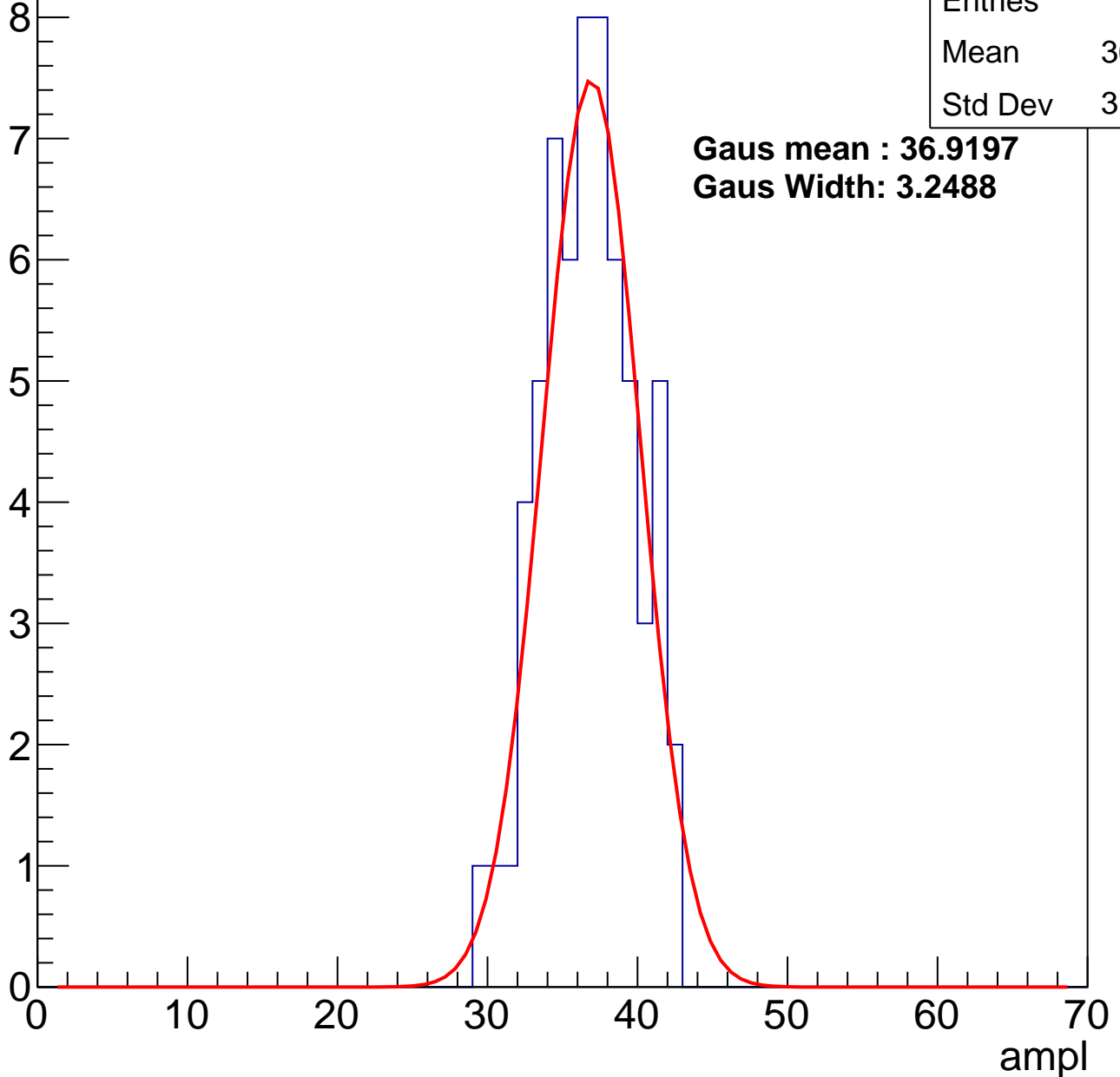
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.24
Std Dev	3.036

Gaus mean : 36.9197

Gaus Width: 3.2488



B1L003S, U13-ch69, adc2

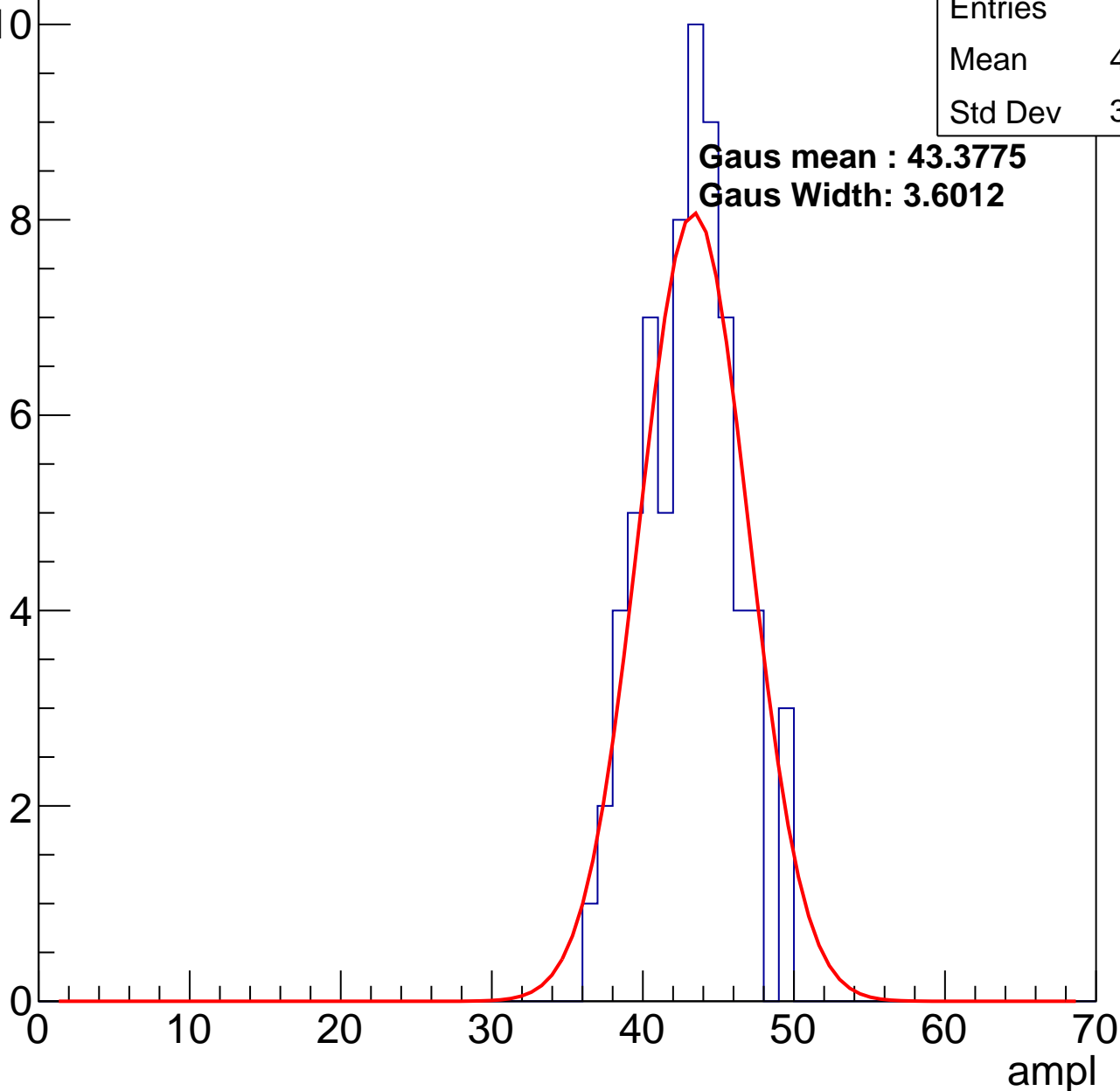
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.58
Std Dev	3.009

Gaus mean : 43.3775

Gaus Width: 3.6012



B1L003S, U13-ch69, adc3

calib_packv5_042523_0143.root, FC#13, port D2

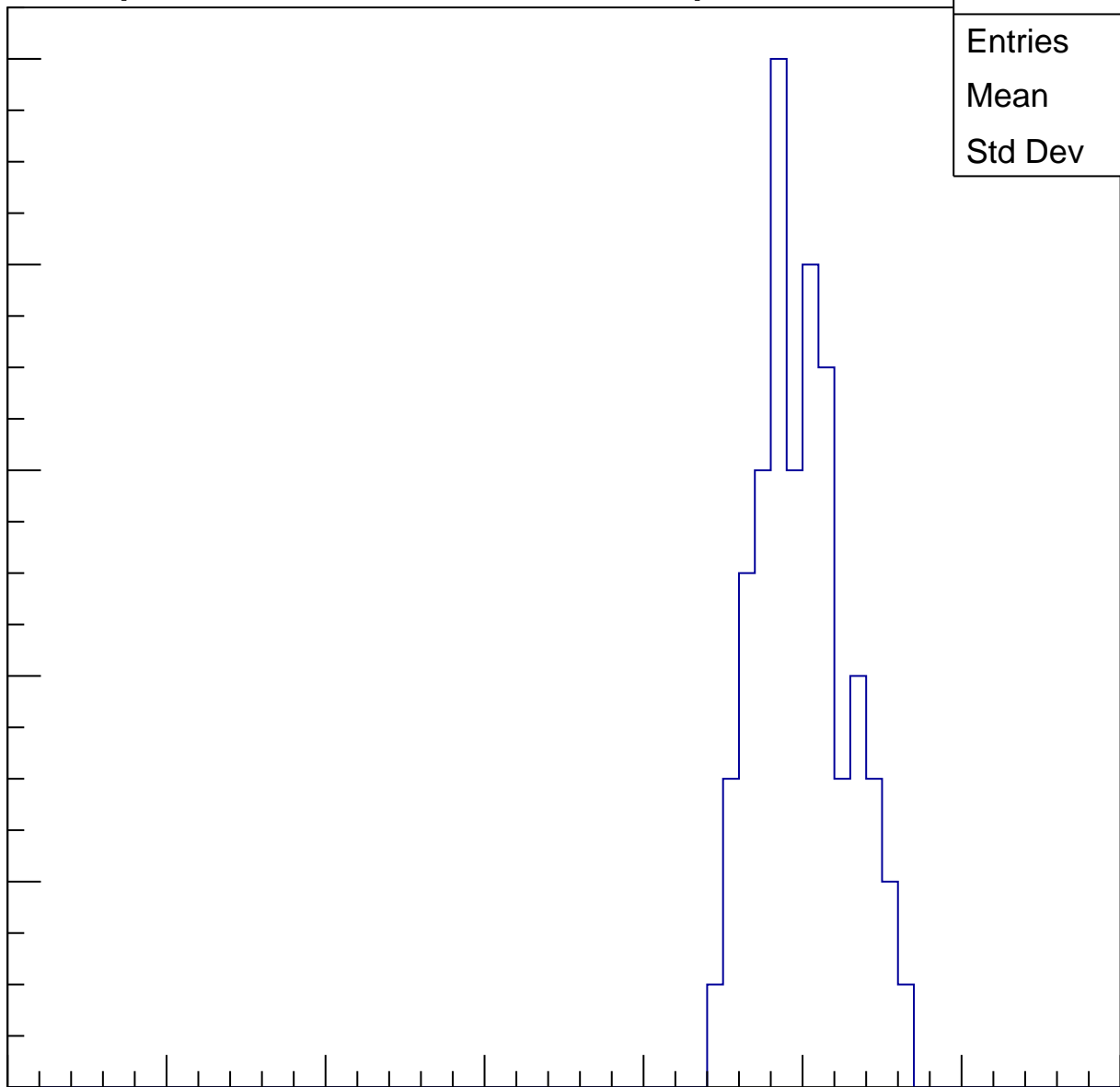
Entries	59
Mean	49.46
Std Dev	2.8

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

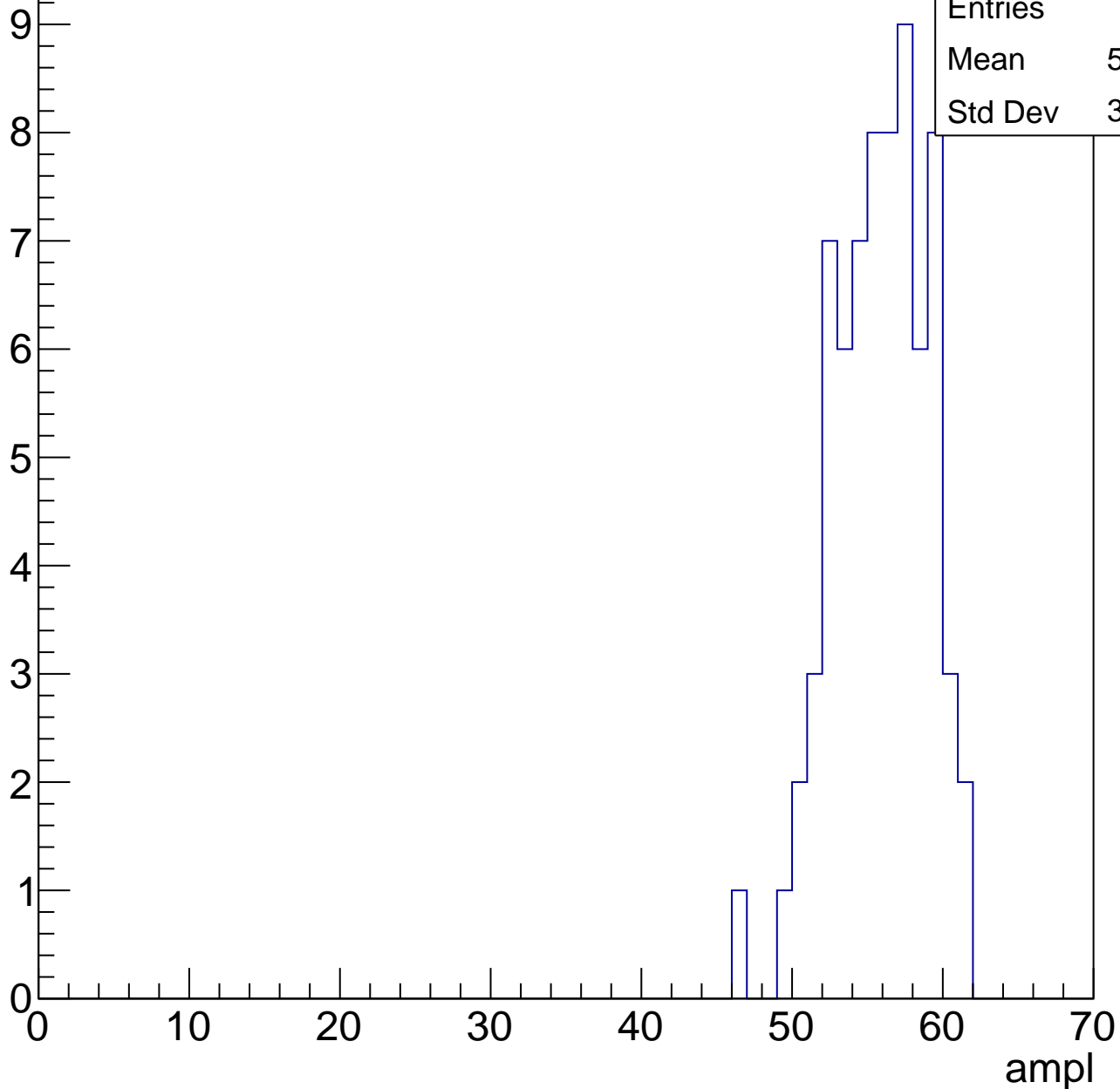
ampl



B1L003S, U13-ch69, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



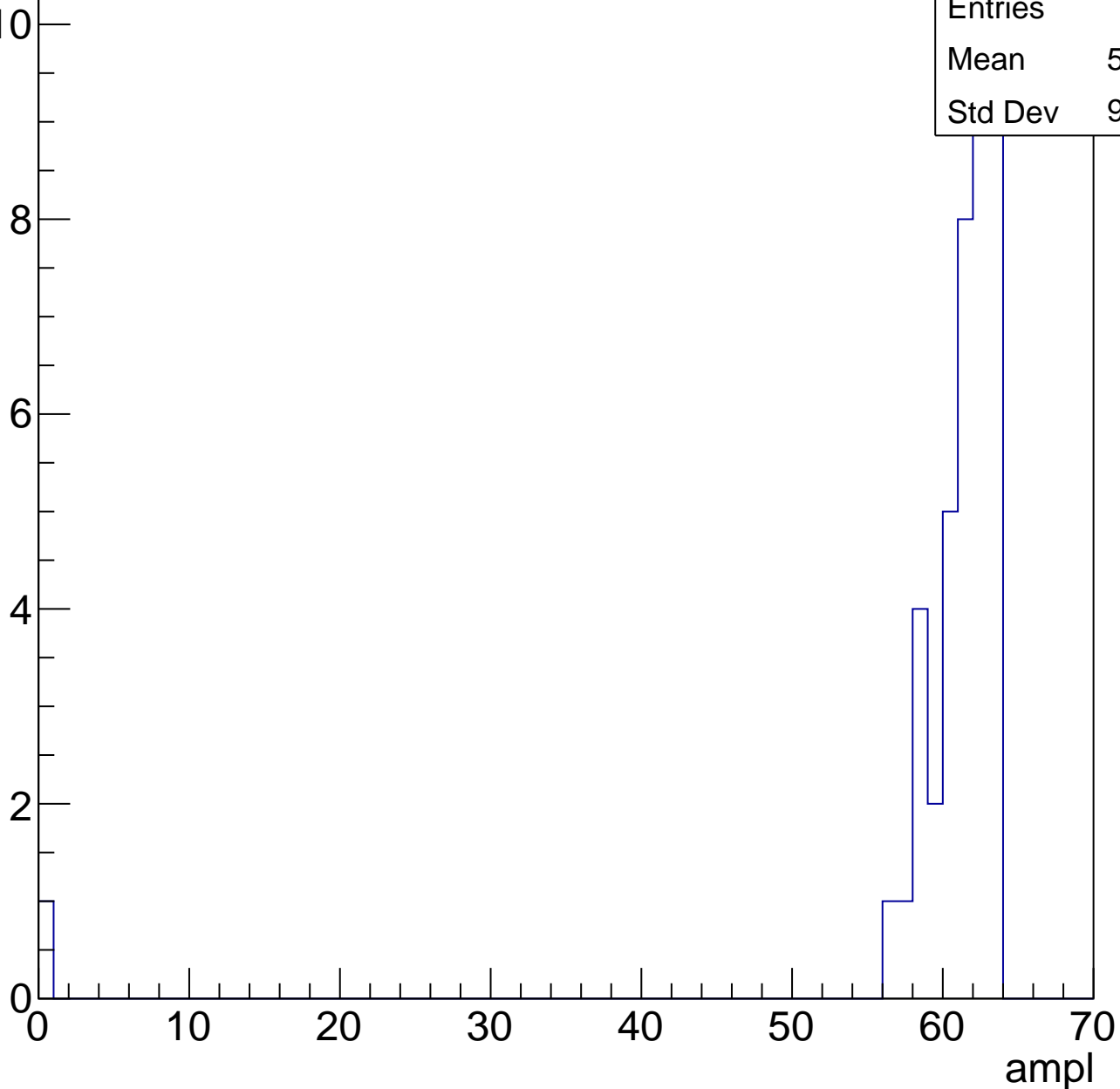
Entries	71
Mean	55.37
Std Dev	3.078

B1L003S, U13-ch69, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	59.46
Std Dev	9.577



B1L003S, U13-ch69, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61
Std Dev	1.414

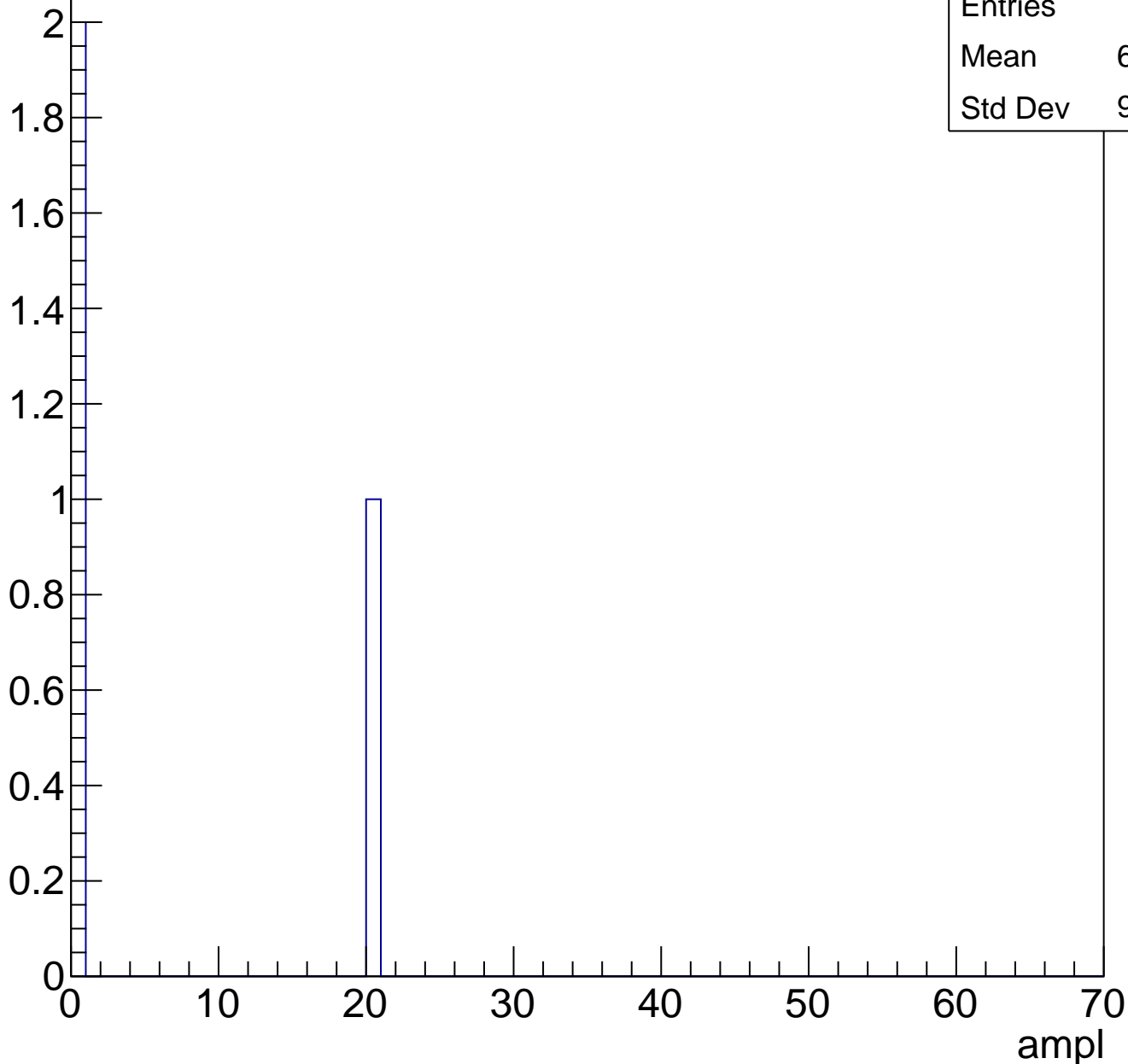
ampl

0 10 20 30 40 50 60 70

B1L003S, U13-ch69, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L003S, U13-ch70, adc0

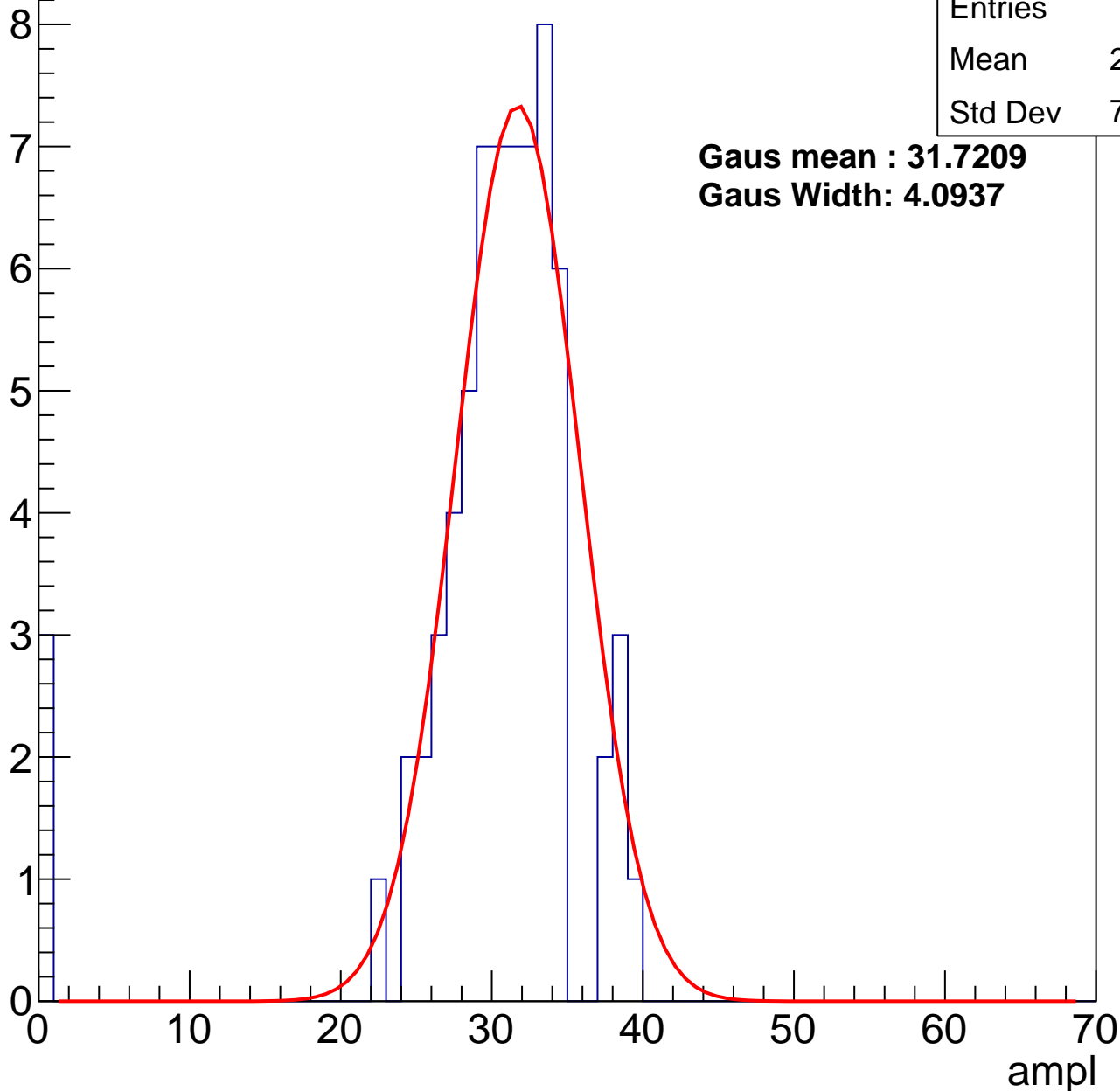
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.34
Std Dev	7.216

Gaus mean : 31.7209

Gaus Width: 4.0937



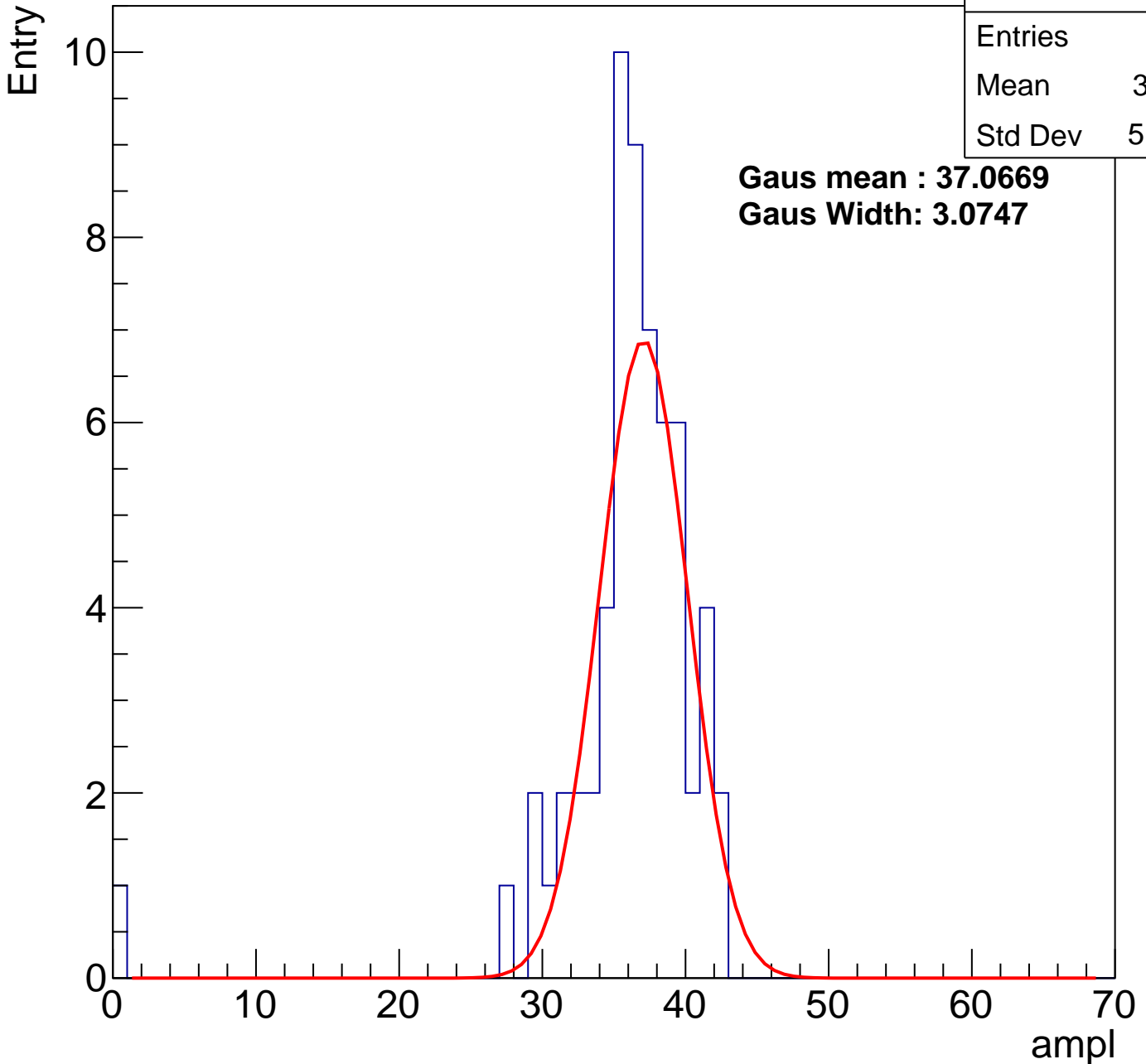
B1L003S, U13-ch70, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	35.51
Std Dev	5.597

Gaus mean : 37.0669

Gaus Width: 3.0747



B1L003S, U13-ch70, adc2

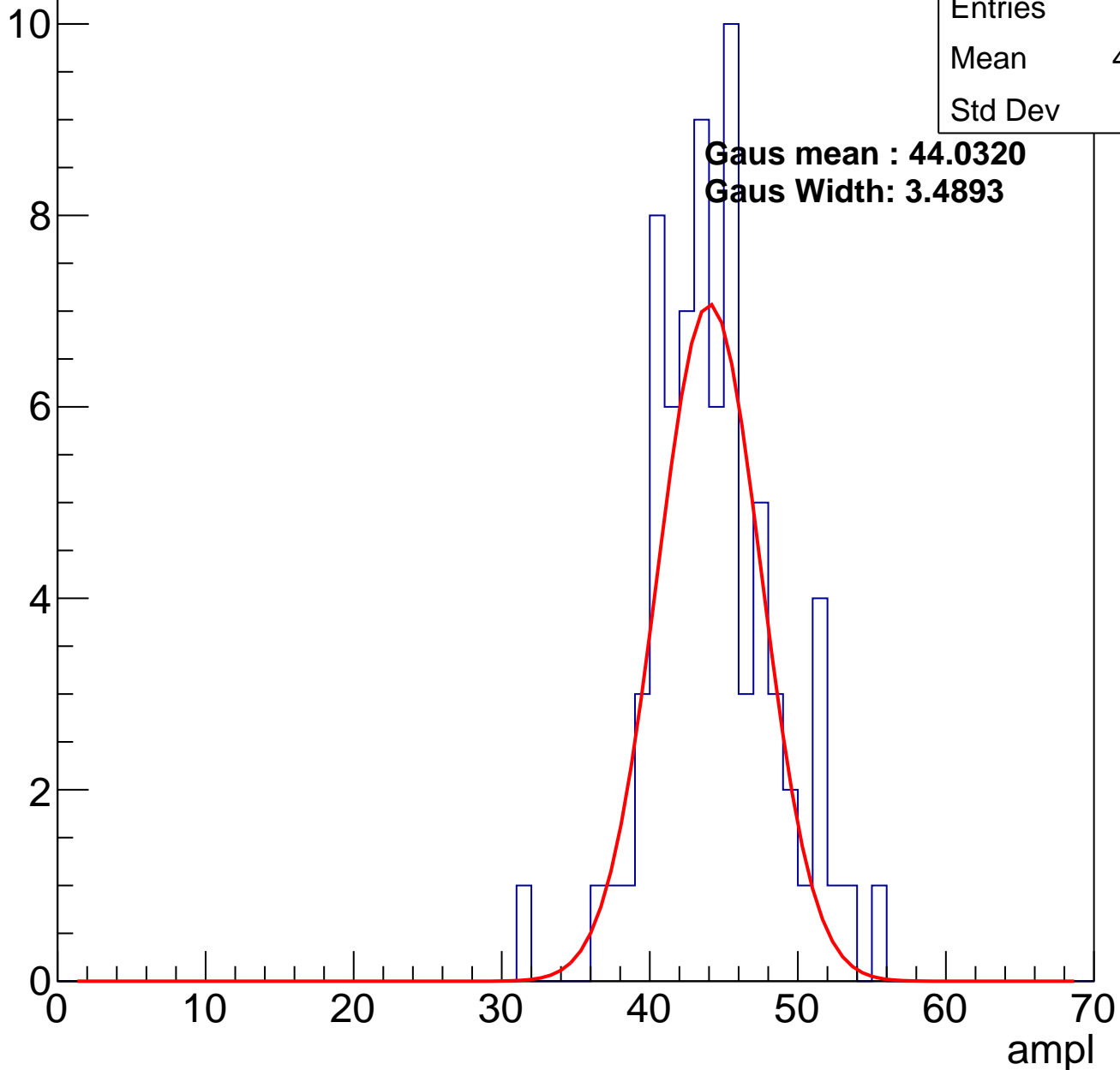
calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	43.91
Std Dev	4.15

Gaus mean : 44.0320

Gaus Width: 3.4893

Entry

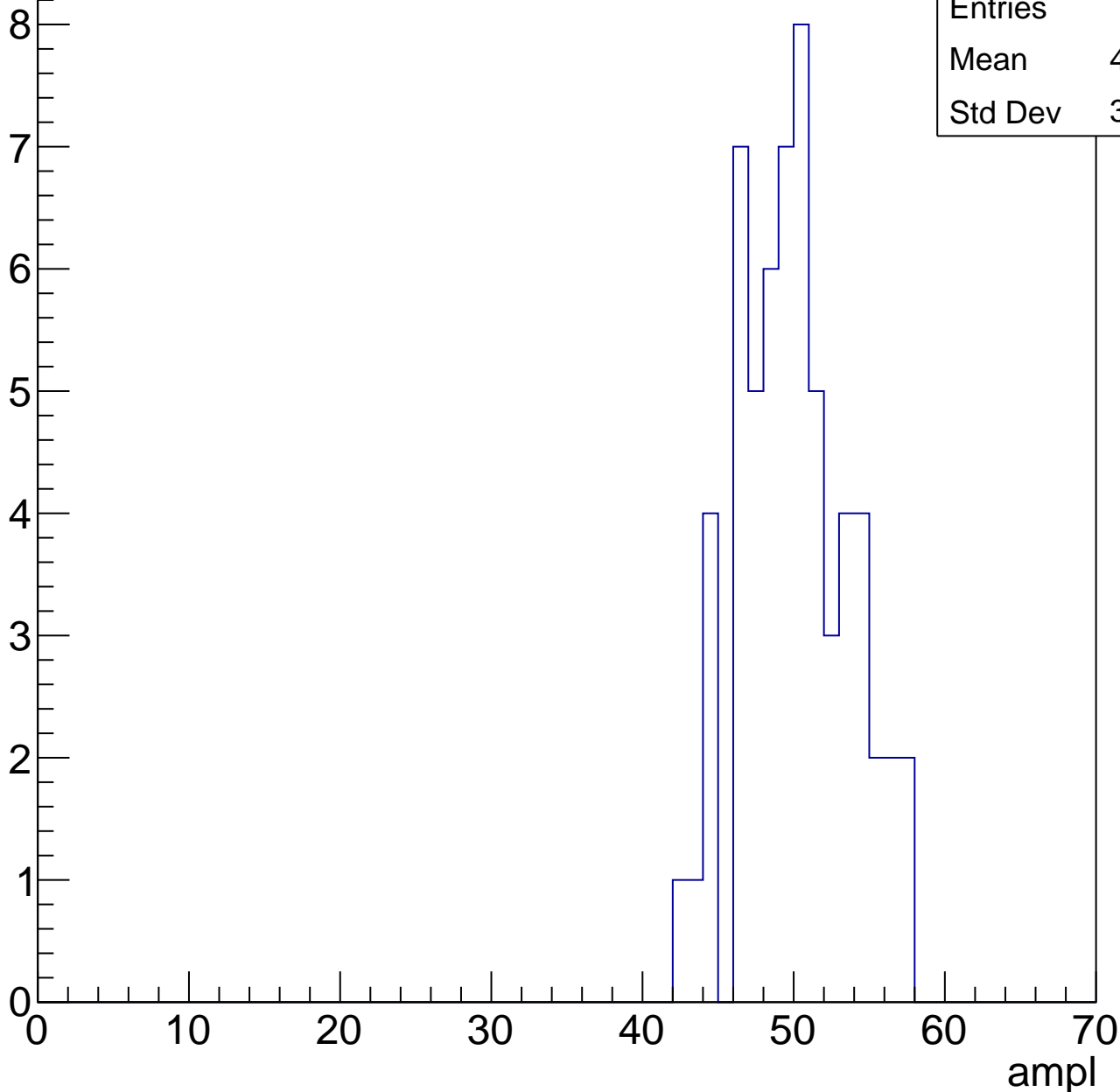


B1L003S, U13-ch70, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	49.57
Std Dev	3.555

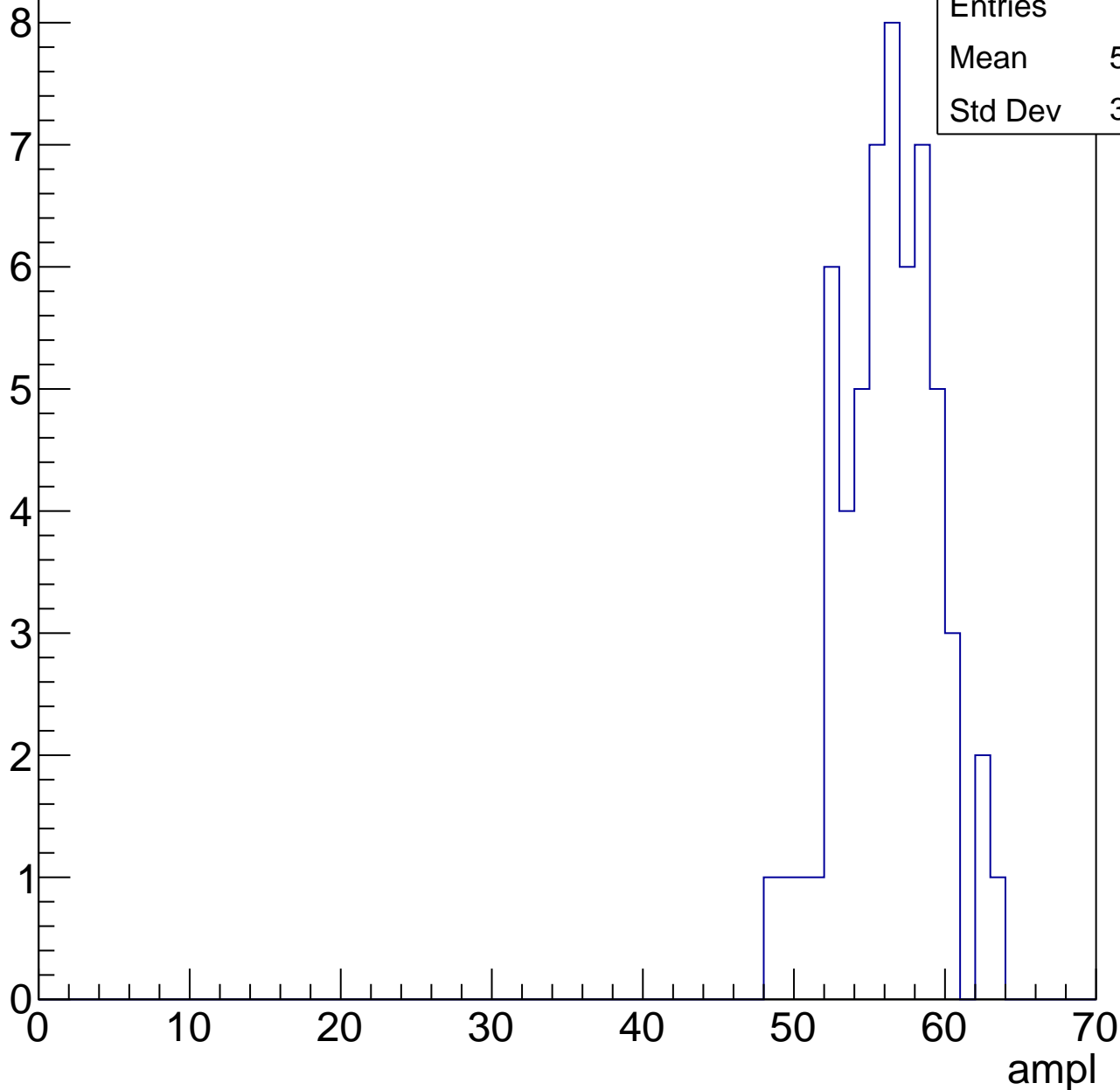


B1L003S, U13-ch70, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.78
Std Dev	3.157

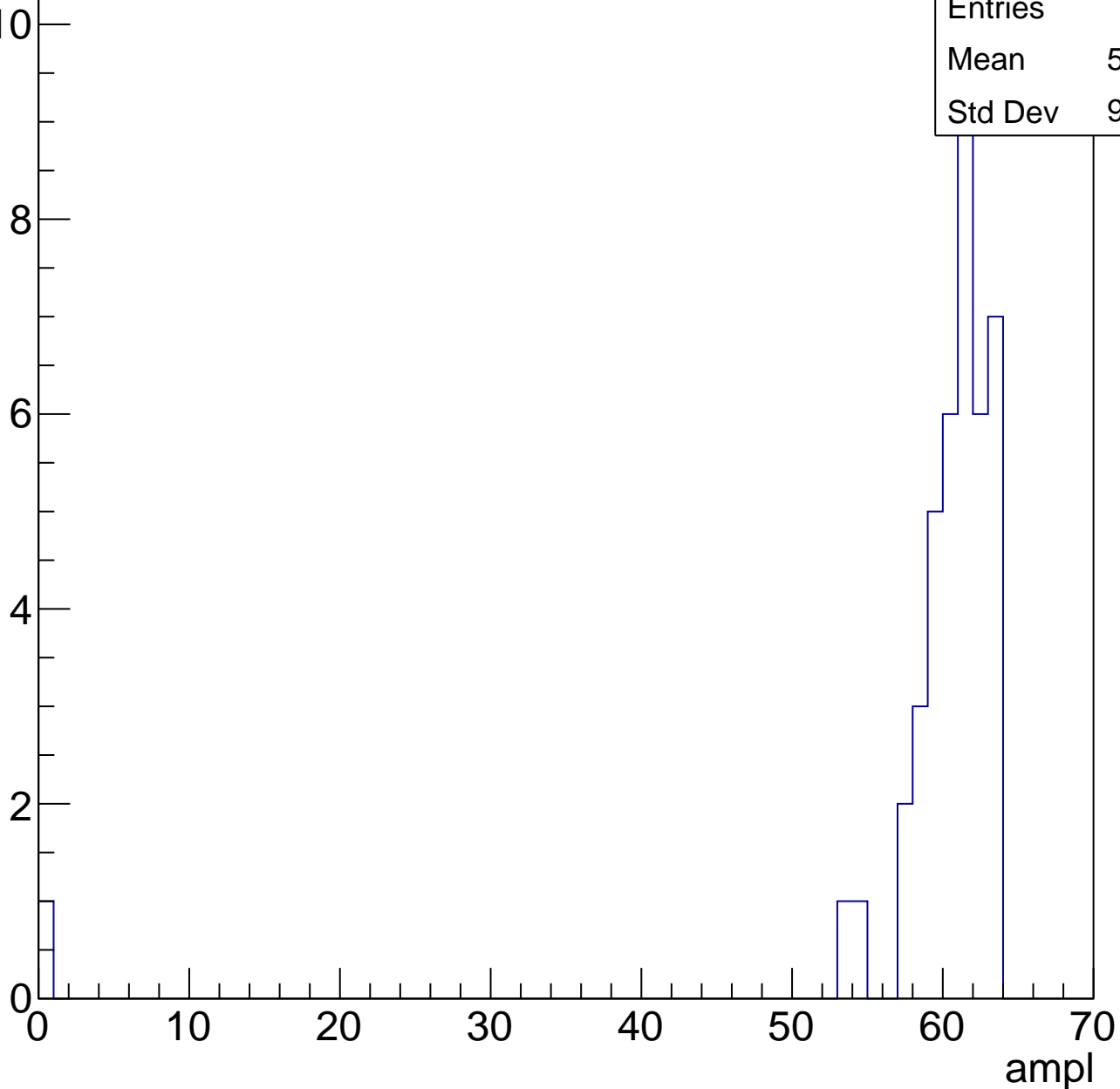


B1L003S, U13-ch70, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.88
Std Dev	9.467



B1L003S, U13-ch70, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	62
Std Dev	1.095

B1L003S, U13-ch70, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch71, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	29.25
Std Dev	6.705

Gaus mean : 31.1072

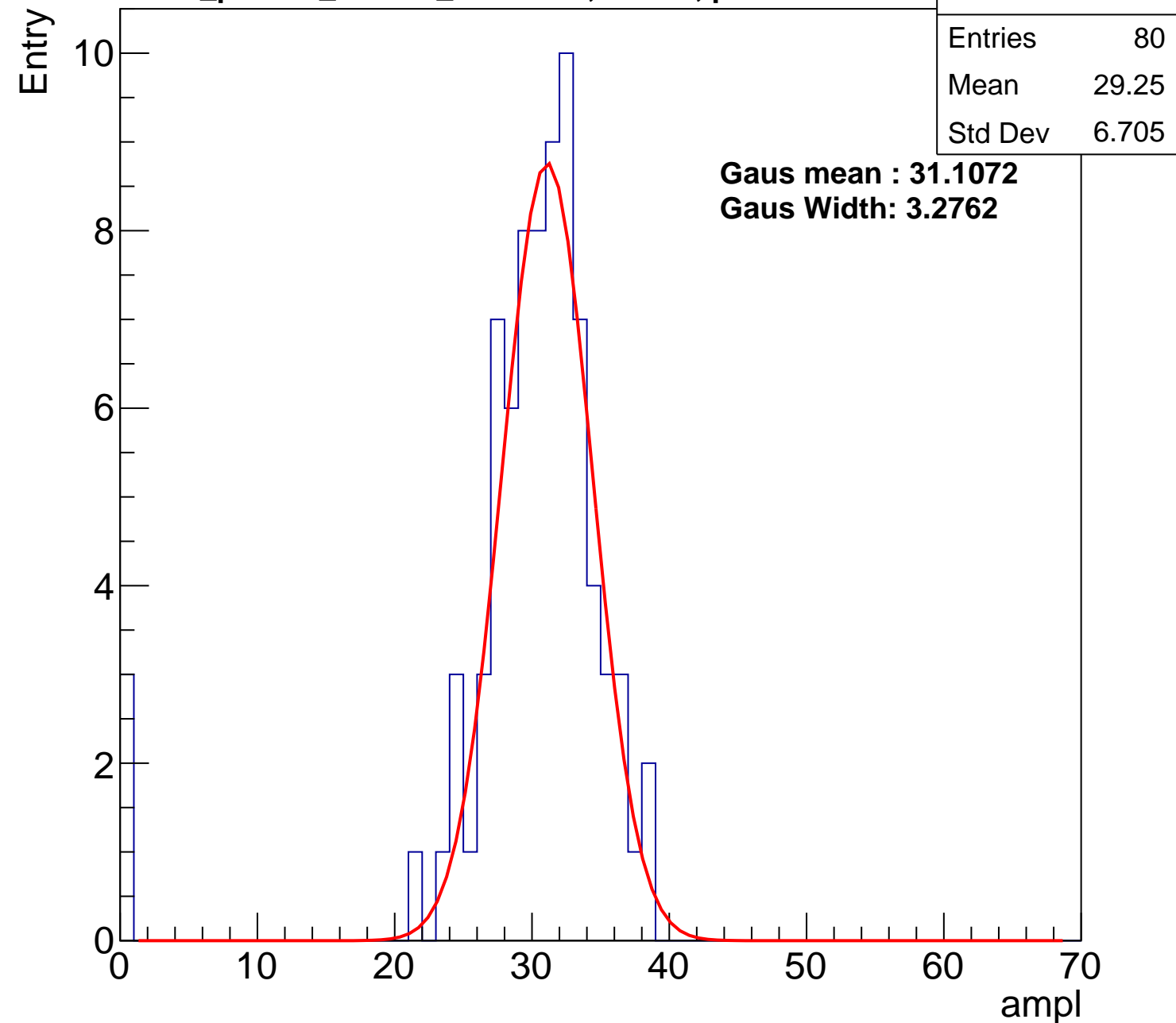
Gaus Width: 3.2762

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch71, adc1

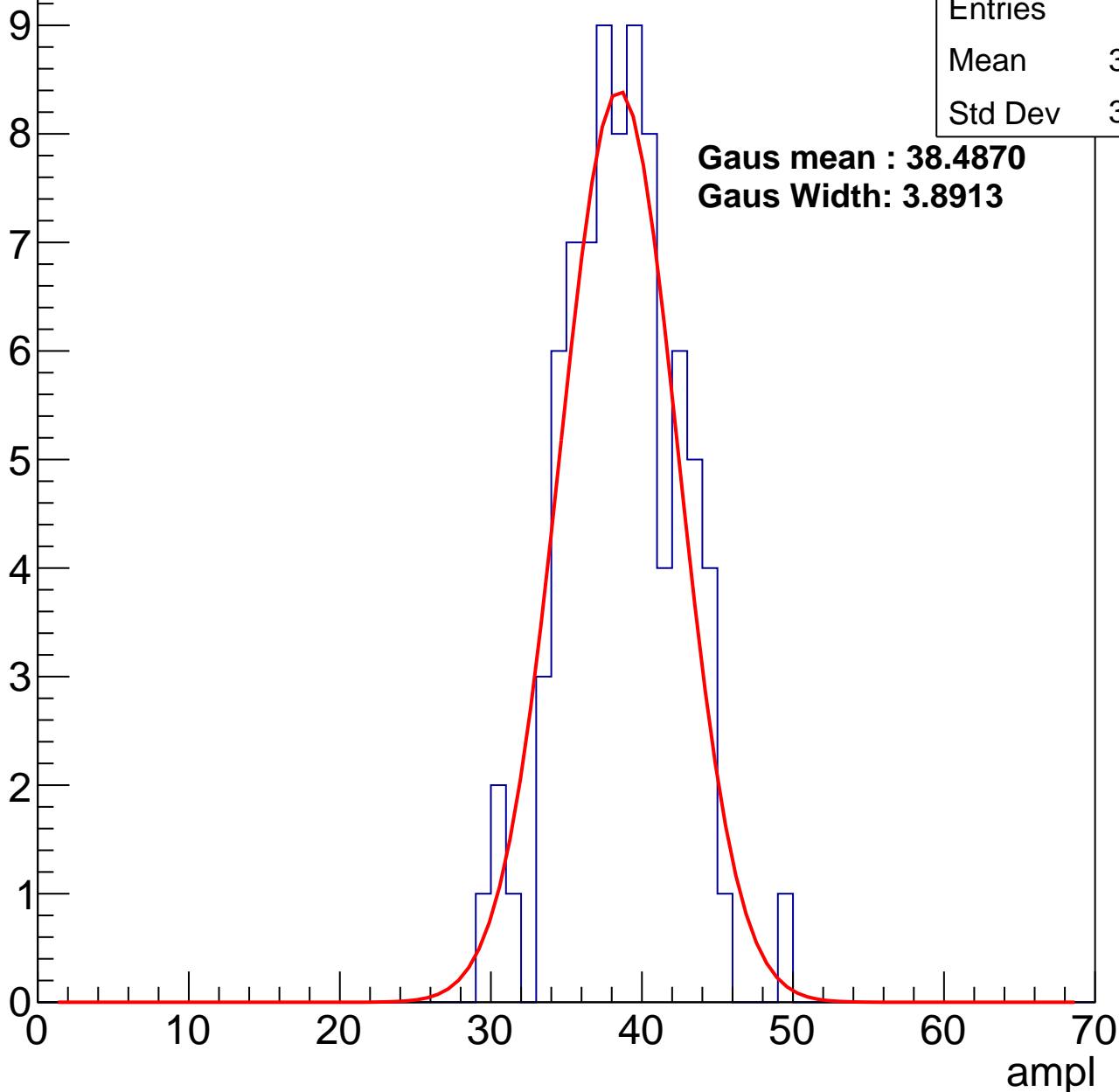
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	38.16
Std Dev	3.727

Gaus mean : 38.4870

Gaus Width: 3.8913



B1L003S, U13-ch71, adc2

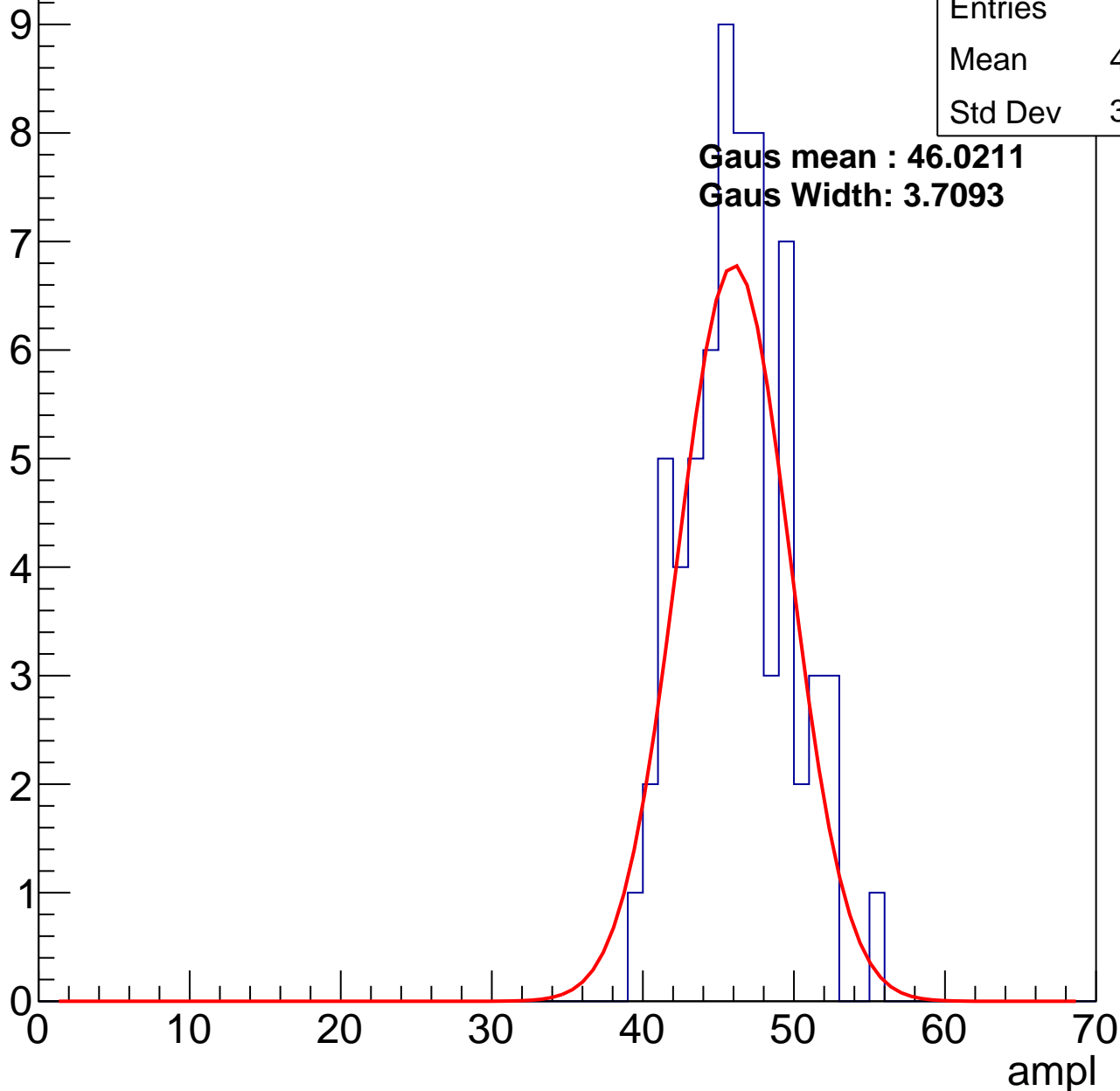
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	45.84
Std Dev	3.379

Gaus mean : 46.0211

Gaus Width: 3.7093

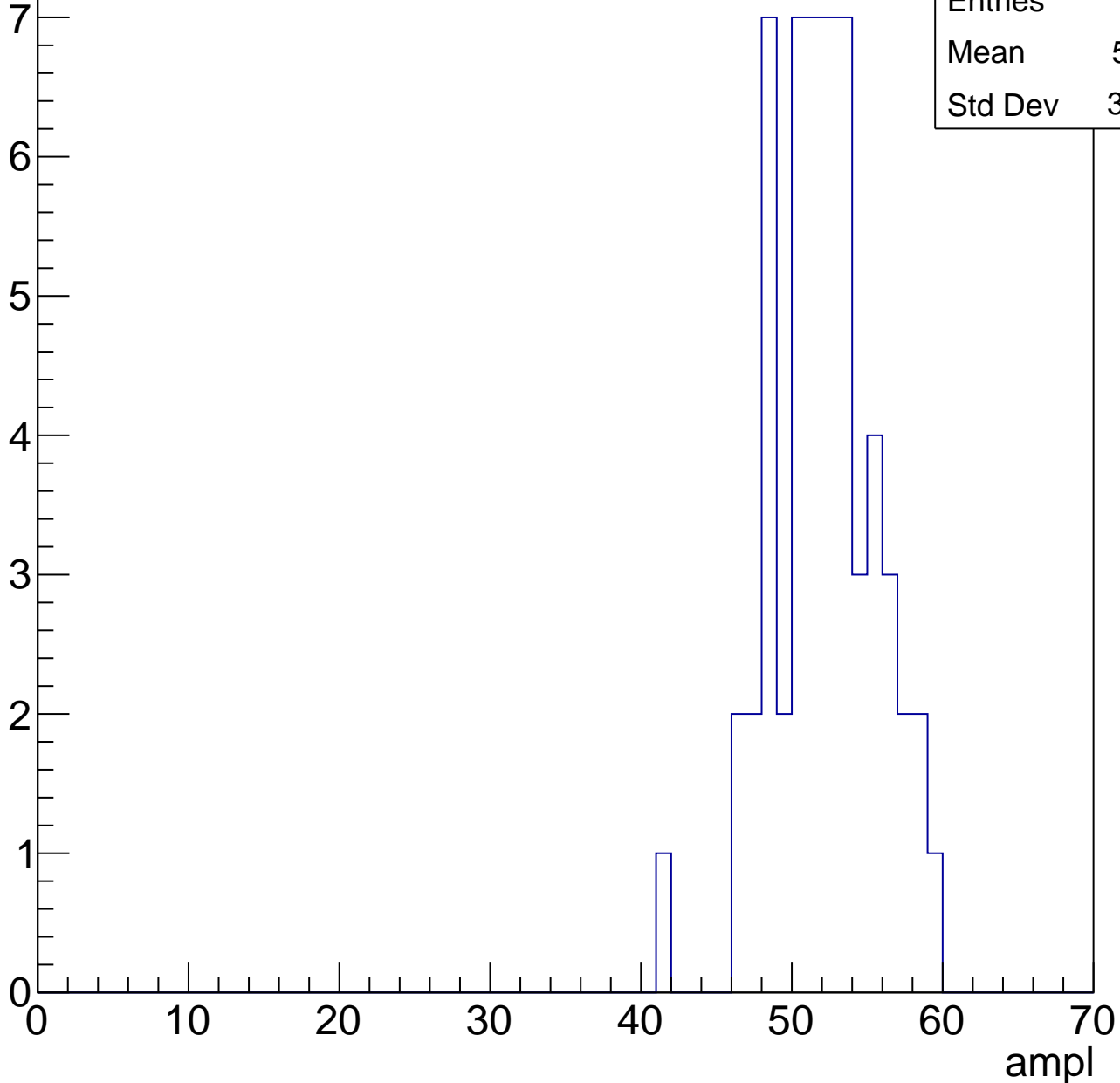


B1L003S, U13-ch71, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	51.61
Std Dev	3.437

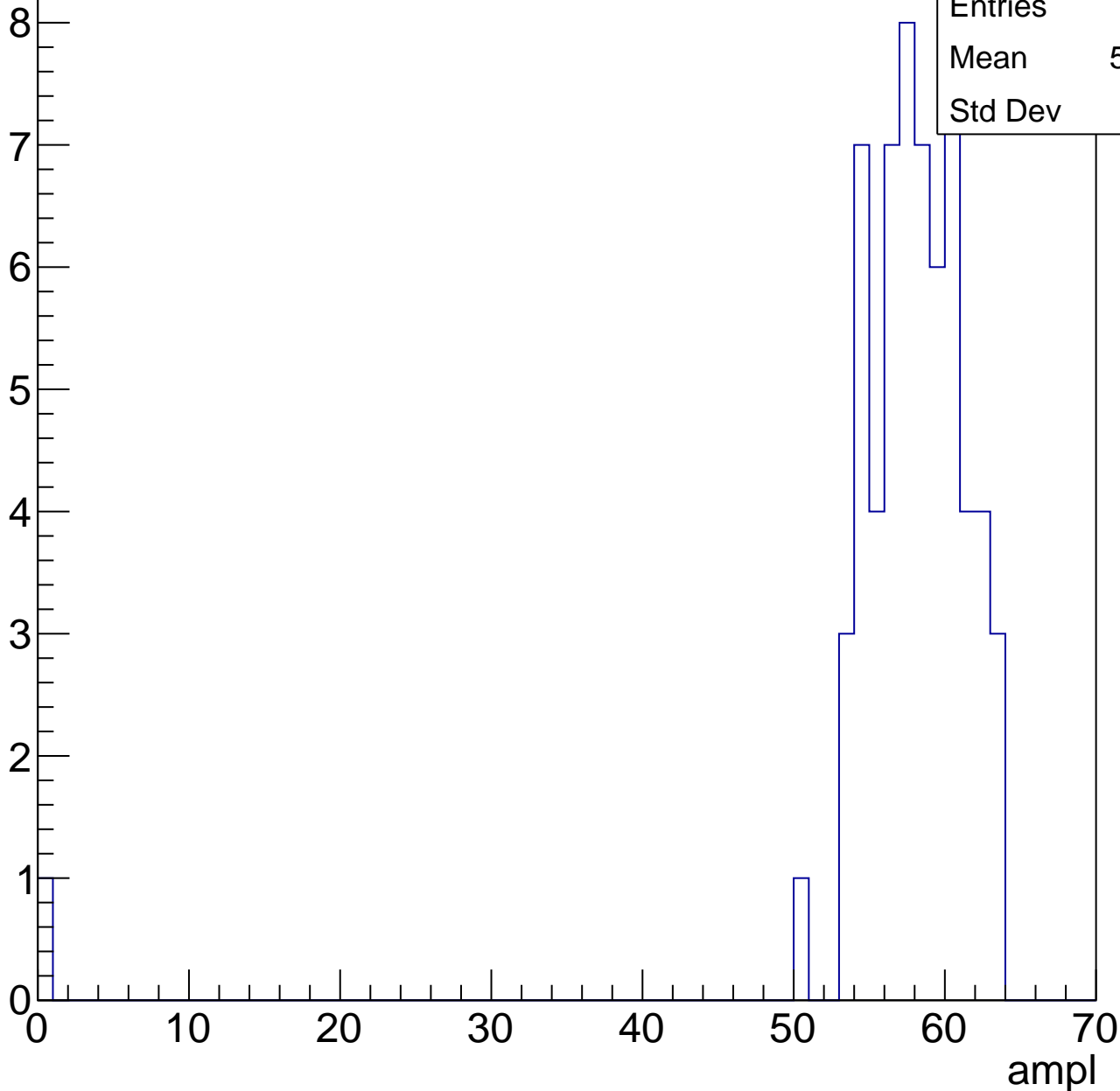


B1L003S, U13-ch71, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.76
Std Dev	7.77

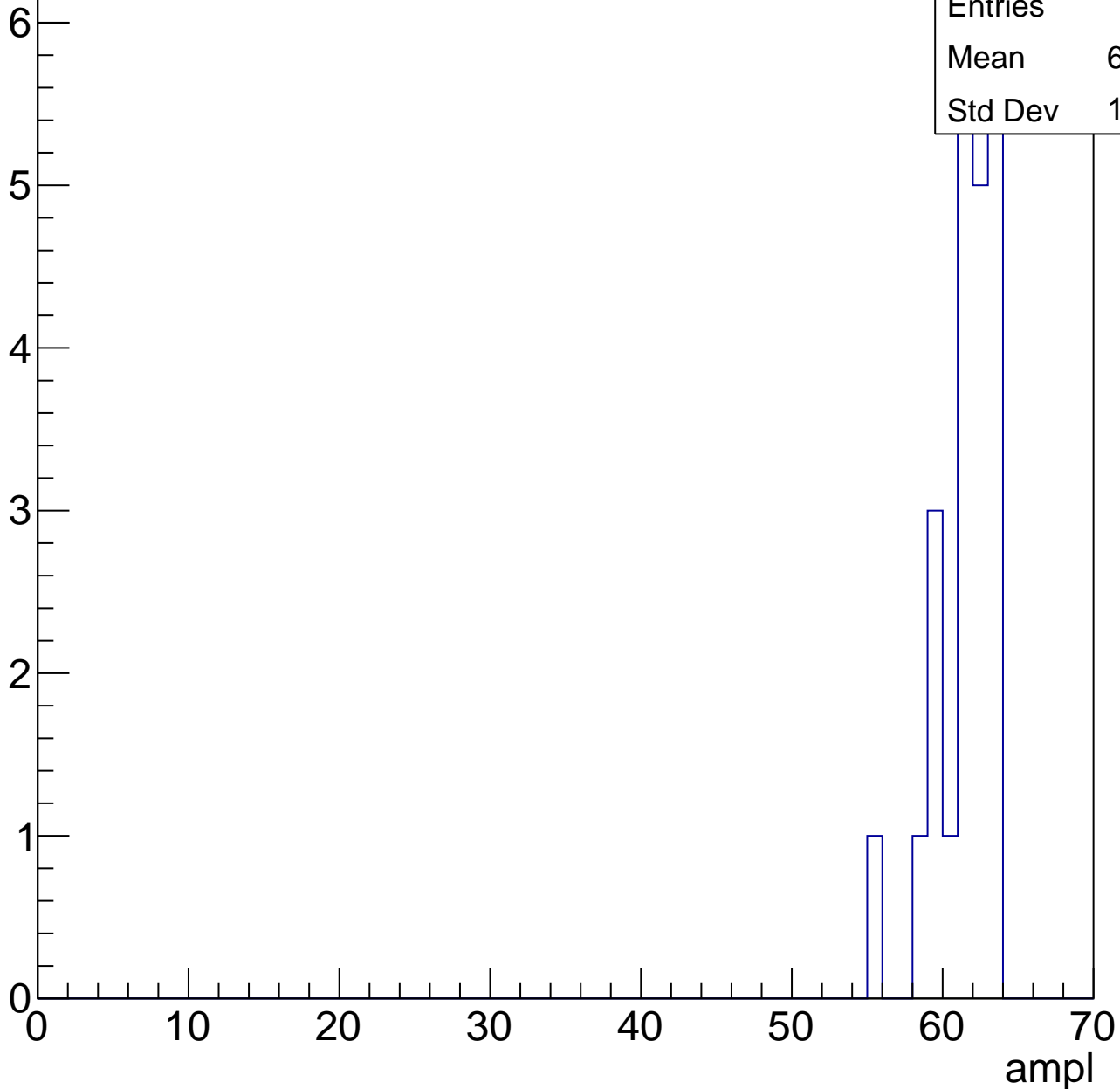


B1L003S, U13-ch71, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	61.04
Std Dev	1.944



B1L003S, U13-ch71, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch71, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch72, adc0

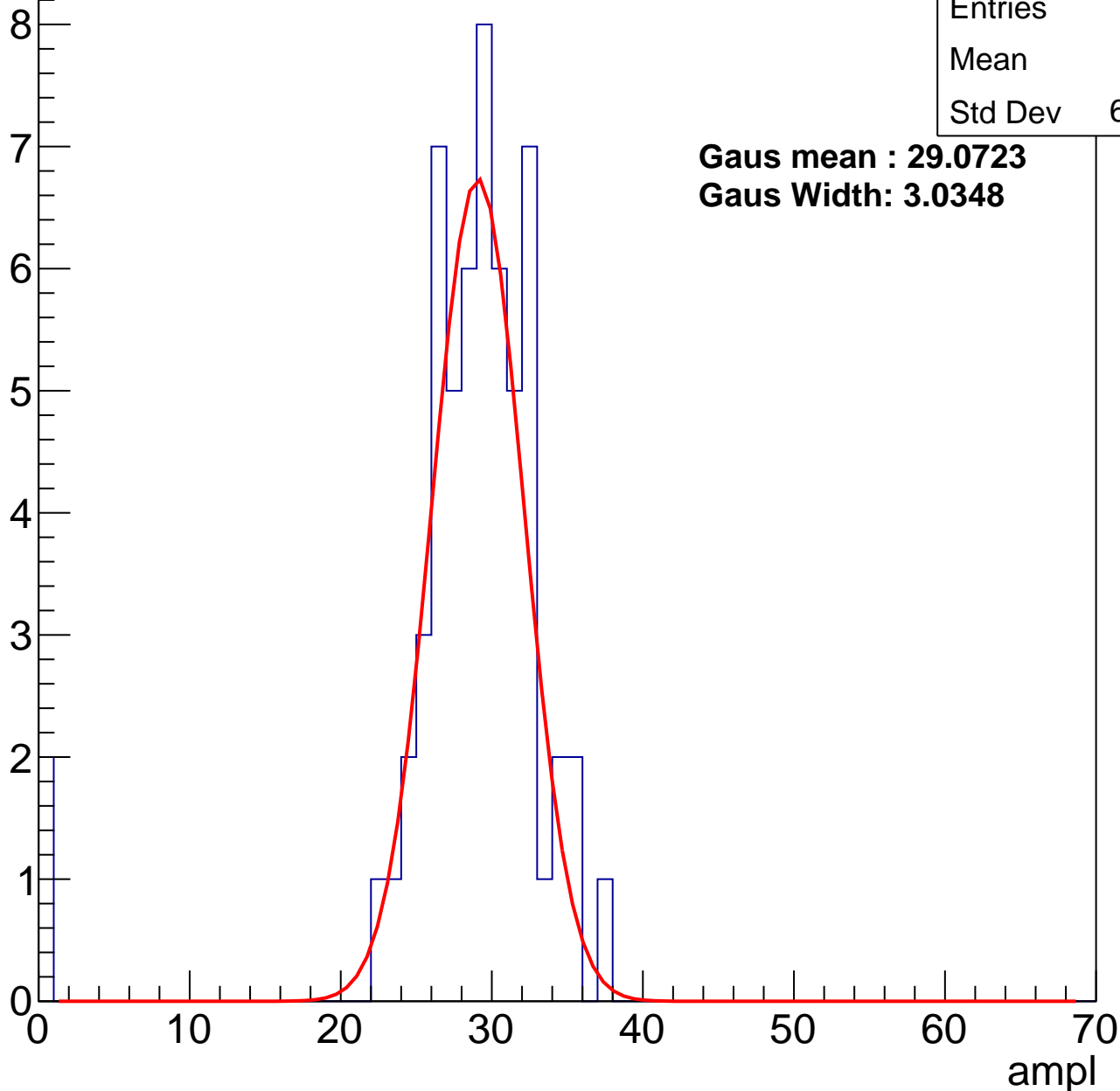
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28
Std Dev	6.087

Gaus mean : 29.0723

Gaus Width: 3.0348



B1L003S, U13-ch72, adc1

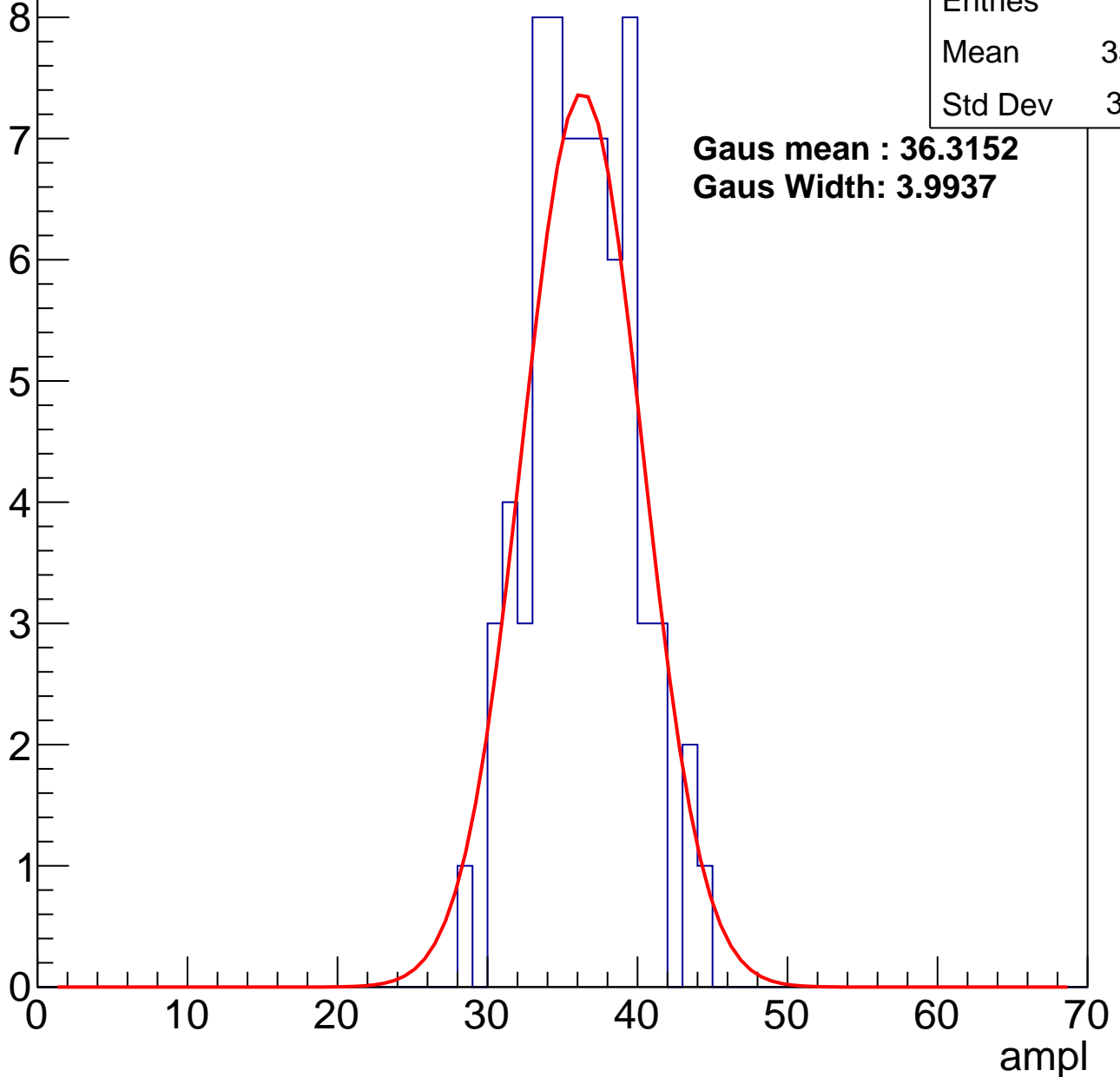
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.82
Std Dev	3.391

Gaus mean : 36.3152

Gaus Width: 3.9937



B1L003S, U13-ch72, adc2

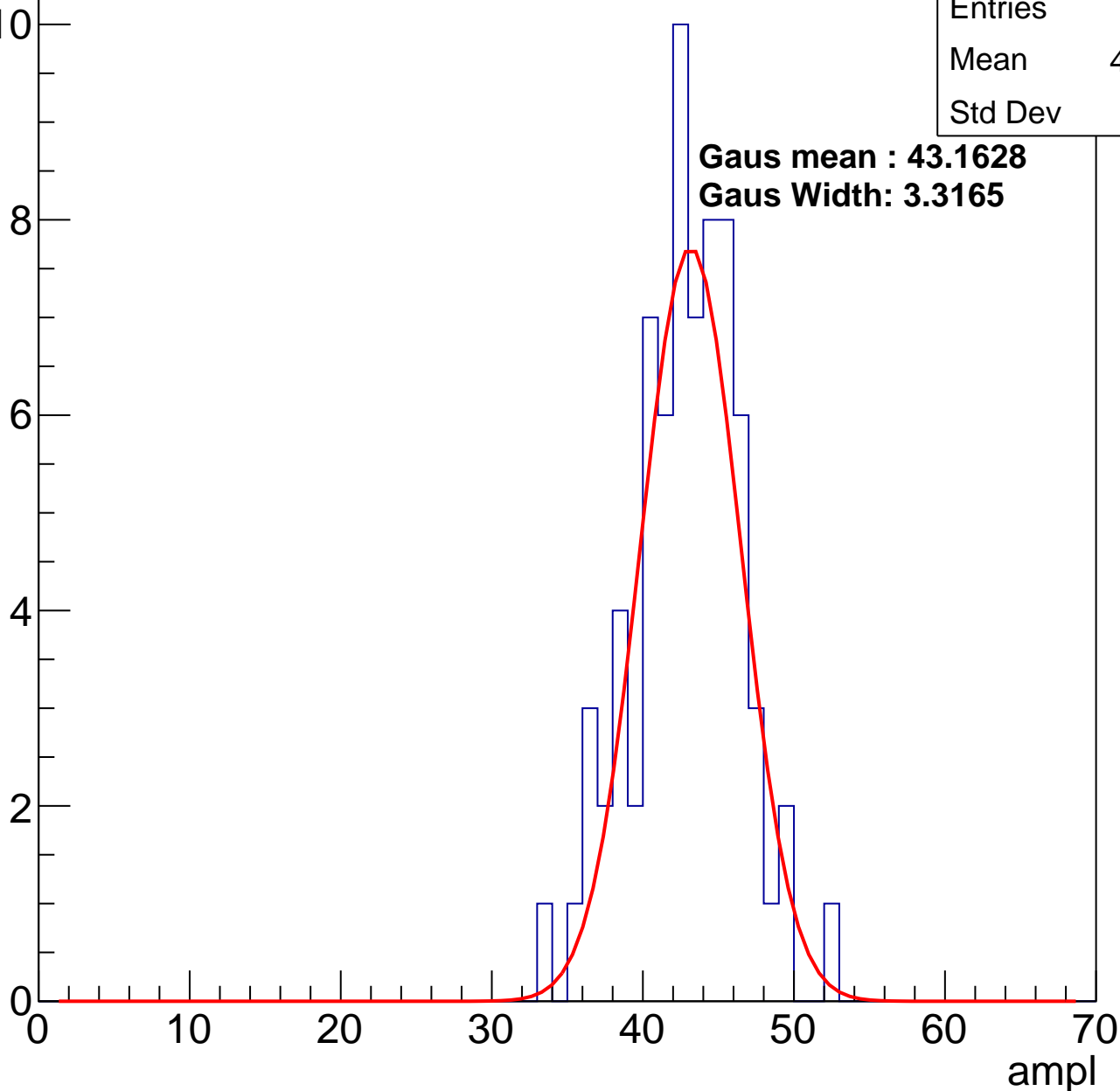
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.42
Std Dev	3.55

Gaus mean : 43.1628

Gaus Width: 3.3165

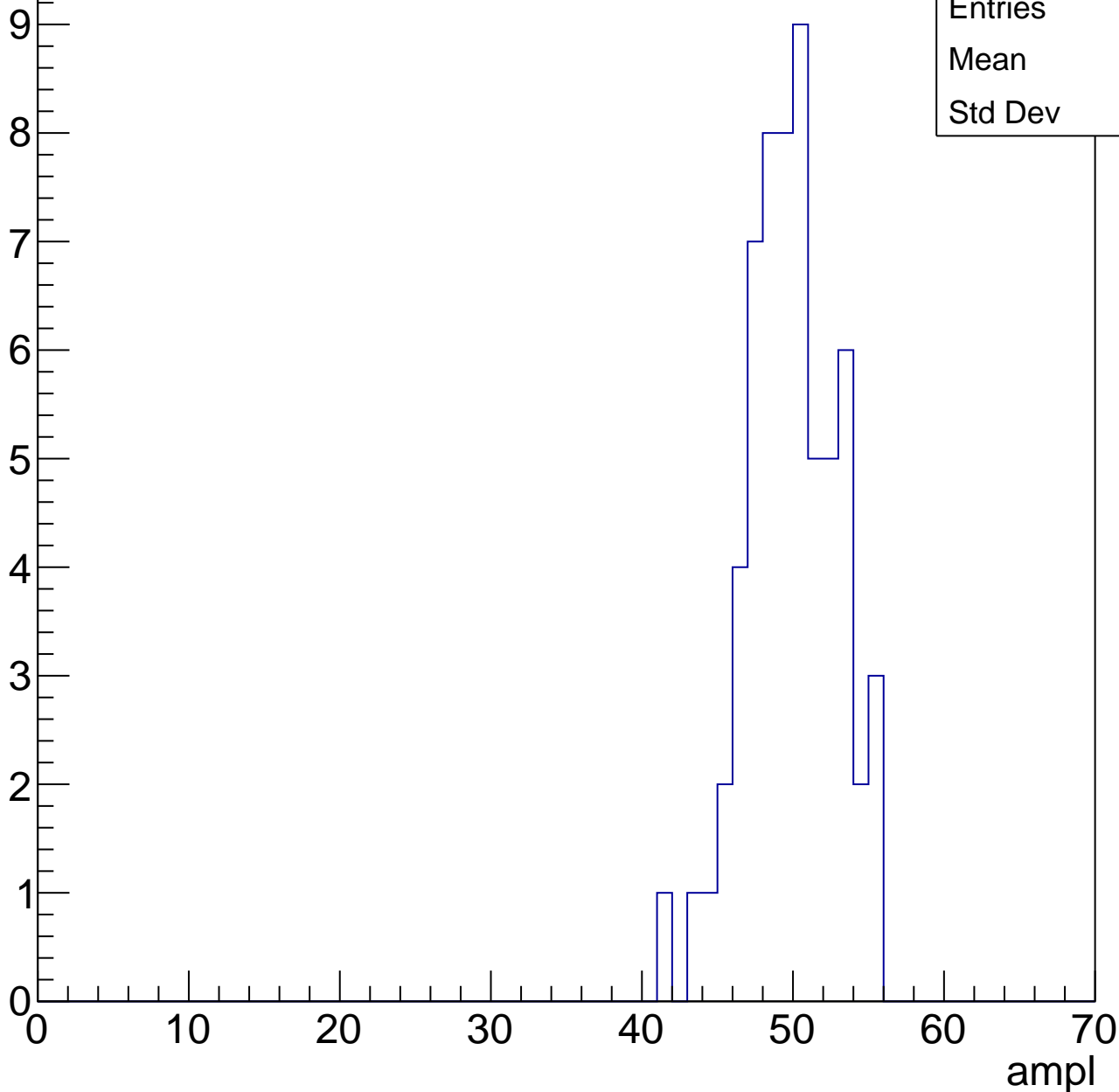


B1L003S, U13-ch72, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.4
Std Dev	2.97

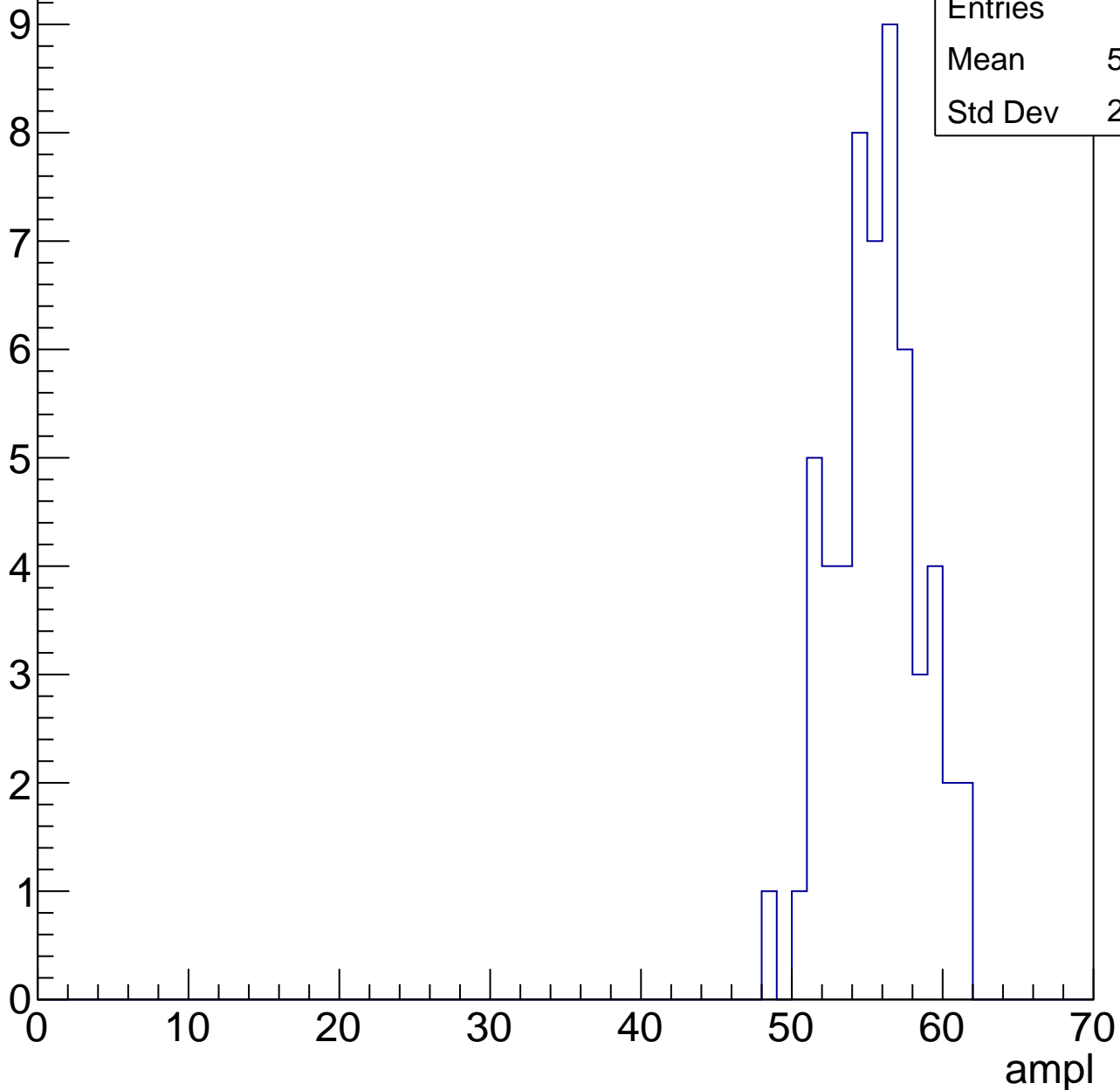


B1L003S, U13-ch72, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.14
Std Dev	2.862

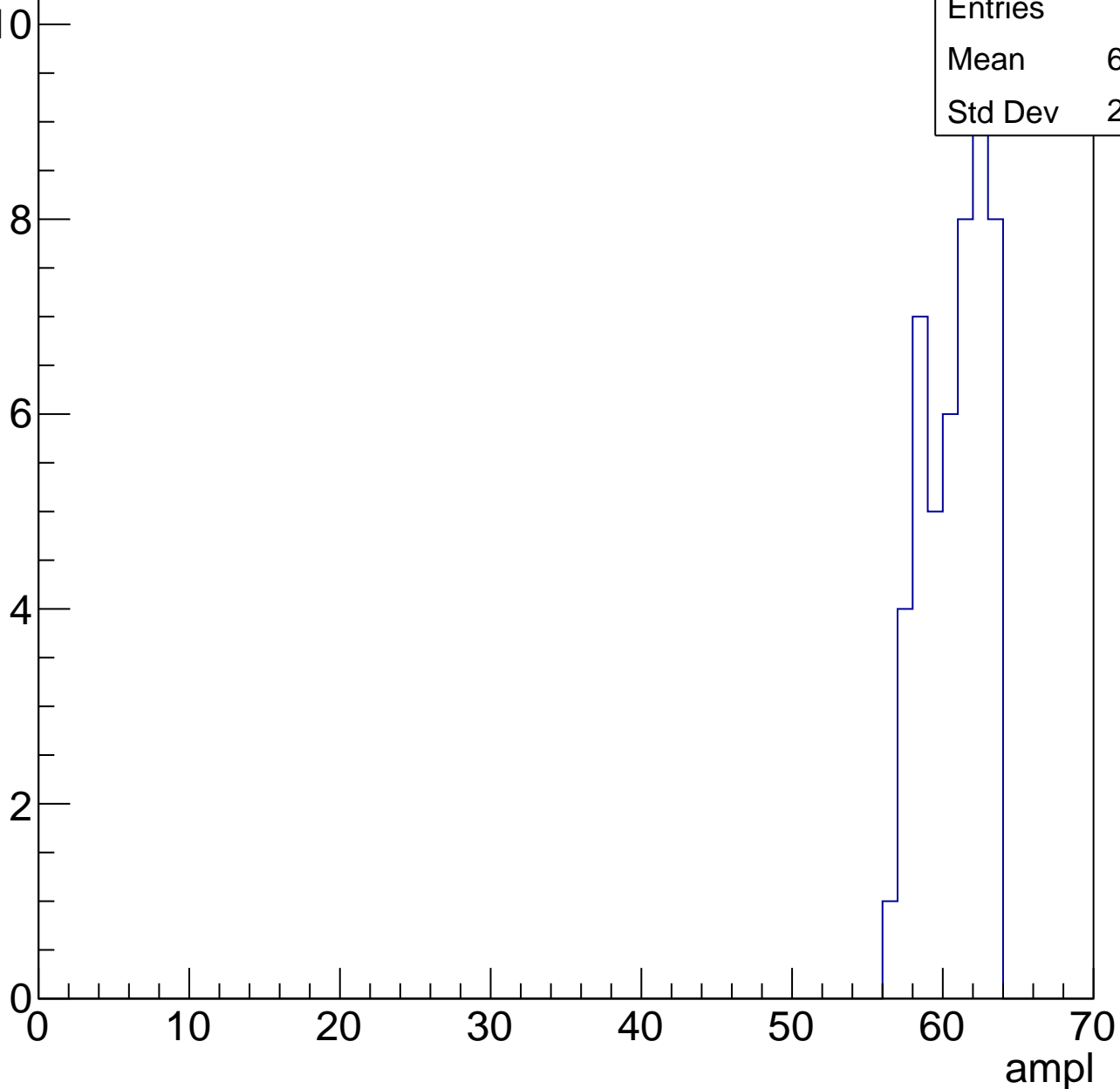


B1L003S, U13-ch72, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	60.35
Std Dev	2.016



B1L003S, U13-ch72, adc6

calib_packv5_042523_0143.root, FC#13, port D2

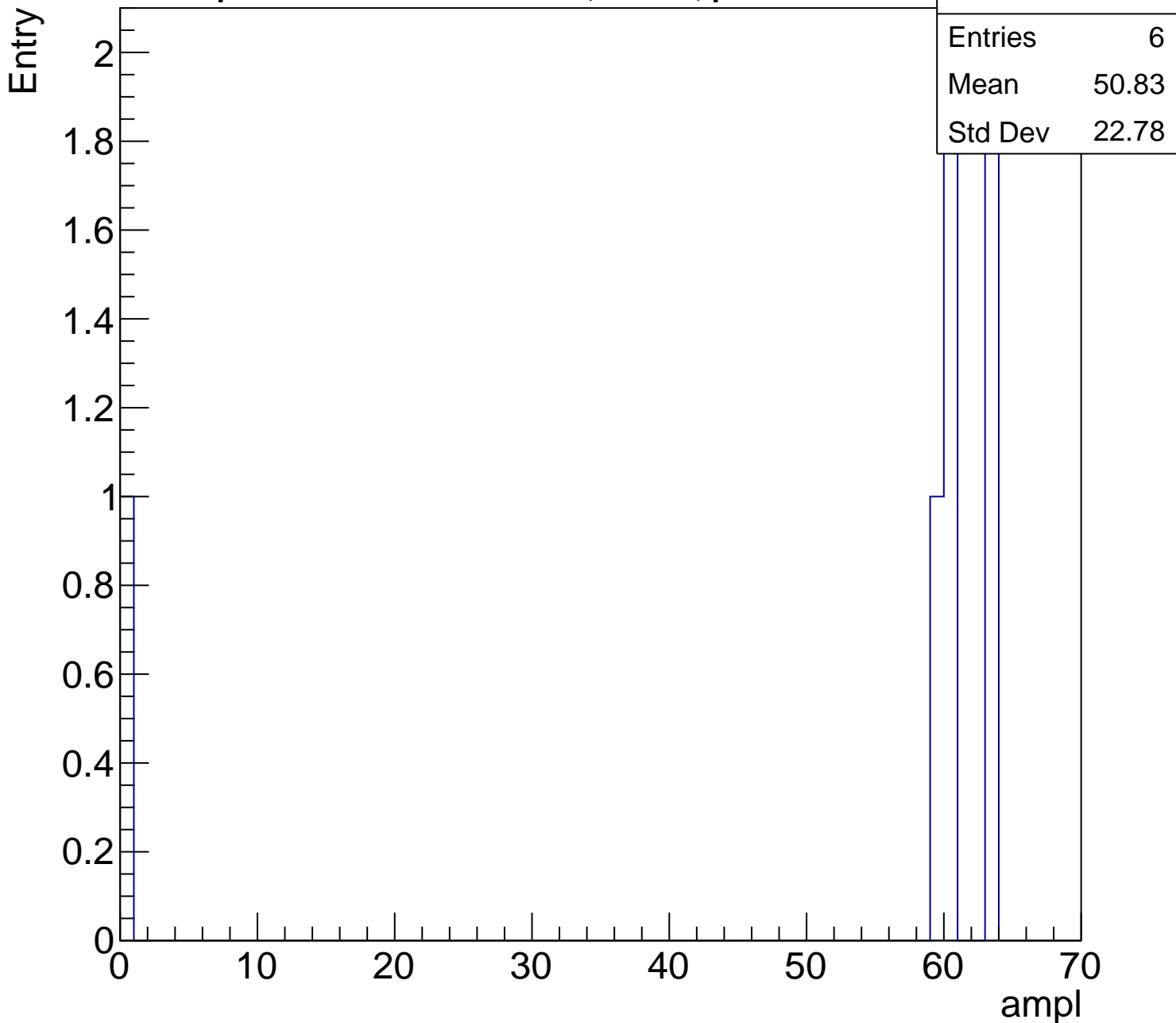
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.83
Std Dev	22.78

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch72, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch73, adc0

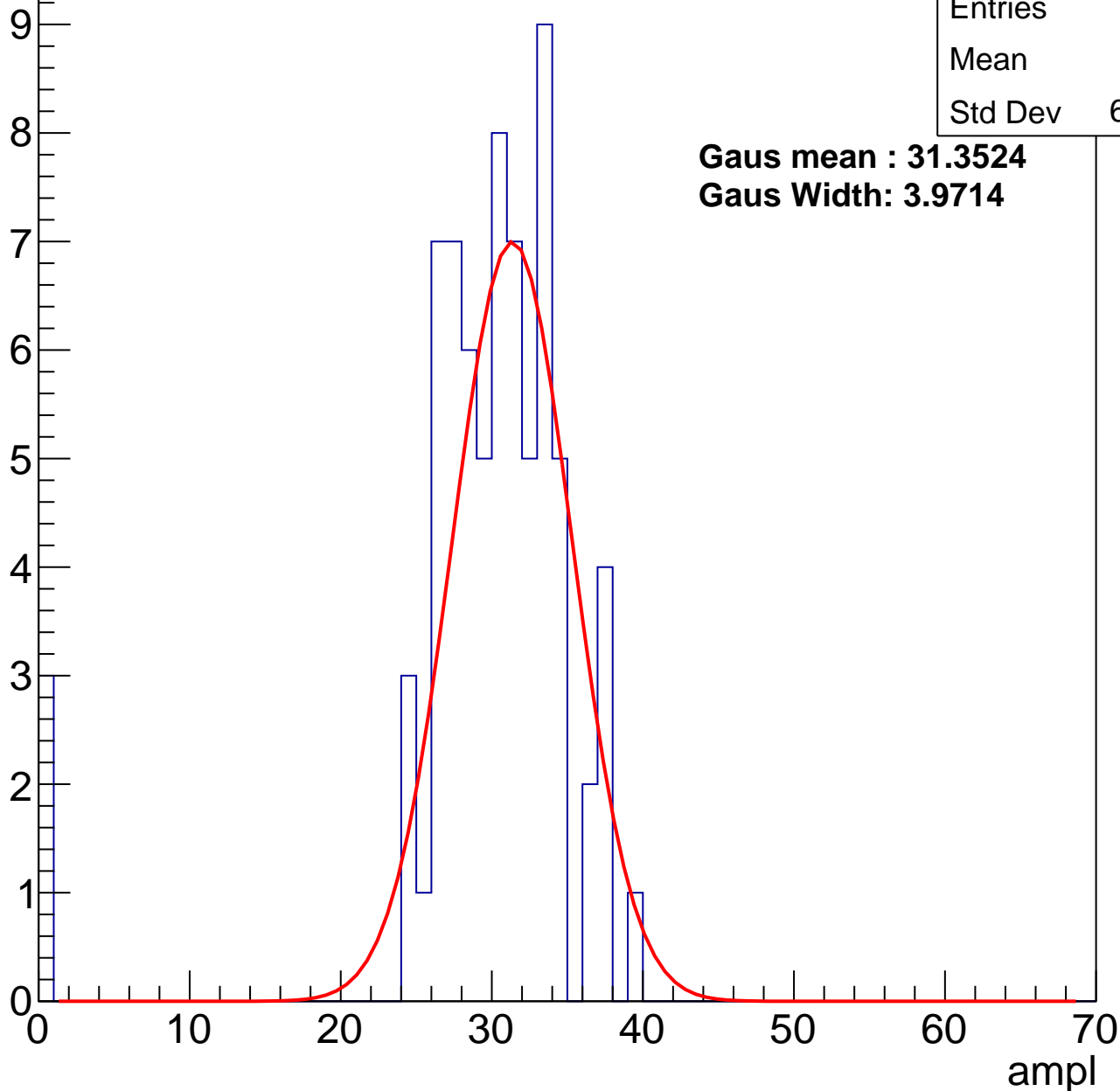
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.1
Std Dev	6.938

Gaus mean : 31.3524

Gaus Width: 3.9714

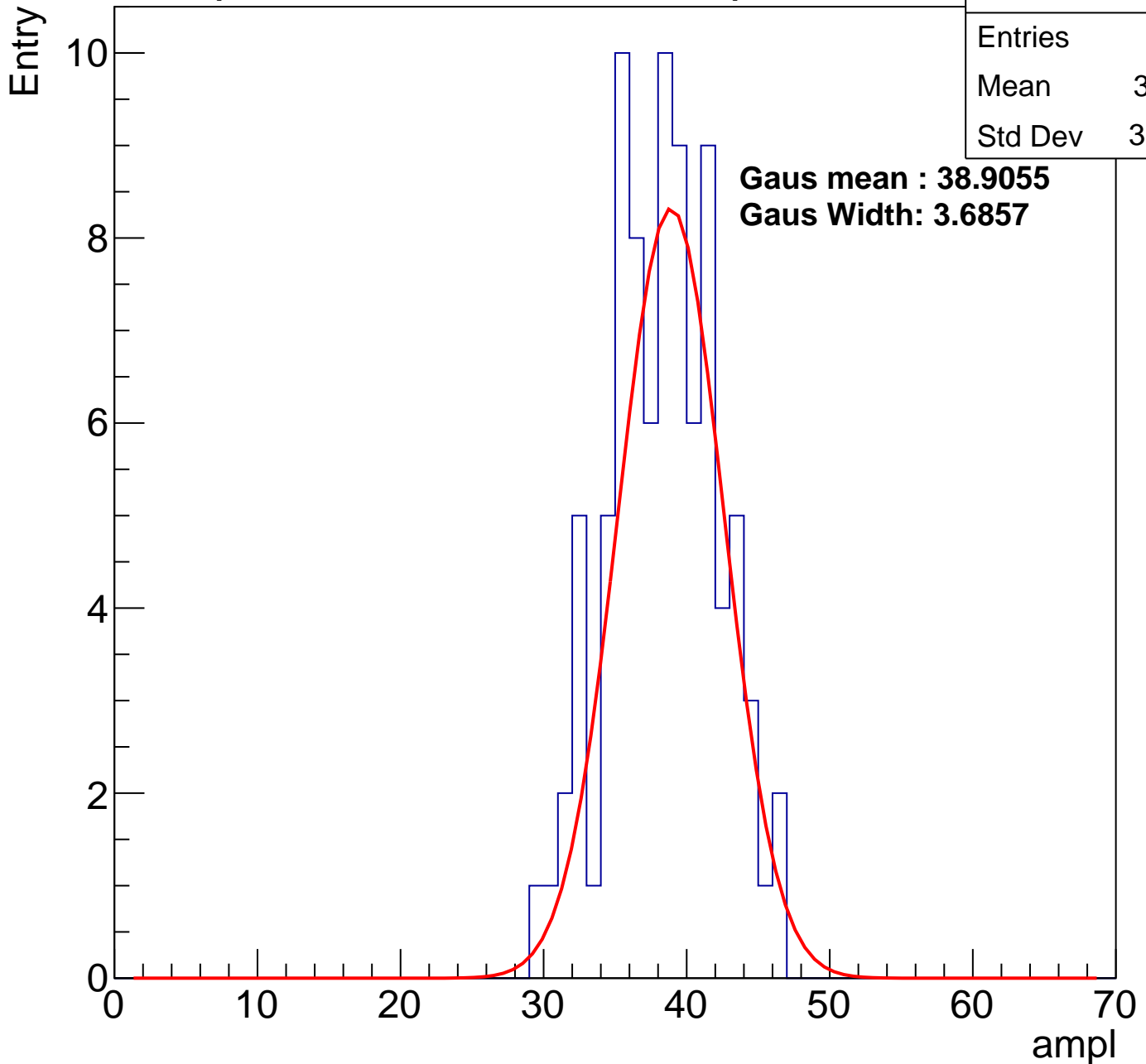


B1L003S, U13-ch73, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	88
Mean	37.91
Std Dev	3.759

Gaus mean : 38.9055
Gaus Width: 3.6857



B1L003S, U13-ch73, adc2

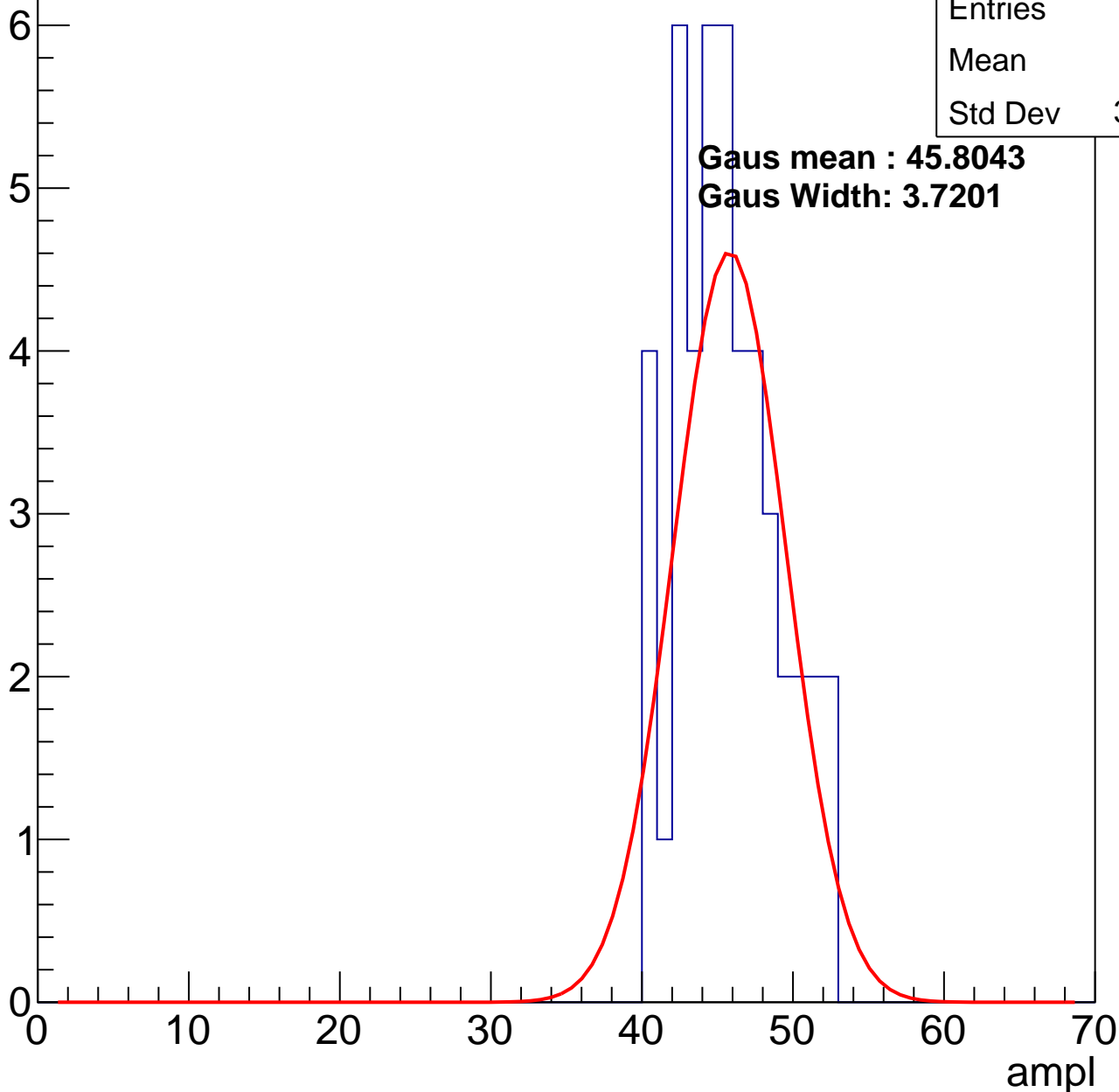
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	45.2
Std Dev	3.261

Gaus mean : 45.8043

Gaus Width: 3.7201

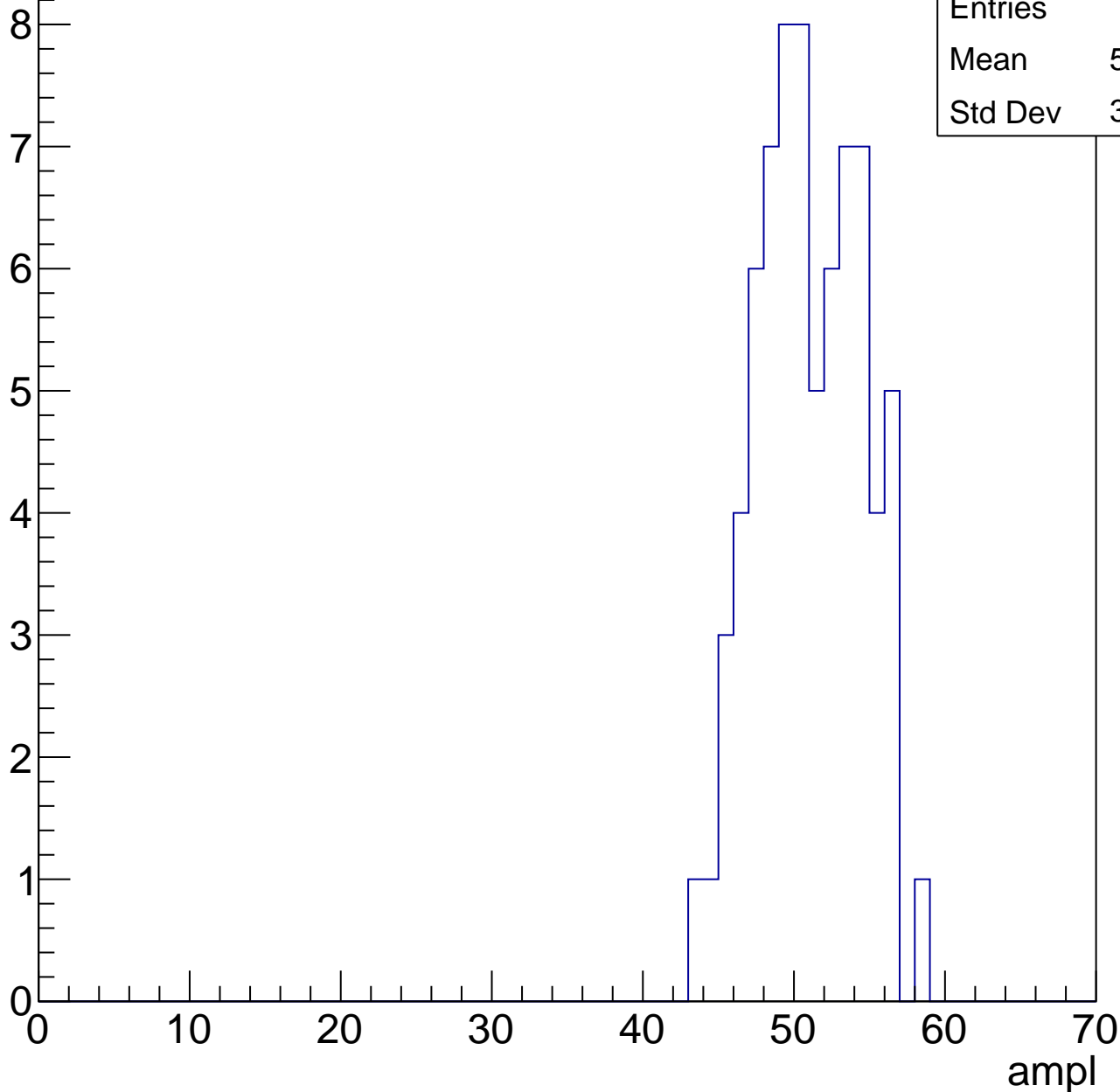


B1L003S, U13-ch73, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	50.55
Std Dev	3.392

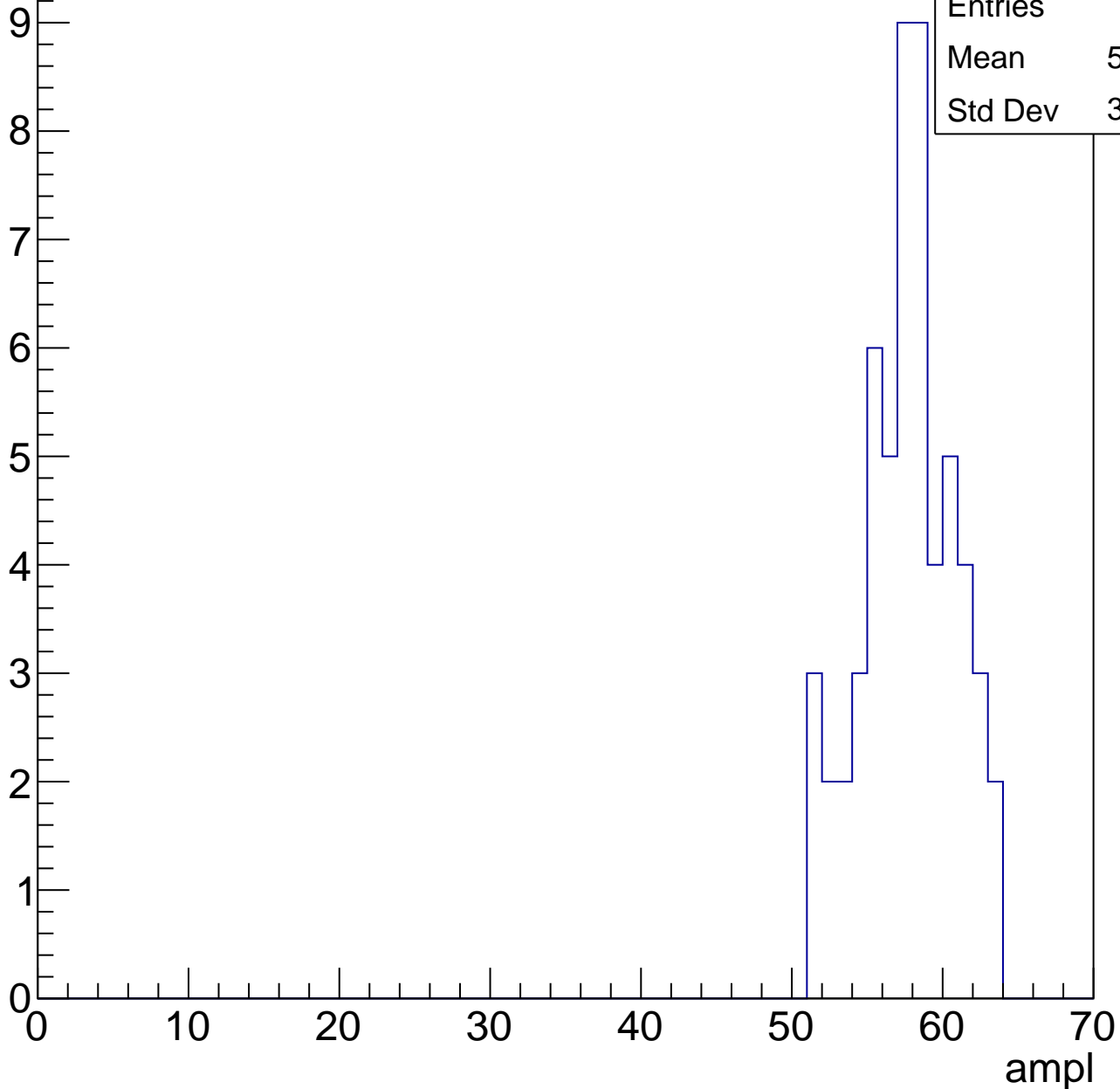


B1L003S, U13-ch73, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57.23
Std Dev	3.032

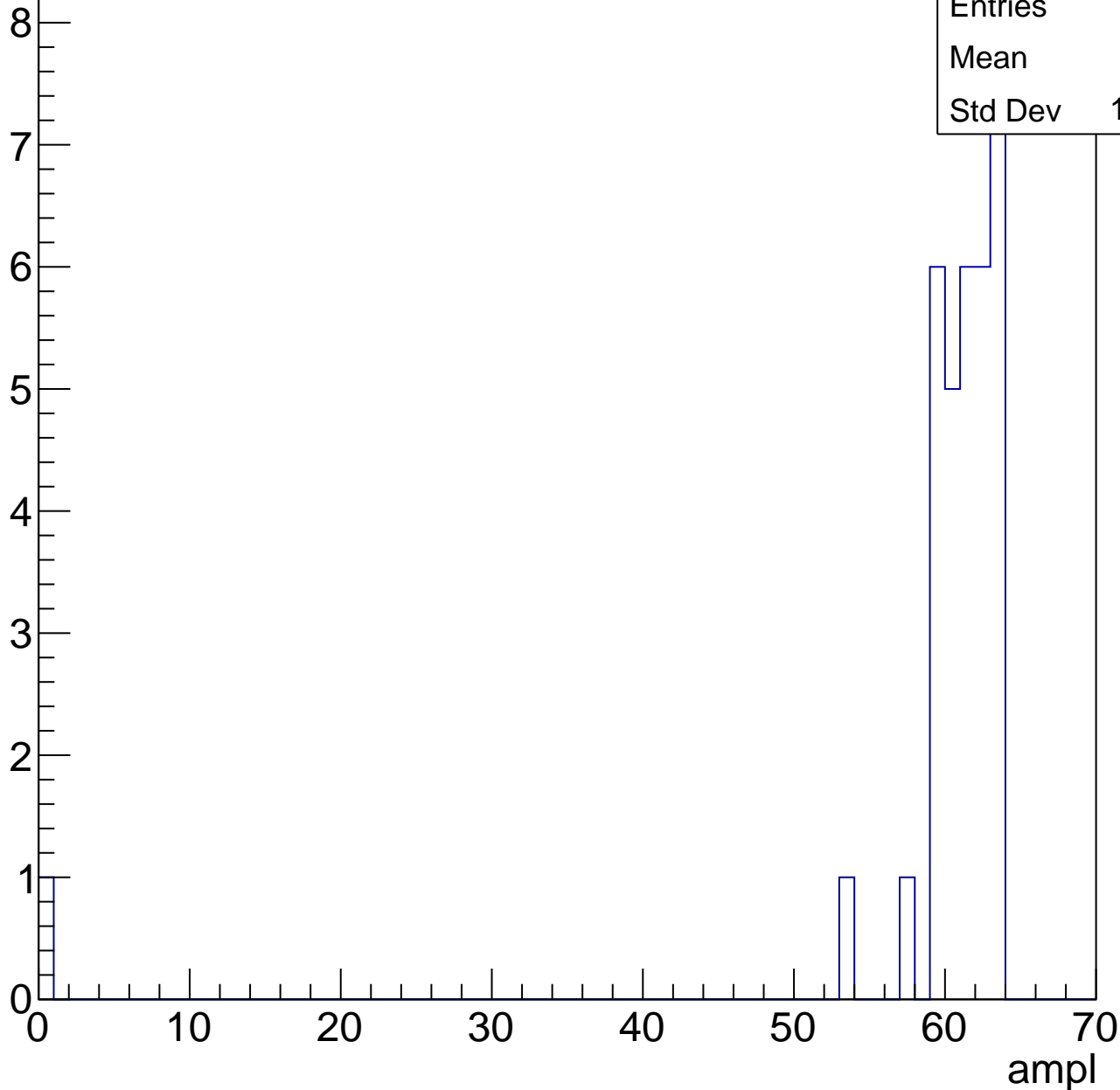


B1L003S, U13-ch73, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	59
Std Dev	10.48



B1L003S, U13-ch73, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

B1L003S, U13-ch73, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L003S, U13-ch74, adc0

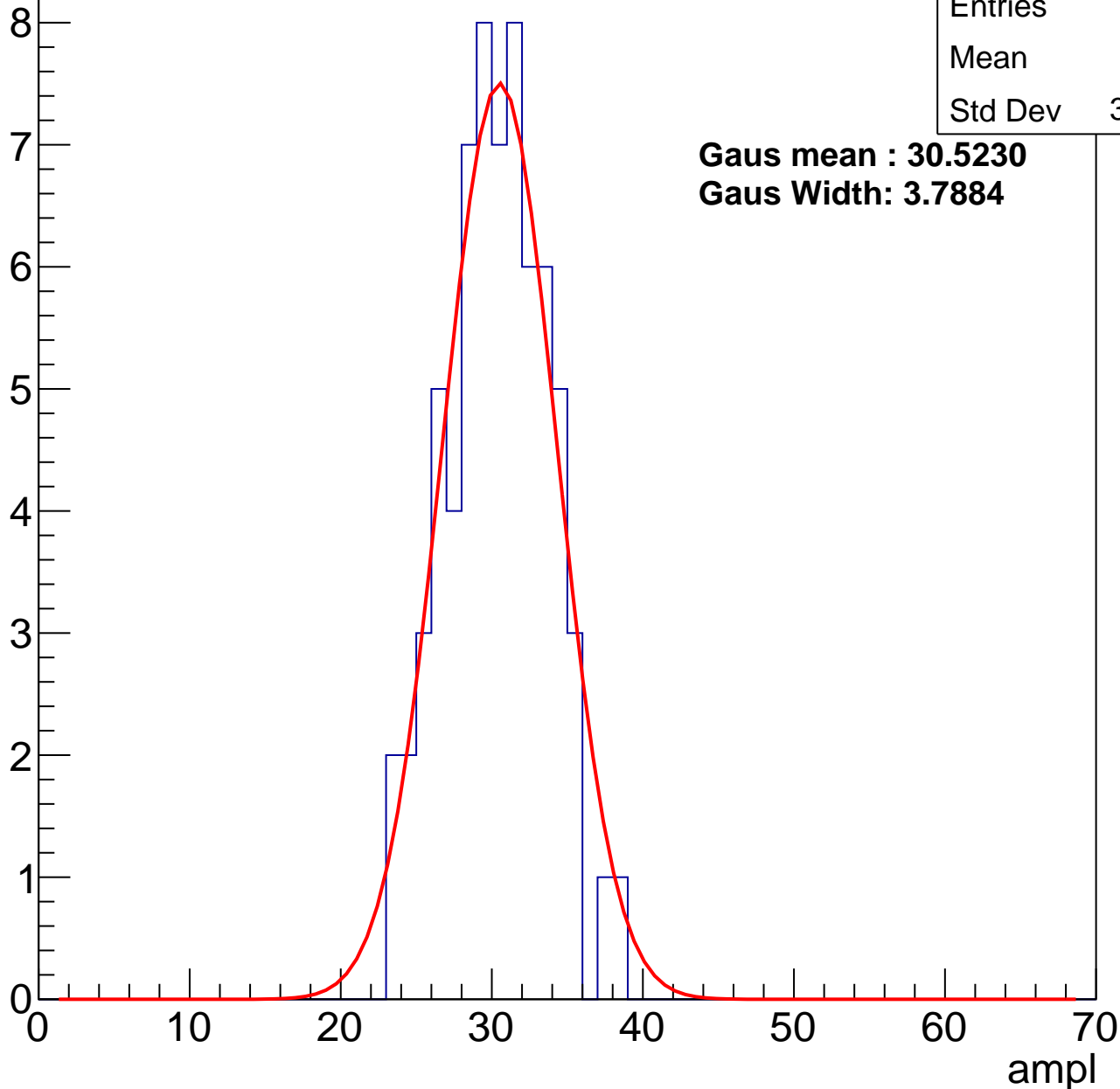
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.9
Std Dev	3.322

Gaus mean : 30.5230

Gaus Width: 3.7884



B1L003S, U13-ch74, adc1

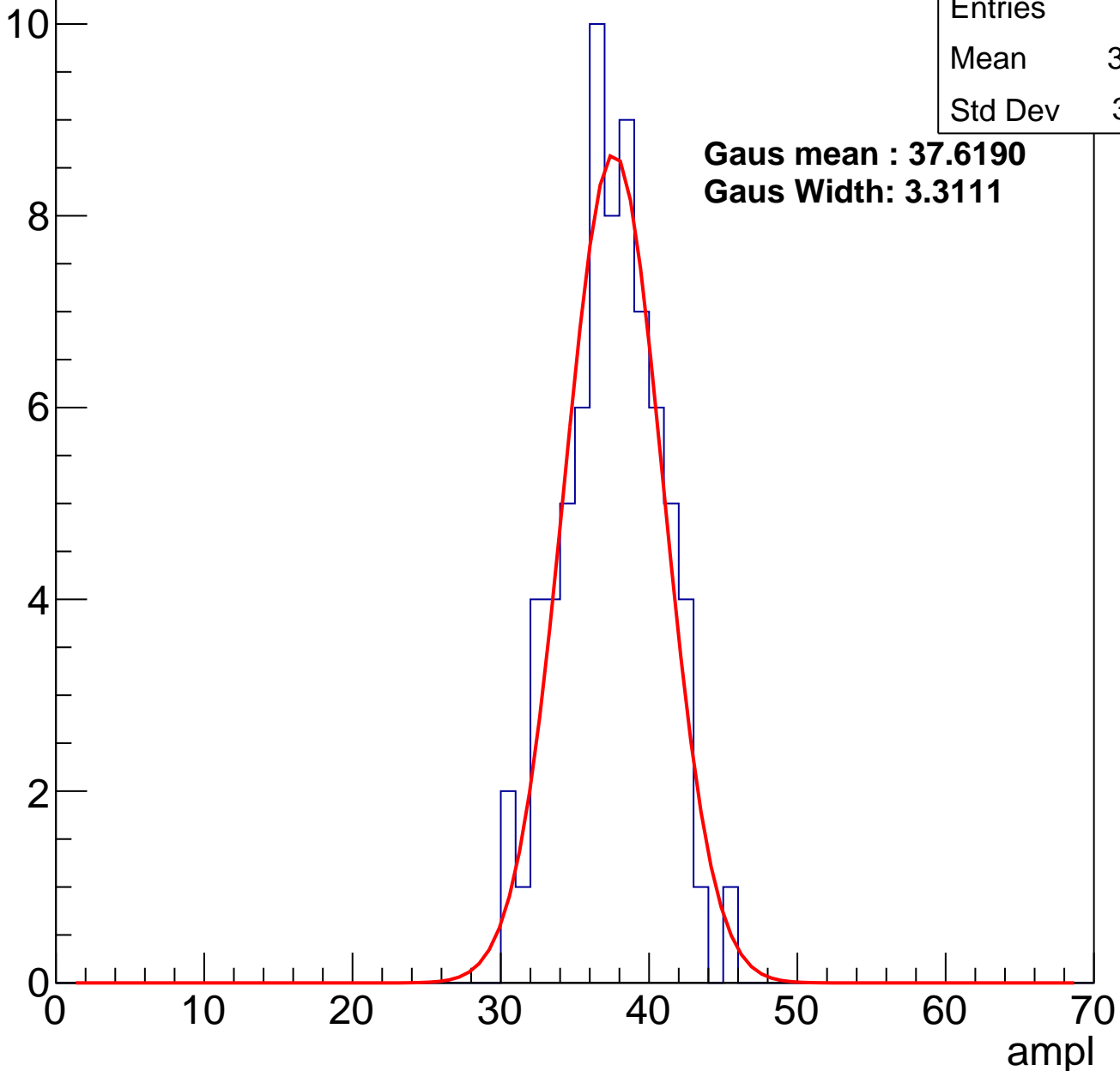
calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	37.03
Std Dev	3.201

Gaus mean : 37.6190

Gaus Width: 3.3111

Entry



B1L003S, U13-ch74, adc2

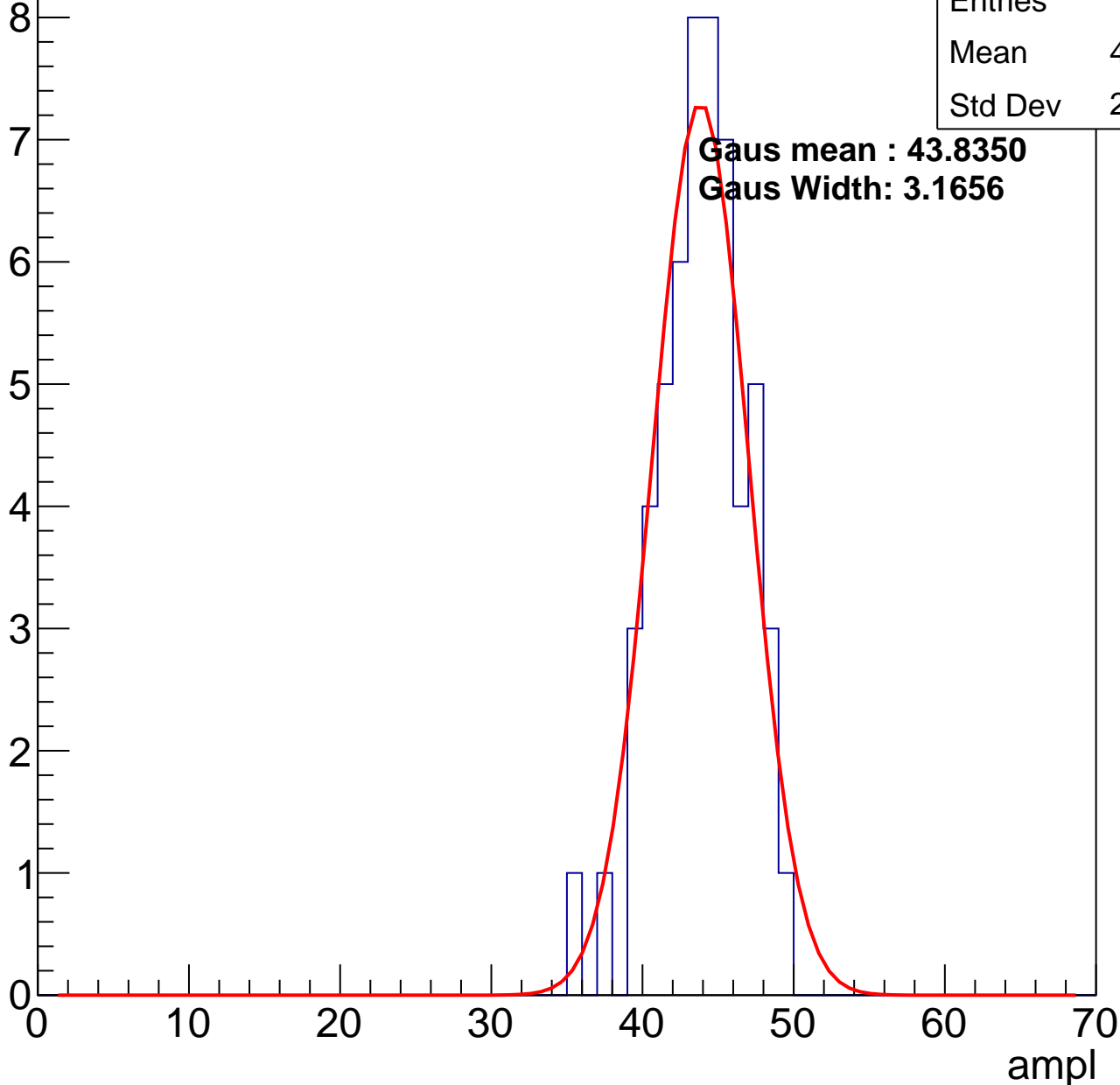
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.38
Std Dev	2.882

Gaus mean : 43.8350

Gaus Width: 3.1656

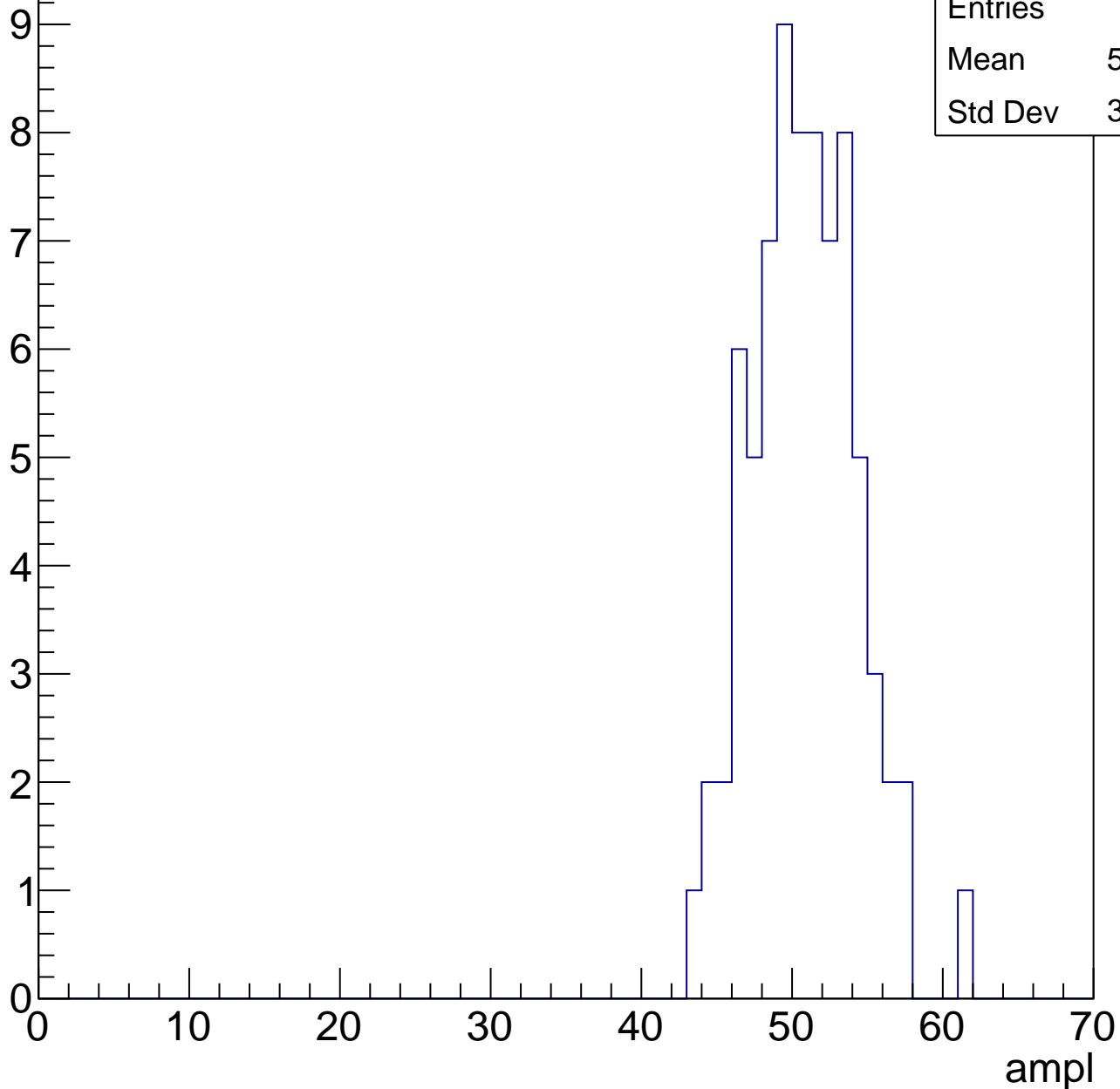


B1L003S, U13-ch74, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	50.36
Std Dev	3.429

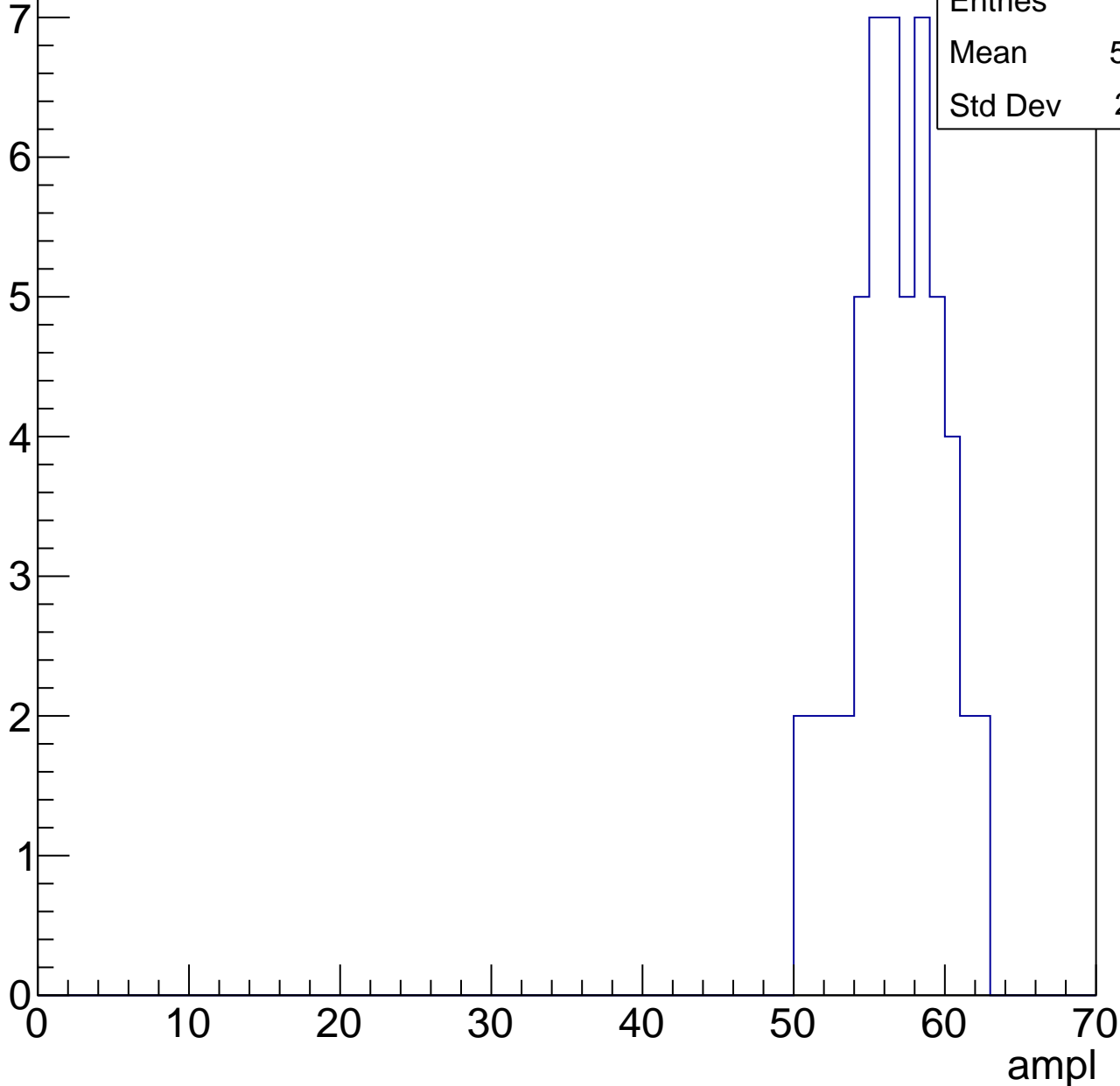


B1L003S, U13-ch74, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	56.37
Std Dev	2.961

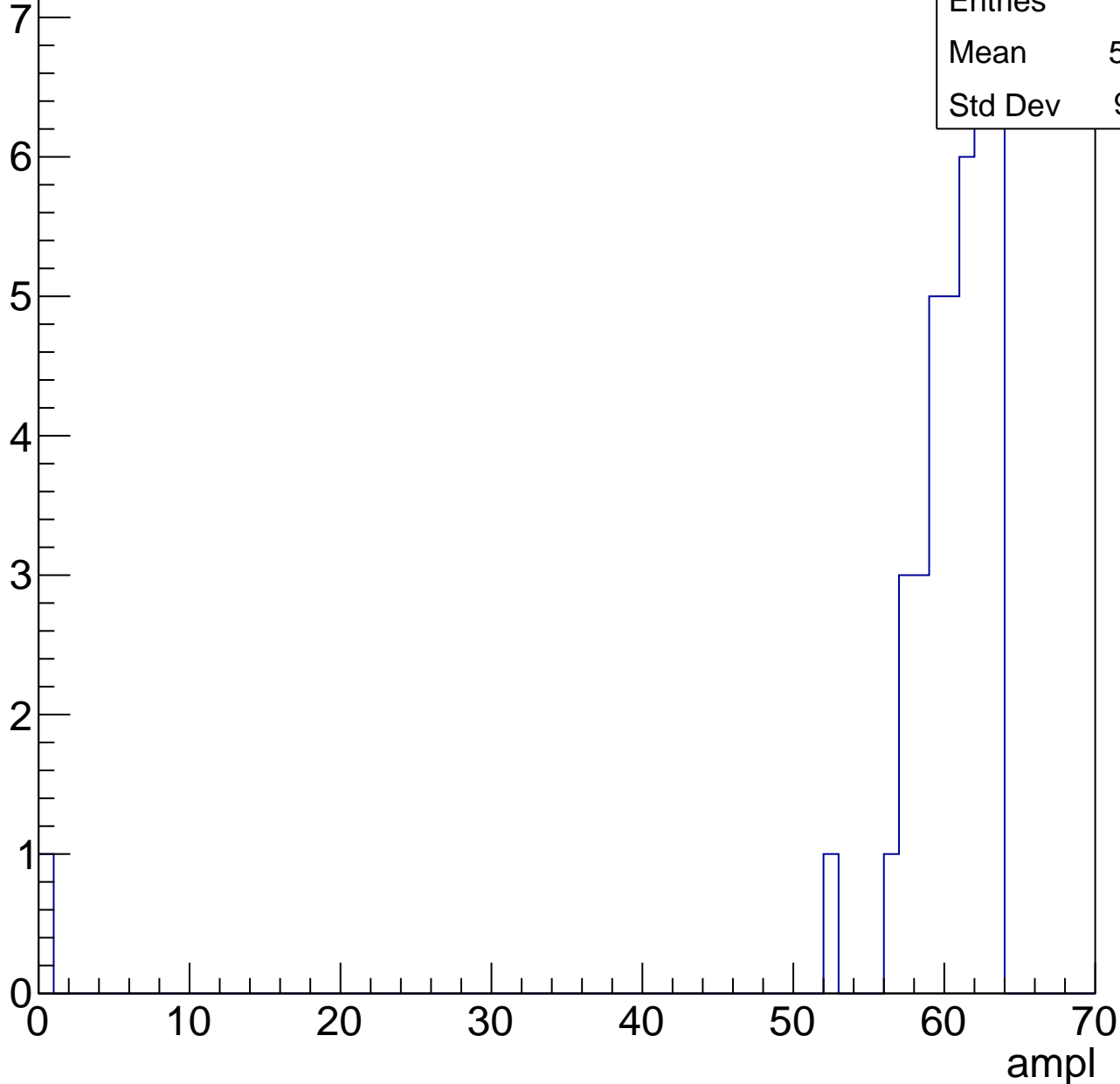


B1L003S, U13-ch74, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

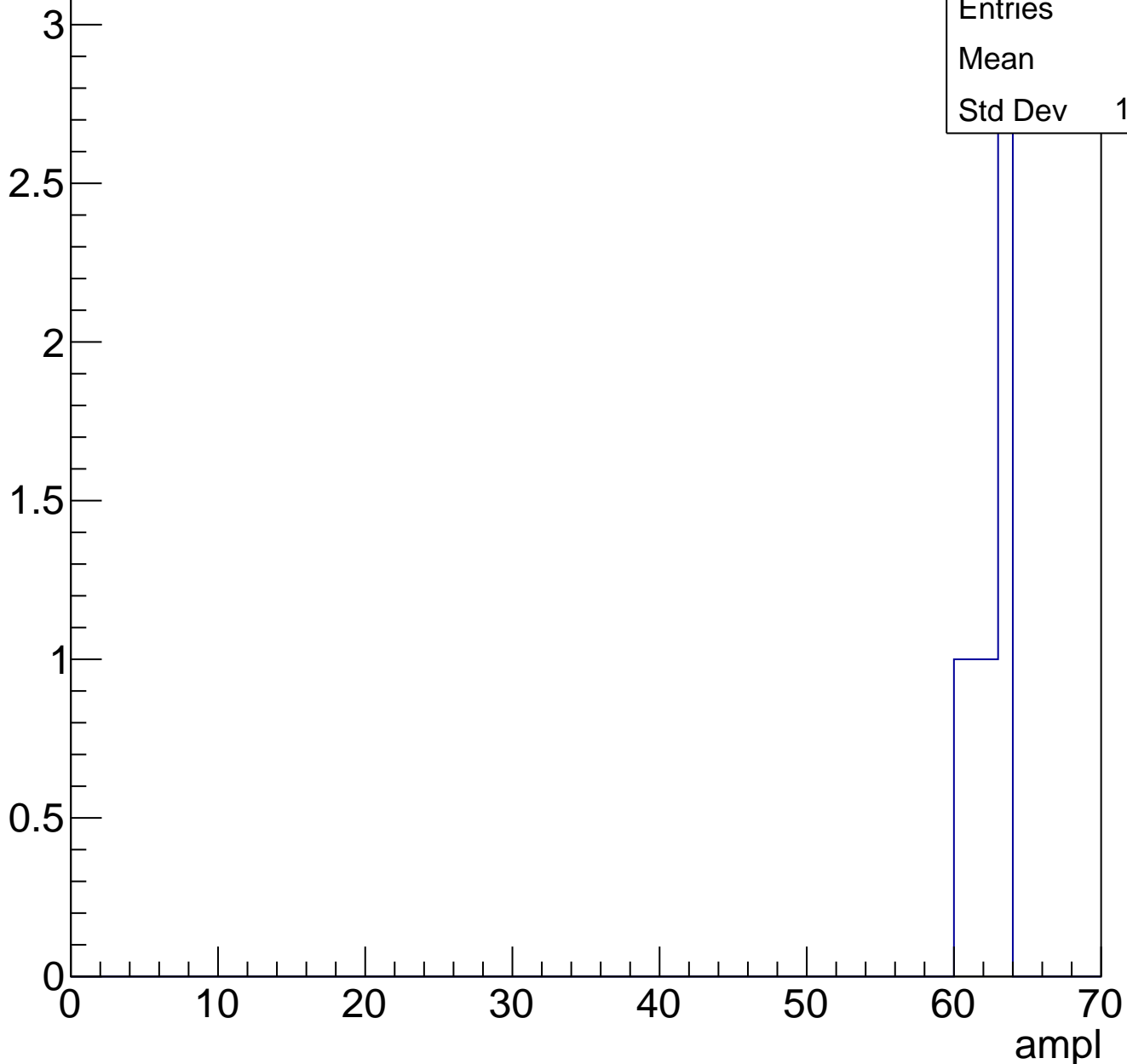
Entries	39
Mean	58.69
Std Dev	9.811



B1L003S, U13-ch74, adc6

calib_packv5_042523_0143.root, FC#13, port D2

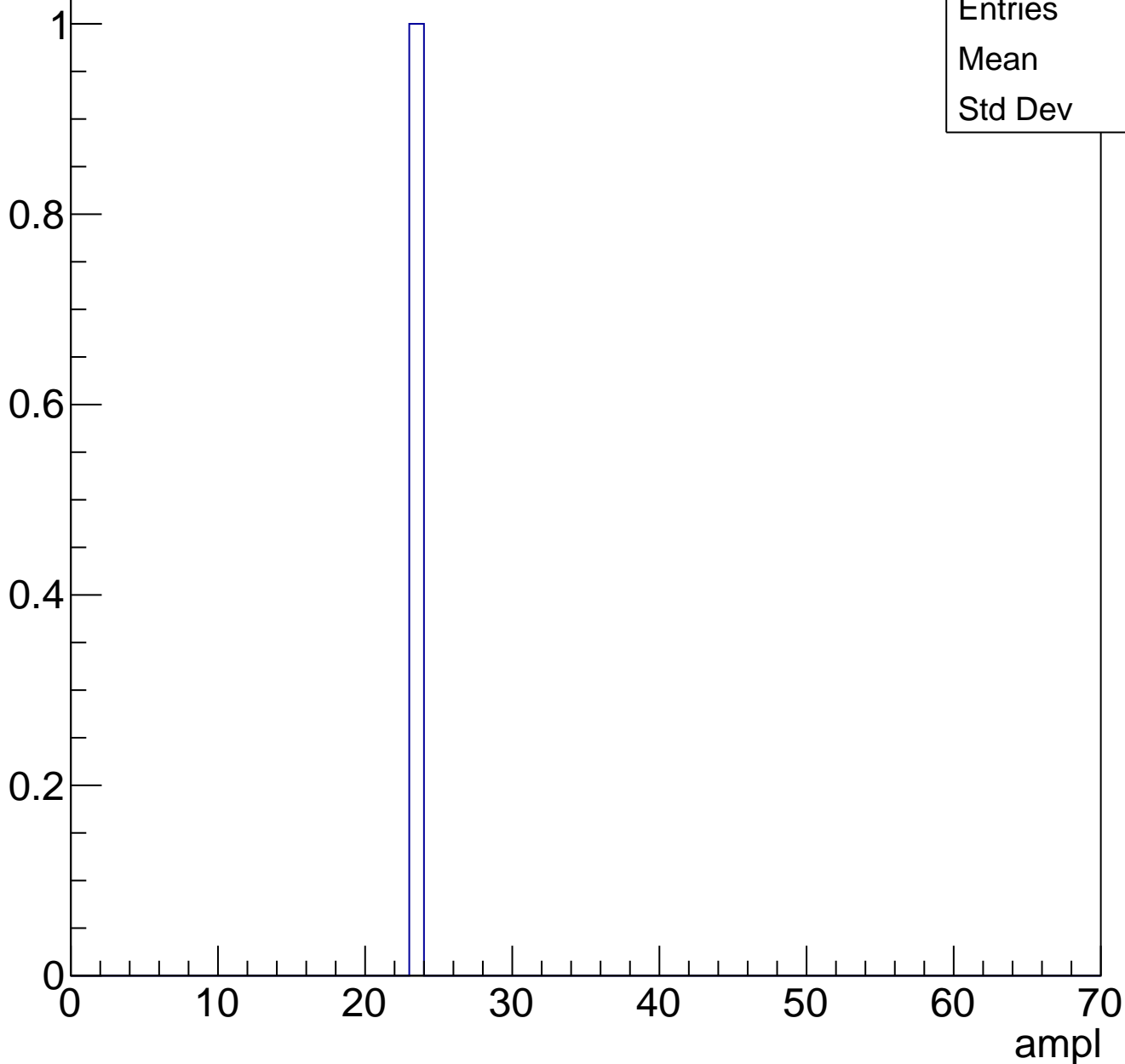
Entry



B1L003S, U13-ch74, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



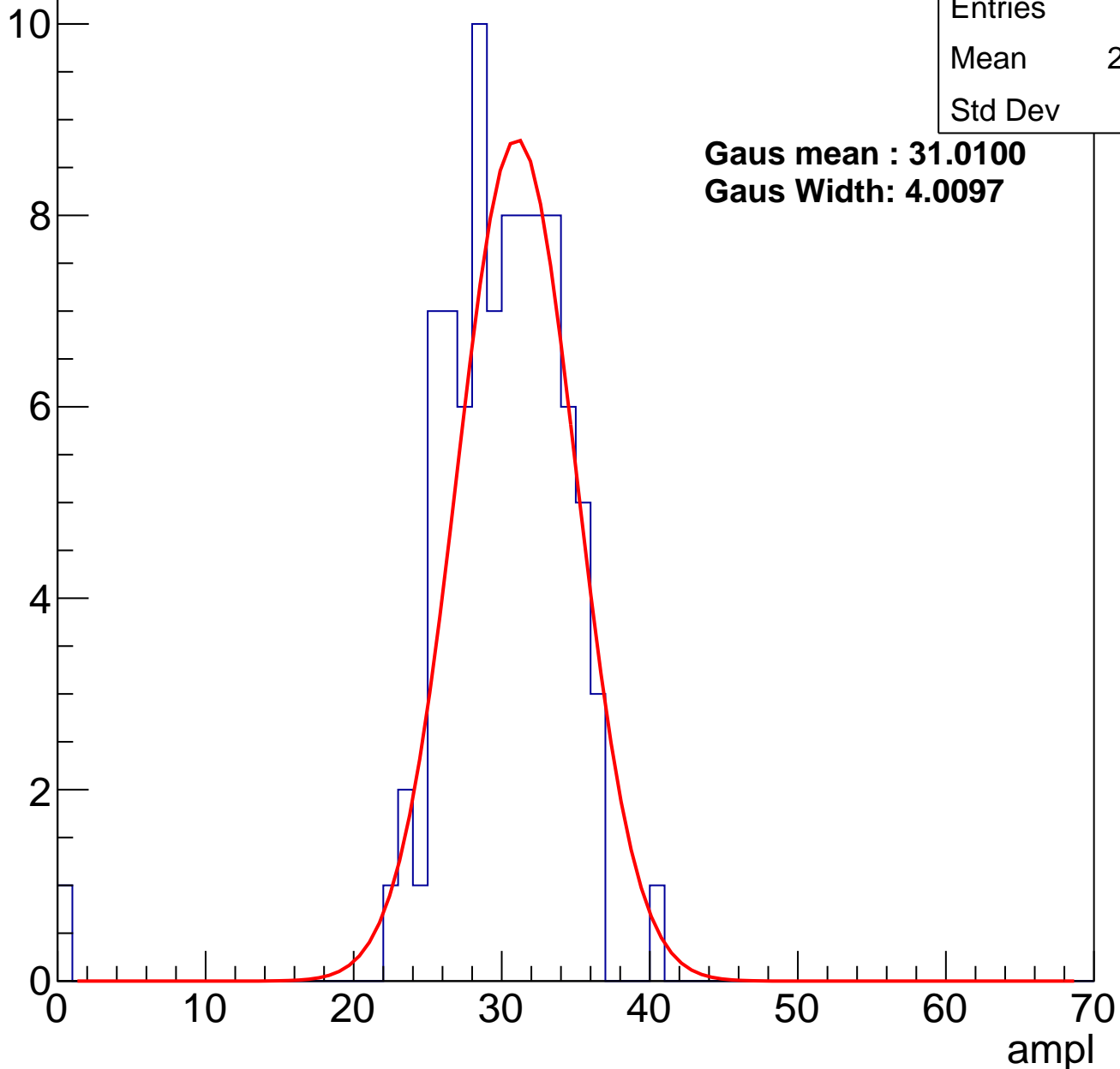
B1L003S, U13-ch75, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	89
Mean	29.54
Std Dev	4.75

Gaus mean : 31.0100
Gaus Width: 4.0097

Entry



B1L003S, U13-ch75, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	37.75
Std Dev	3.58

Gaus mean : 37.9496

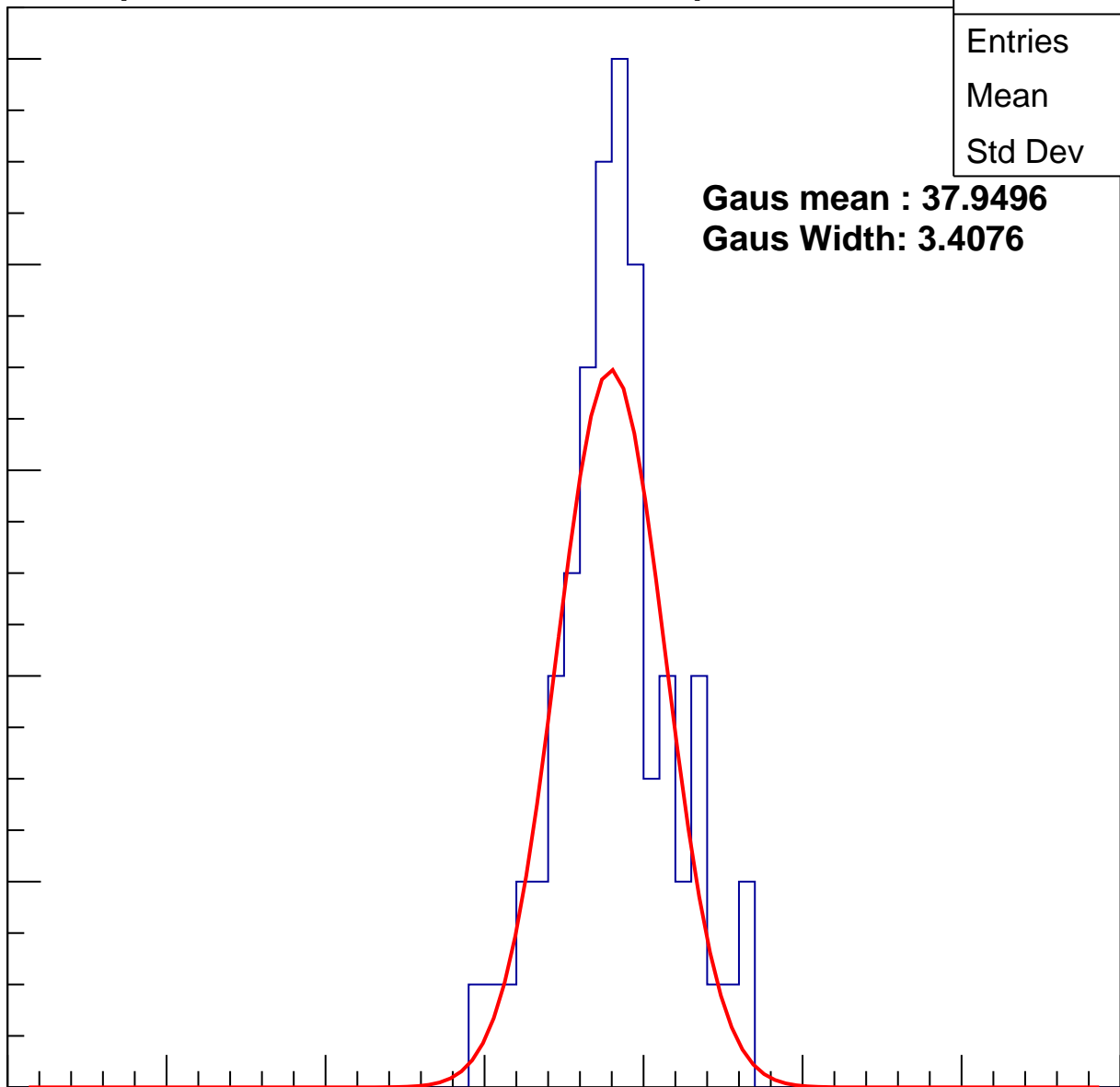
Gaus Width: 3.4076

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch75, adc2

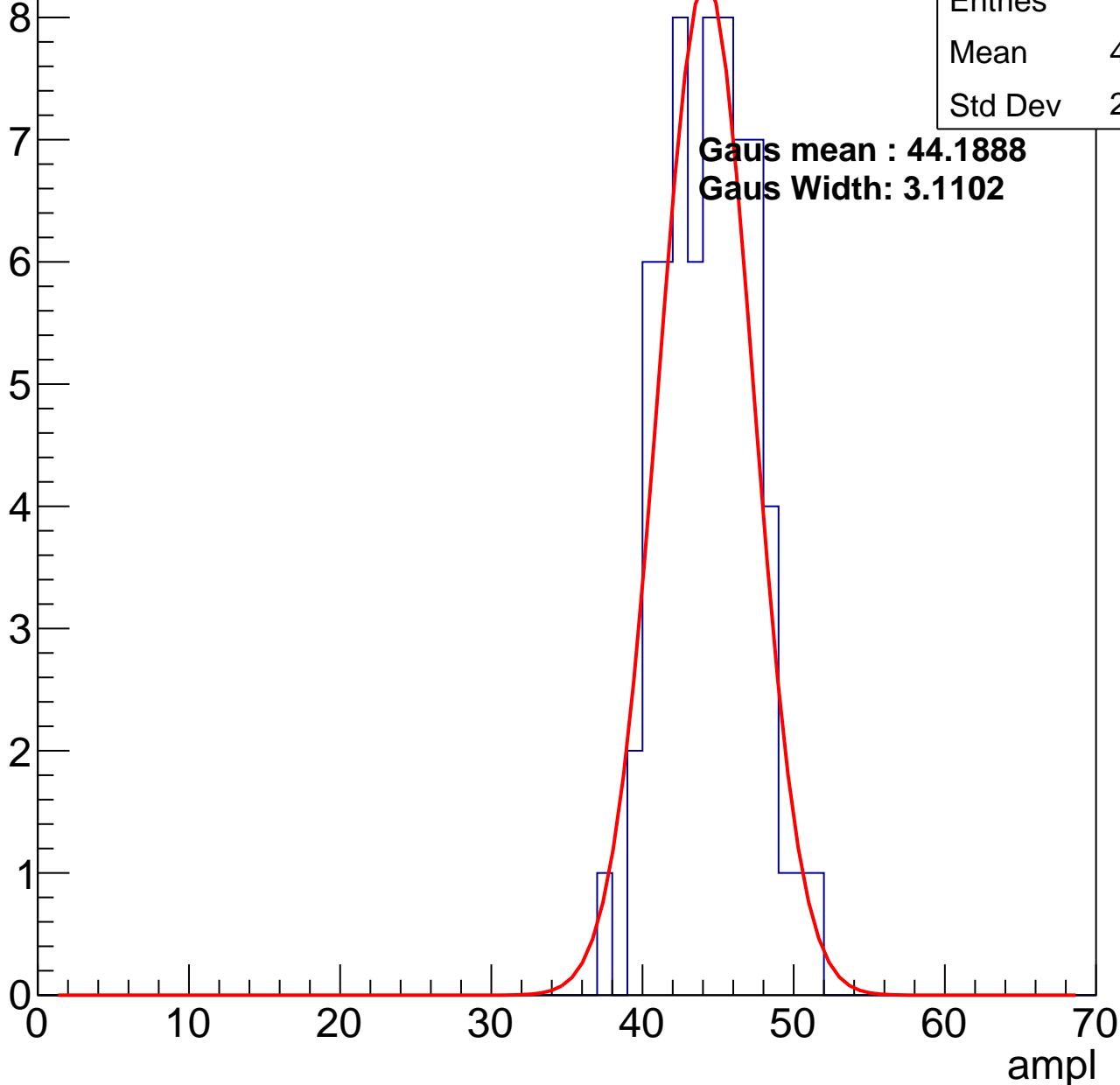
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	43.94
Std Dev	2.912

Gaus mean : 44.1888

Gaus Width: 3.1102

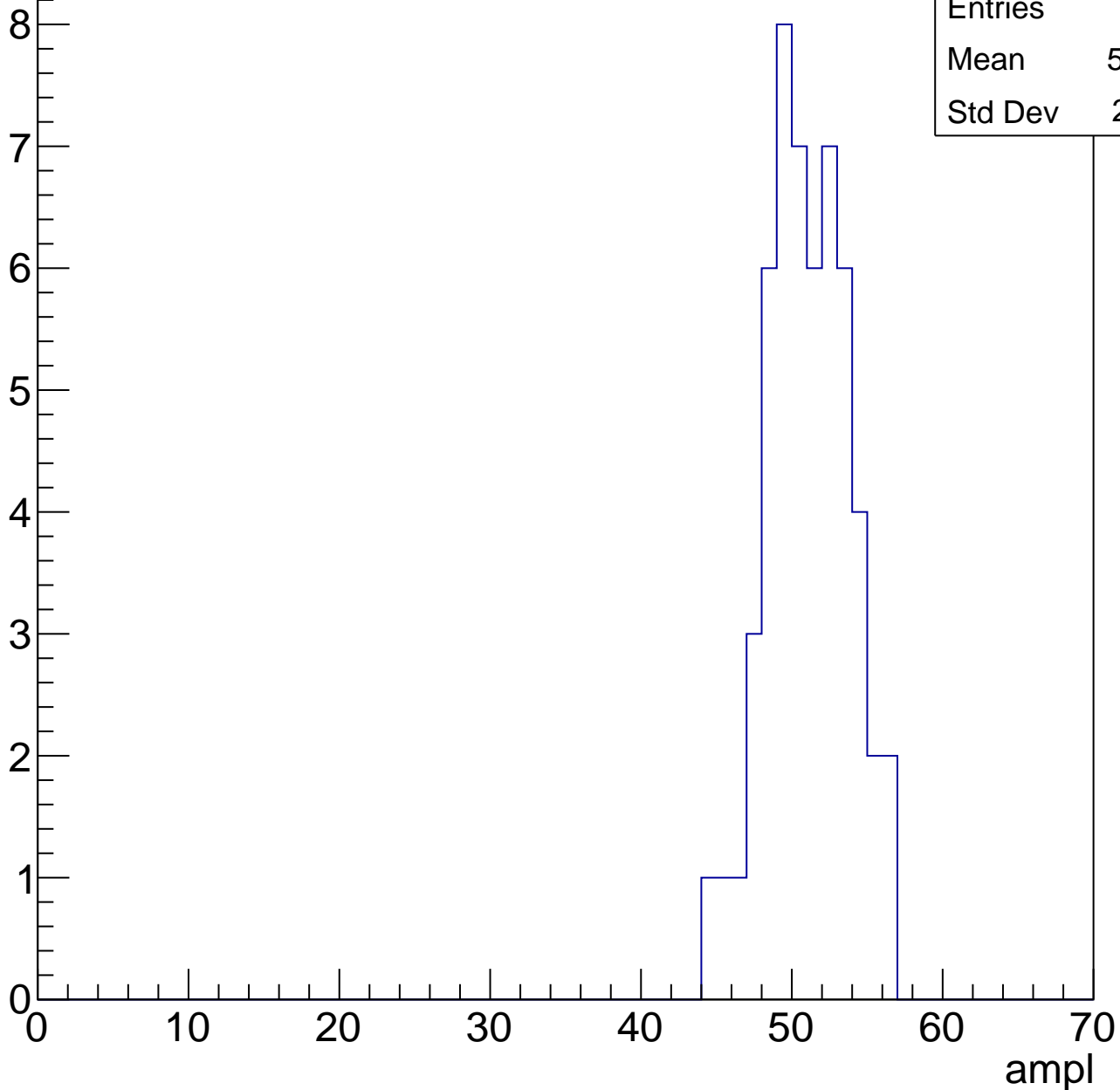


B1L003S, U13-ch75, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	50.59
Std Dev	2.691

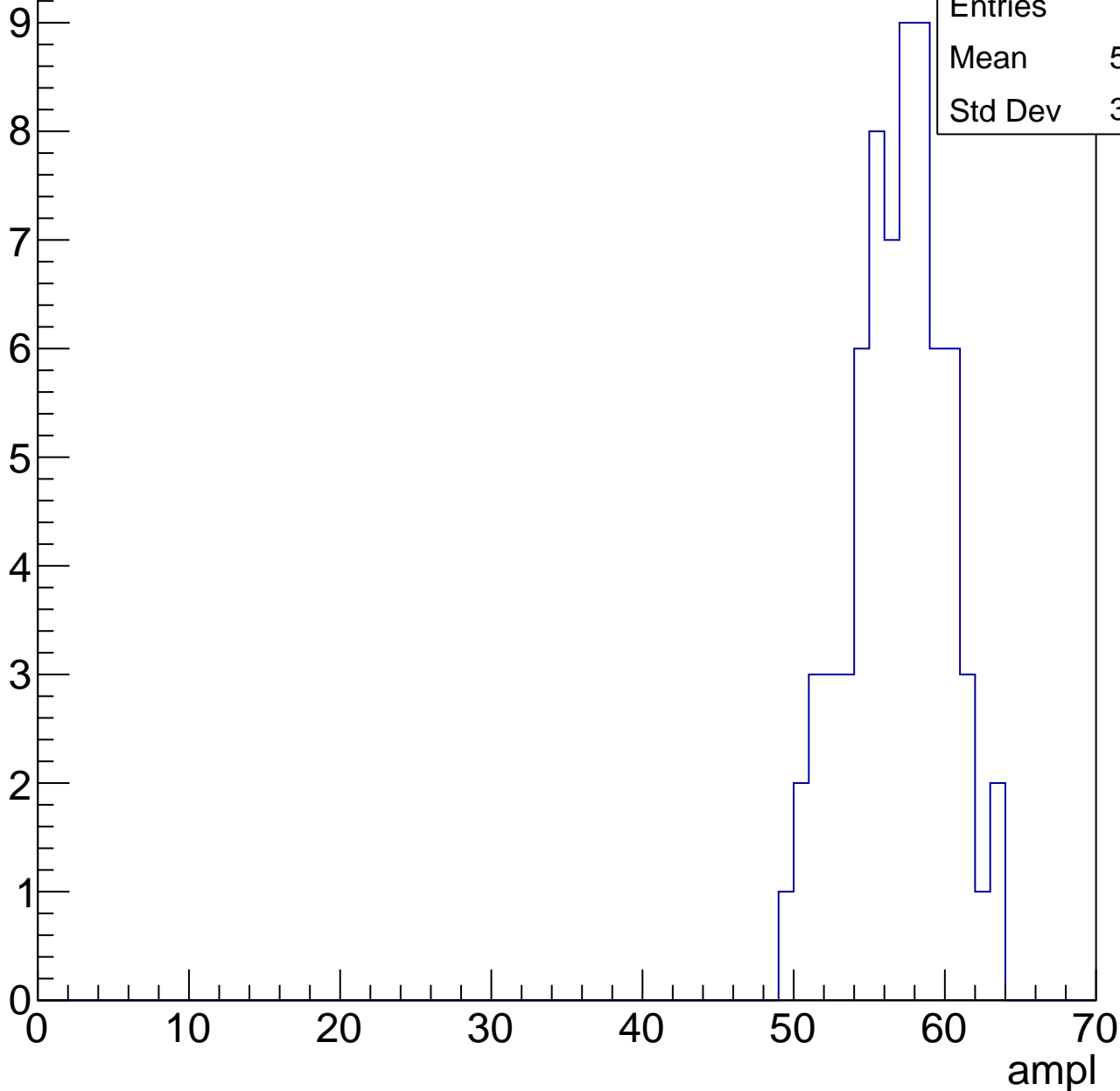


B1L003S, U13-ch75, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	56.42
Std Dev	3.173

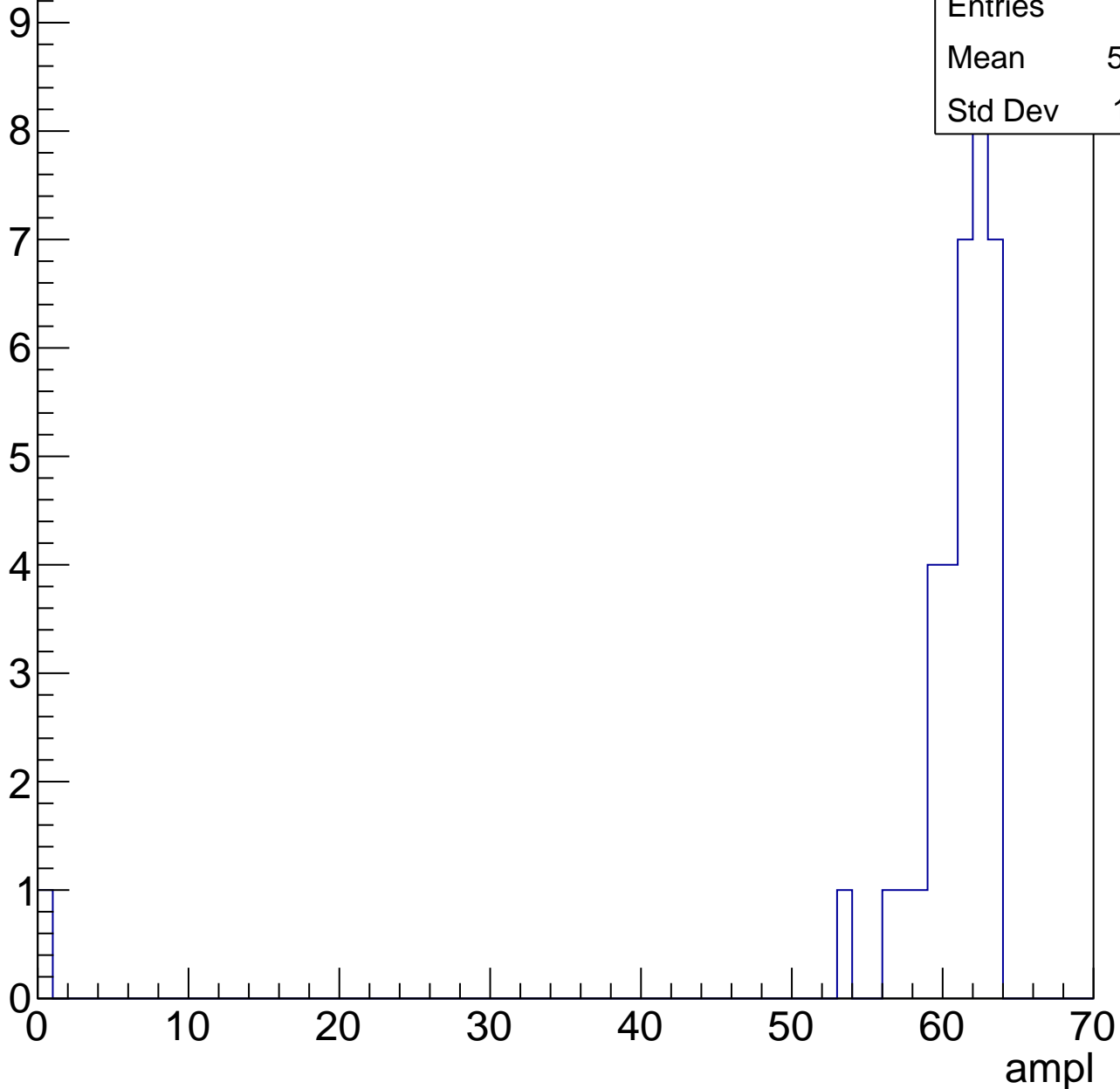


B1L003S, U13-ch75, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.06
Std Dev	10.21



B1L003S, U13-ch75, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U13-ch75, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch76, adc0

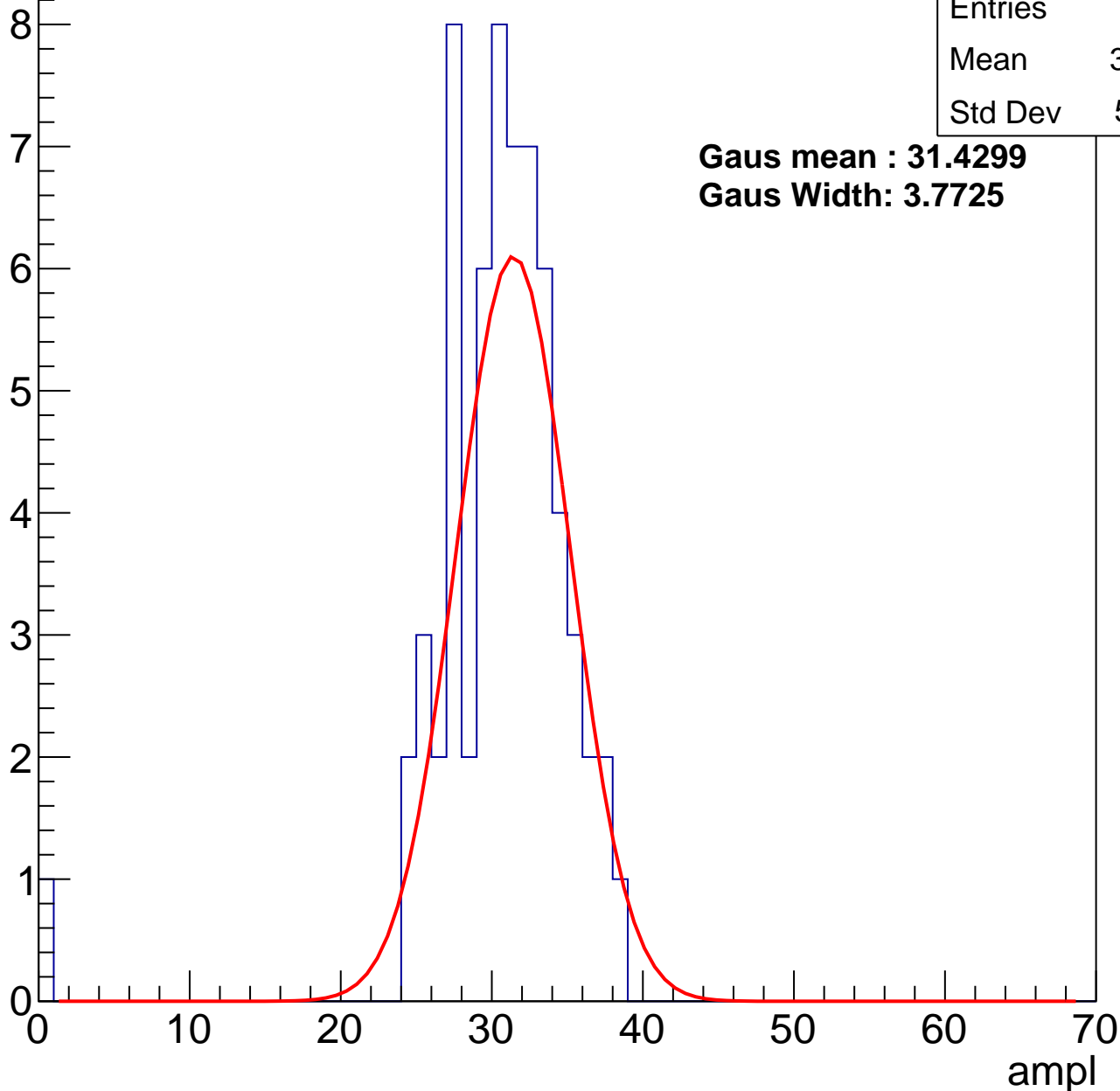
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.08
Std Dev	5.041

Gaus mean : 31.4299

Gaus Width: 3.7725



B1L003S, U13-ch76, adc1

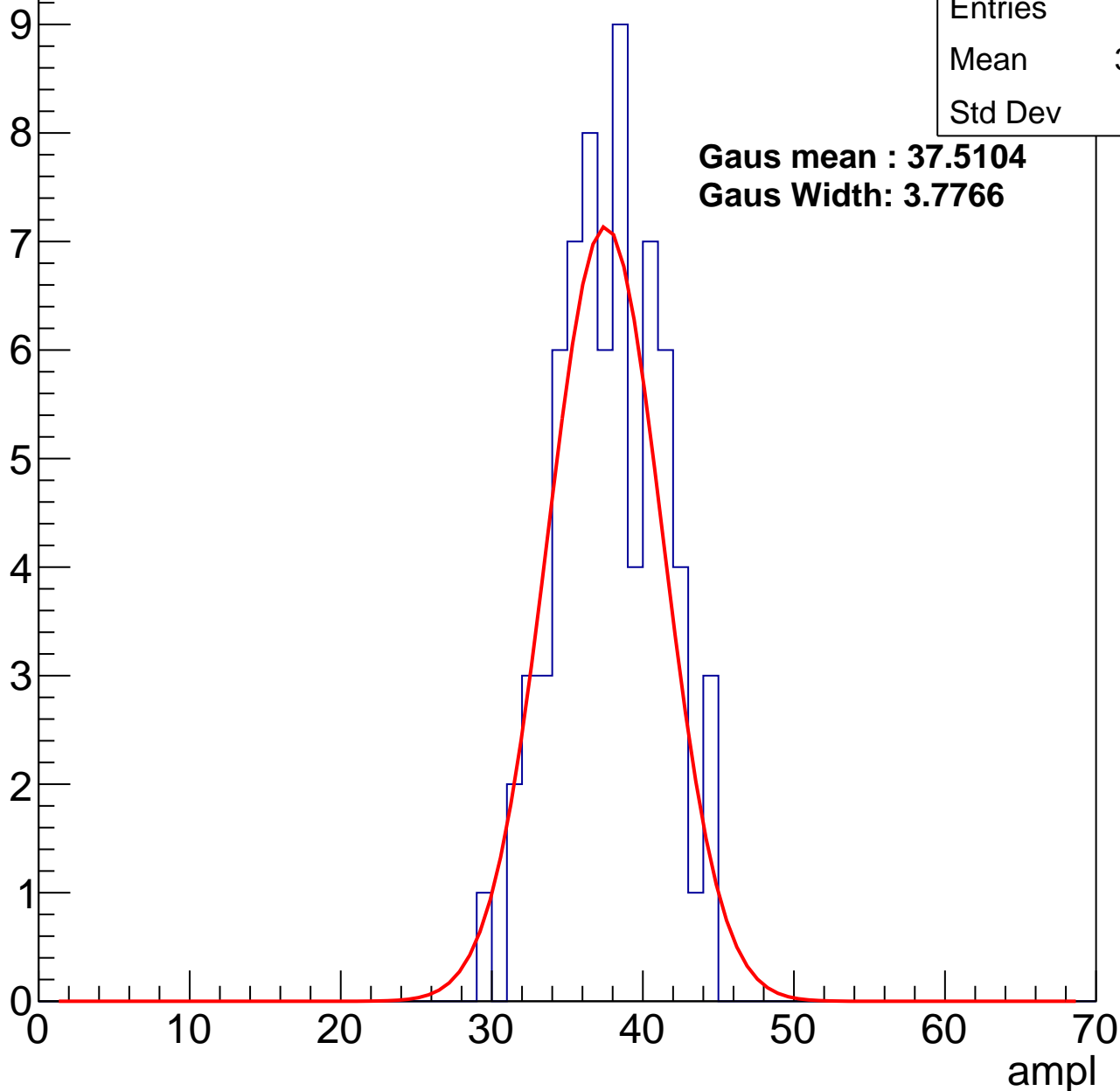
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.31
Std Dev	3.4

Gaus mean : 37.5104

Gaus Width: 3.7766



B1L003S, U13-ch76, adc2

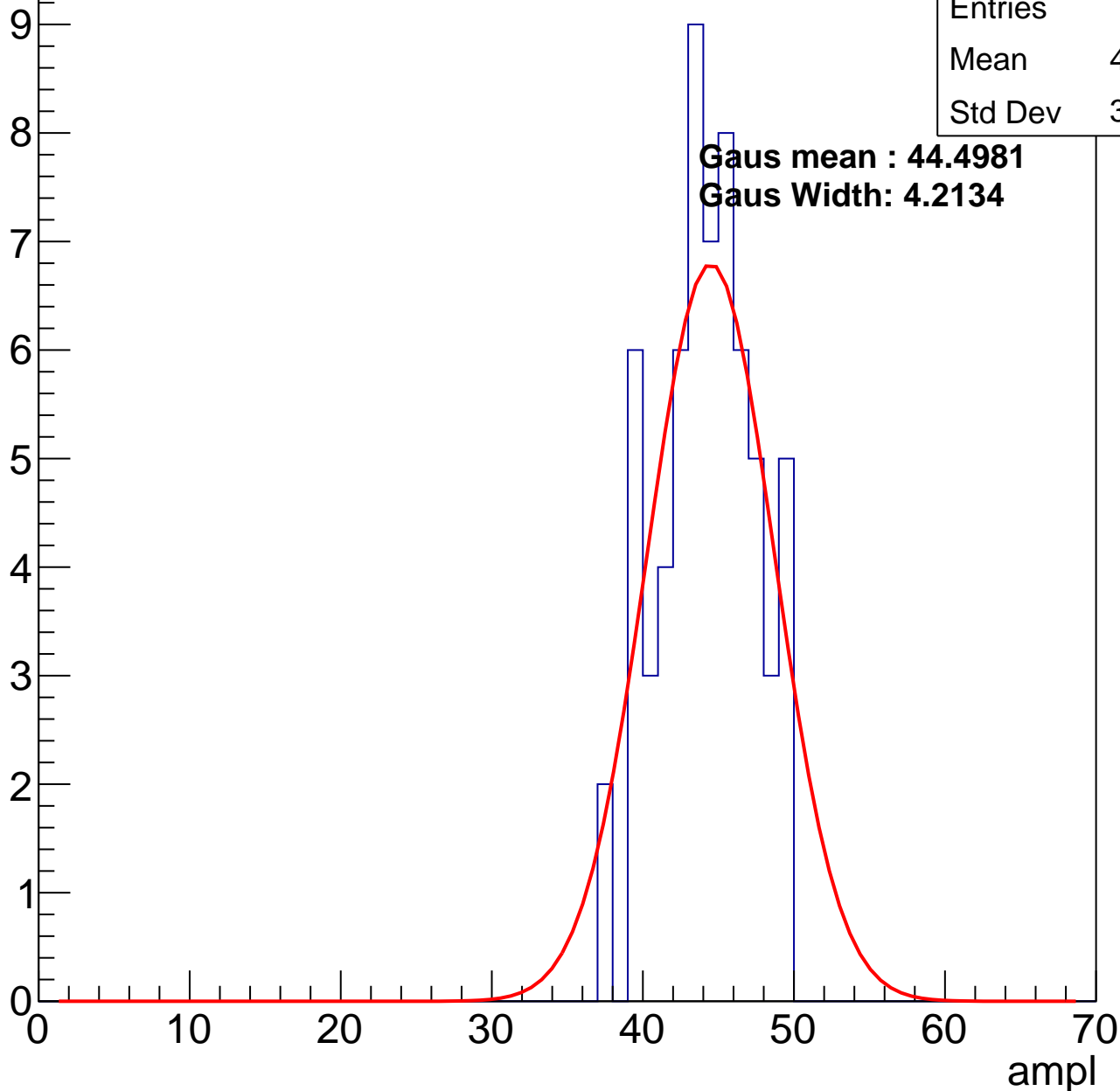
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.73
Std Dev	3.088

Gaus mean : 44.4981

Gaus Width: 4.2134

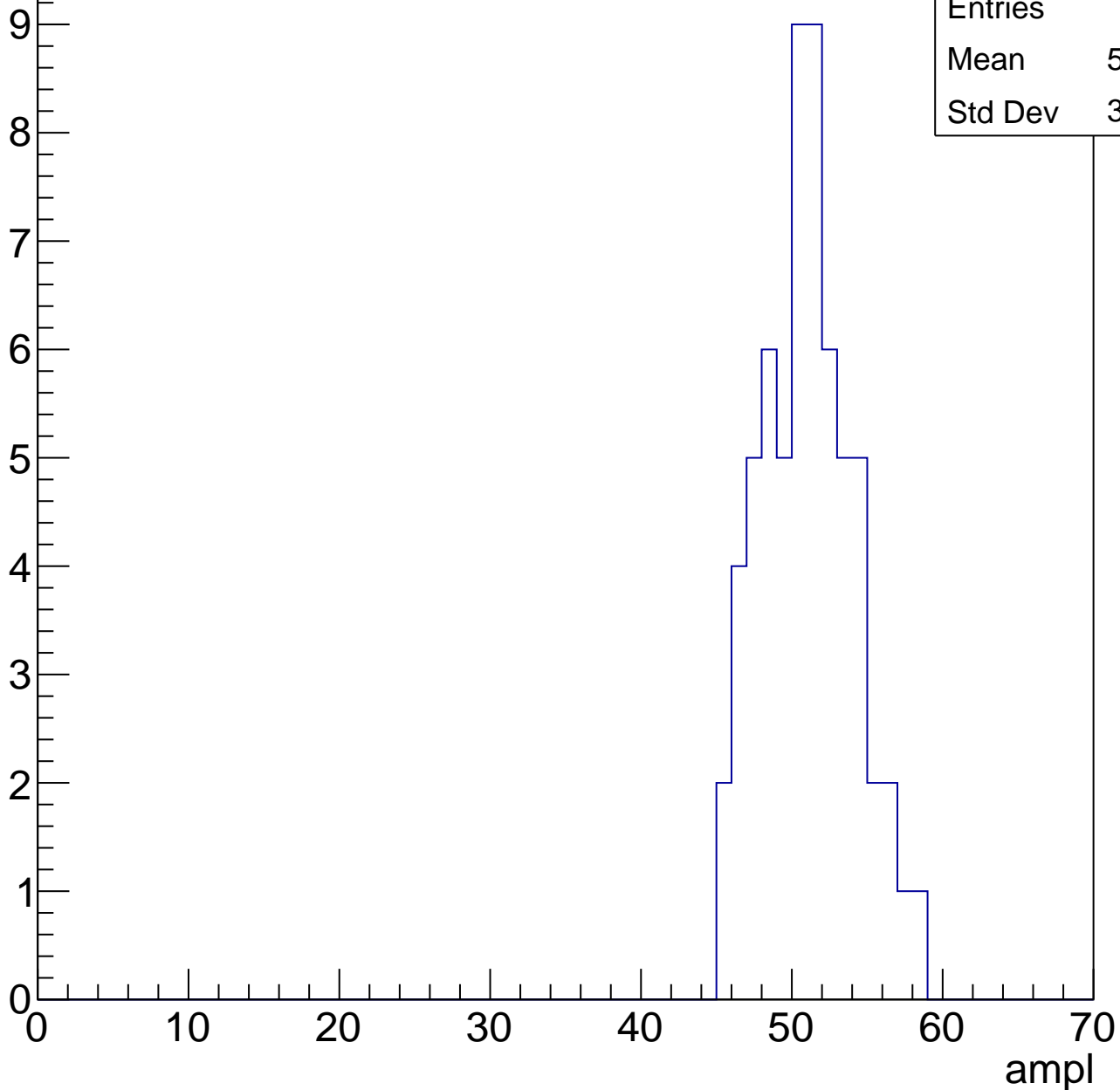


B1L003S, U13-ch76, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	50.56
Std Dev	3.009

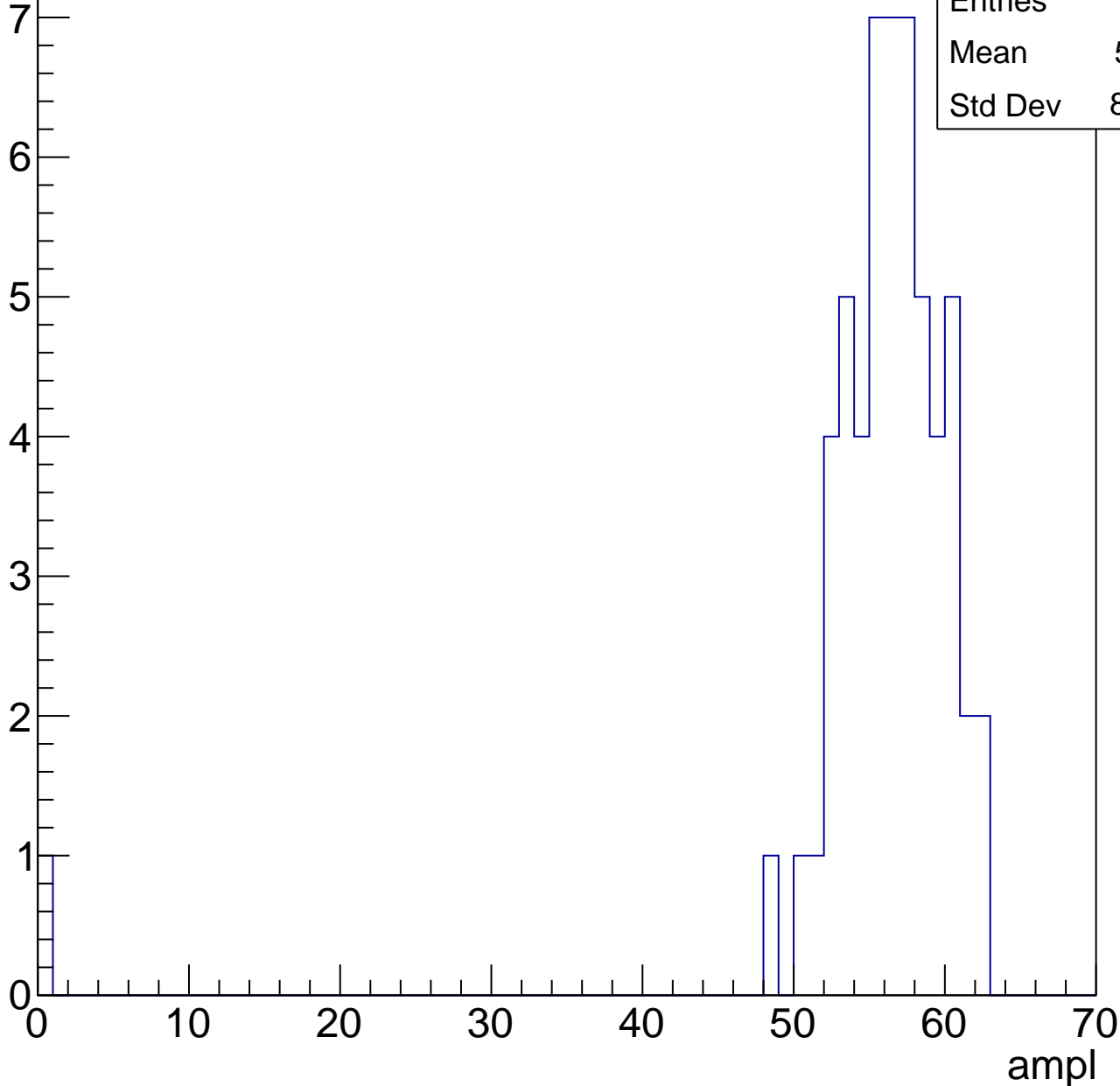


B1L003S, U13-ch76, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.11
Std Dev	8.033

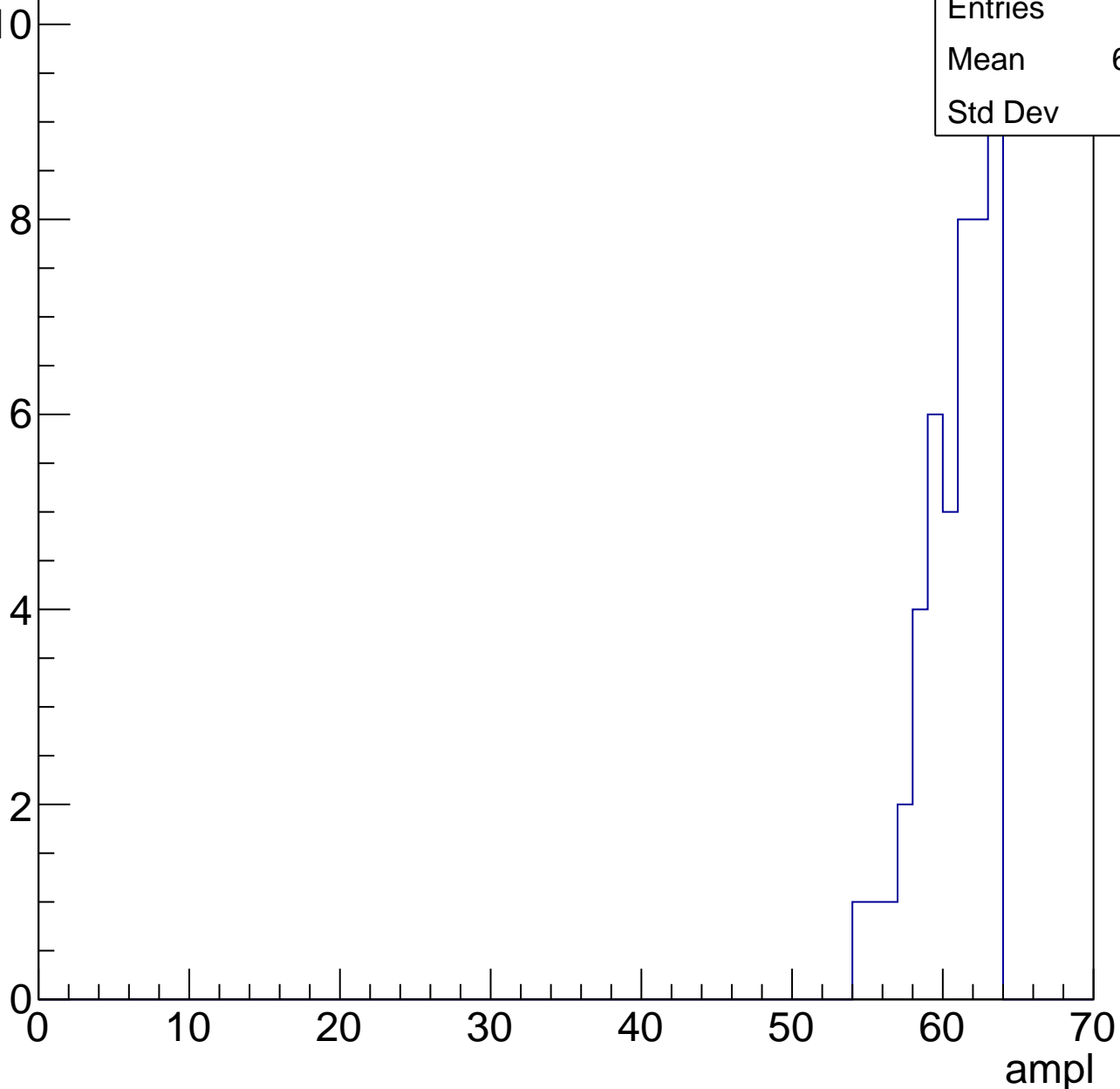


B1L003S, U13-ch76, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	60.41
Std Dev	2.28



B1L003S, U13-ch76, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch76, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch77, adc0

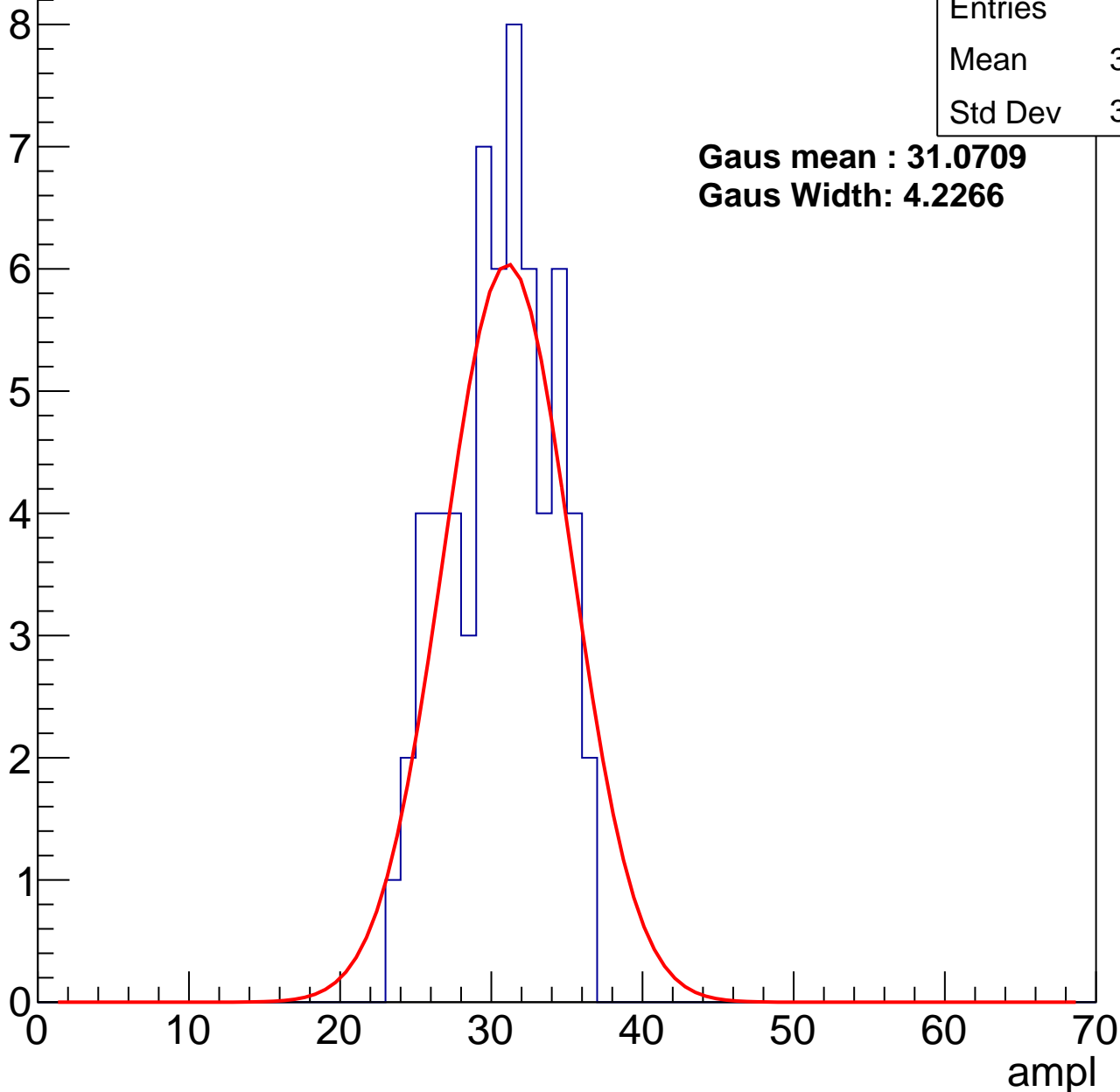
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	30.13
Std Dev	3.326

Gaus mean : 31.0709

Gaus Width: 4.2266



B1L003S, U13-ch77, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	37.17
Std Dev	3.613

Gaus mean : 36.4917

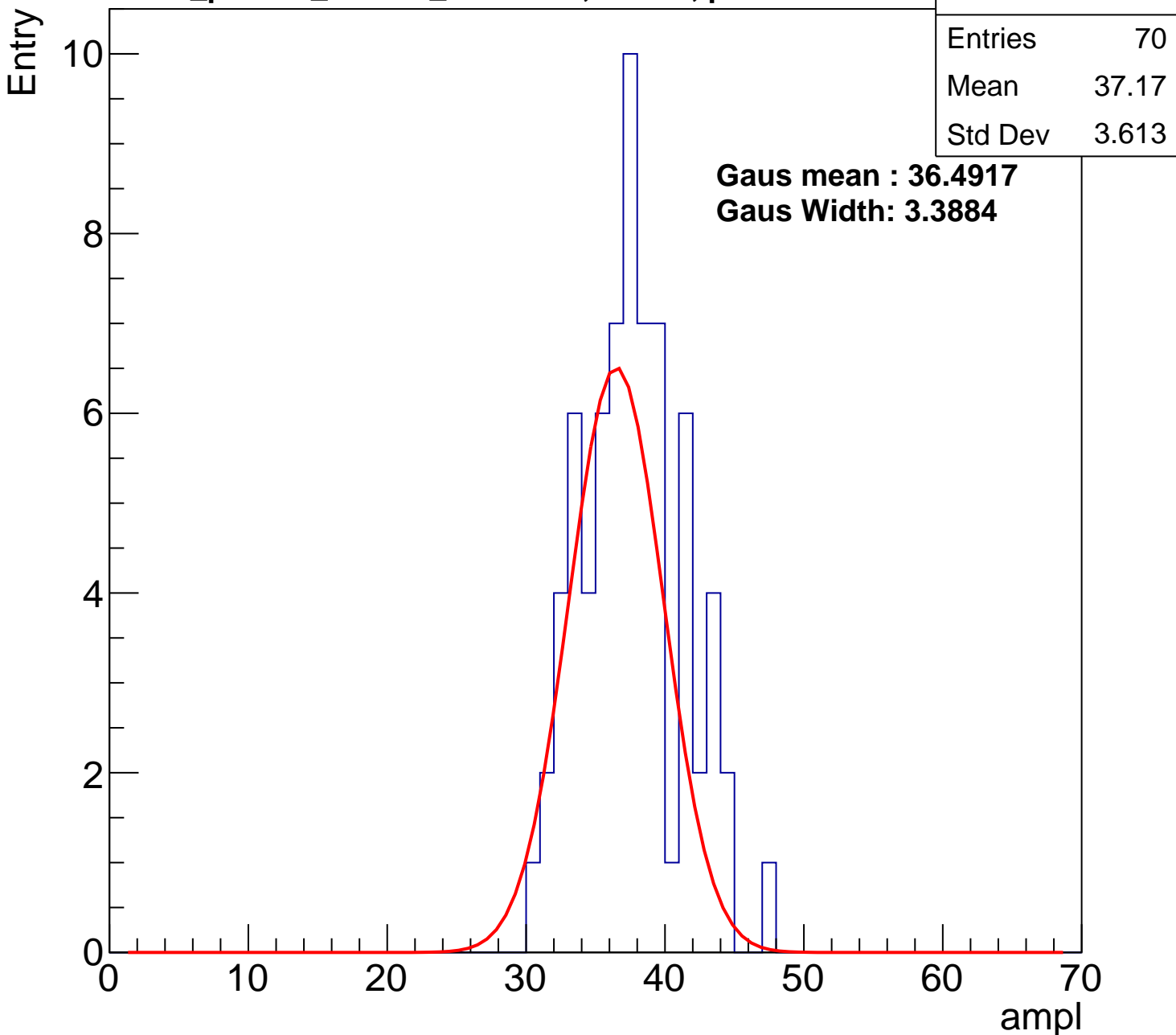
Gaus Width: 3.3884

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch77, adc2

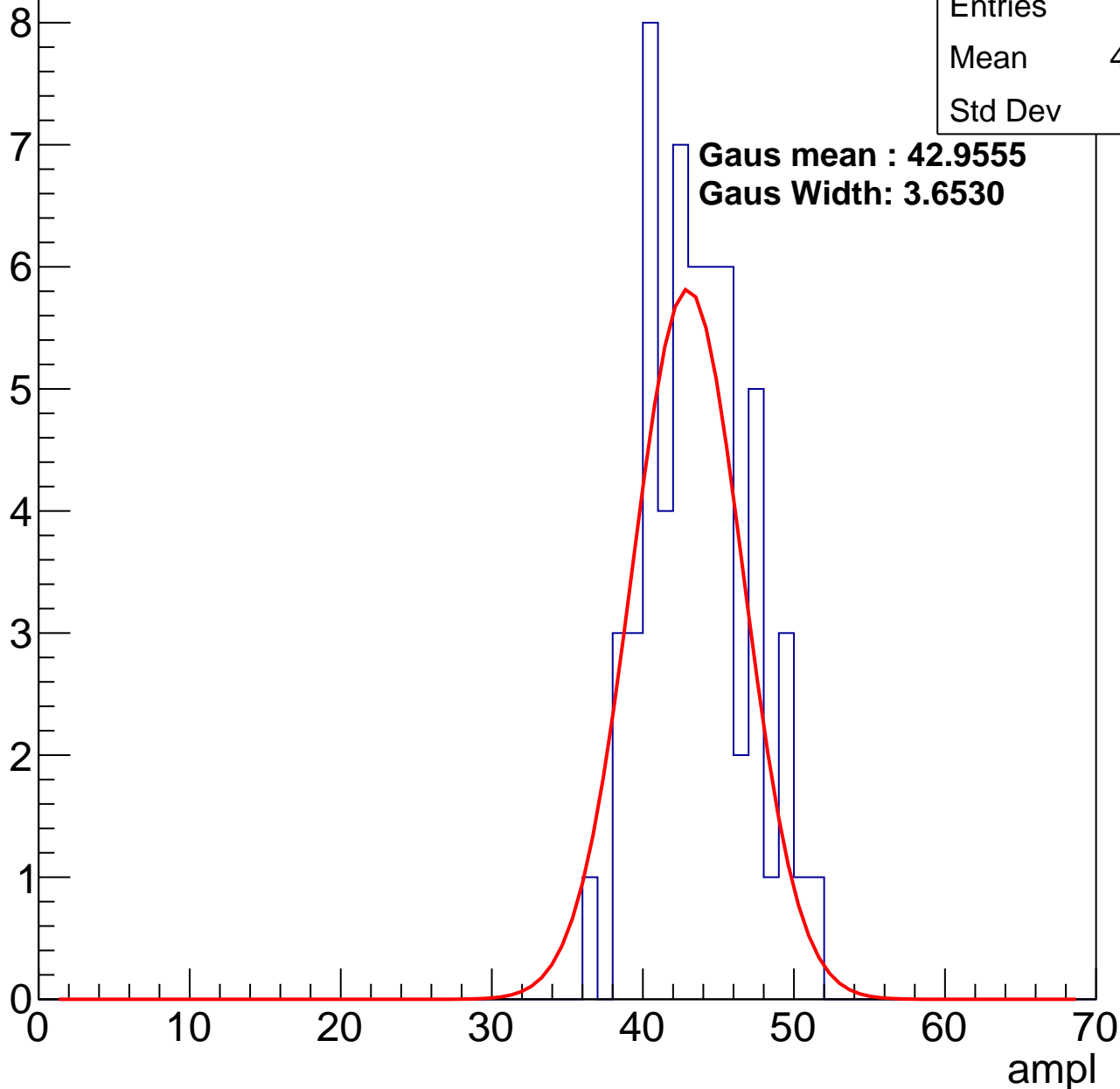
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.16
Std Dev	3.35

Gaus mean : 42.9555

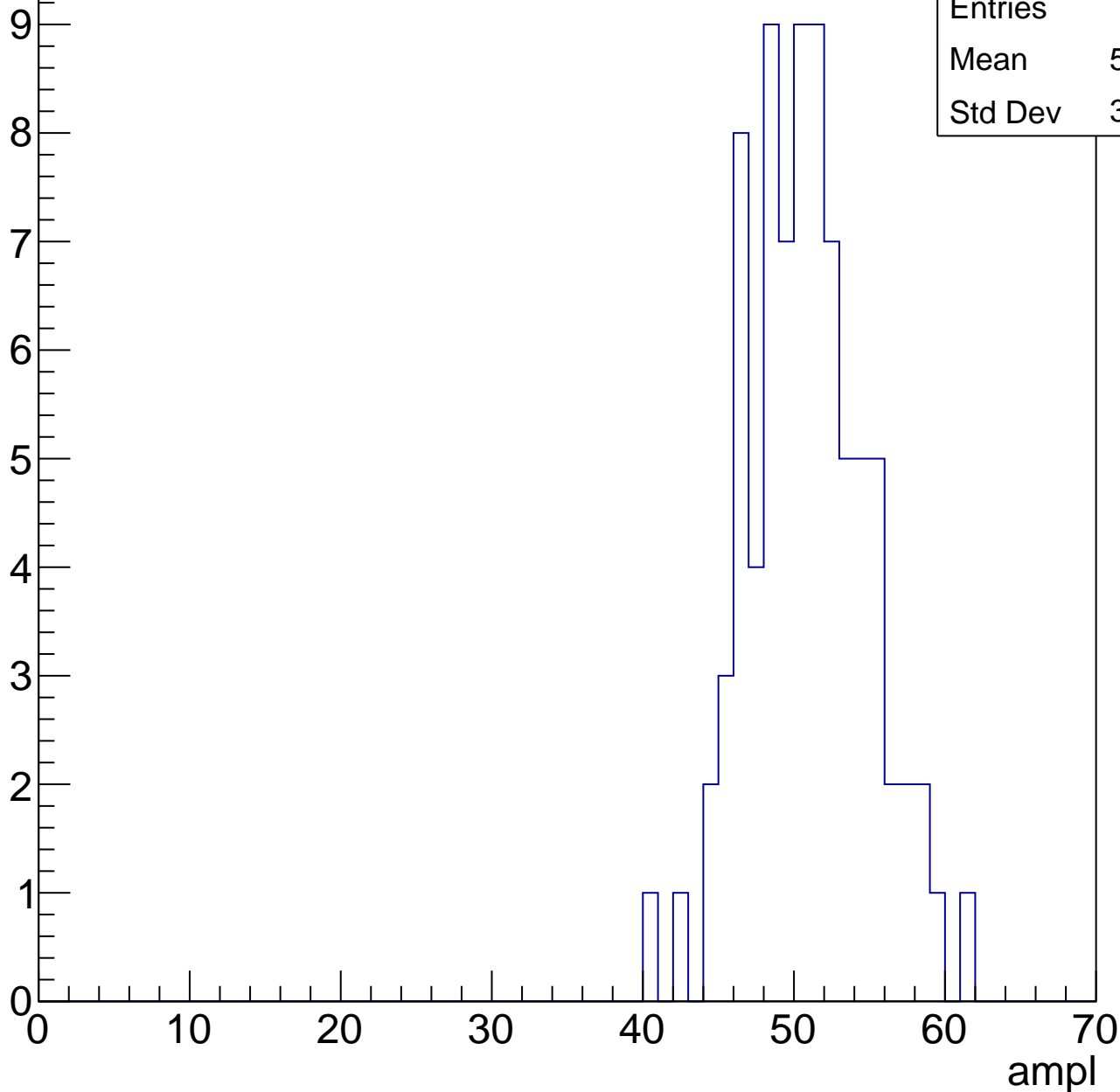
Gaus Width: 3.6530



B1L003S, U13-ch77, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

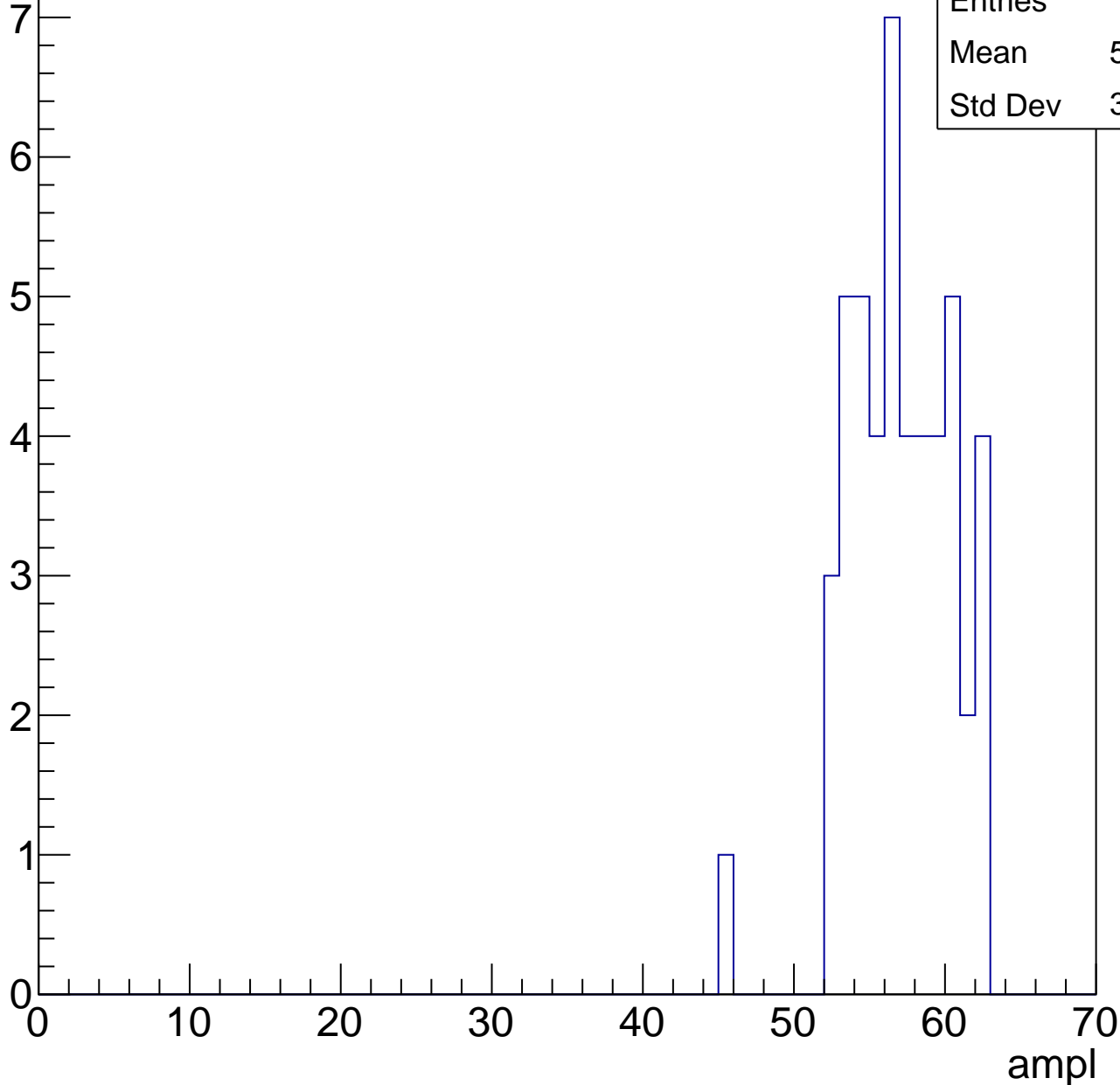


B1L003S, U13-ch77, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	56.54
Std Dev	3.397



B1L003S, U13-ch77, adc5

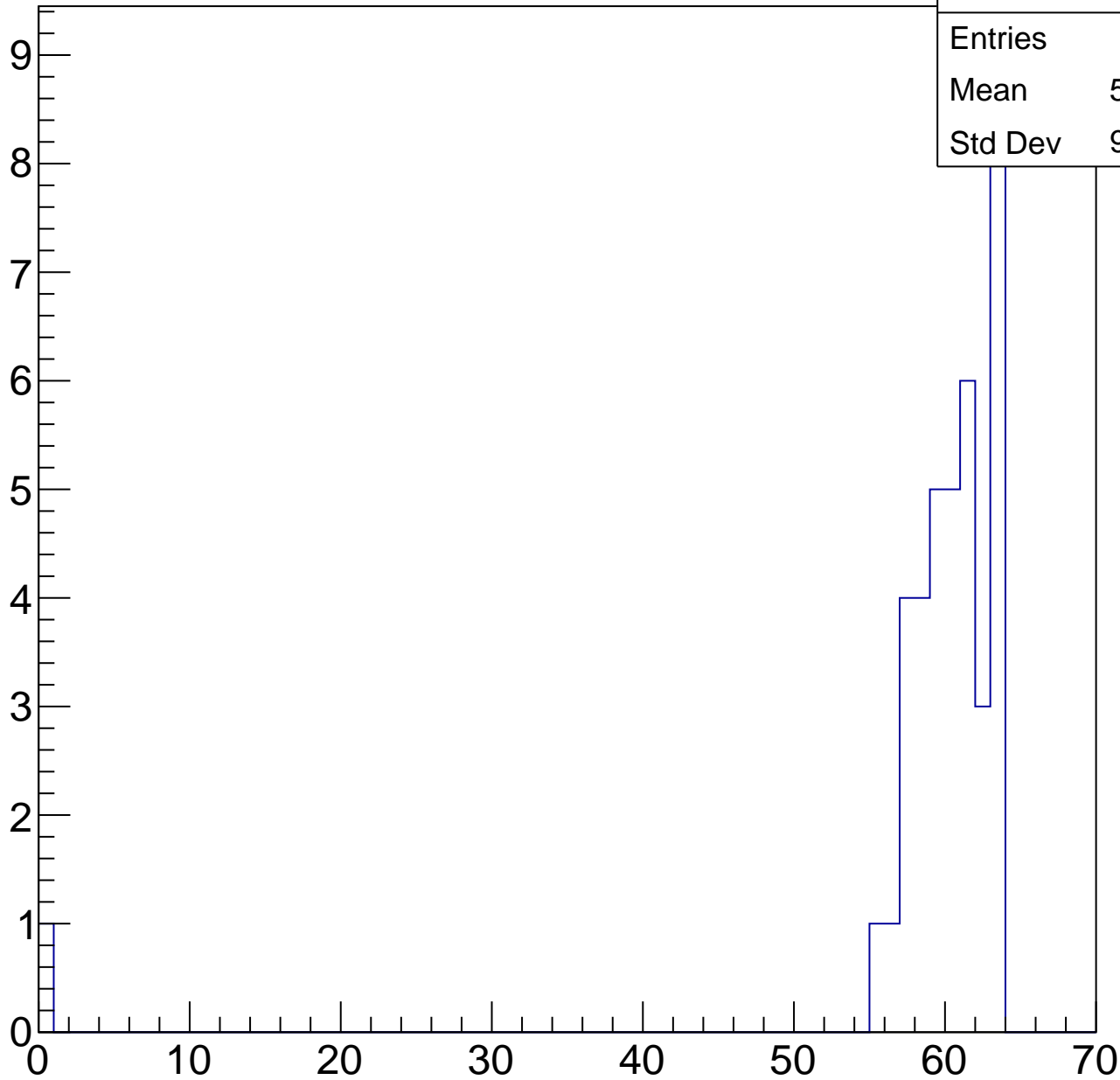
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	58.59
Std Dev	9.766

ampl



B1L003S, U13-ch77, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch77, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch78, adc0

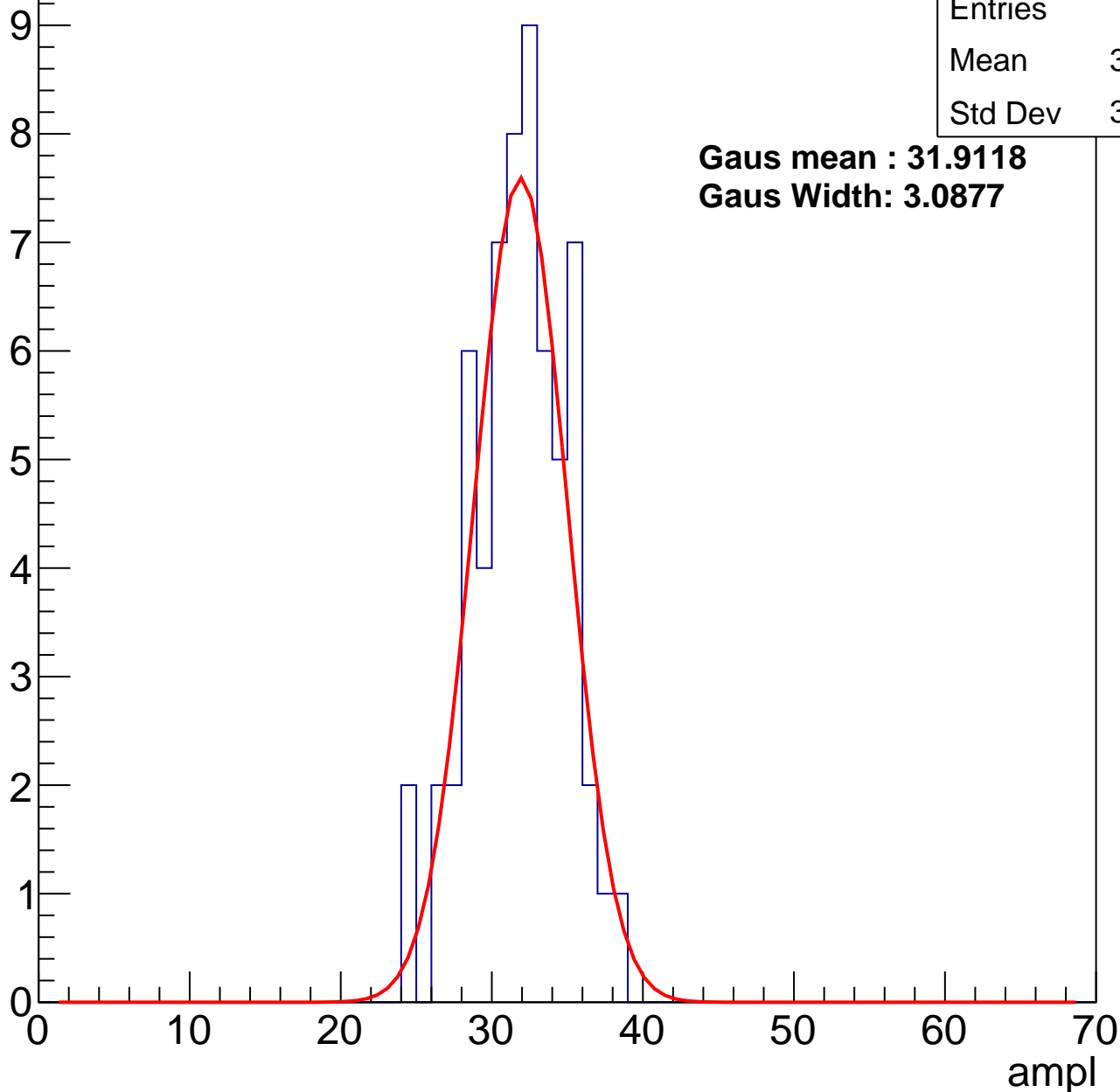
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	31.35
Std Dev	3.043

Gaus mean : 31.9118

Gaus Width: 3.0877



B1L003S, U13-ch78, adc1

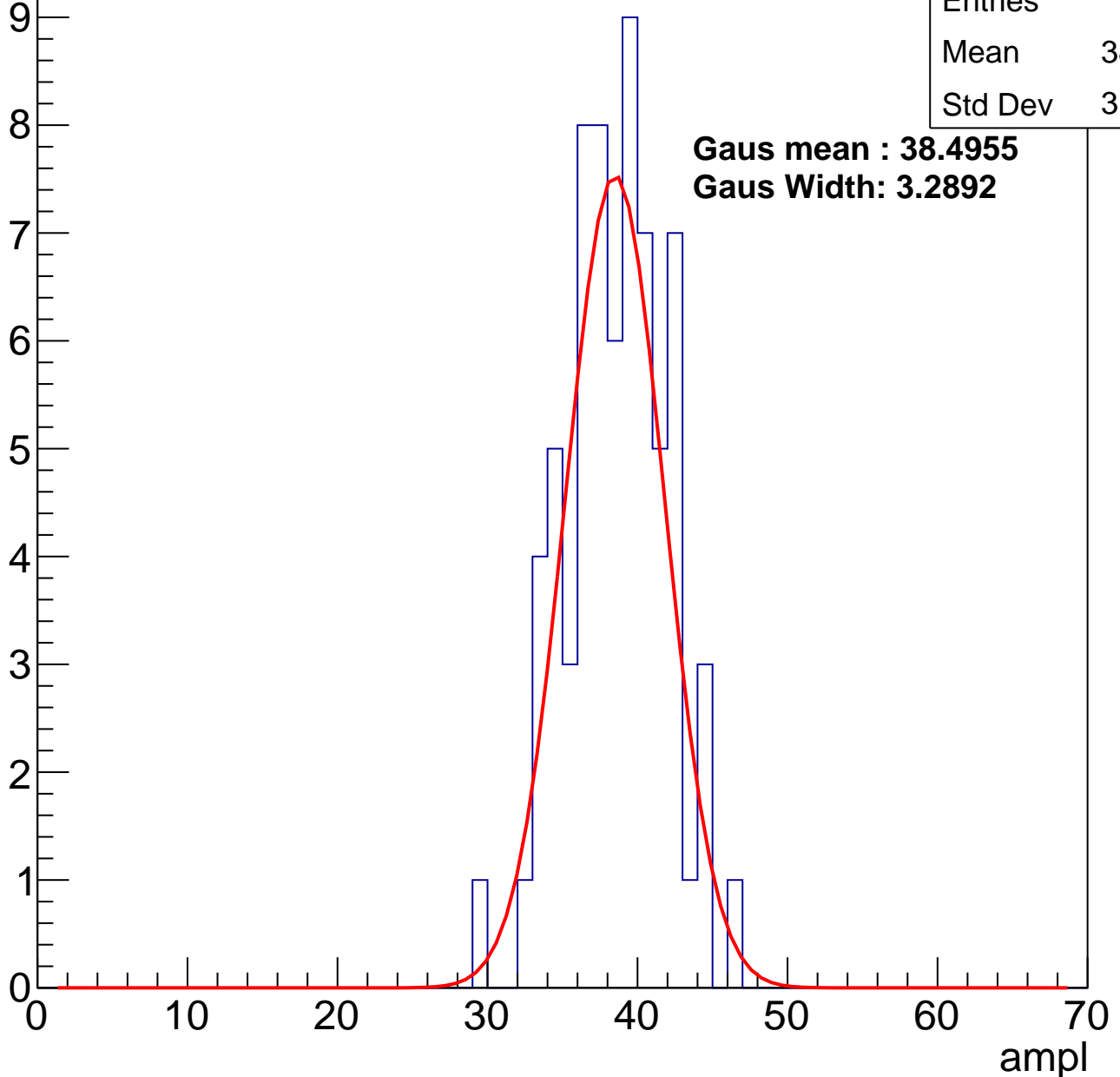
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	38.13
Std Dev	3.305

Gaus mean : 38.4955

Gaus Width: 3.2892



B1L003S, U13-ch78, adc2

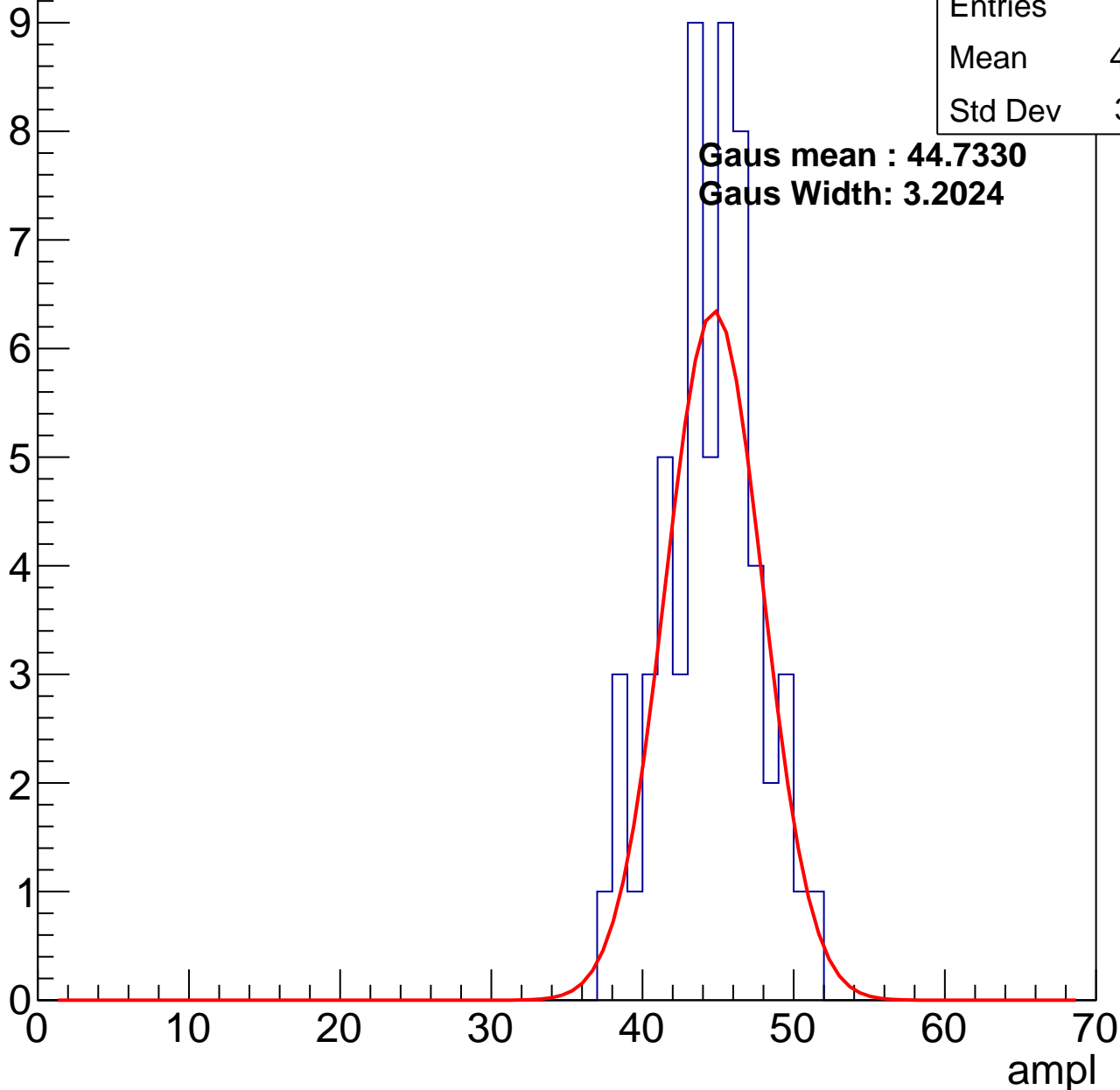
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.02
Std Dev	3.121

Gaus mean : 44.7330

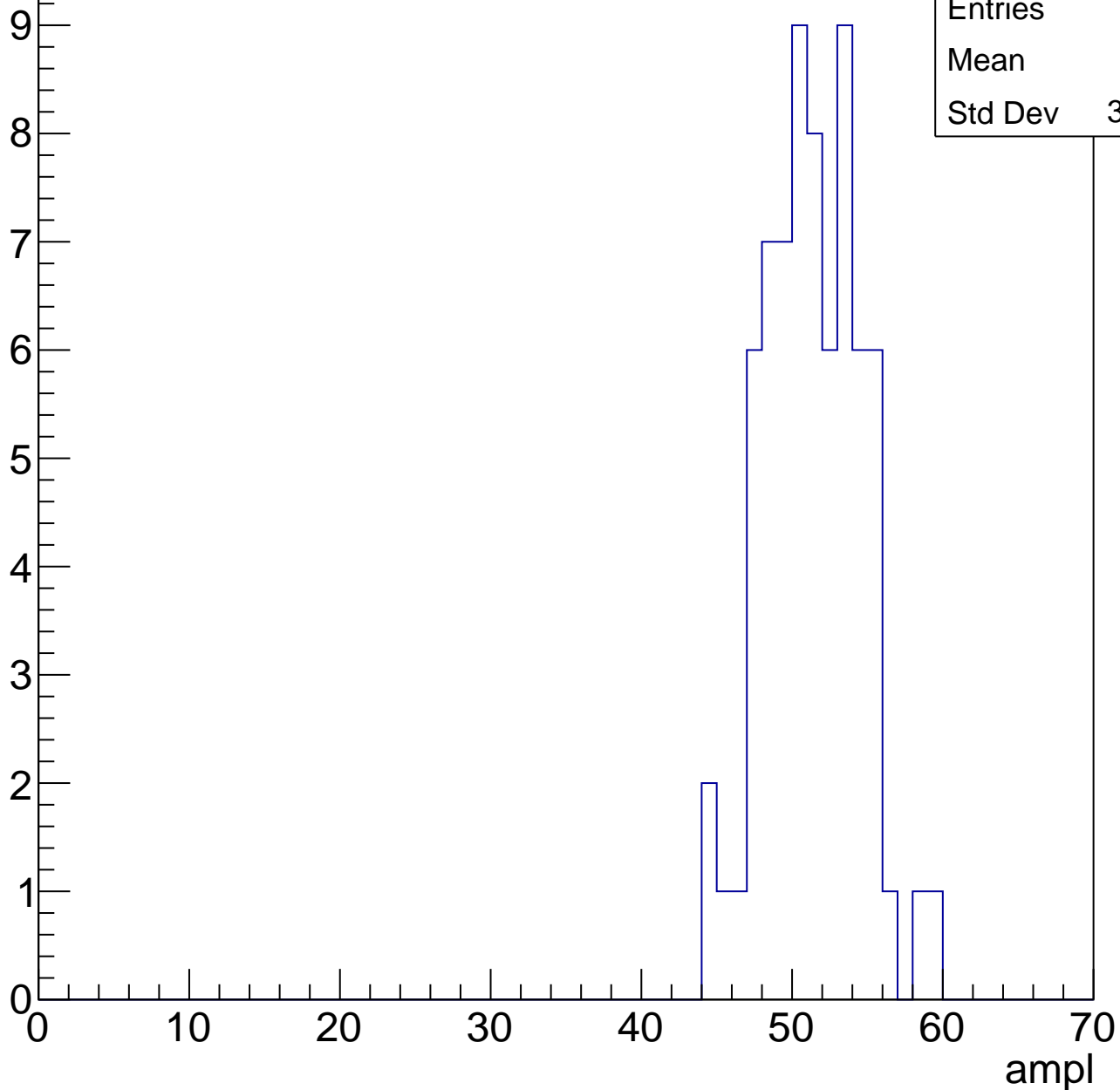
Gaus Width: 3.2024



B1L003S, U13-ch78, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

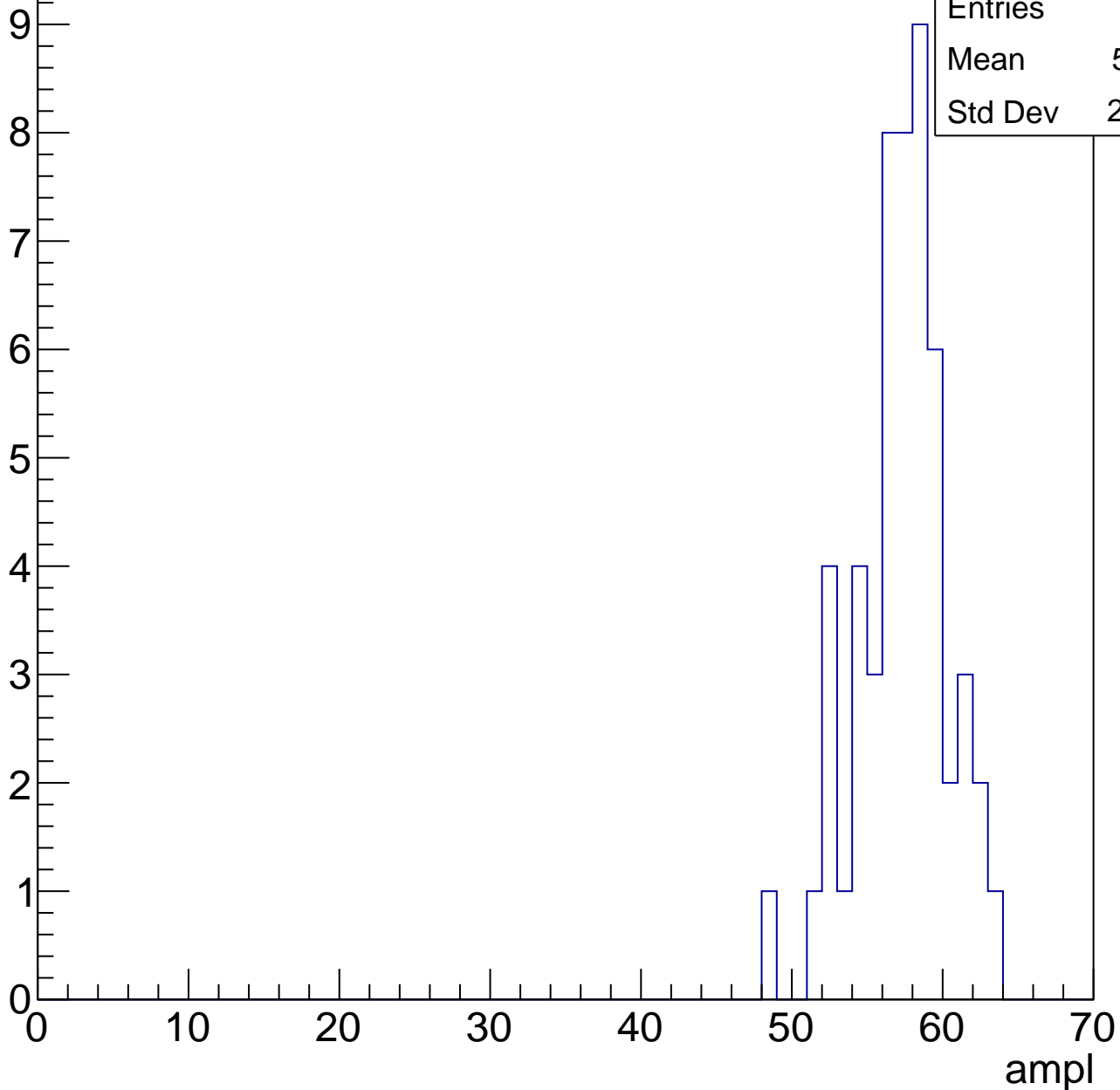


B1L003S, U13-ch78, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

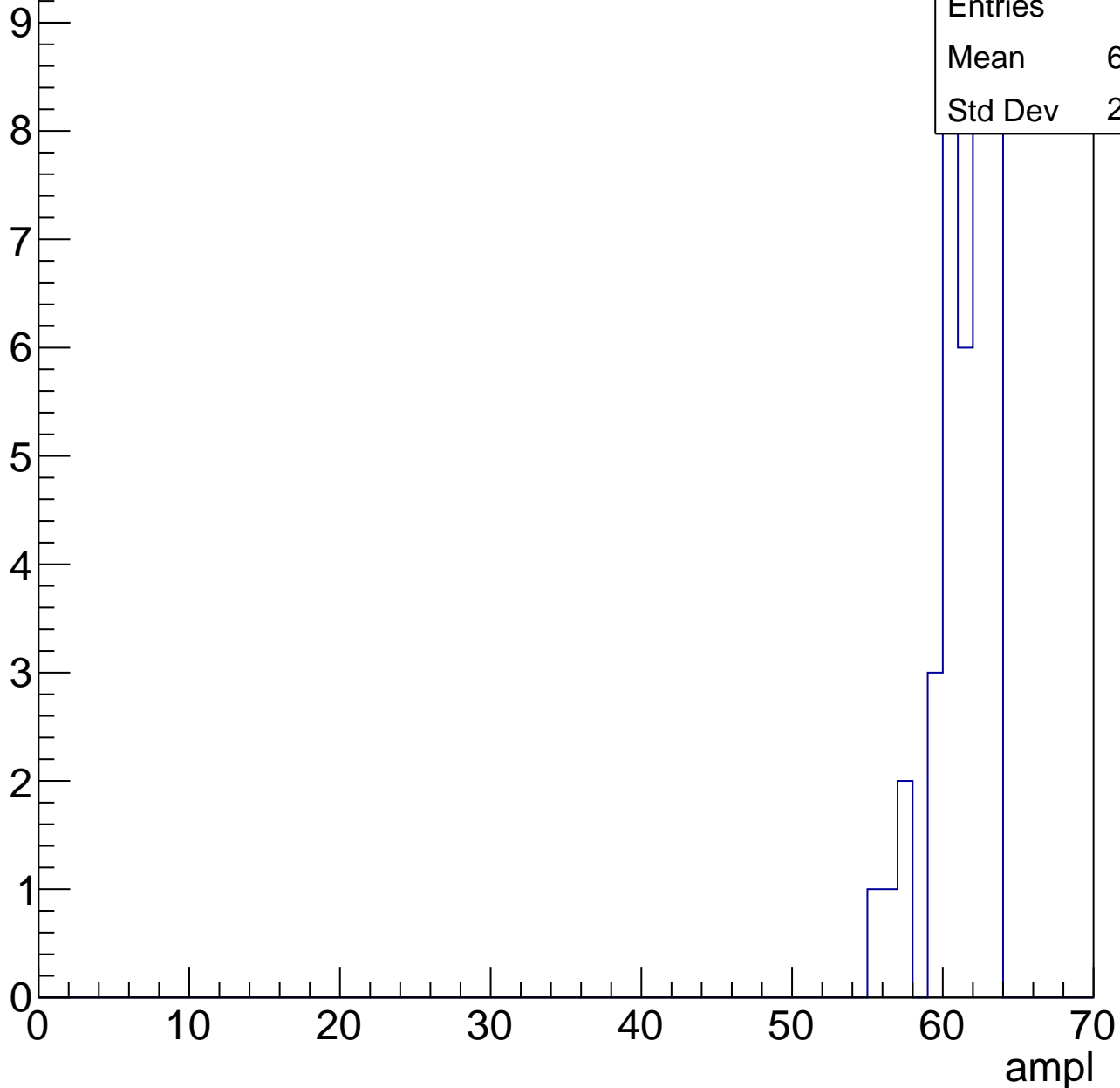
Entries	53
Mean	56.81
Std Dev	2.985



B1L003S, U13-ch78, adc5

calib_packv5_042523_0143.root, FC#13, port D2

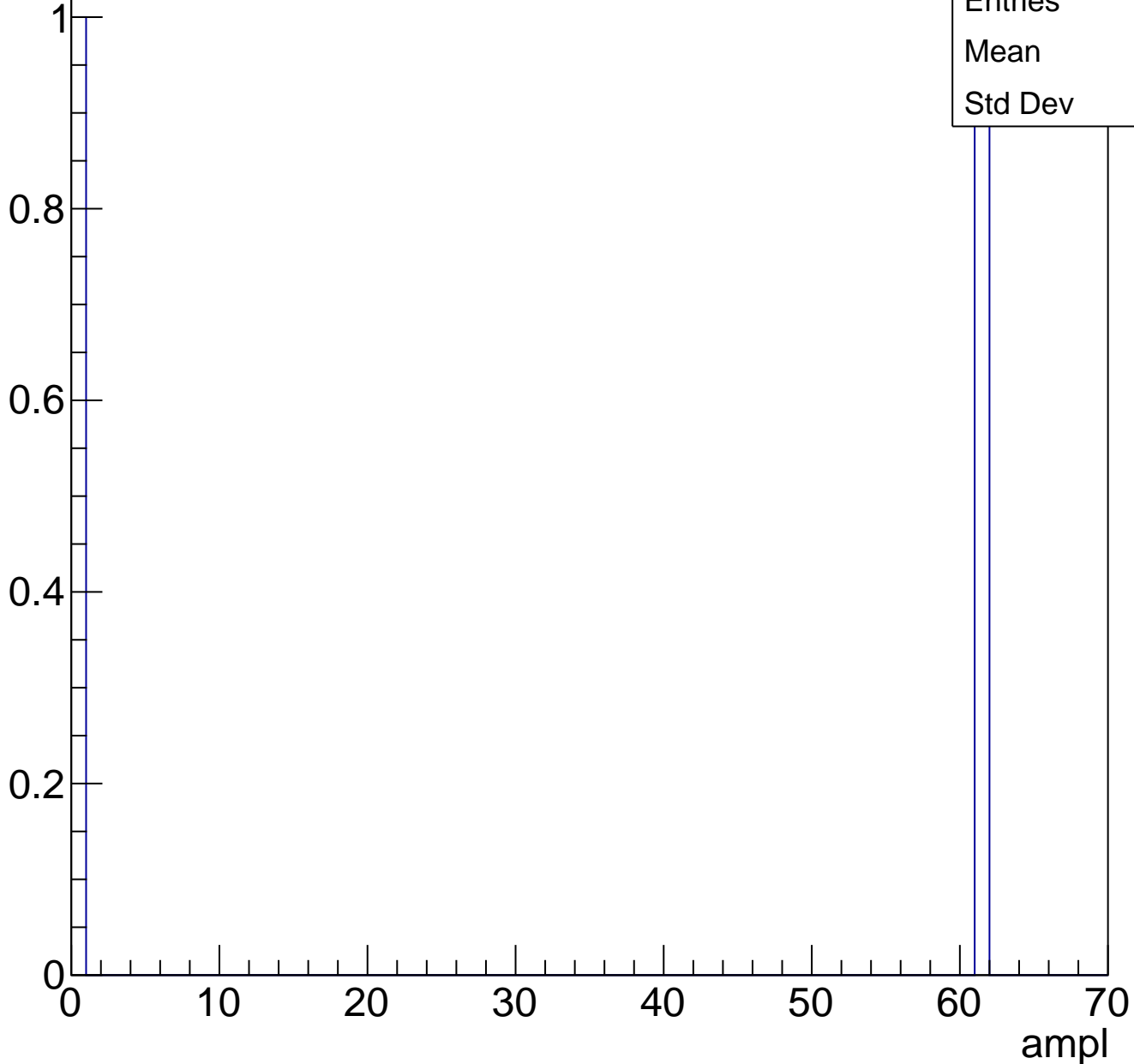
Entry



B1L003S, U13-ch78, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch78, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch79, adc0

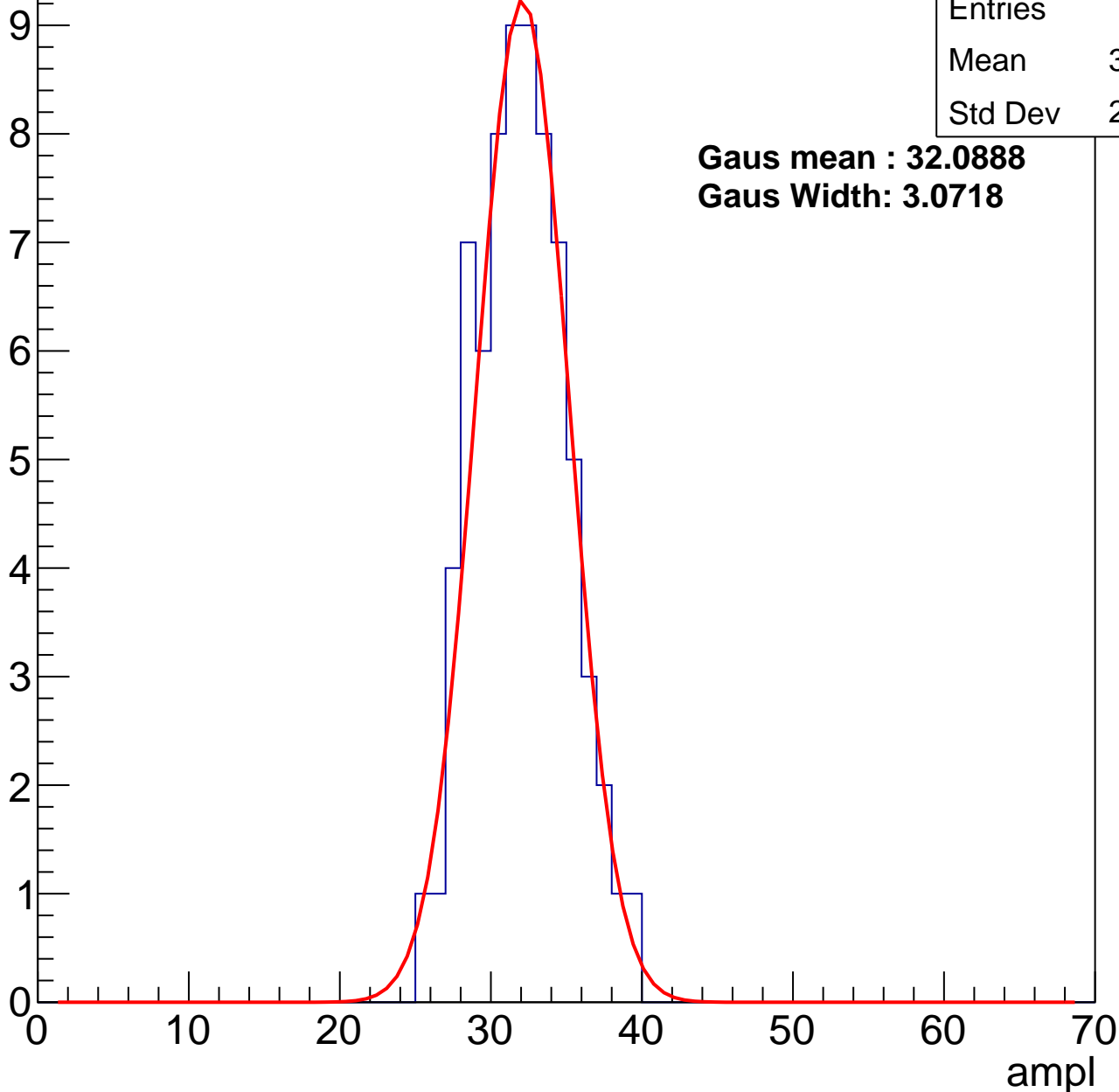
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	31.56
Std Dev	2.986

Gaus mean : 32.0888

Gaus Width: 3.0718



B1L003S, U13-ch79, adc1

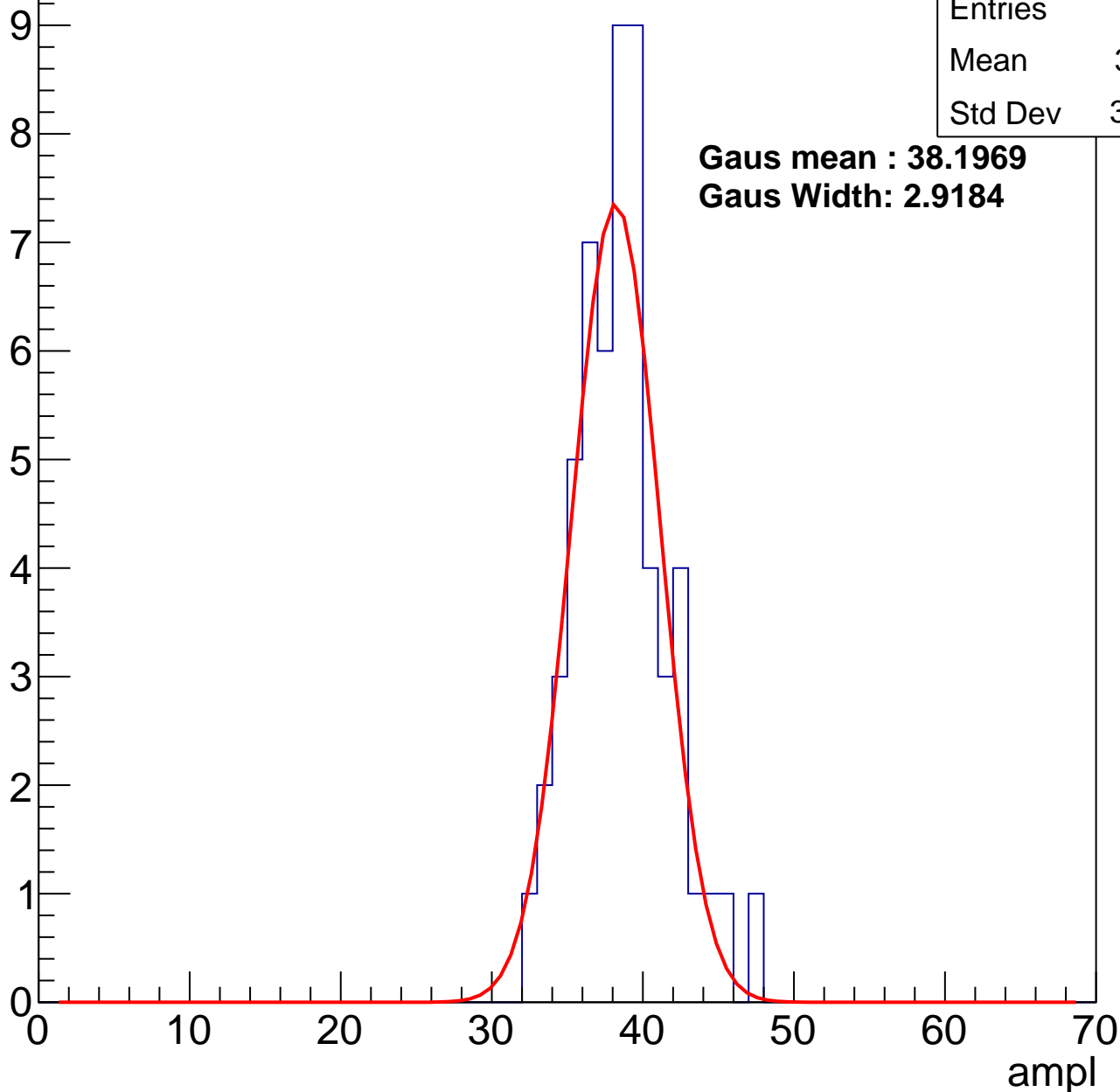
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	38.11
Std Dev	3.019

Gaus mean : 38.1969

Gaus Width: 2.9184



B1L003S, U13-ch79, adc2

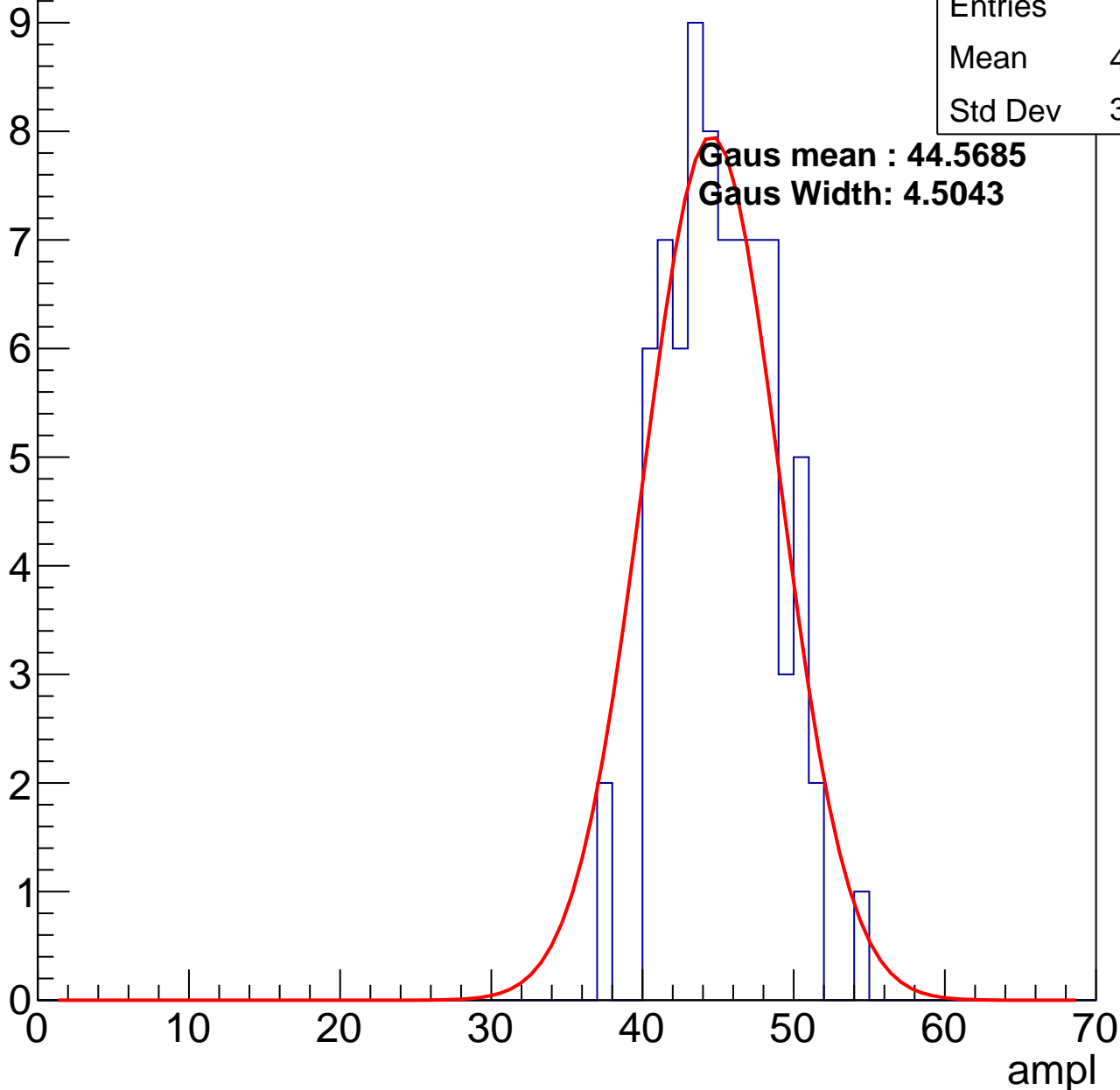
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	44.77
Std Dev	3.434

Gaus mean : 44.5685

Gaus Width: 4.5043

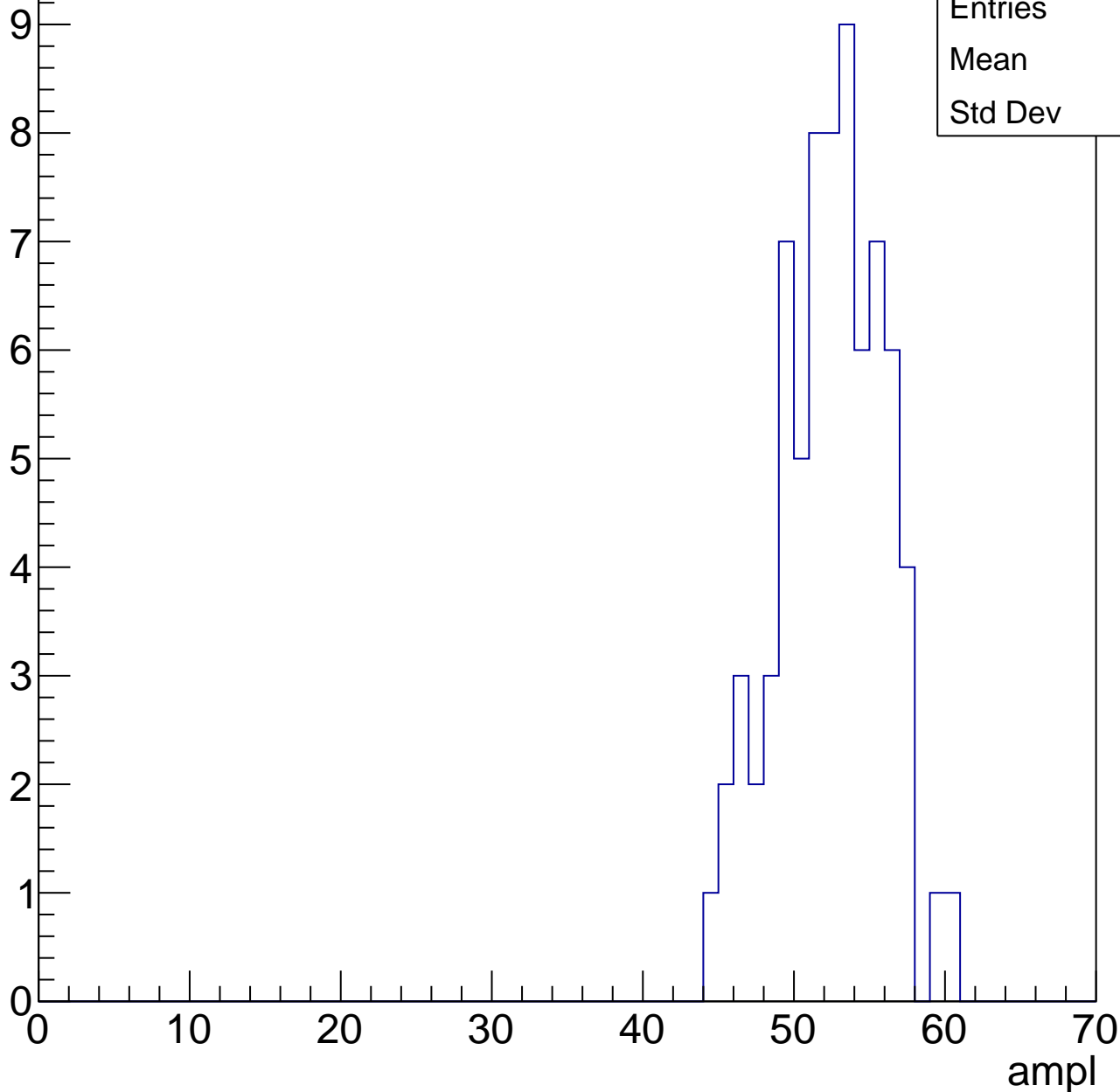


B1L003S, U13-ch79, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	52
Std Dev	3.44



B1L003S, U13-ch79, adc4

calib_packv5_042523_0143.root, FC#13, port D2

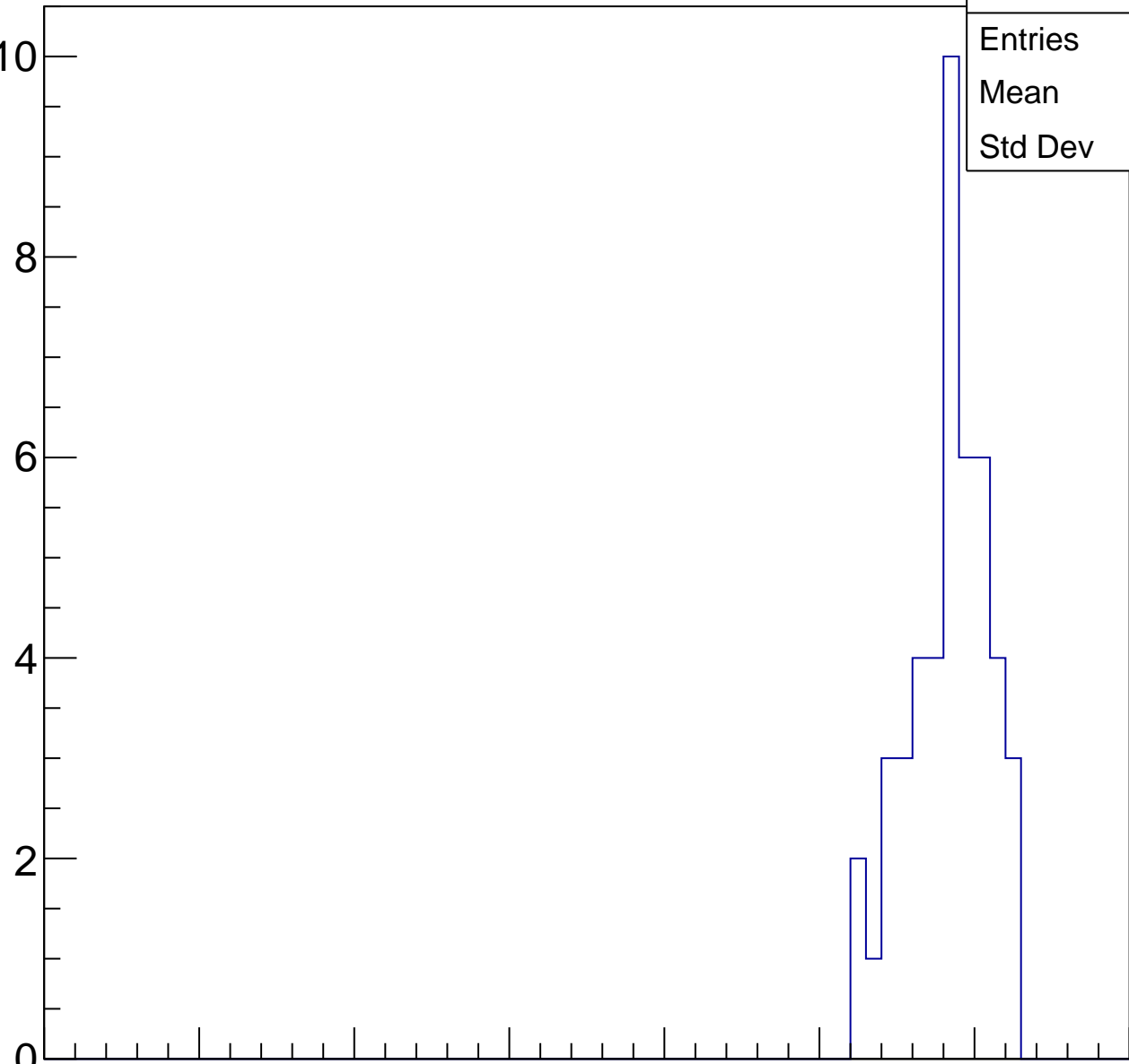
Entries	46
Mean	57.83
Std Dev	2.573

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

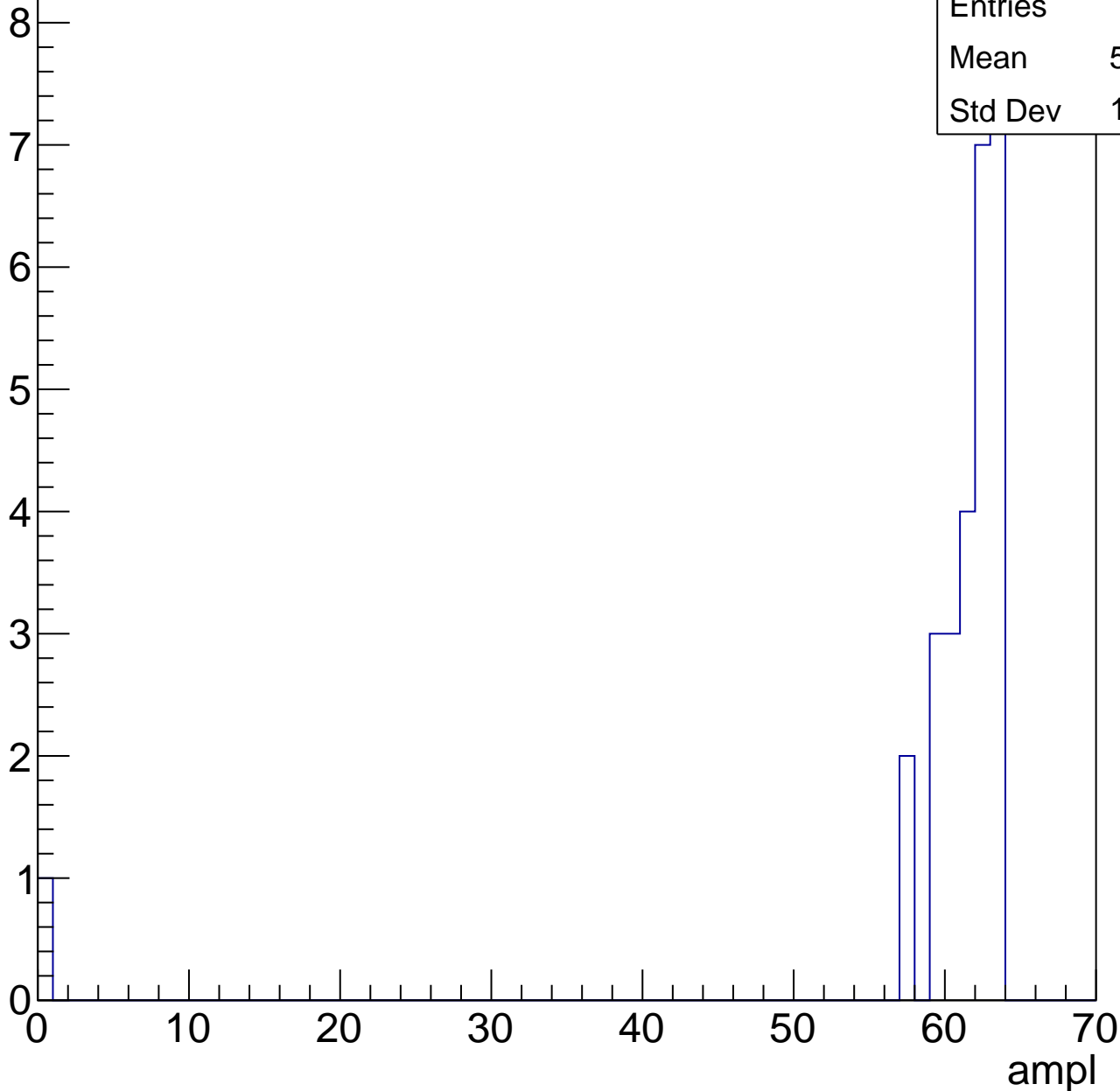


B1L003S, U13-ch79, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	28
Mean	59.04
Std Dev	11.49



B1L003S, U13-ch79, adc6

calib_packv5_042523_0143.root, FC#13, port D2

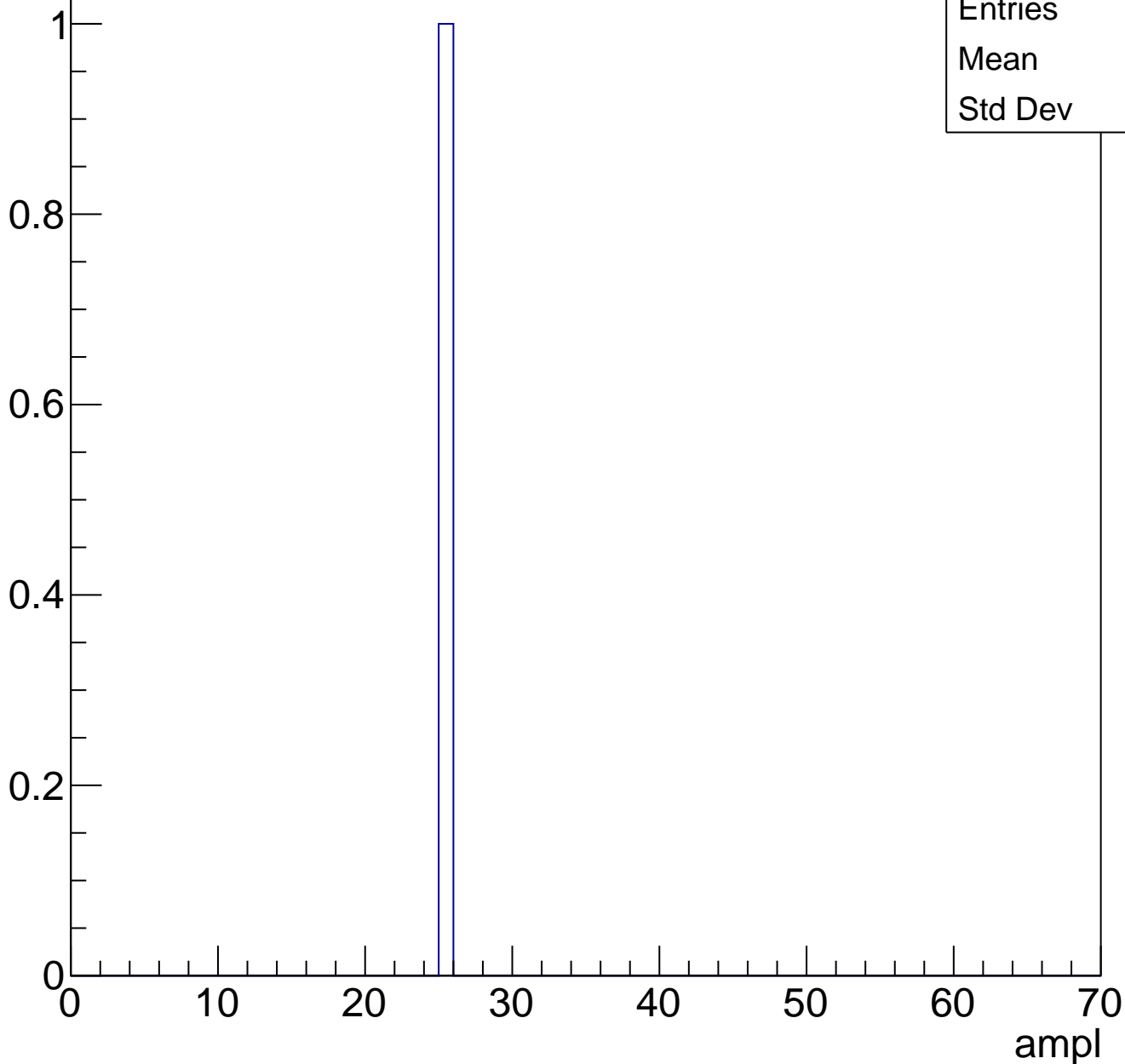
Entry



B1L003S, U13-ch79, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch80, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	29.23
Std Dev	3.404

Gaus mean : 29.7694

Gaus Width: 3.2606

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

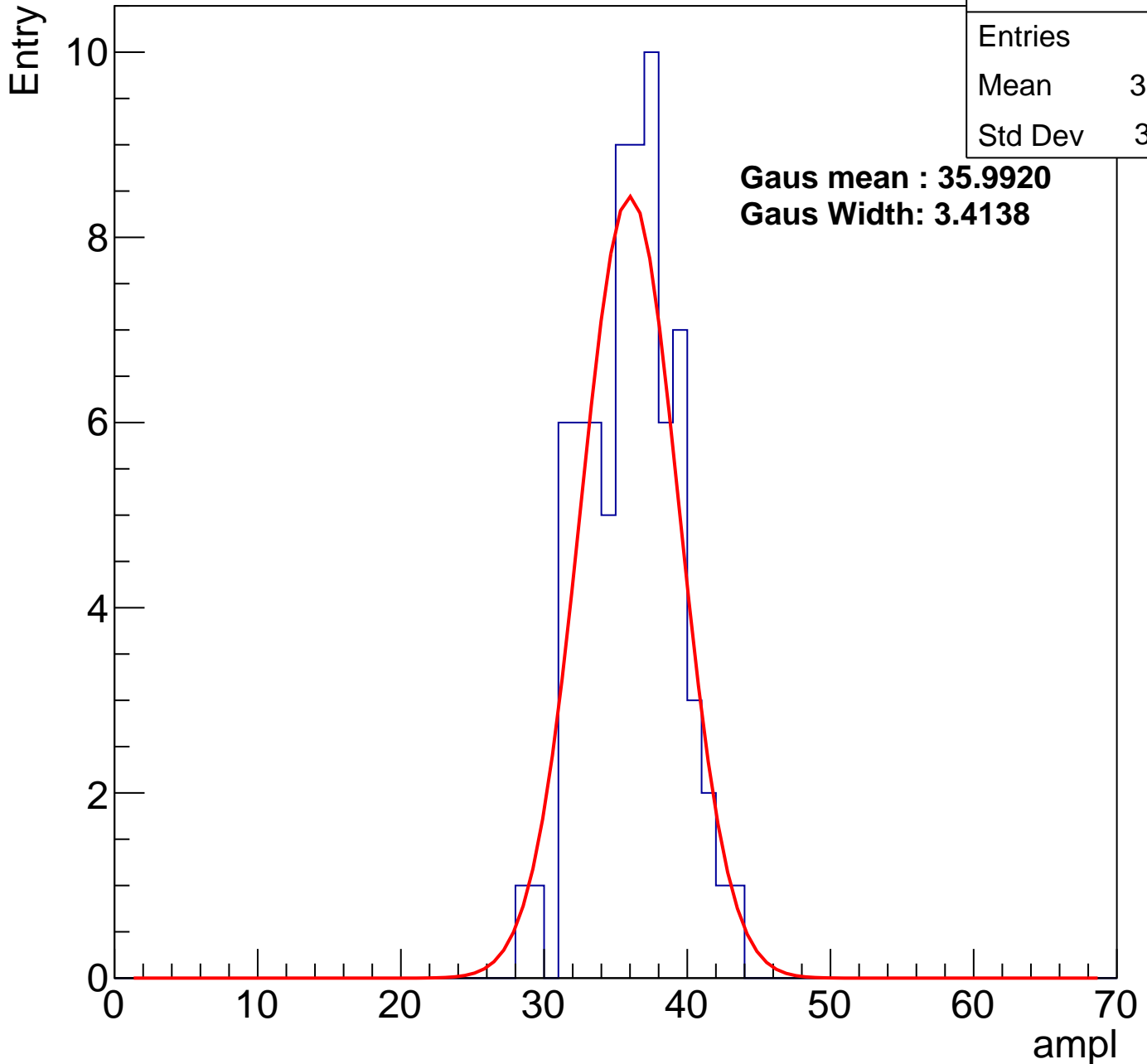
70

B1L003S, U13-ch80, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	35.62
Std Dev	3.121

Gaus mean : 35.9920
Gaus Width: 3.4138



B1L003S, U13-ch80, adc2

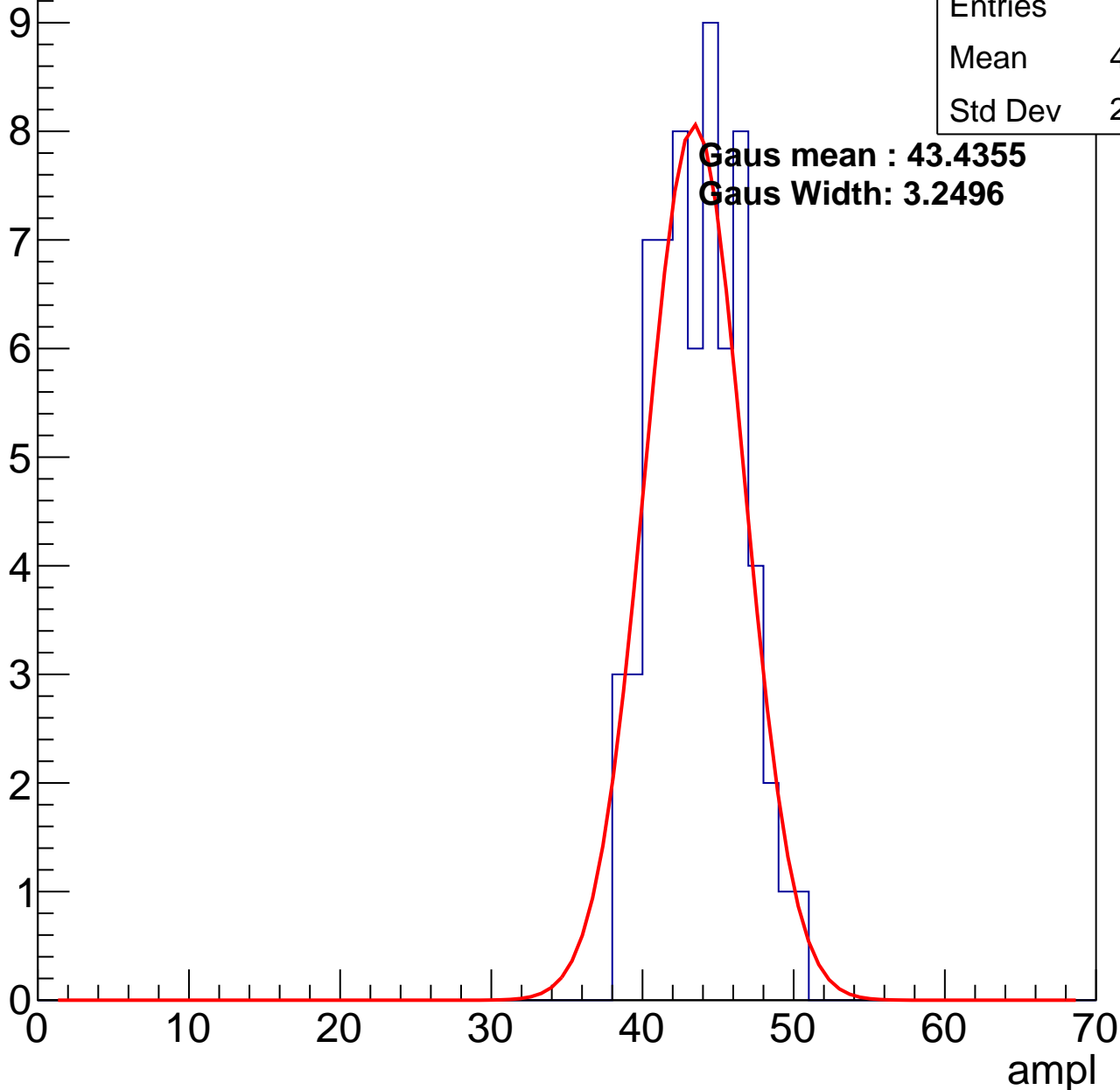
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.22
Std Dev	2.837

Gaus mean : 43.4355

Gaus Width: 3.2496

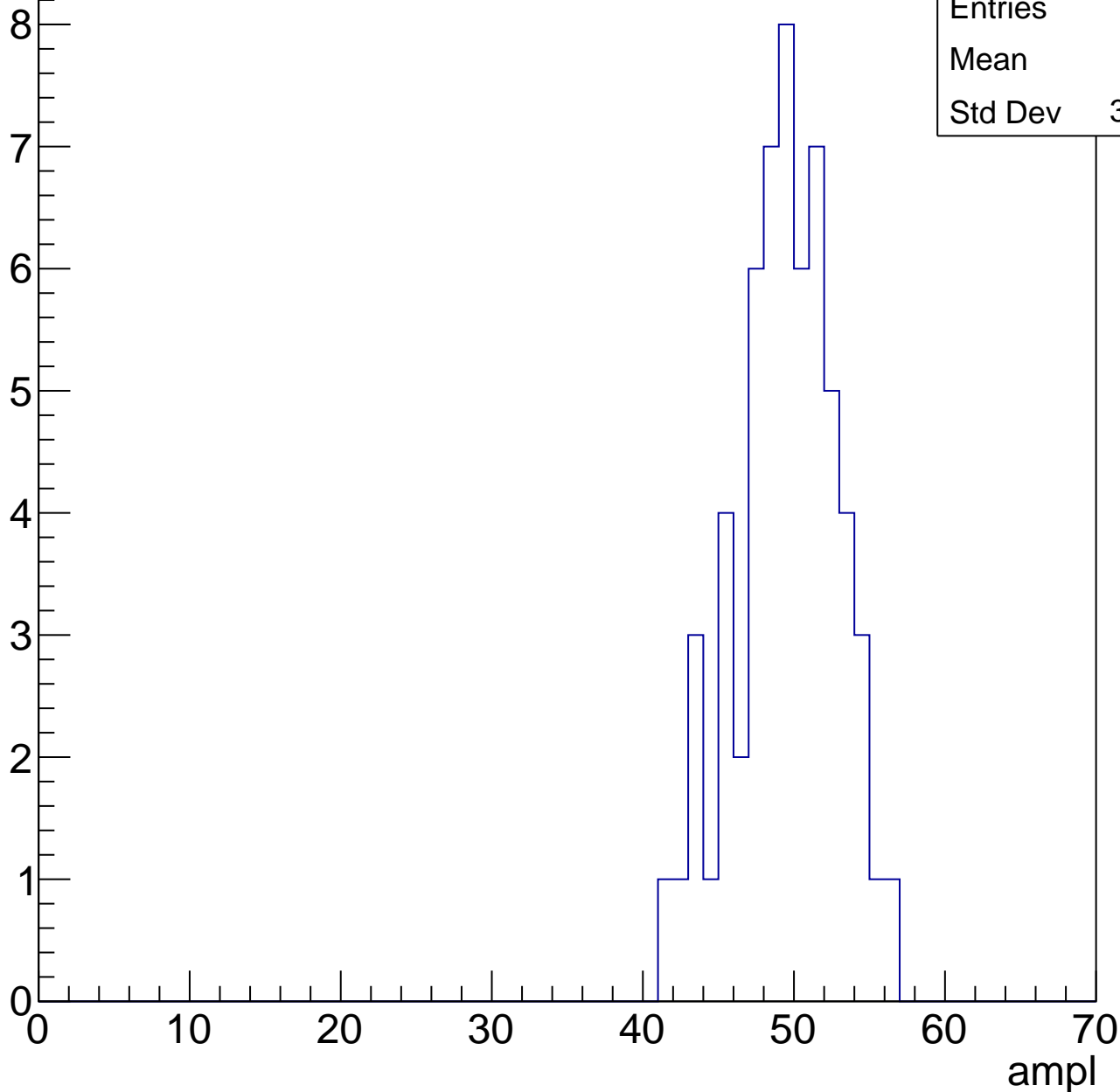


B1L003S, U13-ch80, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

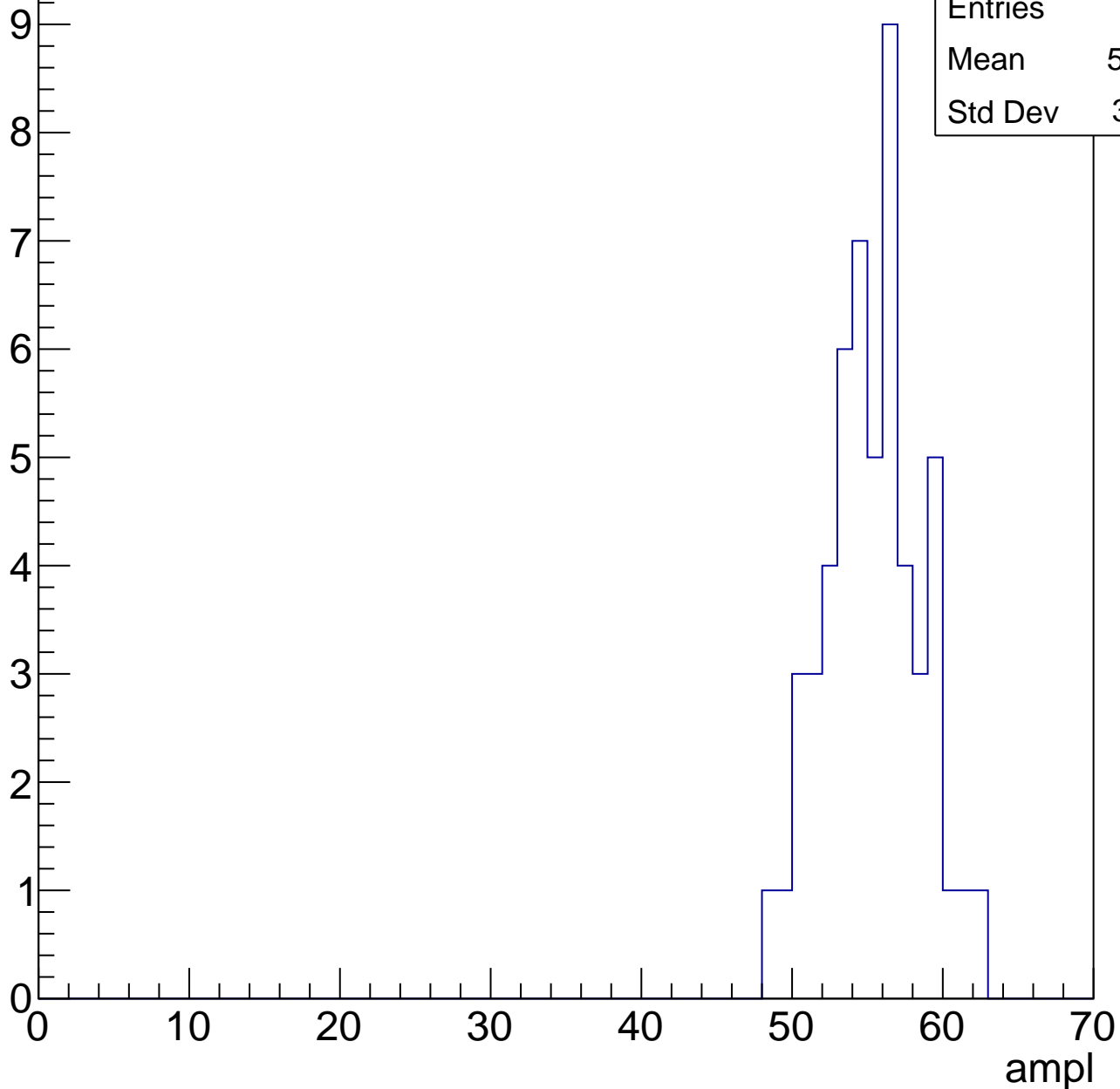
Entries	60
Mean	49
Std Dev	3.322



B1L003S, U13-ch80, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

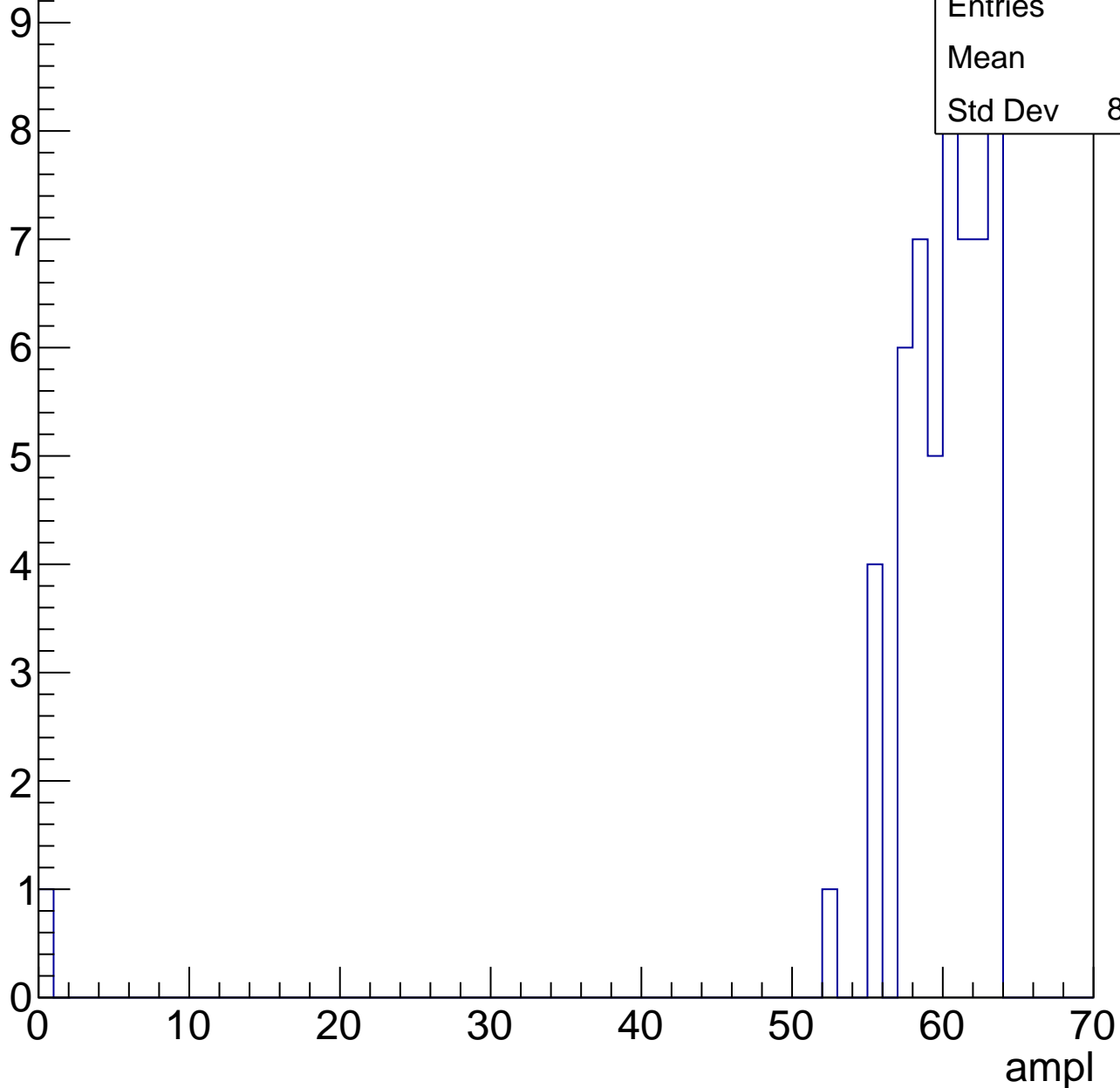


Entries	54
Mean	54.87
Std Dev	3.091

B1L003S, U13-ch80, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch80, adc6

calib_packv5_042523_0143.root, FC#13, port D2

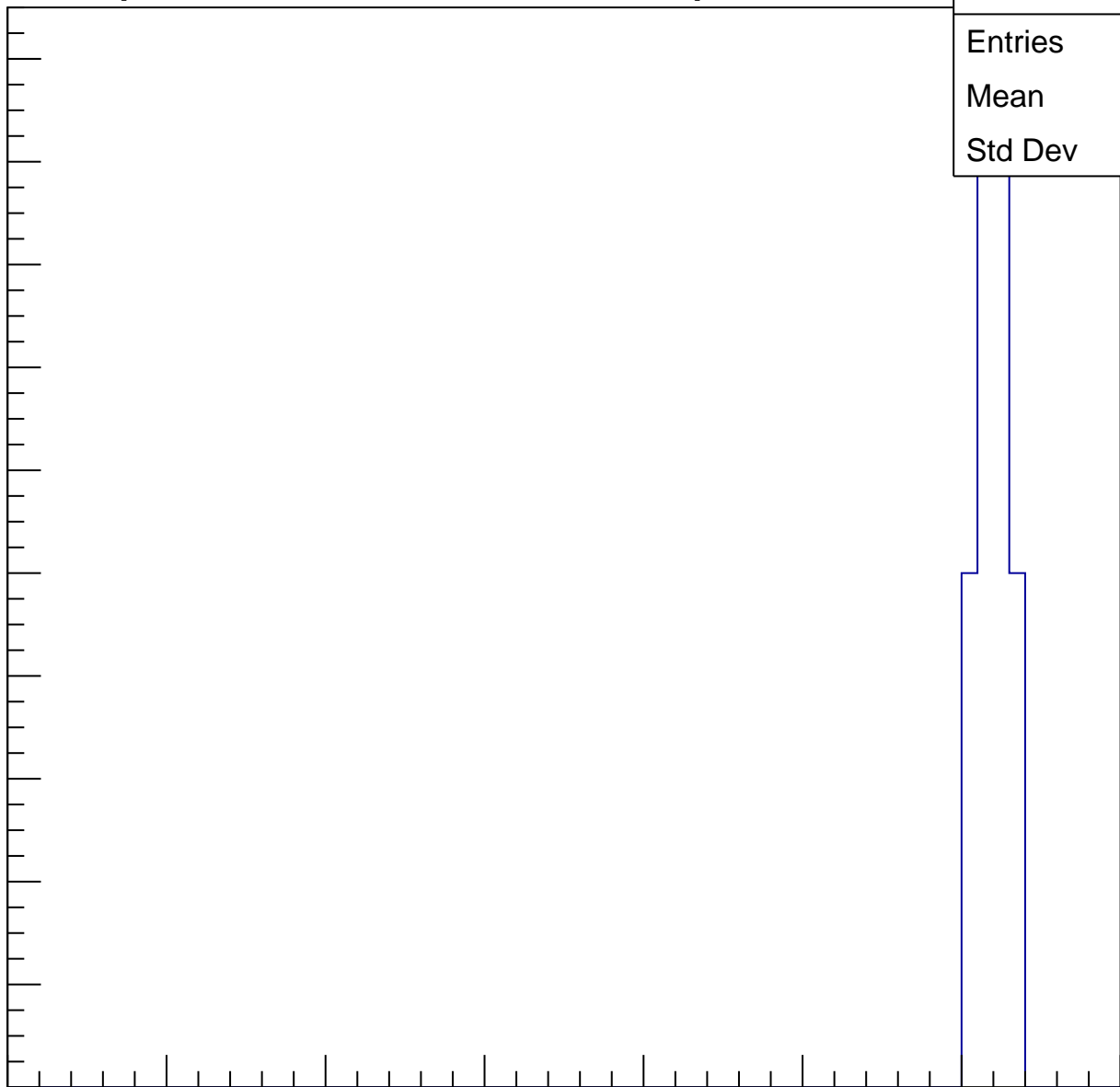
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	0.9574

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch80, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch81, adc0

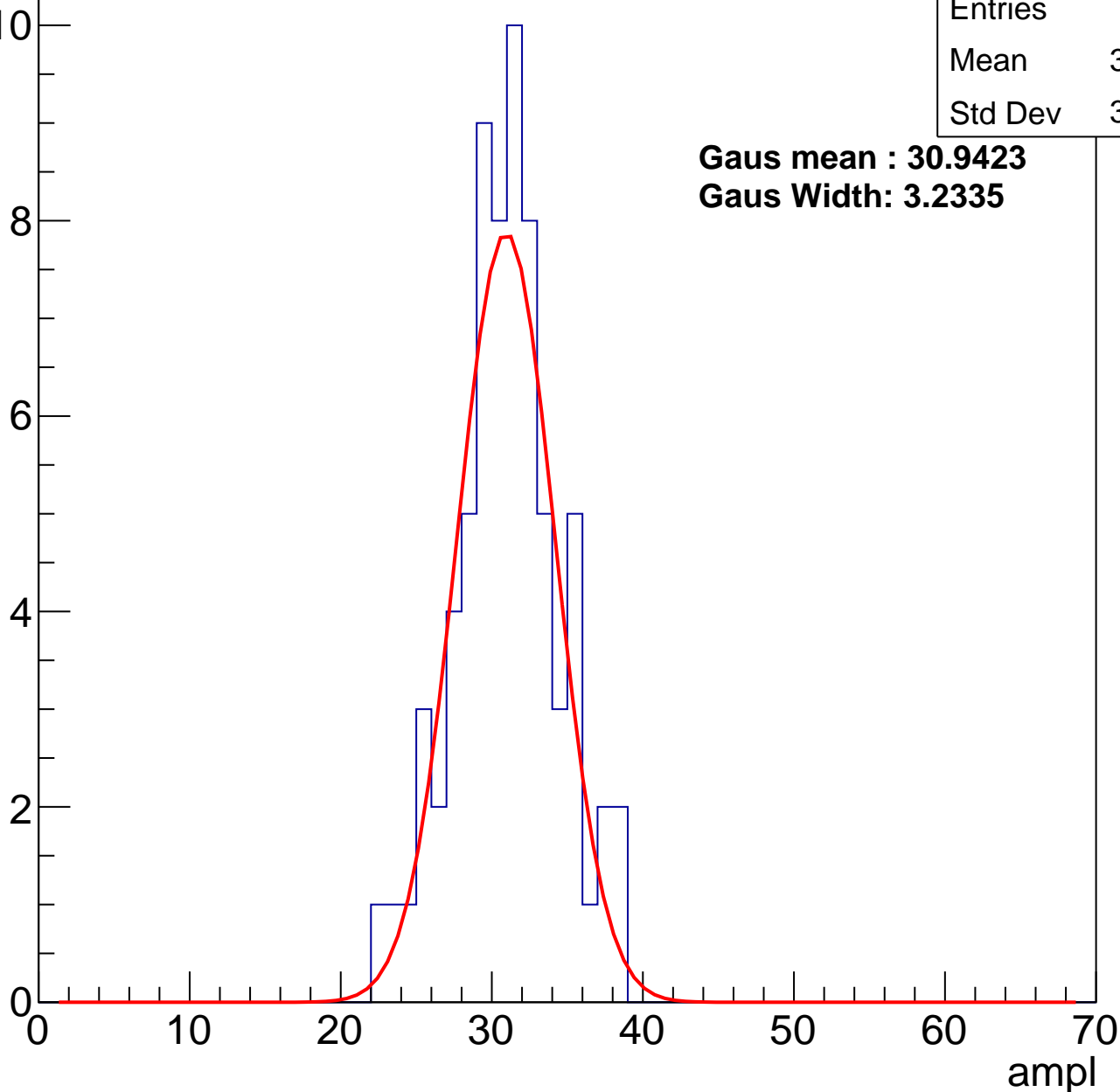
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.56
Std Dev	3.425

Gaus mean : 30.9423

Gaus Width: 3.2335



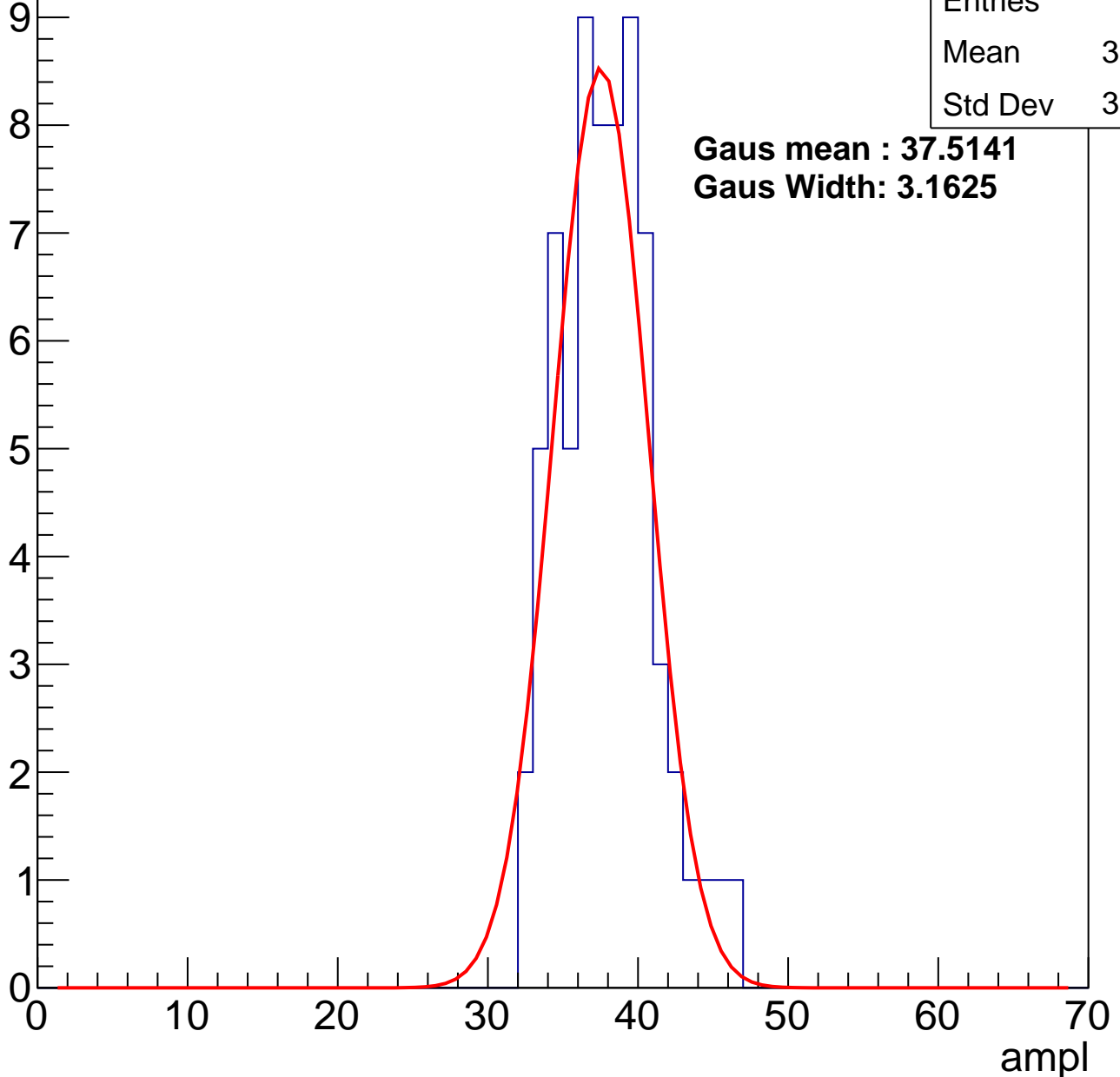
B1L003S, U13-ch81, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	37.42
Std Dev	3.057

Gaus mean : 37.5141
Gaus Width: 3.1625



B1L003S, U13-ch81, adc2

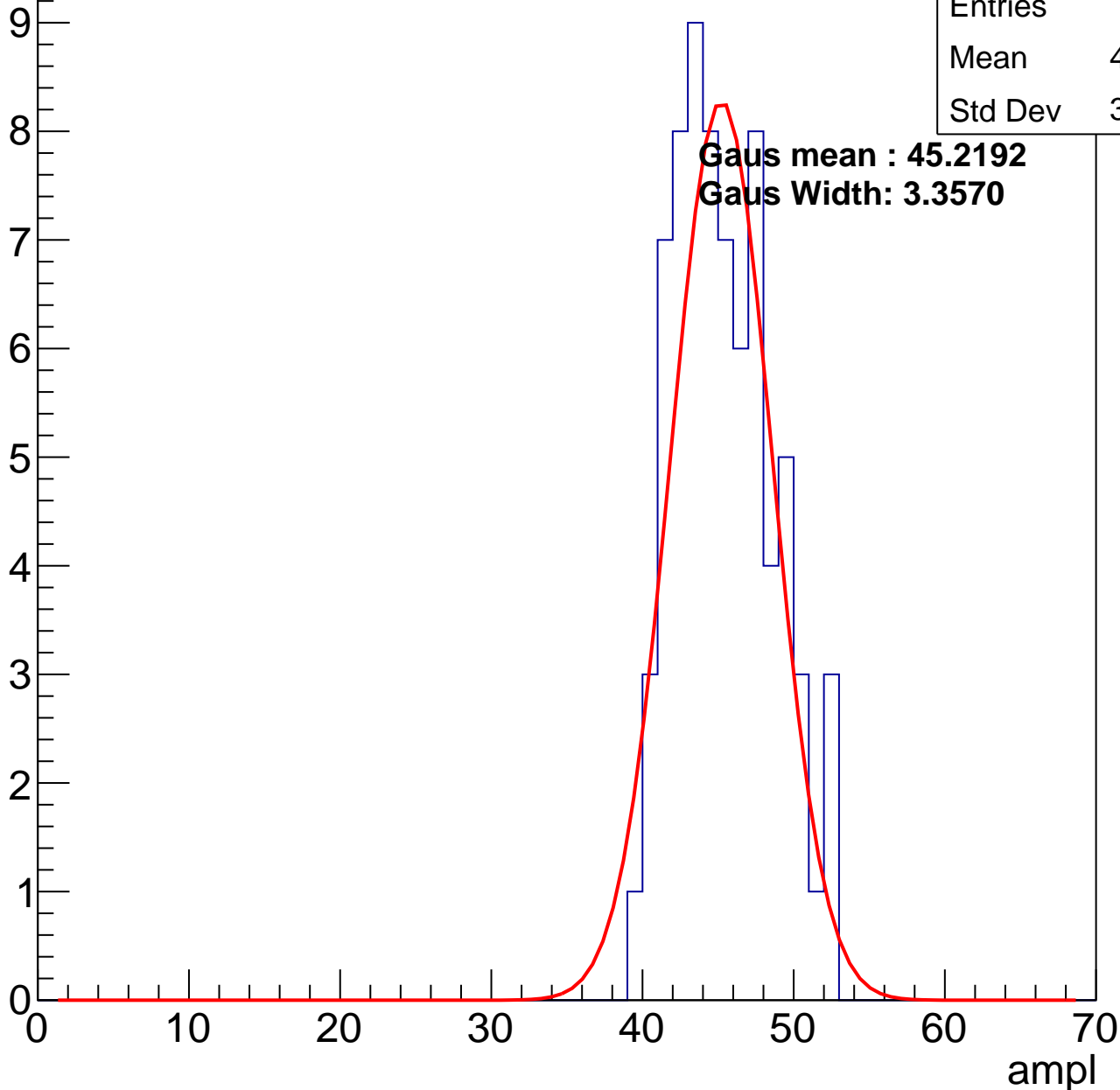
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	44.96
Std Dev	3.207

Gaus mean : 45.2192

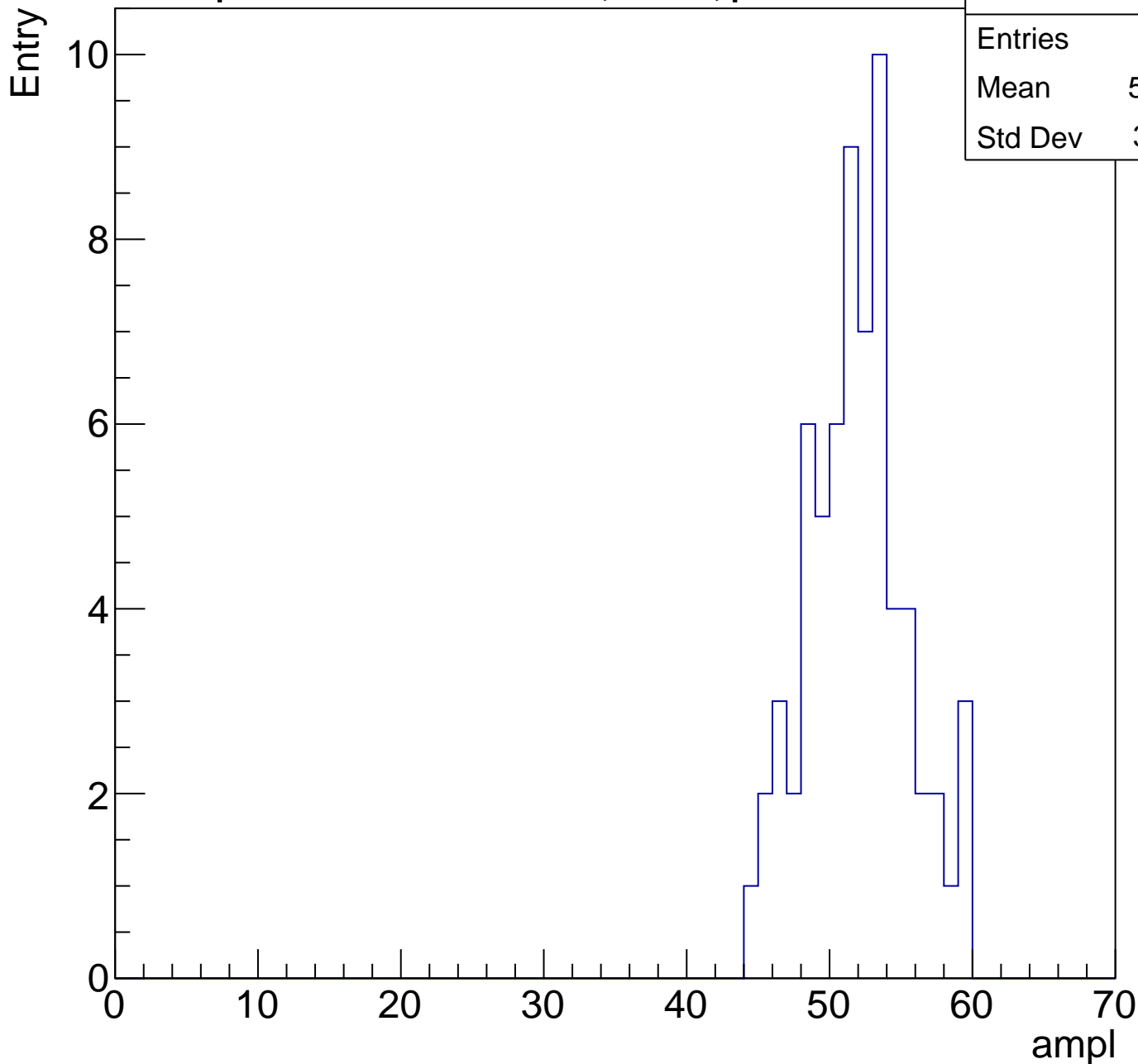
Gaus Width: 3.3570



B1L003S, U13-ch81, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	51.48
Std Dev	3.461

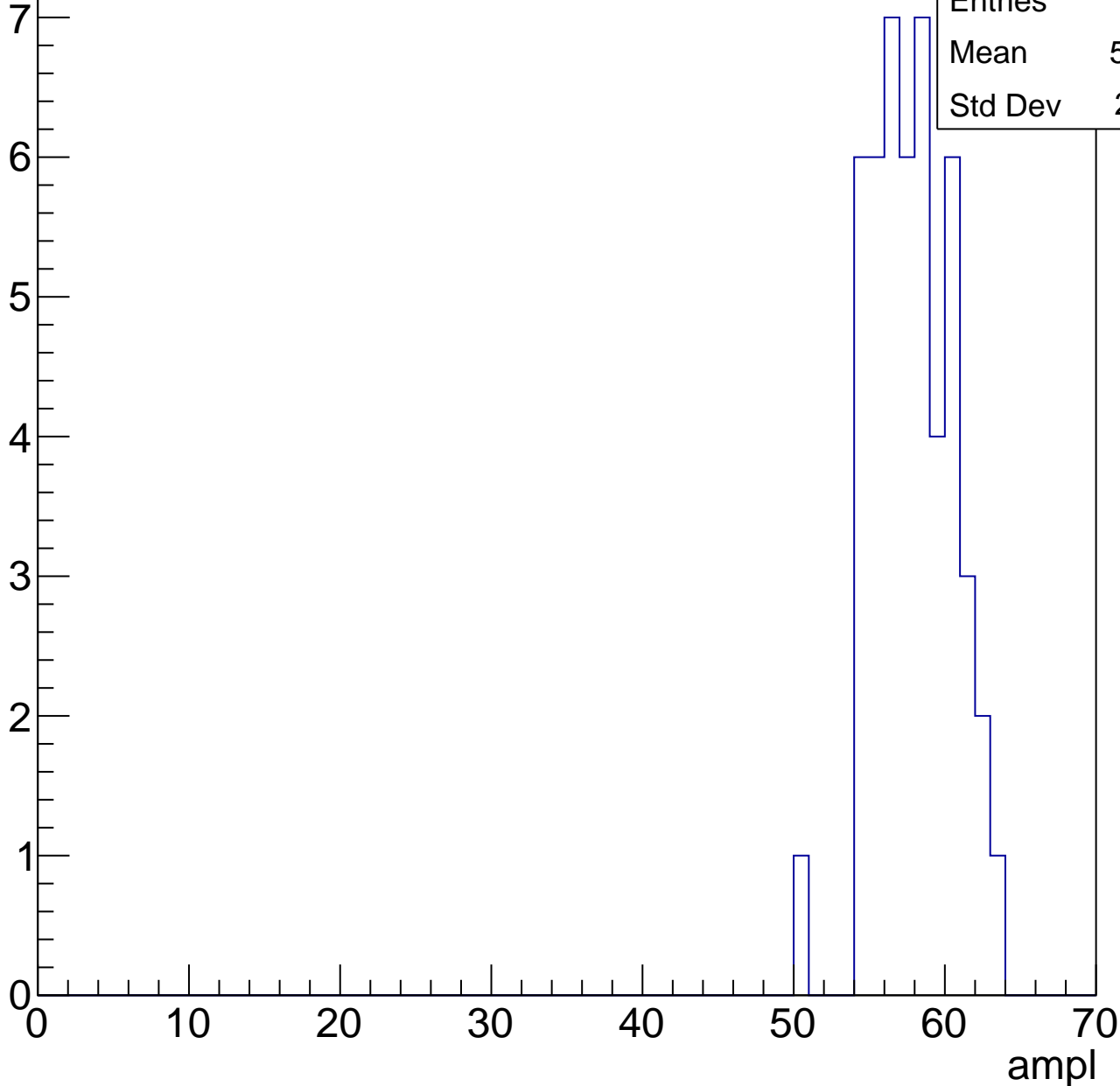


B1L003S, U13-ch81, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	57.35
Std Dev	2.631

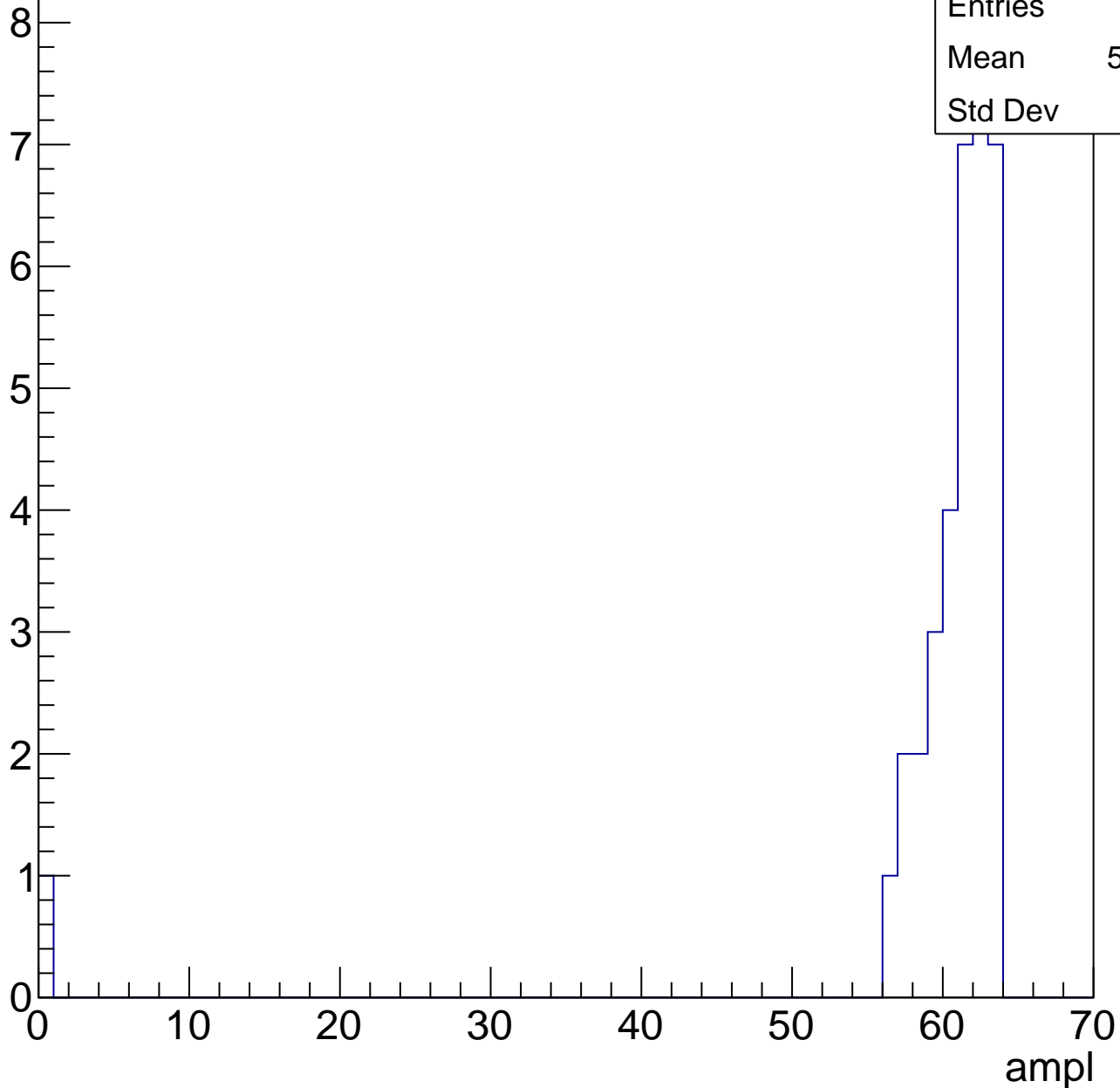


B1L003S, U13-ch81, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	59.06
Std Dev	10.3



B1L003S, U13-ch81, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U13-ch81, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch82, adc0

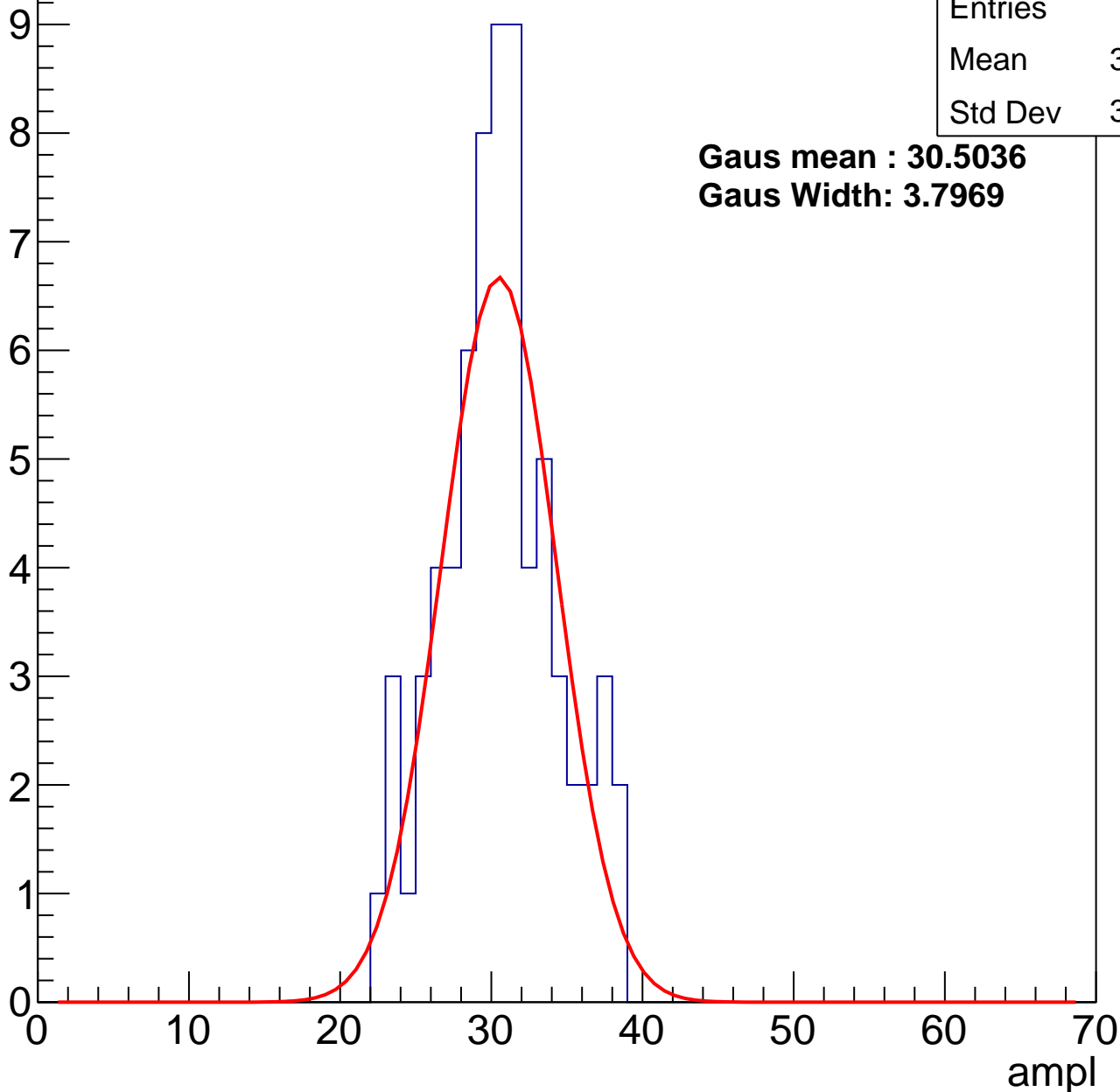
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	30.07
Std Dev	3.747

Gaus mean : 30.5036

Gaus Width: 3.7969



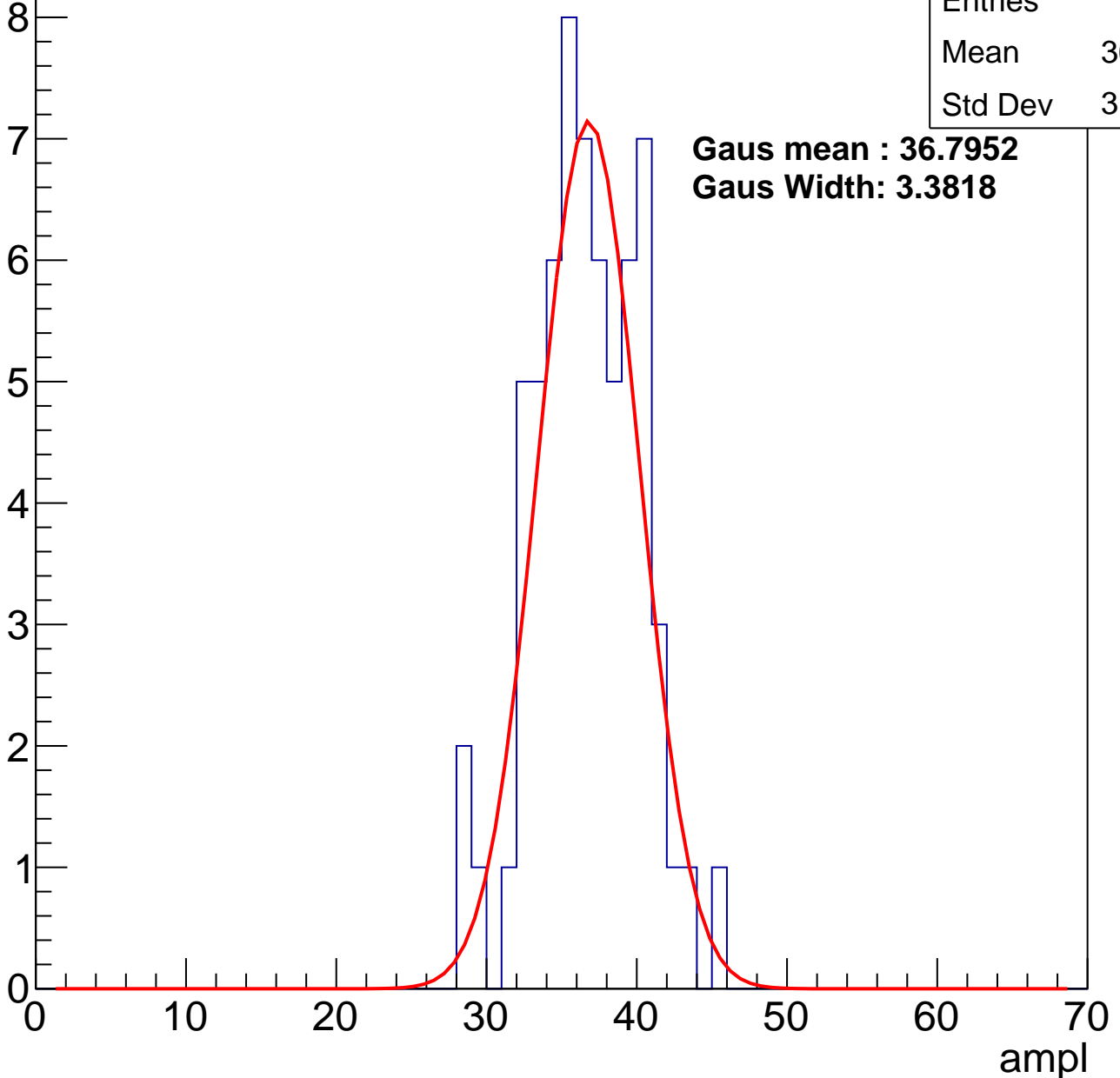
B1L003S, U13-ch82, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.25
Std Dev	3.482

Gaus mean : 36.7952
Gaus Width: 3.3818



B1L003S, U13-ch82, adc2

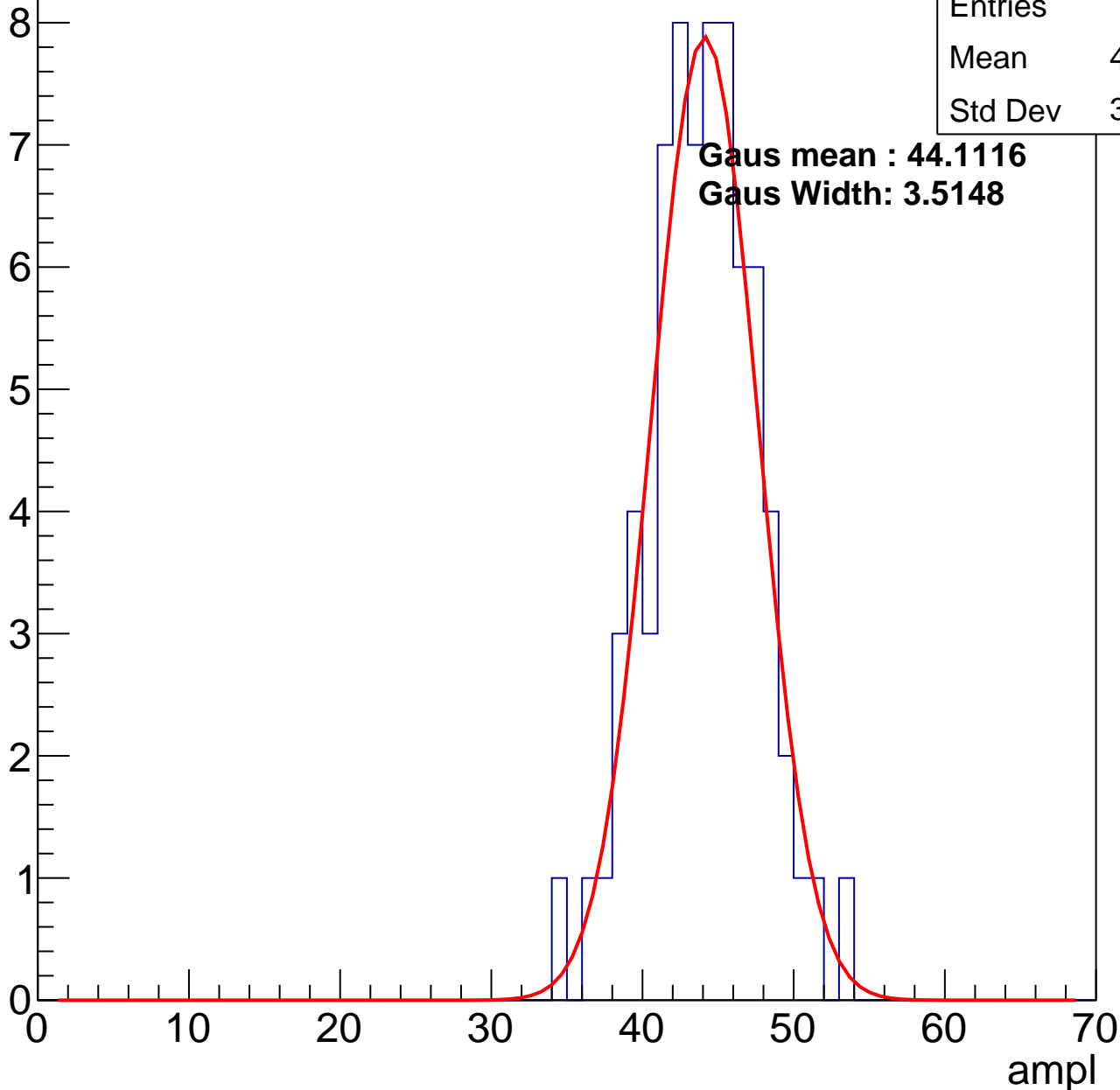
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.54
Std Dev	3.578

Gaus mean : 44.1116

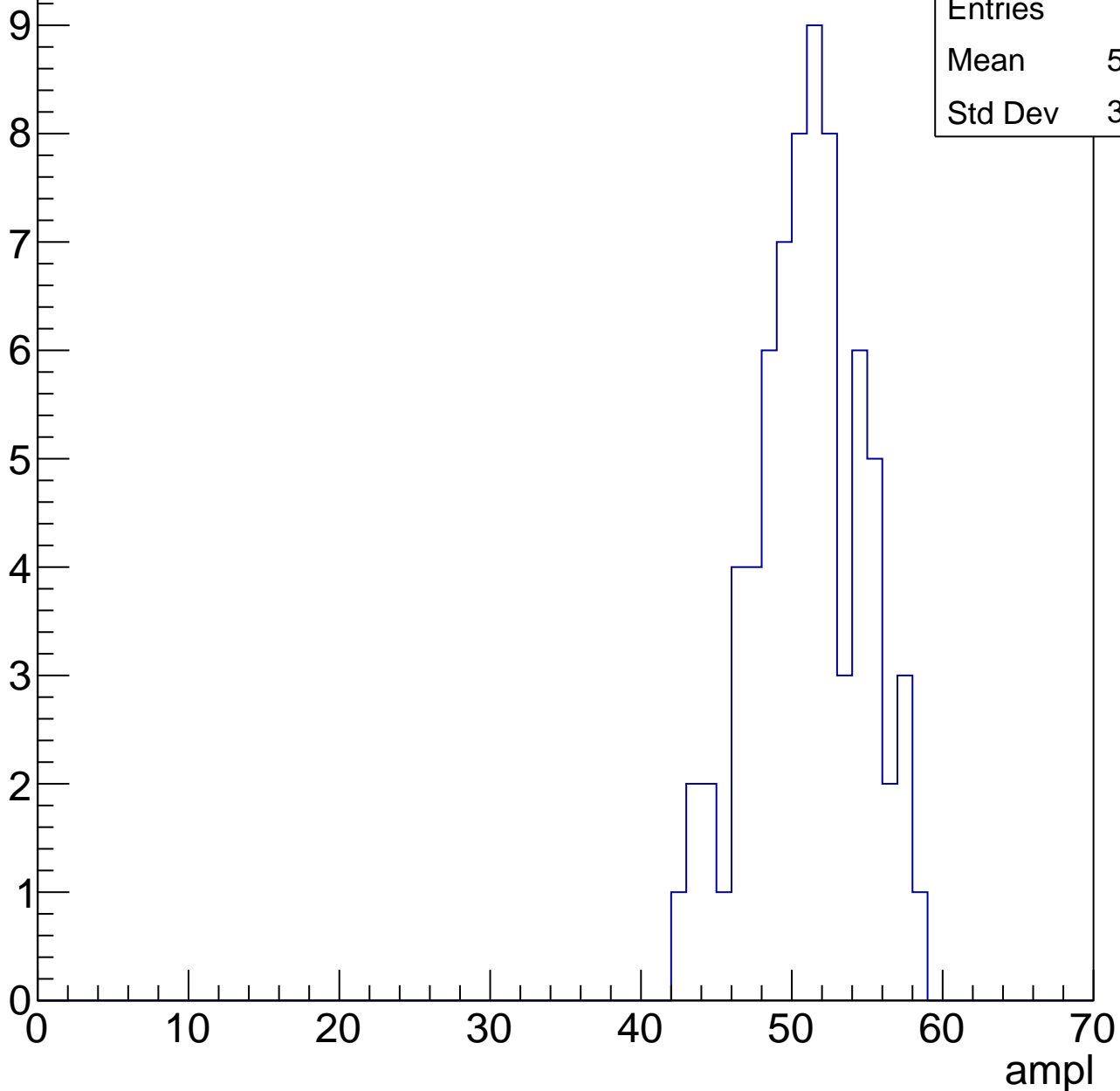
Gaus Width: 3.5148



B1L003S, U13-ch82, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



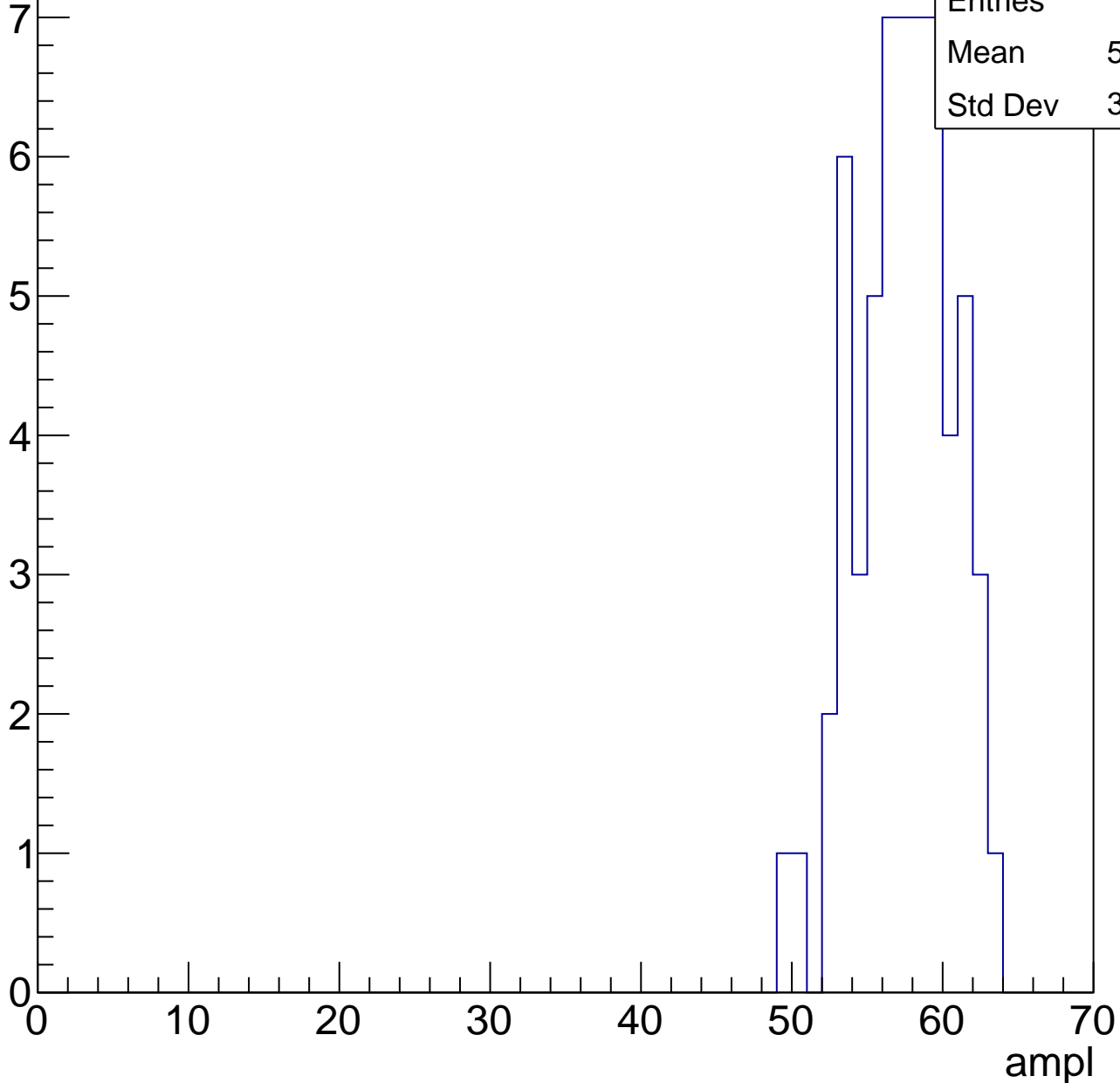
Entries	72
Mean	50.53
Std Dev	3.617

B1L003S, U13-ch82, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.98
Std Dev	3.122



B1L003S, U13-ch82, adc5

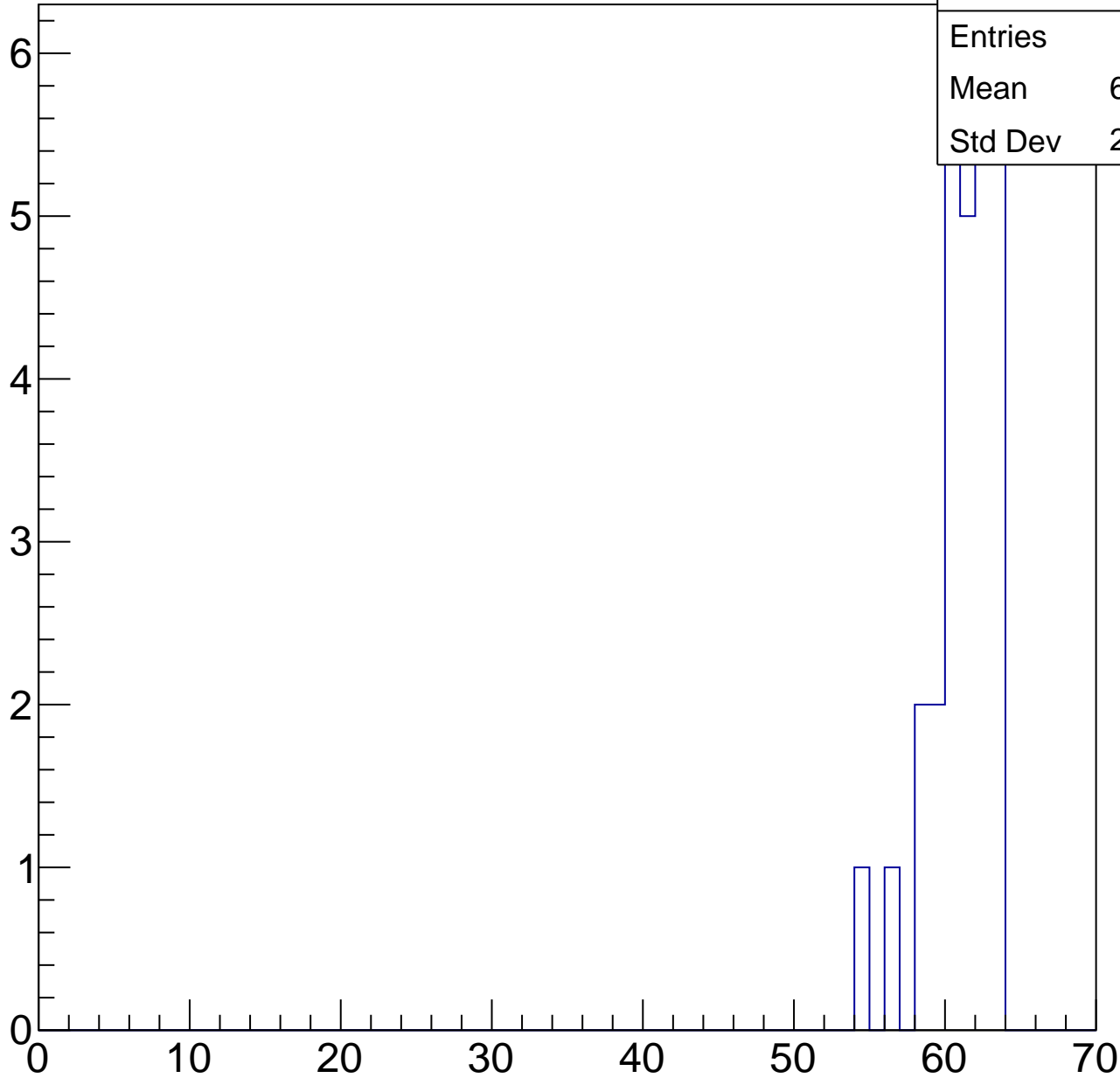
calib_packv5_042523_0143.root, FC#13, port D2

Entry

6
5
4
3
2
1
0

Entries	29
Mean	60.66
Std Dev	2.138

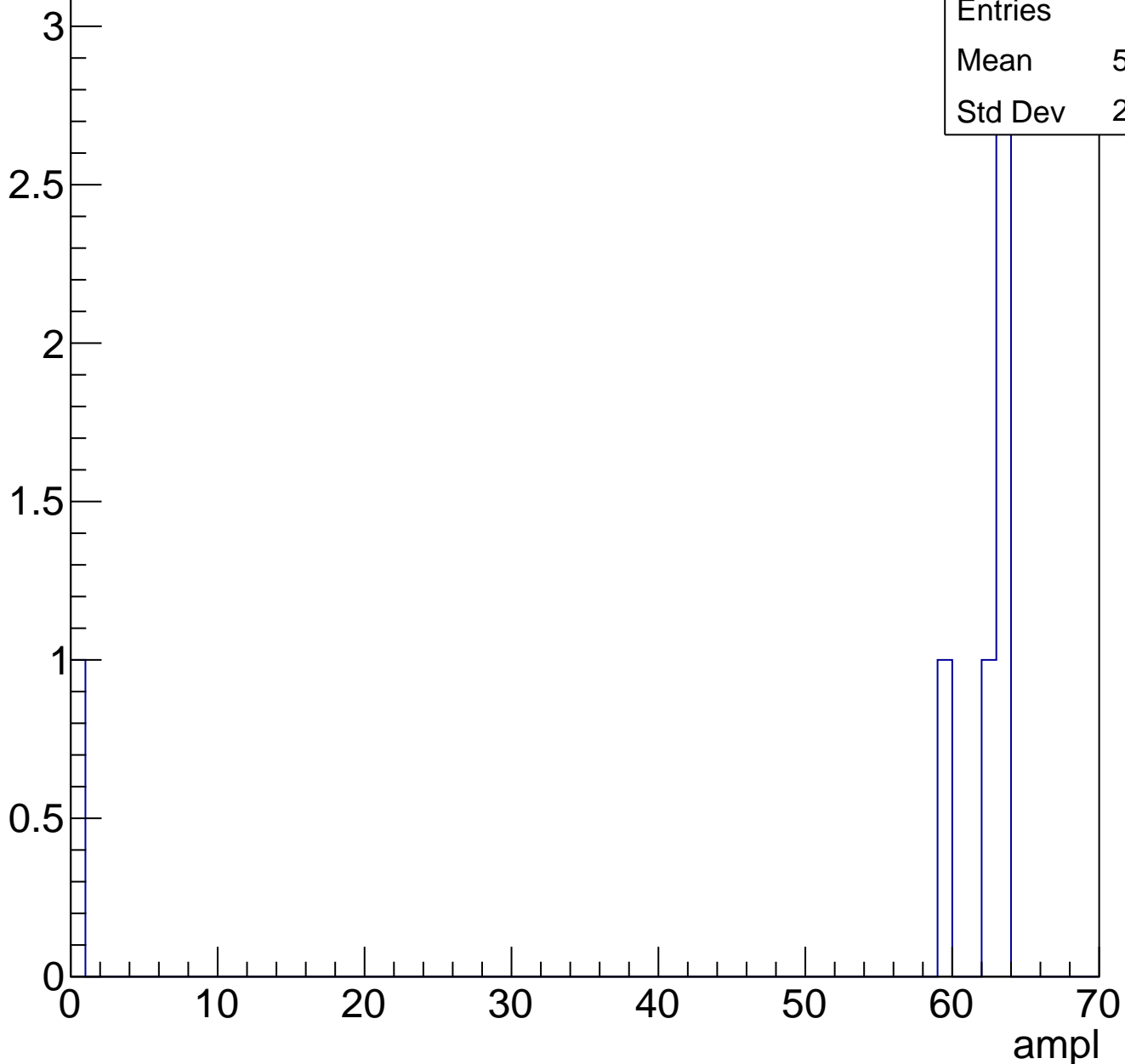
ampl



B1L003S, U13-ch82, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch82, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch83, adc0

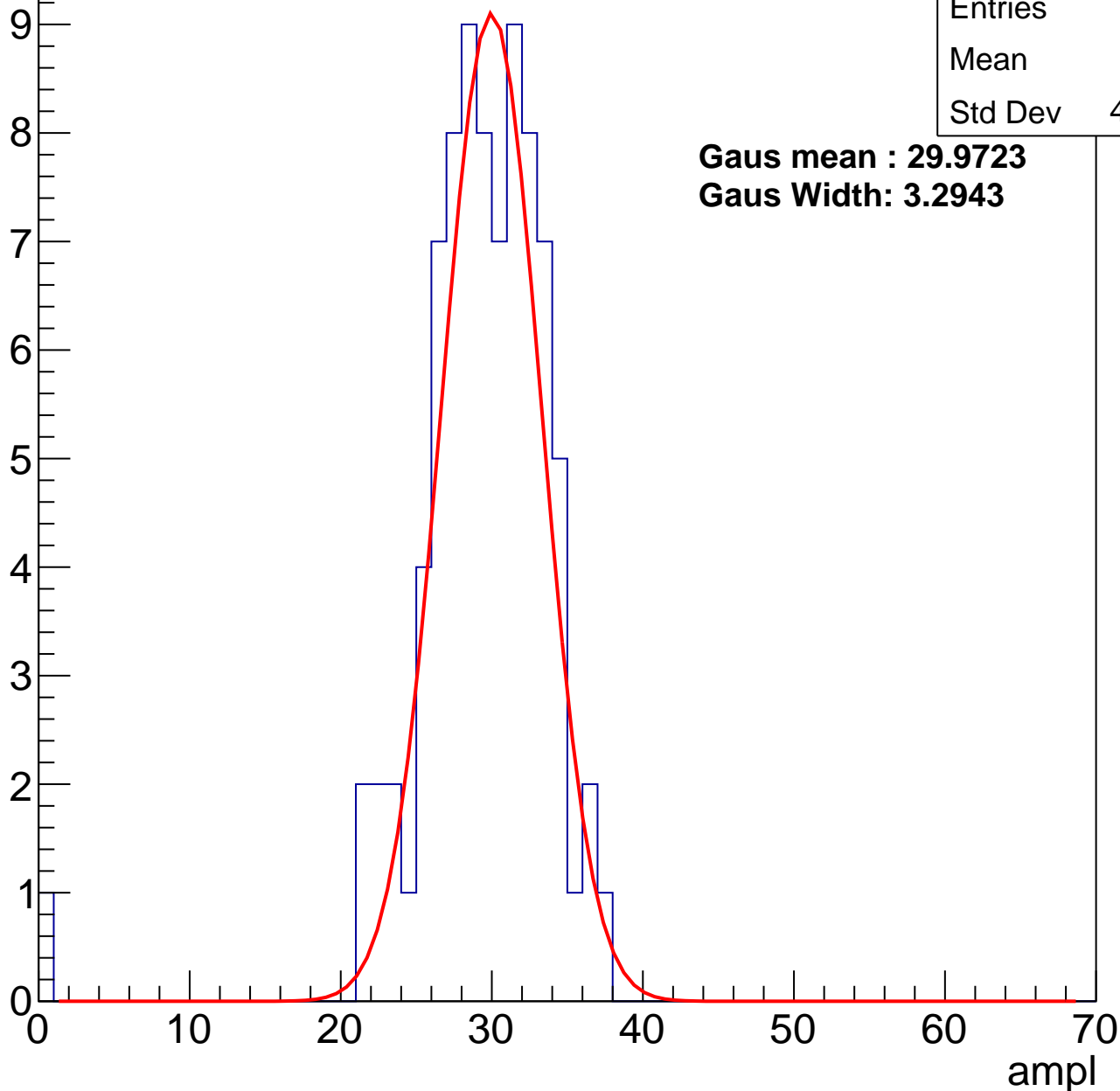
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	28.9
Std Dev	4.732

Gaus mean : 29.9723

Gaus Width: 3.2943



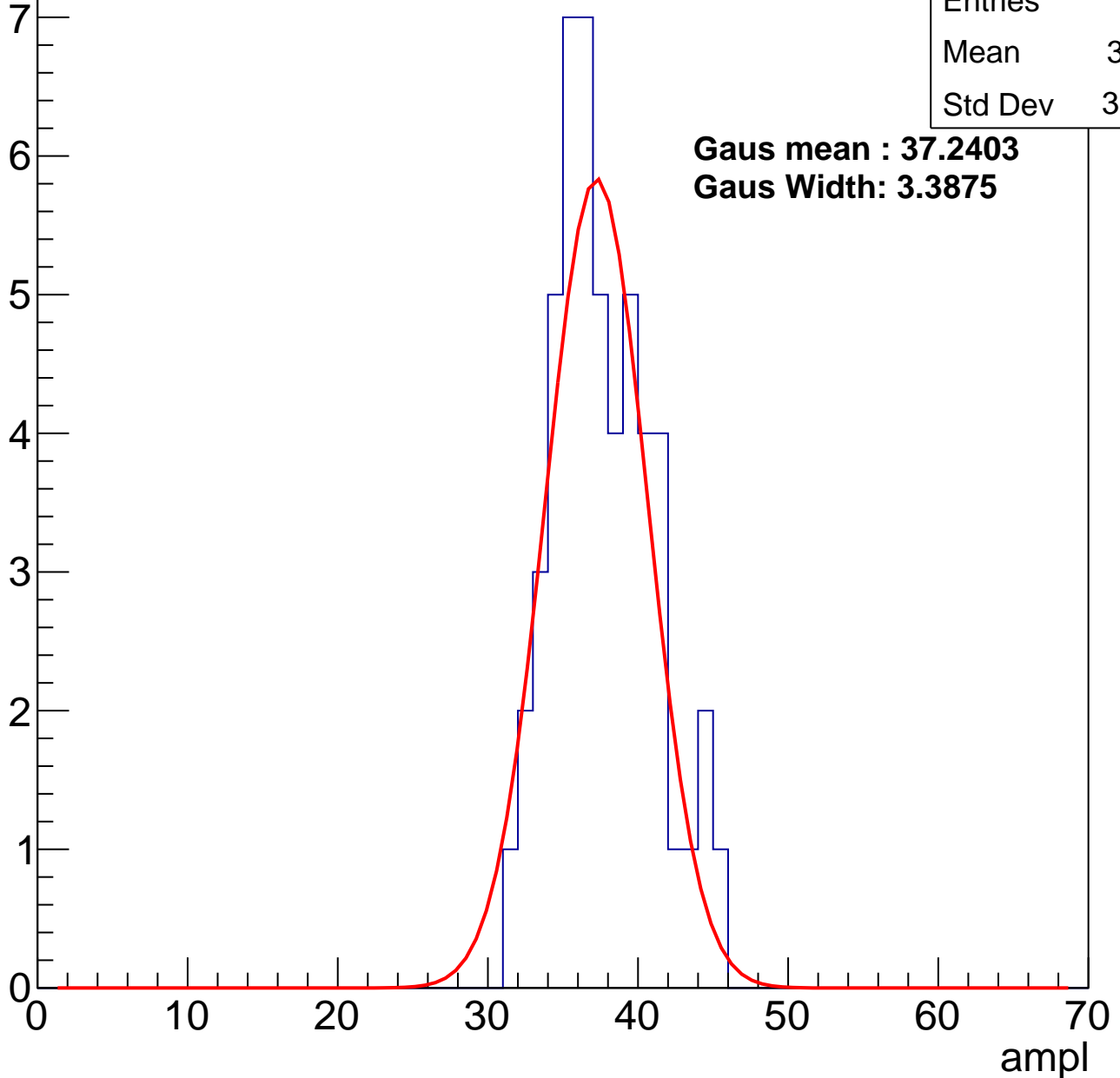
B1L003S, U13-ch83, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	37.21
Std Dev	3.278

Gaus mean : 37.2403
Gaus Width: 3.3875



B1L003S, U13-ch83, adc2

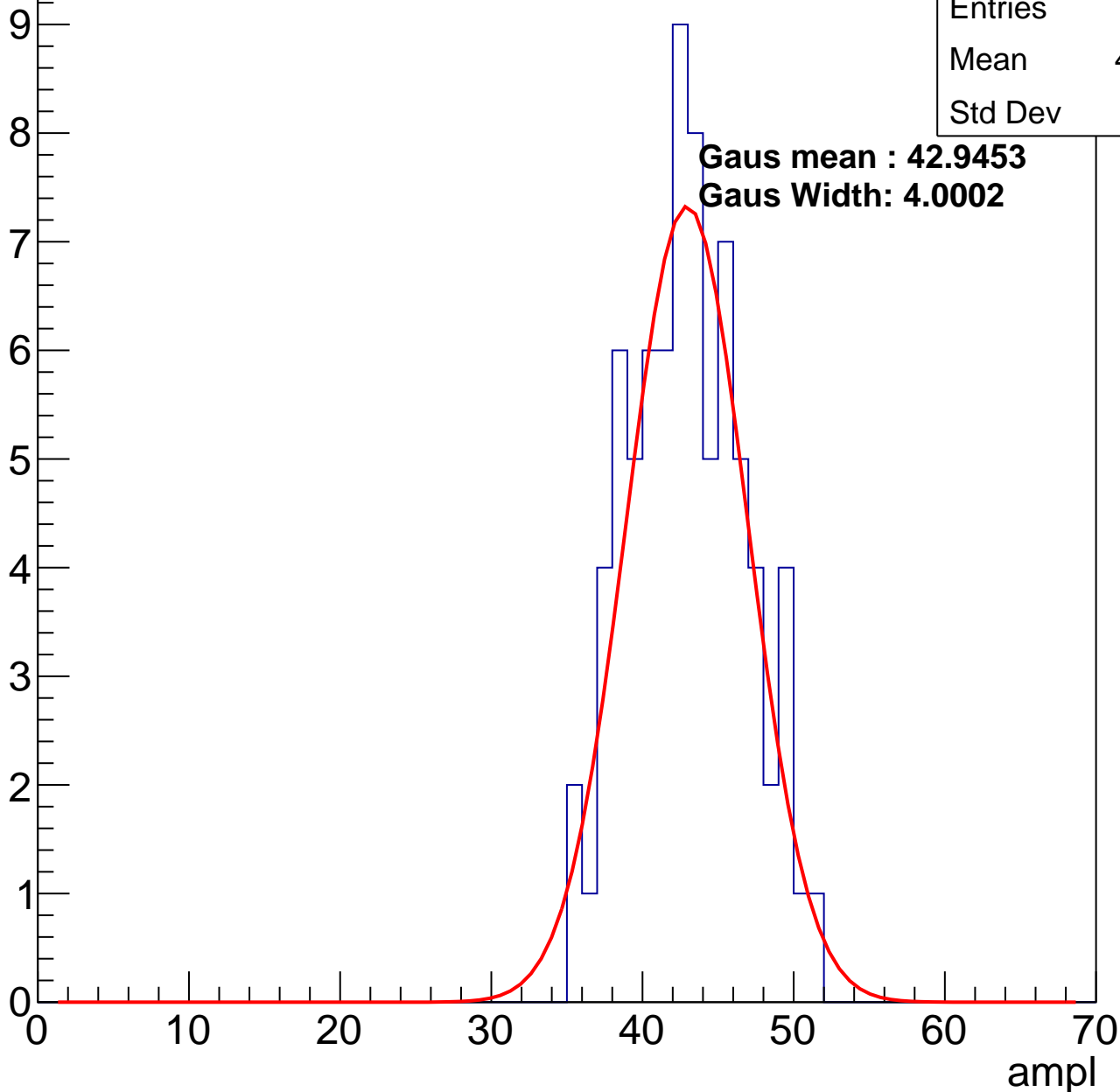
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.51
Std Dev	3.75

Gaus mean : 42.9453

Gaus Width: 4.0002

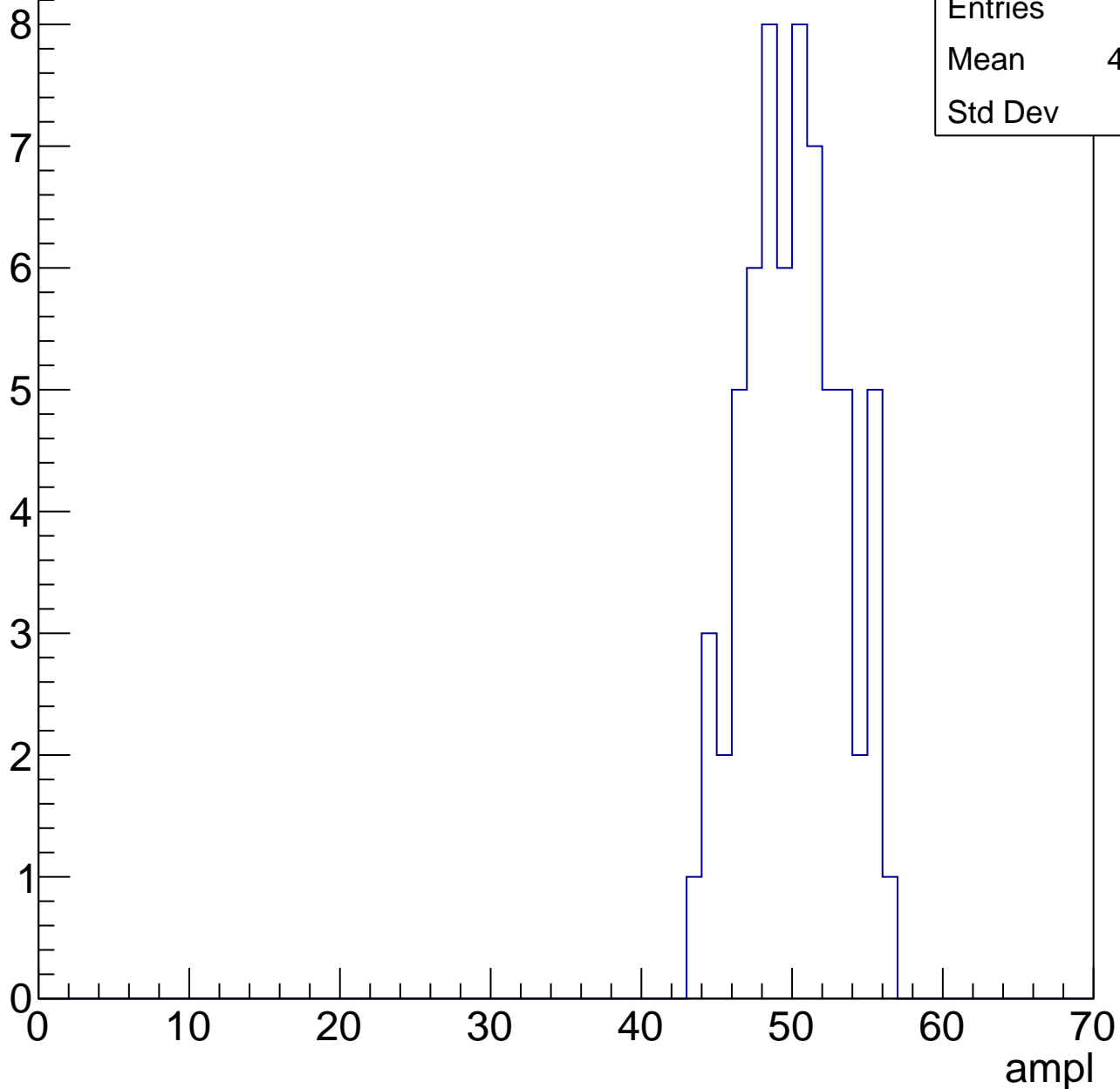


B1L003S, U13-ch83, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

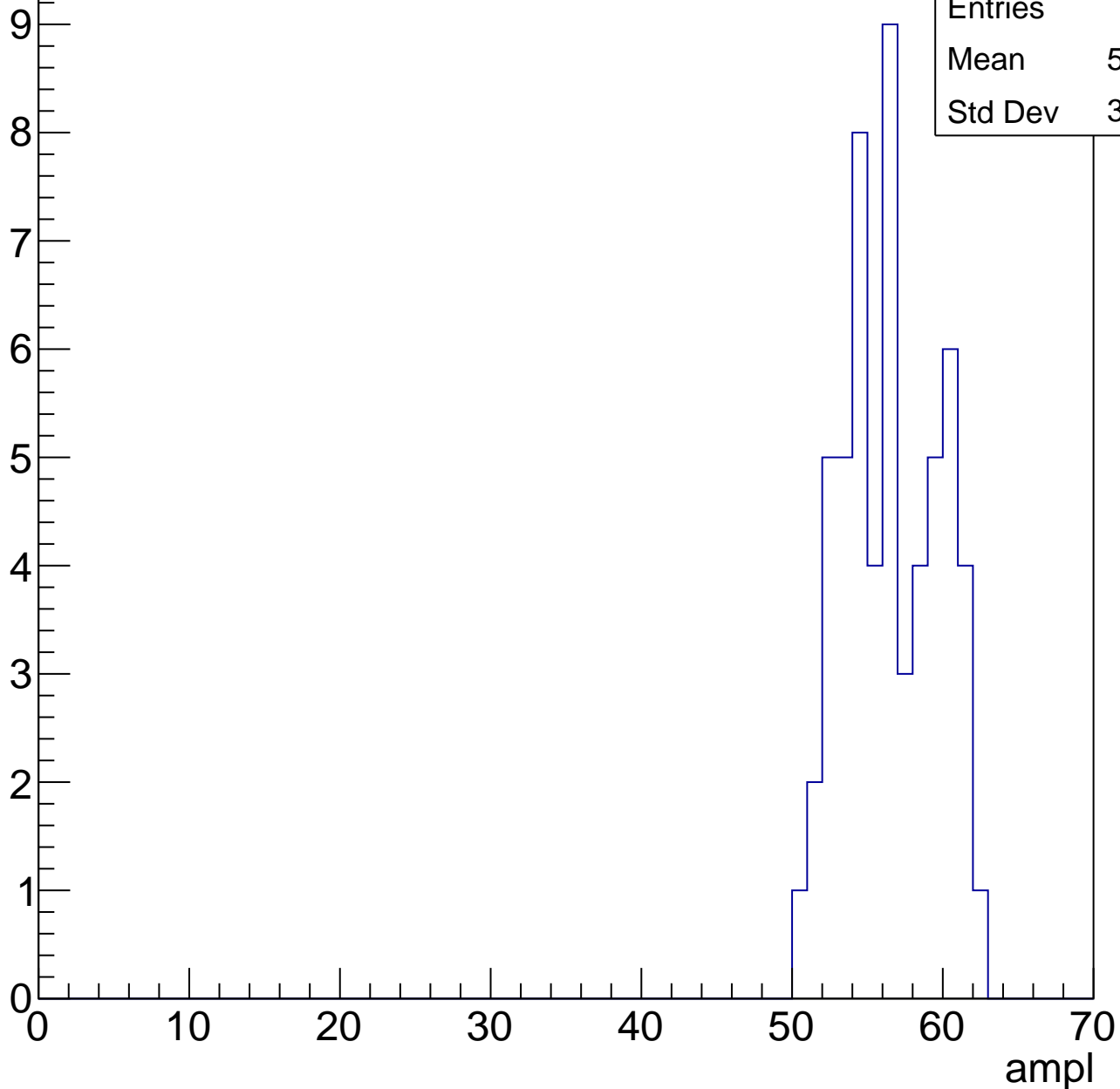
Entries	64
Mean	49.62
Std Dev	3.15



B1L003S, U13-ch83, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



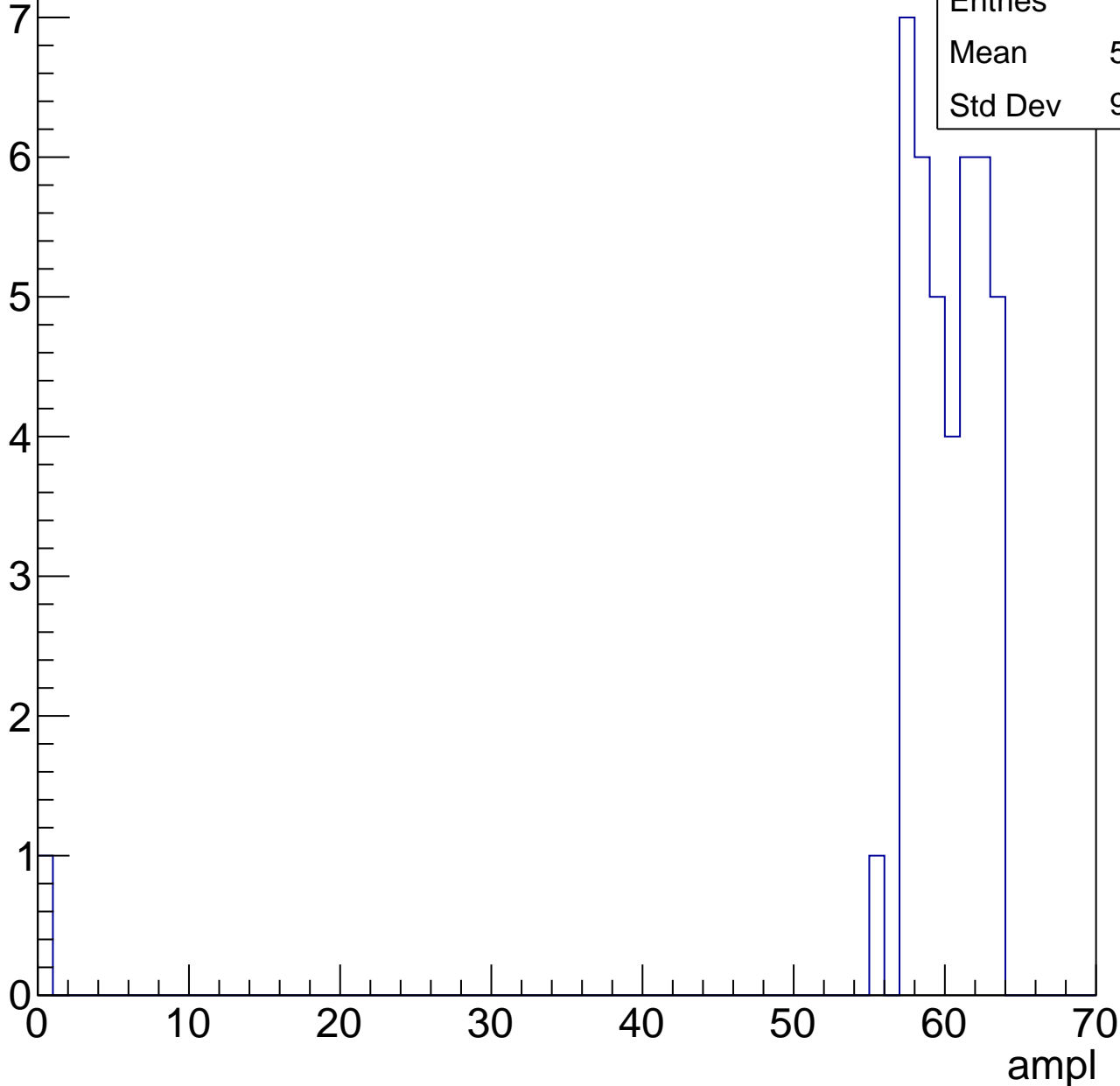
Entries	57
Mean	56.09
Std Dev	3.085

B1L003S, U13-ch83, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.29
Std Dev	9.464



B1L003S, U13-ch83, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	6
Mean	62.67
Std Dev	0.4714

B1L003S, U13-ch83, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch84, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	30.15
Std Dev	3.401

Gaus mean : 30.5444

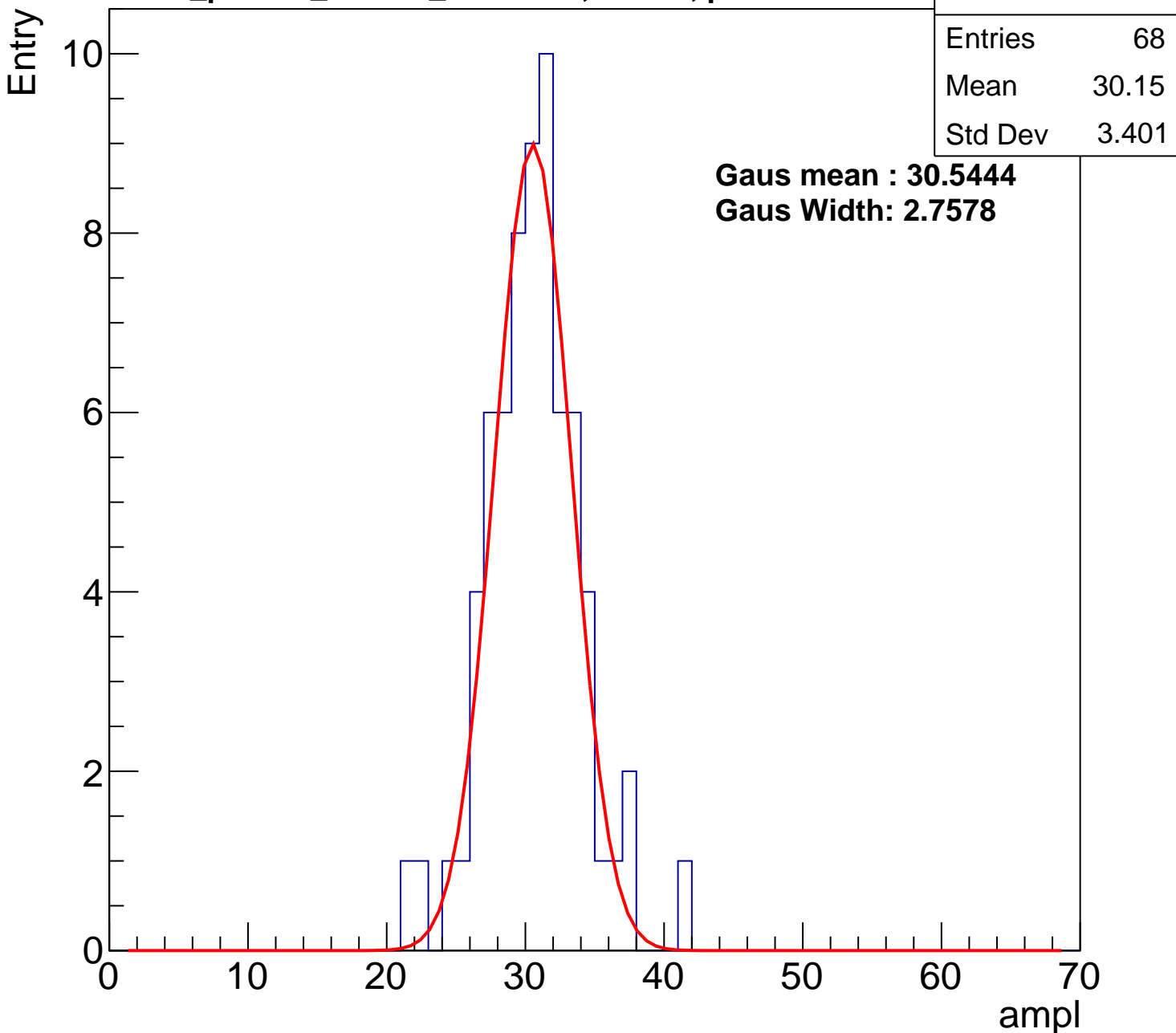
Gaus Width: 2.7578

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch84, adc1

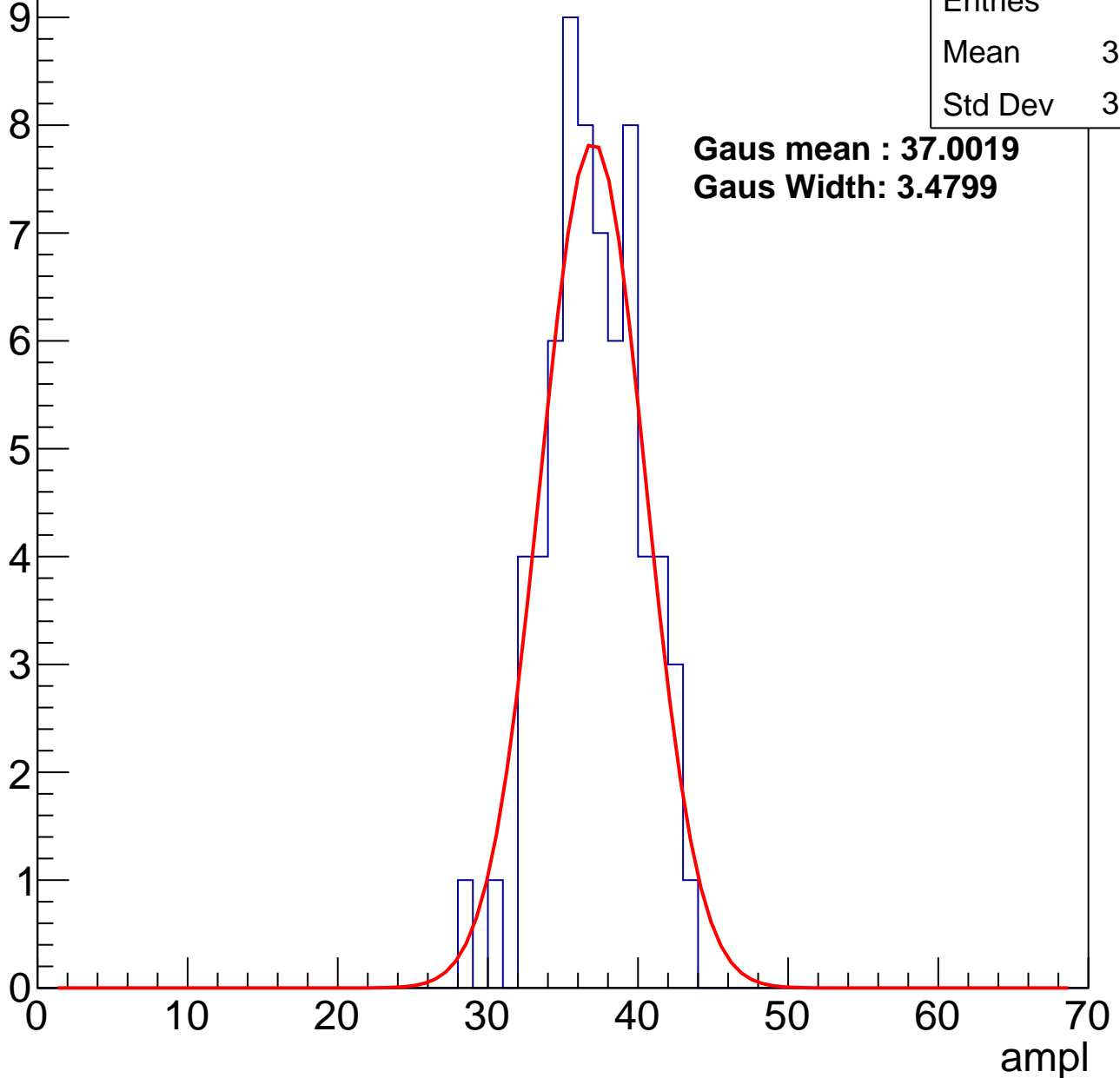
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.62
Std Dev	3.093

Gaus mean : 37.0019

Gaus Width: 3.4799



B1L003S, U13-ch84, adc2

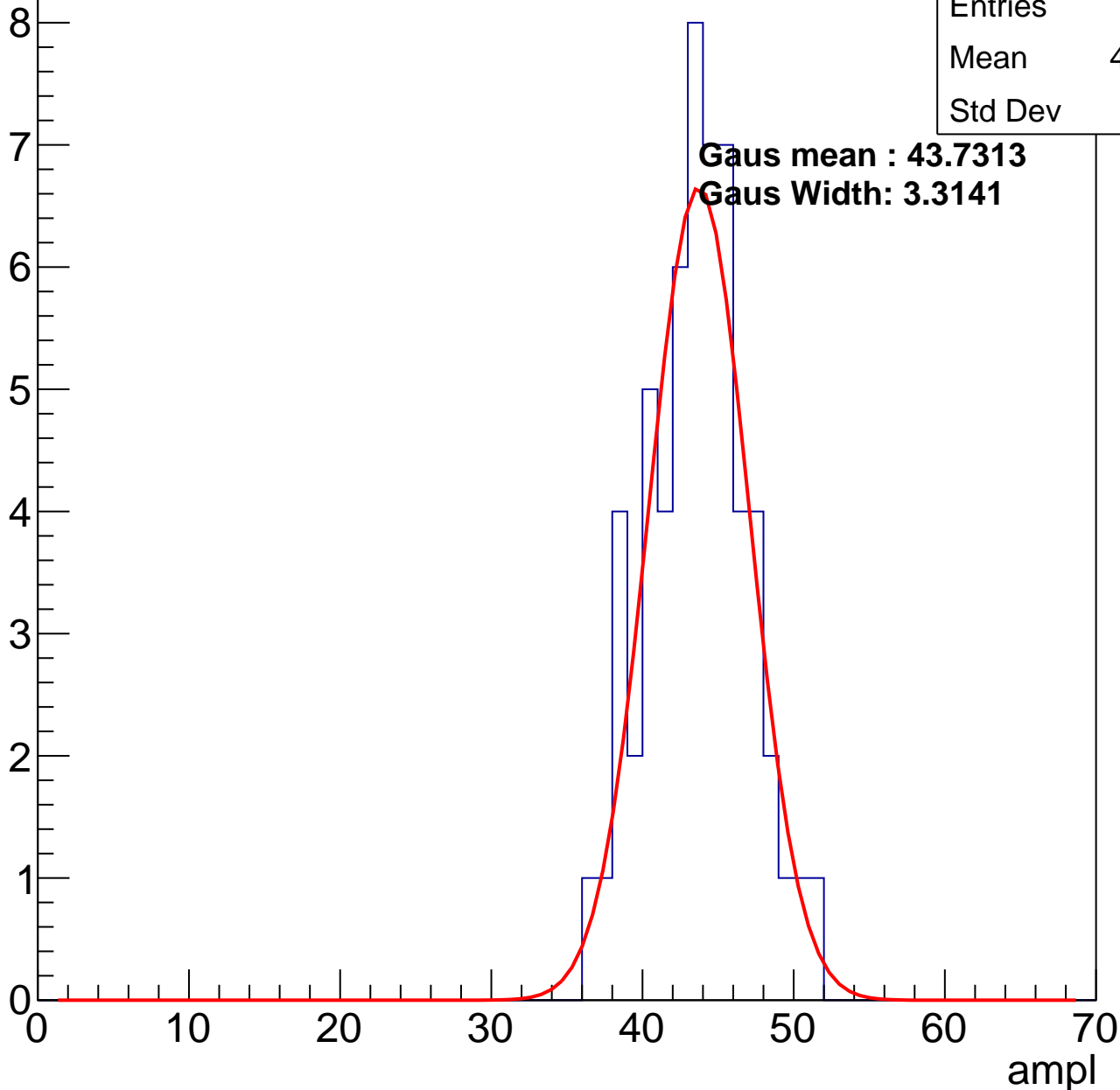
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.17
Std Dev	3.26

Gaus mean : 43.7313

Gaus Width: 3.3141

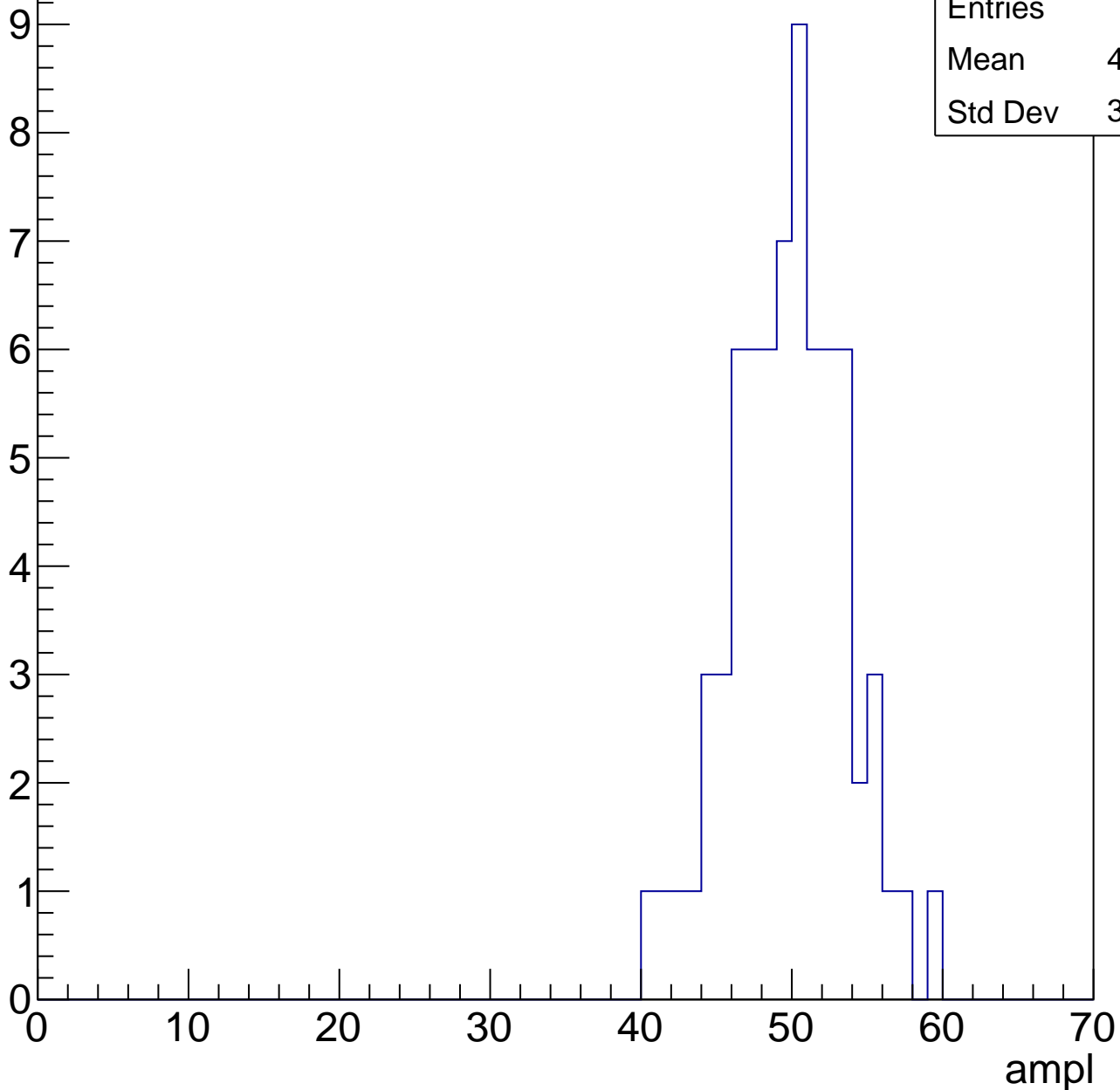


B1L003S, U13-ch84, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	49.33
Std Dev	3.748

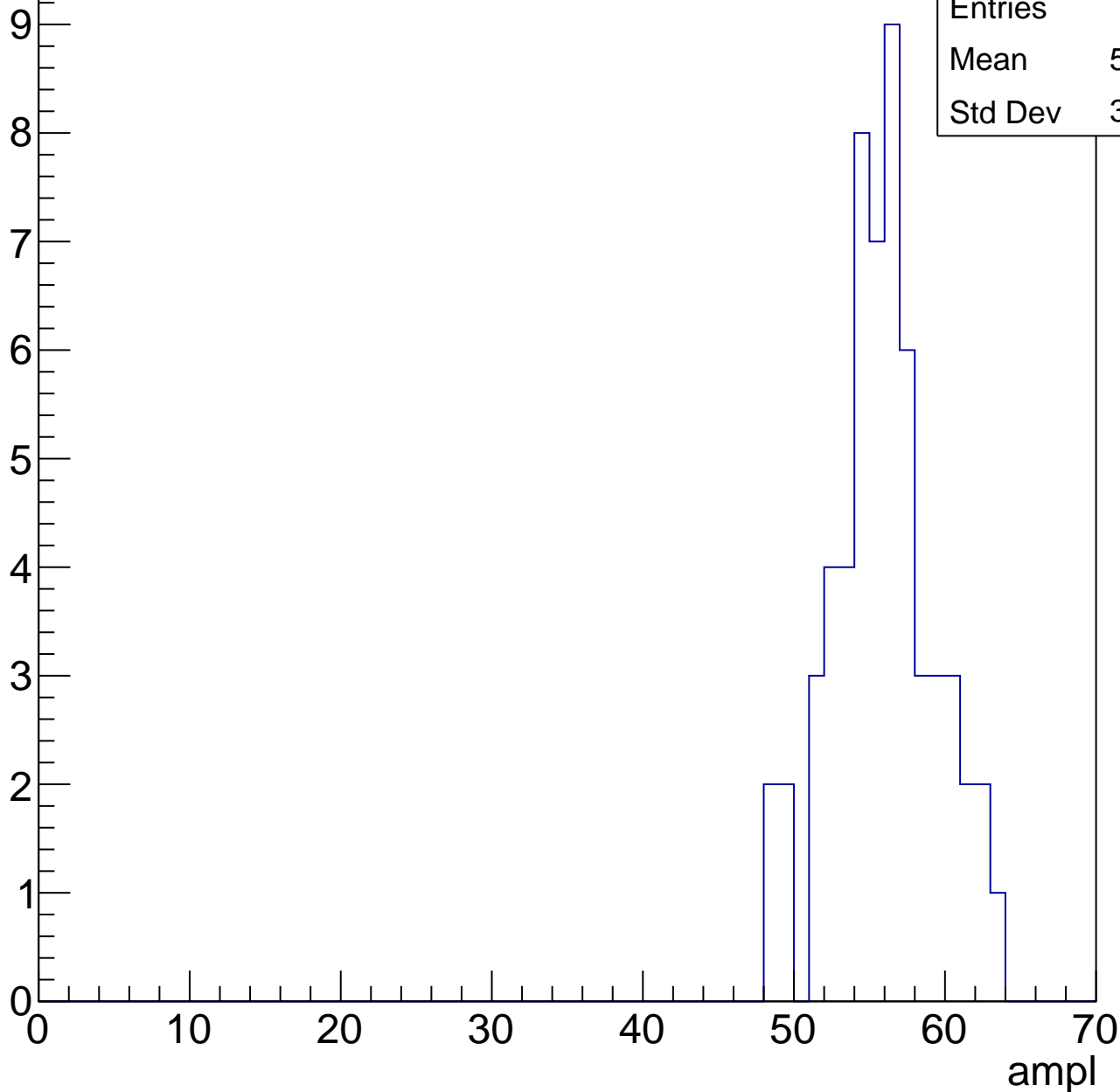


B1L003S, U13-ch84, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.42
Std Dev	3.406

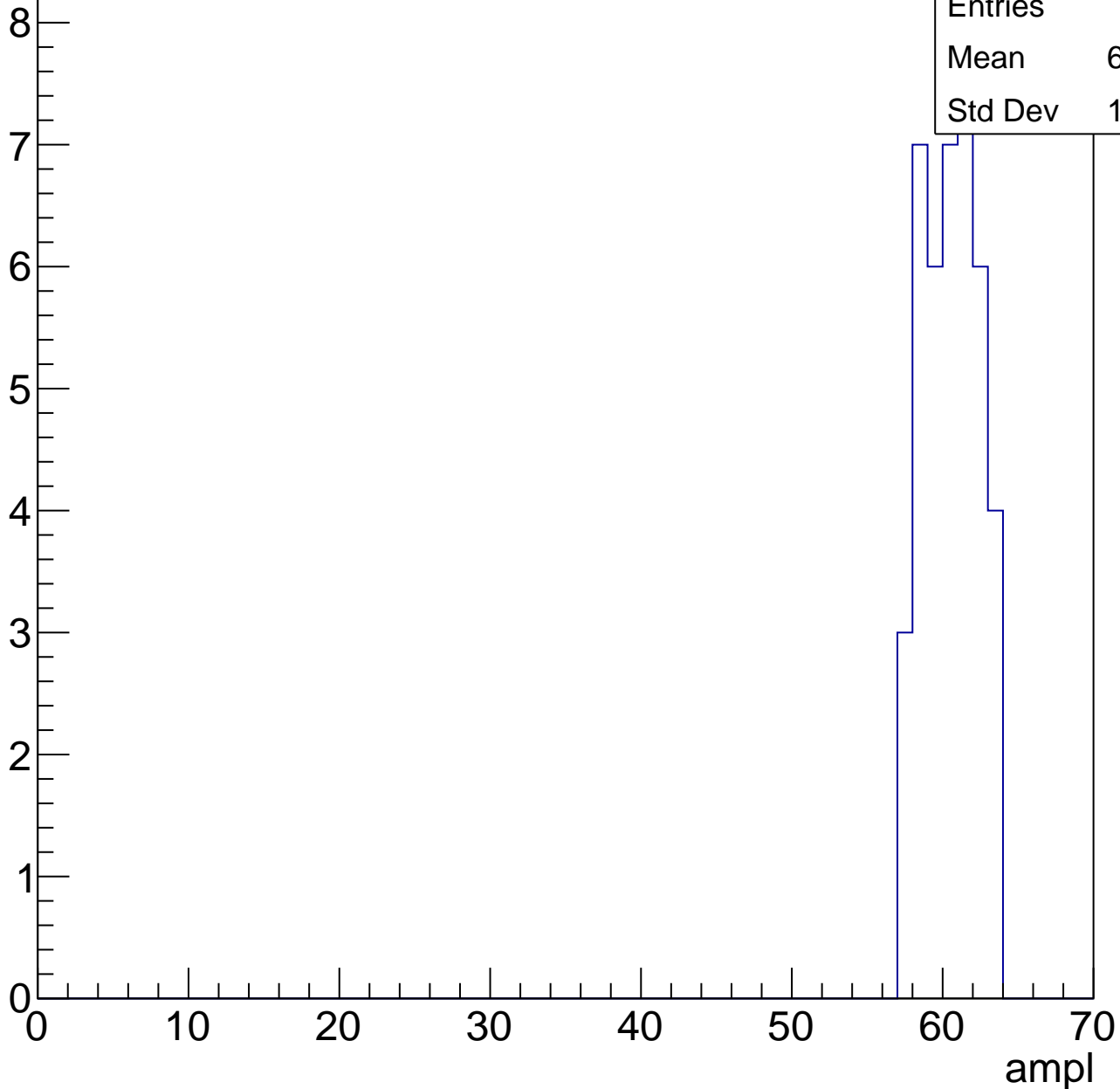


B1L003S, U13-ch84, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	60.07
Std Dev	1.772

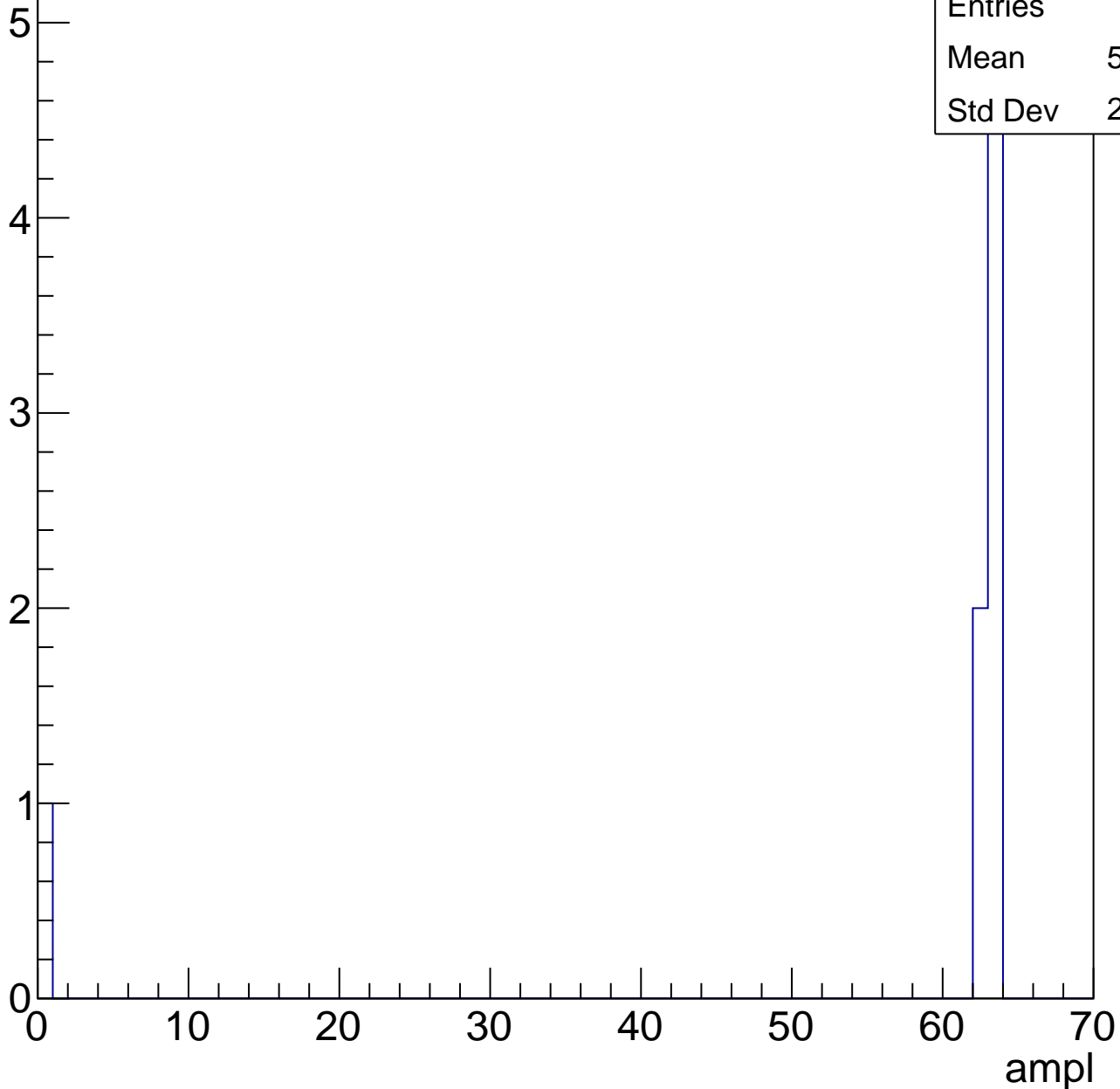


B1L003S, U13-ch84, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B1L003S, U13-ch84, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch85, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	29.61
Std Dev	6.229

Gaus mean : 31.4877

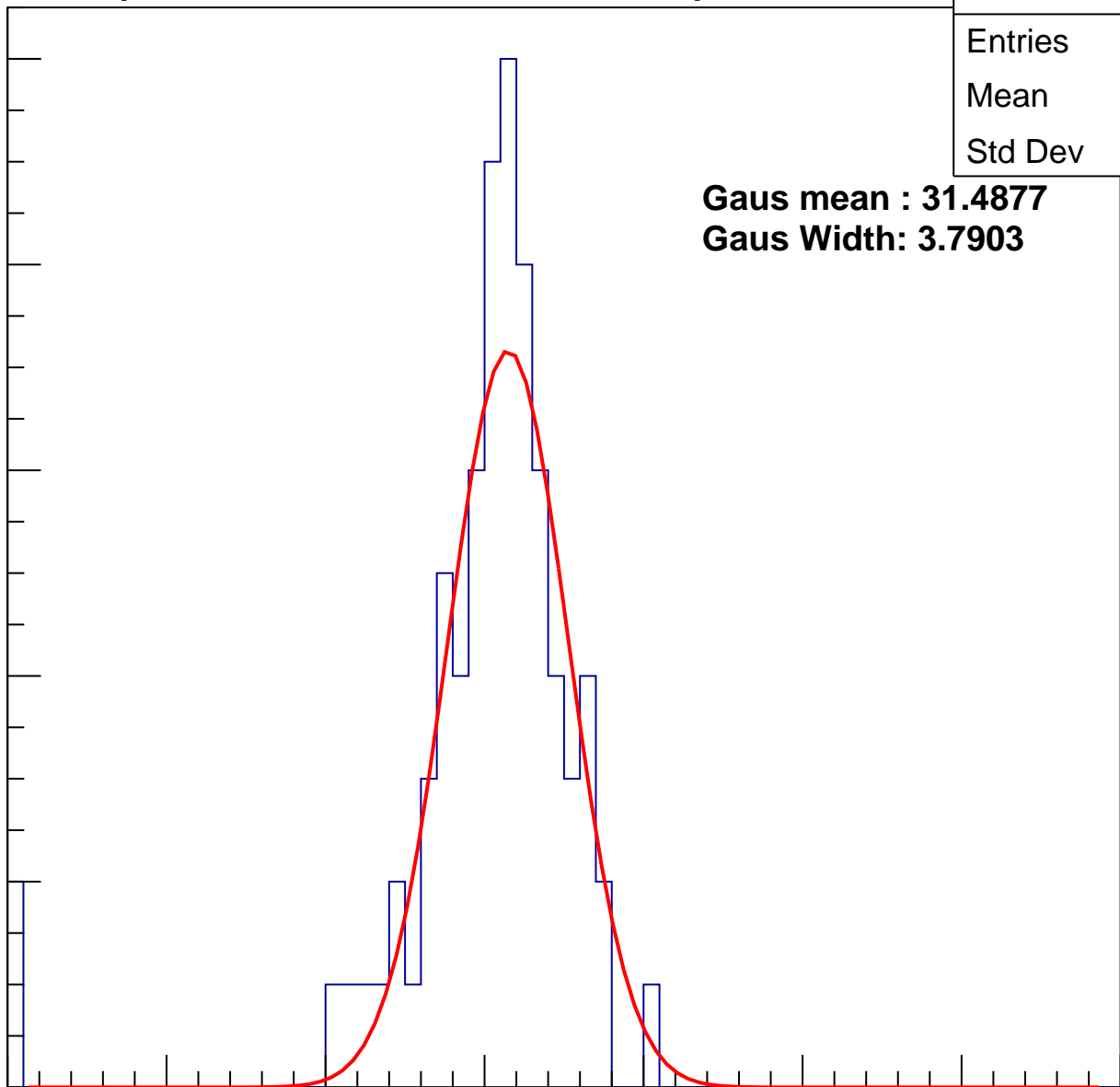
Gaus Width: 3.7903

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch85, adc1

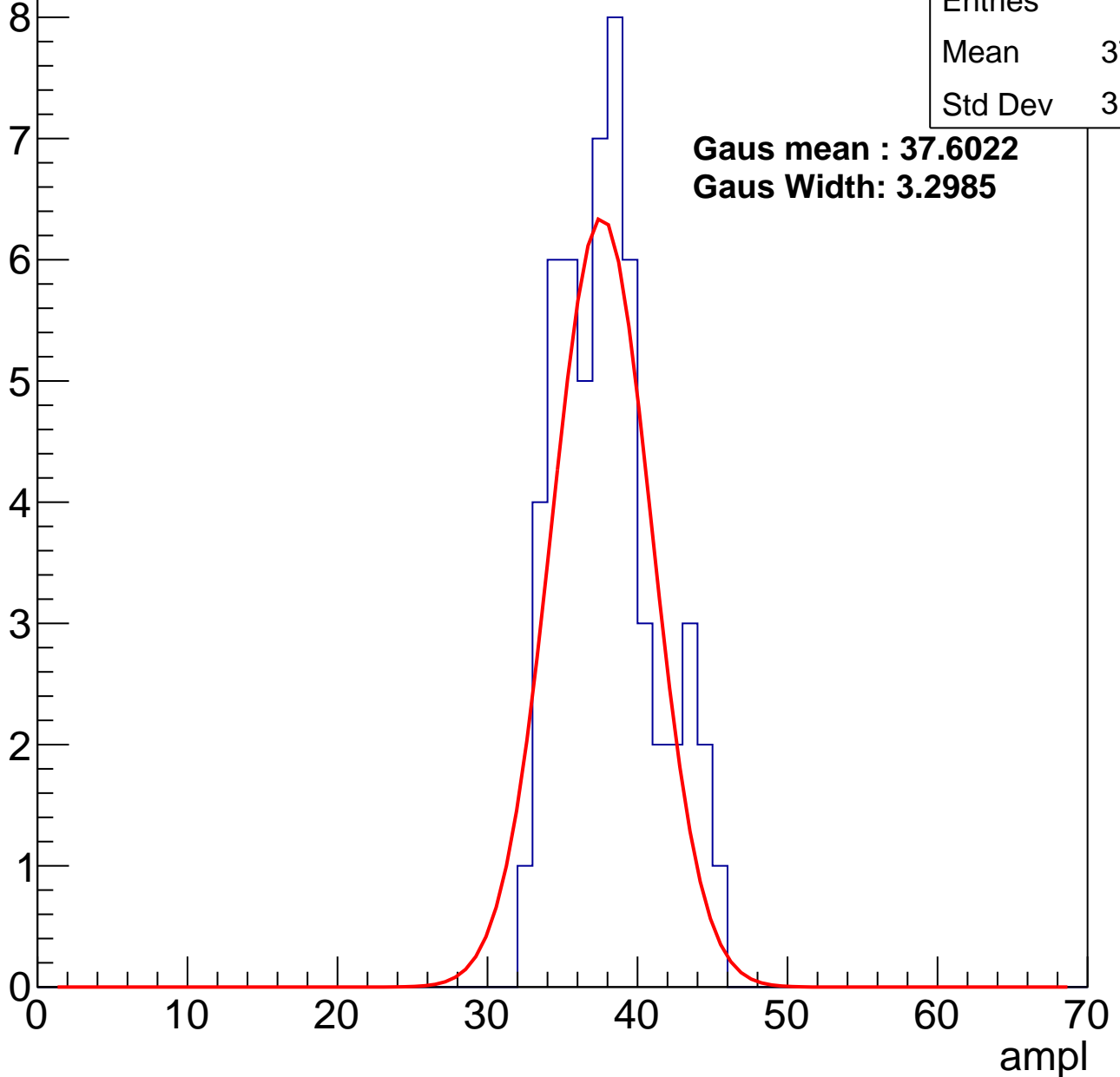
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	37.55
Std Dev	3.179

Gaus mean : 37.6022

Gaus Width: 3.2985



B1L003S, U13-ch85, adc2

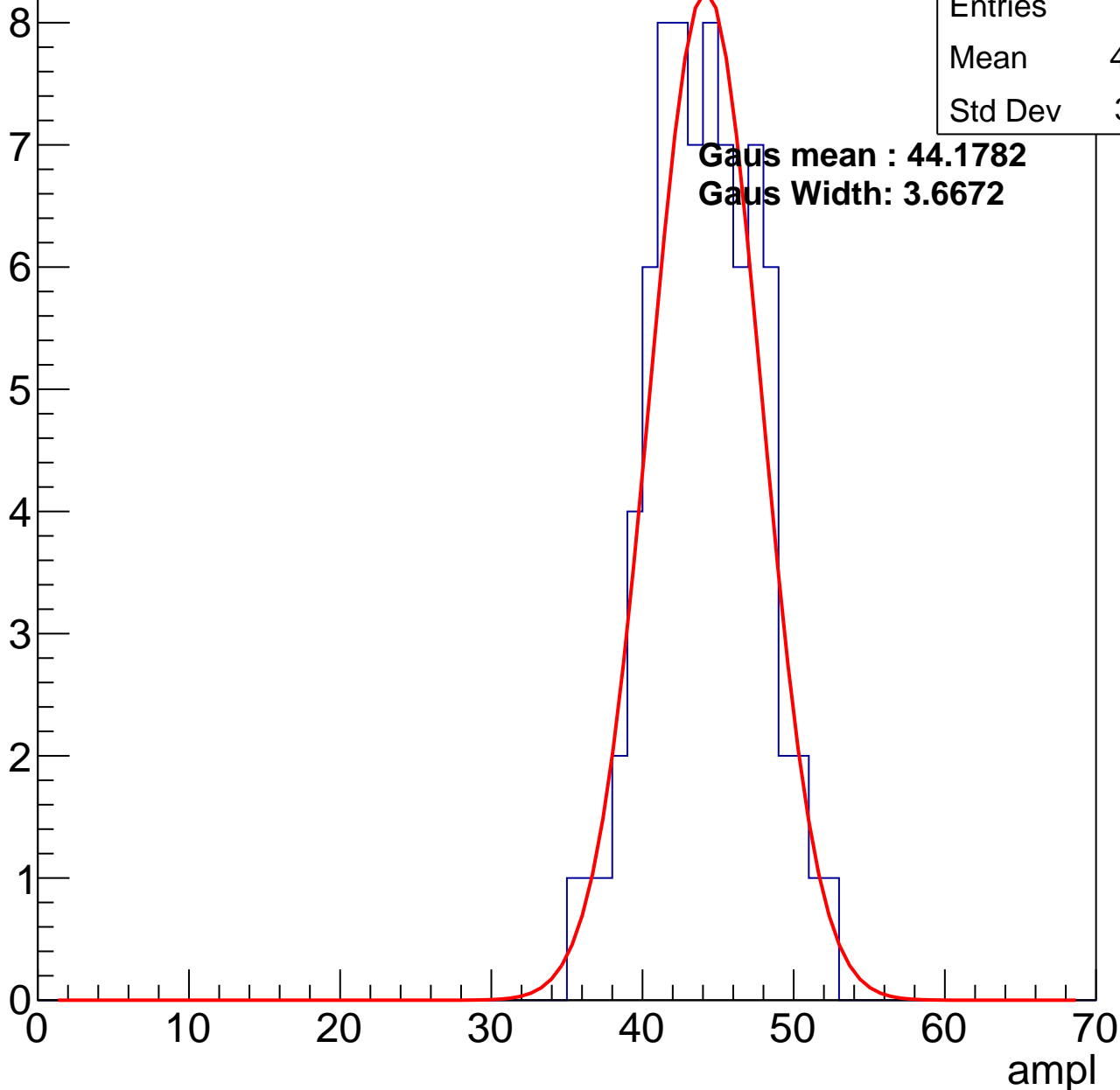
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	43.67
Std Dev	3.561

Gaus mean : 44.1782

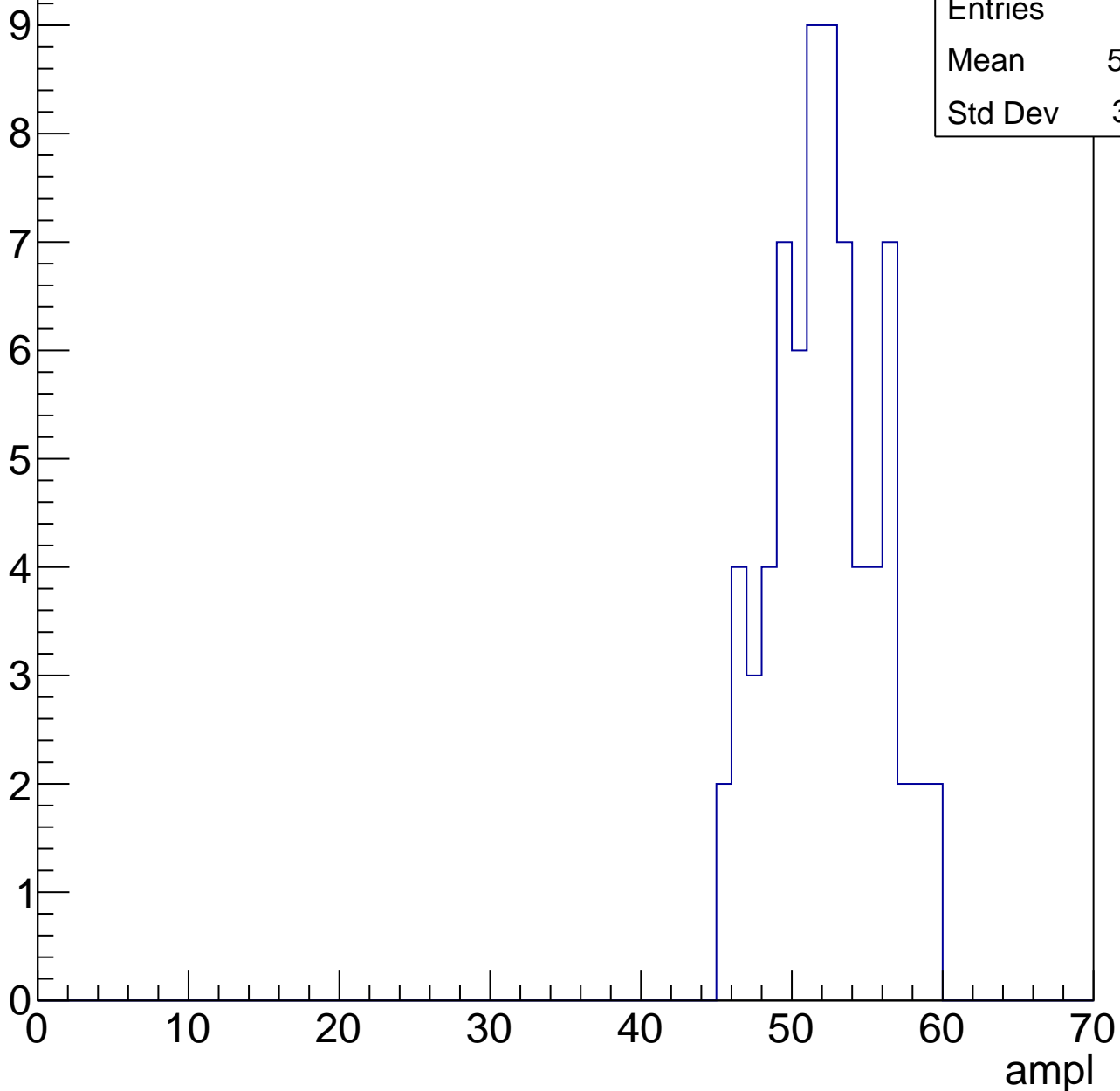
Gaus Width: 3.6672



B1L003S, U13-ch85, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

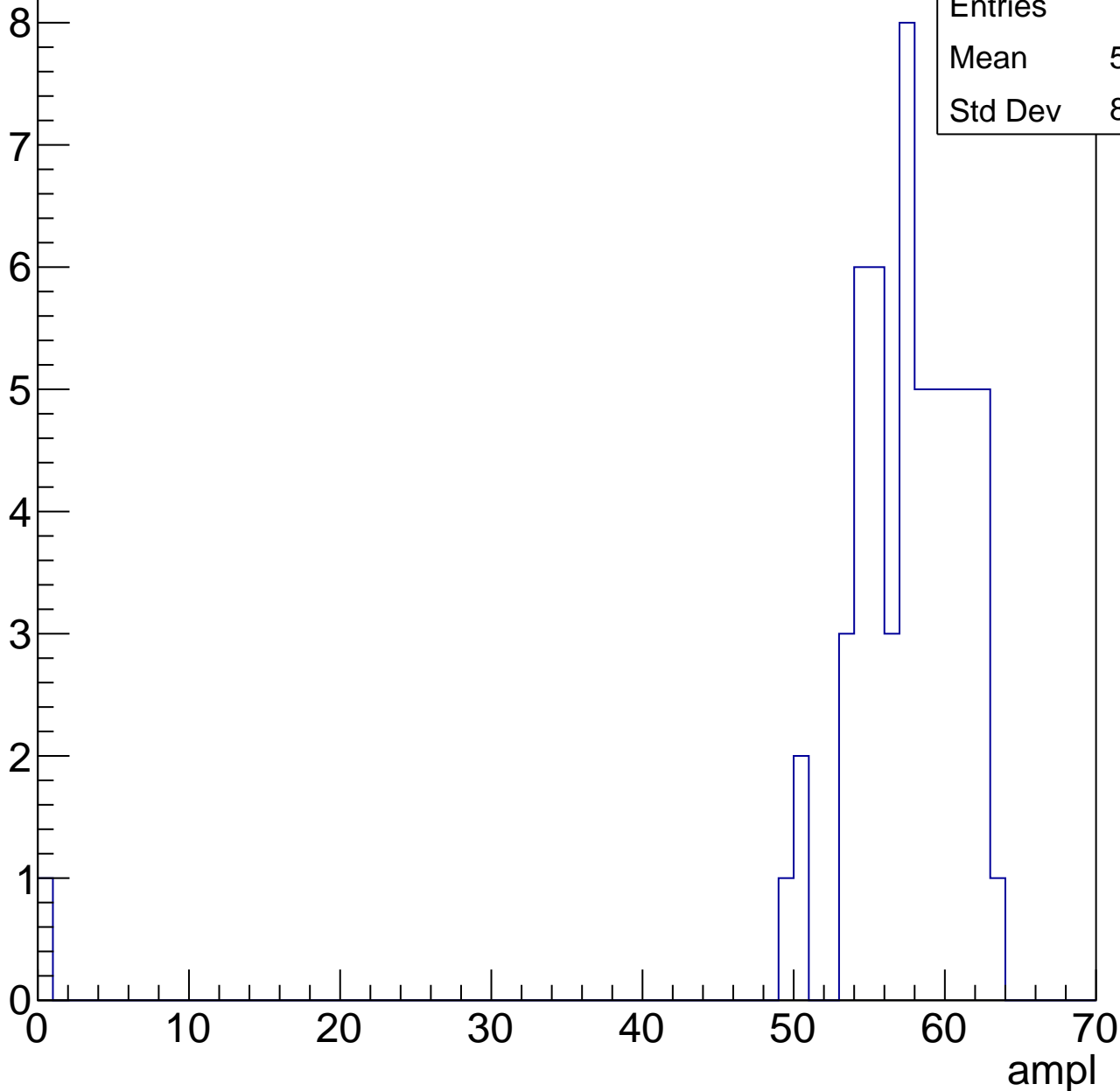


B1L003S, U13-ch85, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

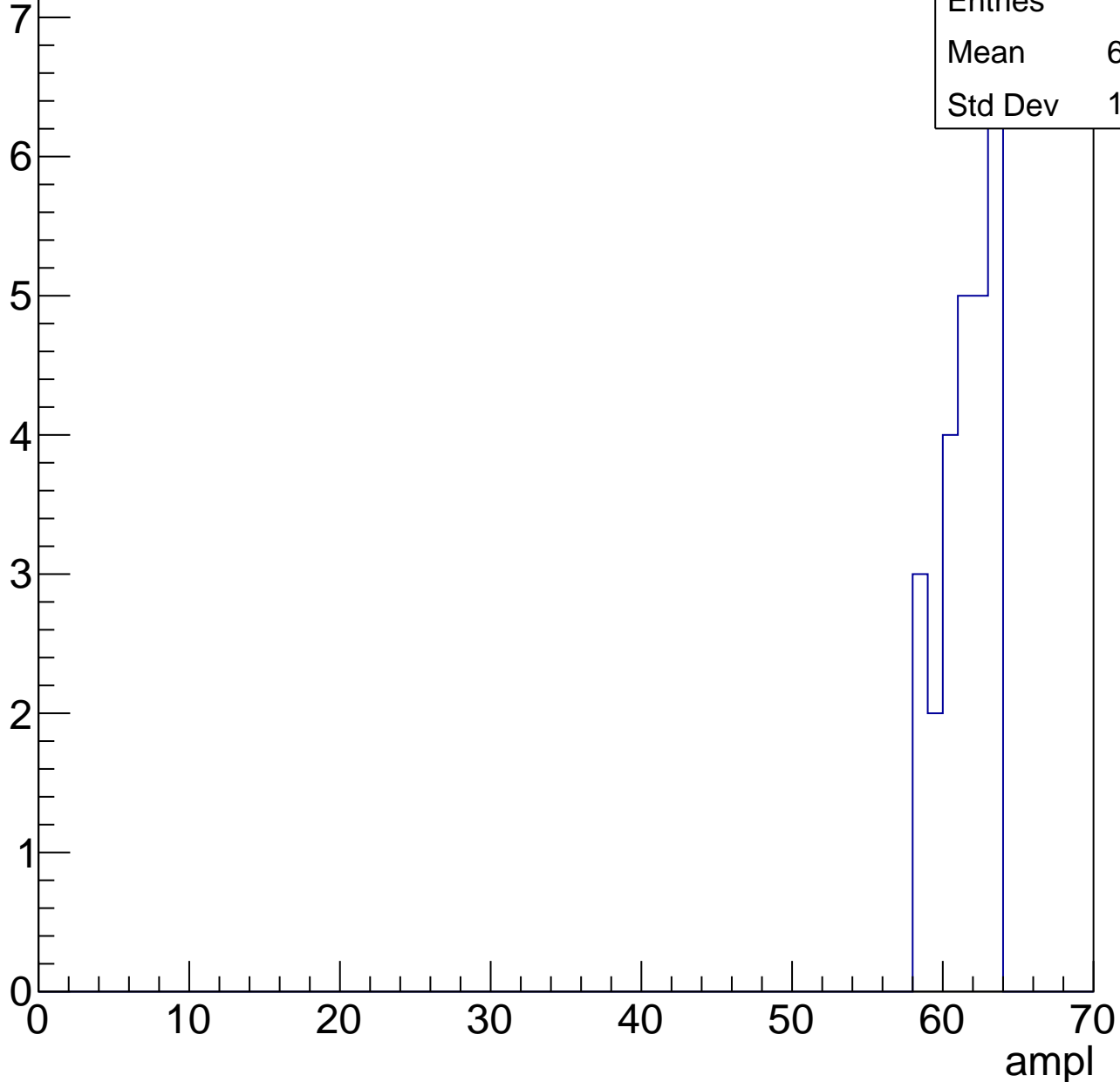
Entries	56
Mean	56.23
Std Dev	8.257



B1L003S, U13-ch85, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch85, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch85, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch86, adc0

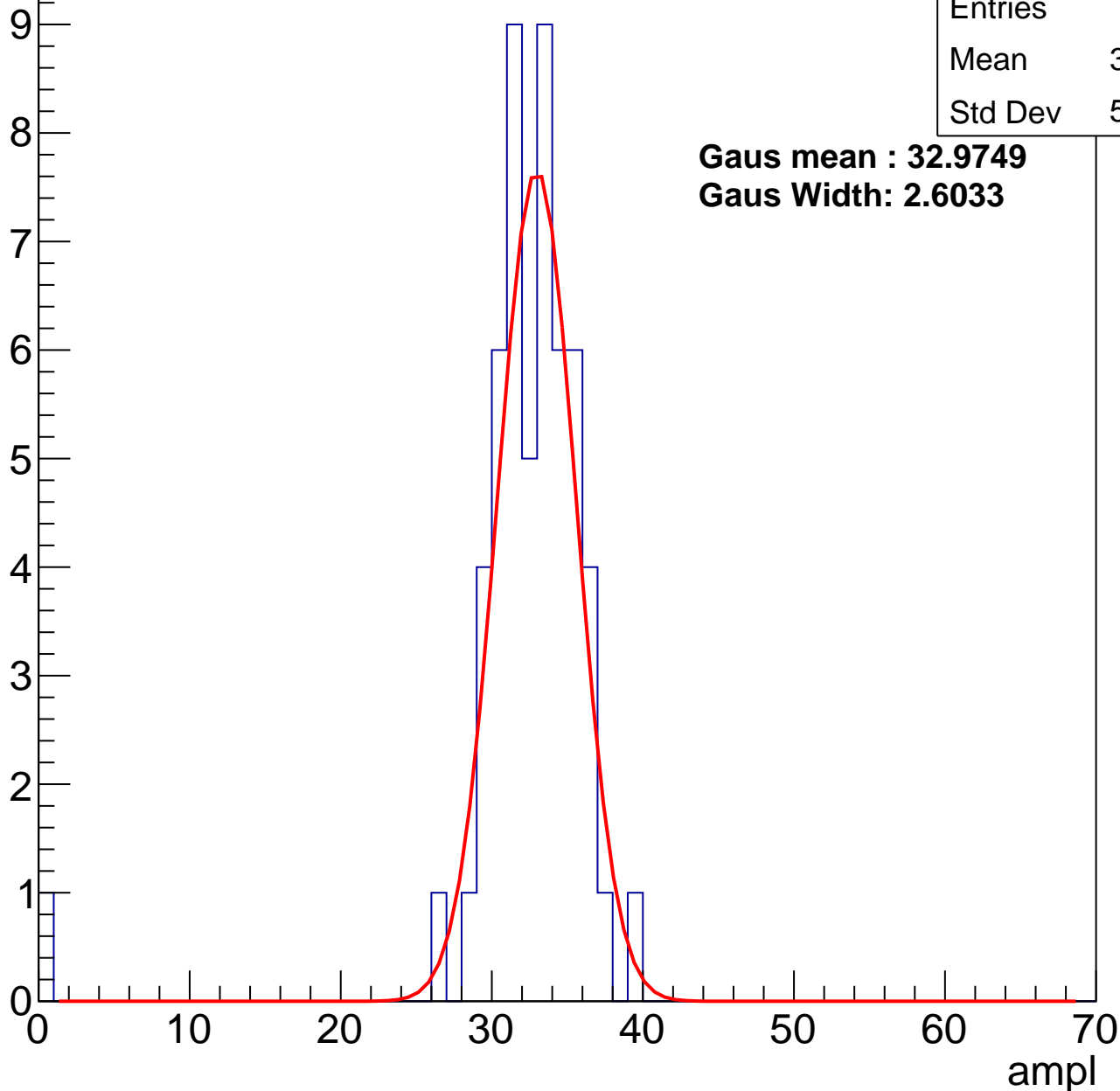
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	31.85
Std Dev	5.035

Gaus mean : 32.9749

Gaus Width: 2.6033

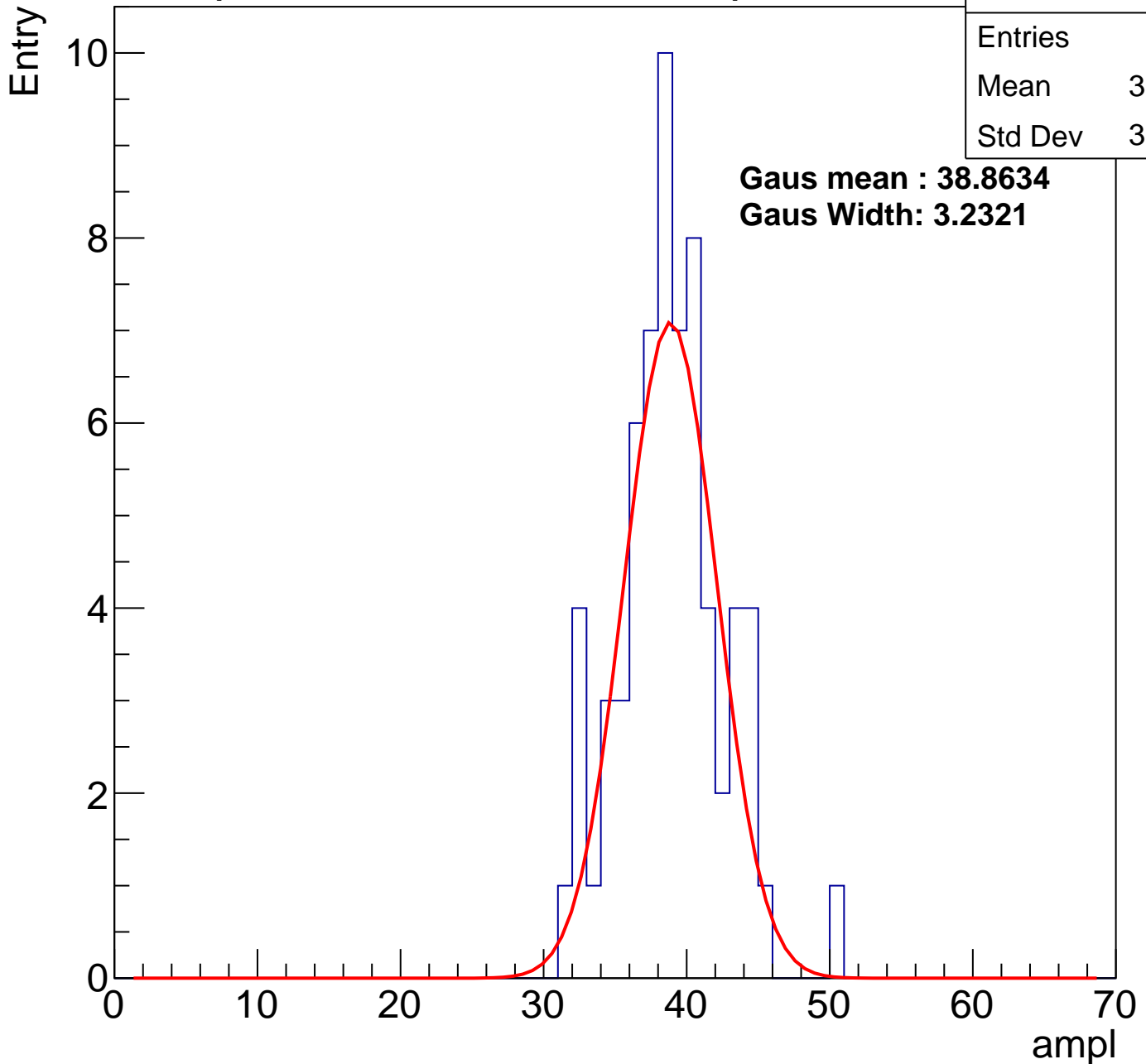


B1L003S, U13-ch86, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	38.45
Std Dev	3.598

Gaus mean : 38.8634
Gaus Width: 3.2321



B1L003S, U13-ch86, adc2

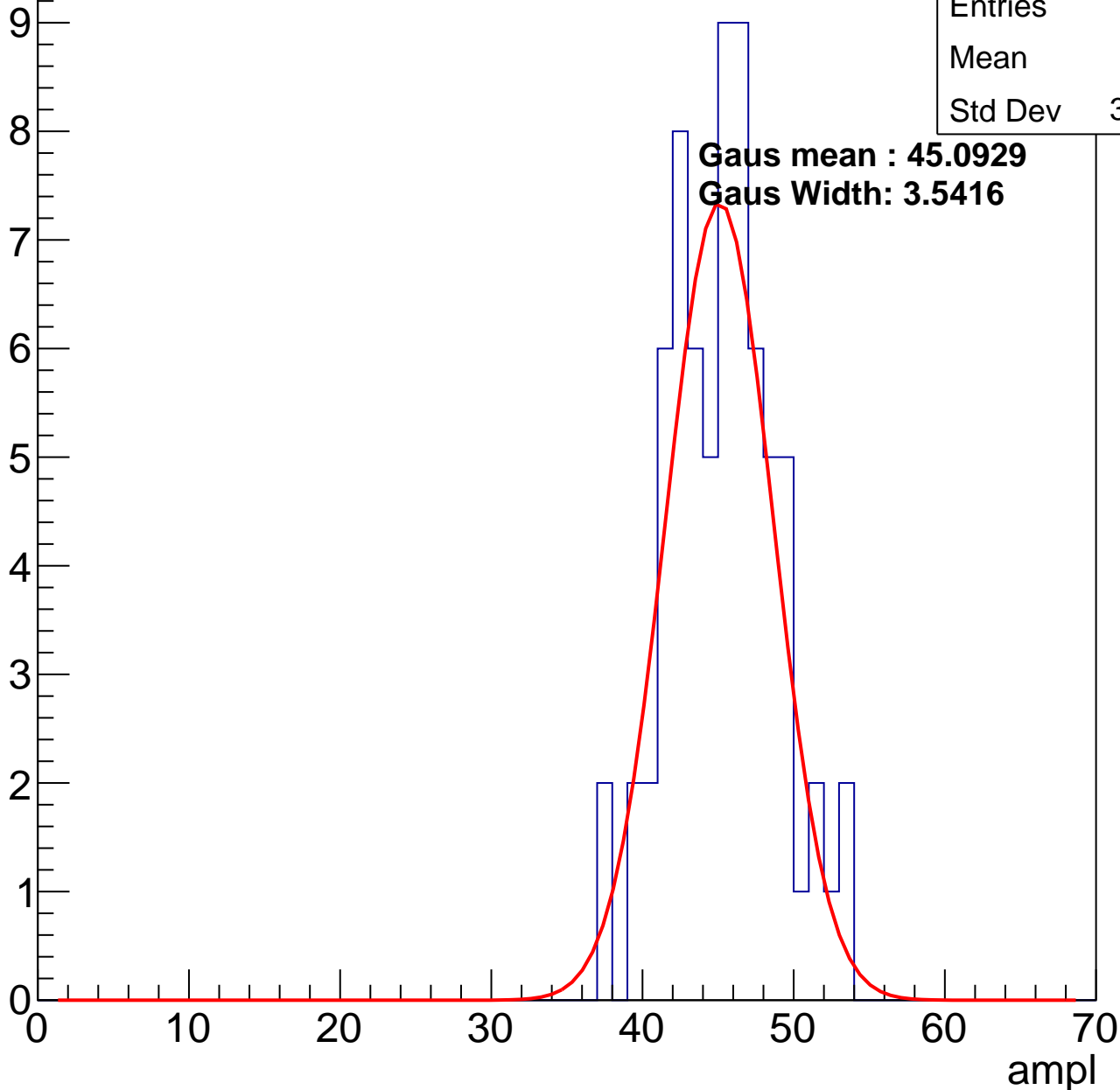
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	44.9
Std Dev	3.517

Gaus mean : 45.0929

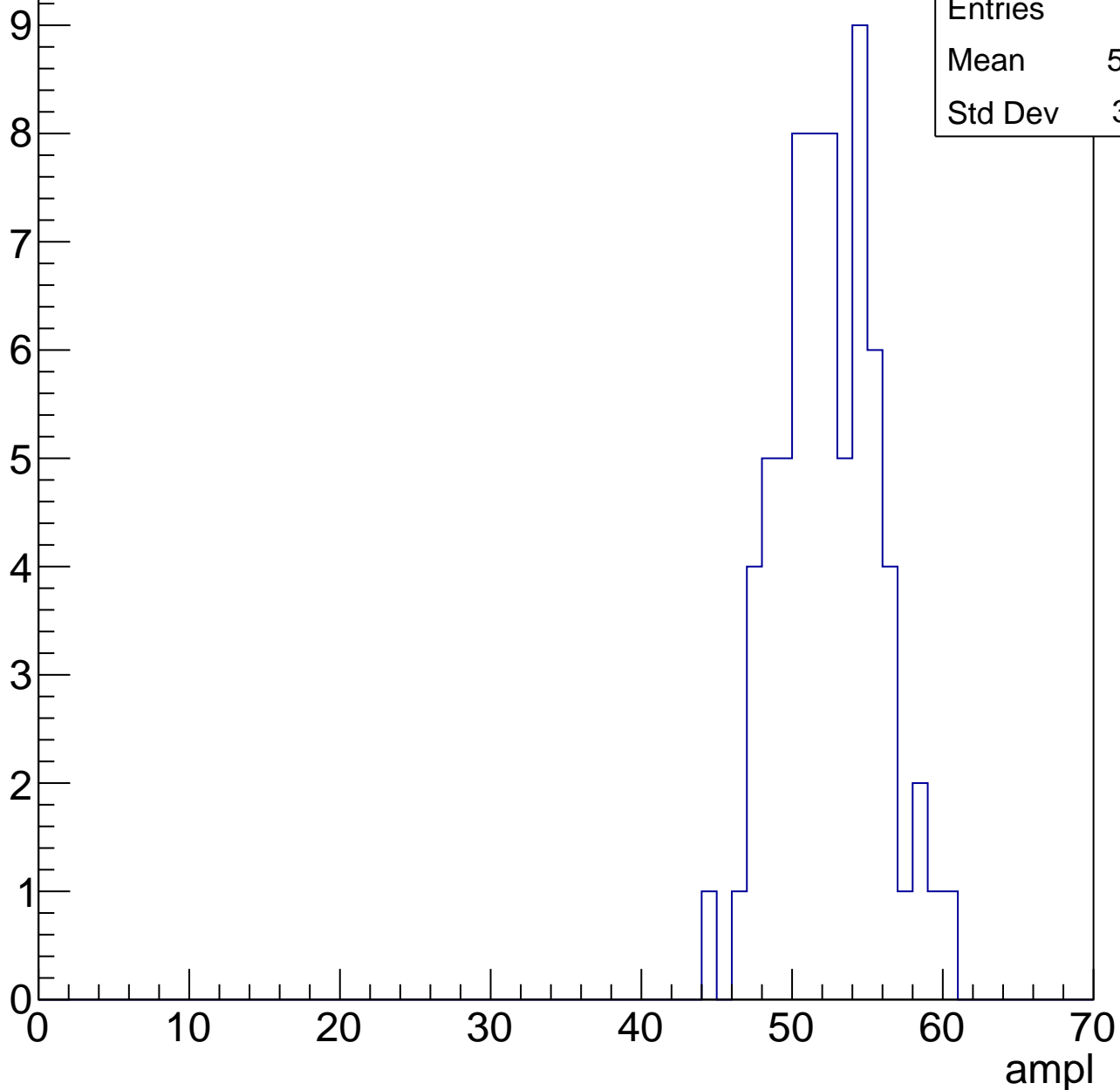
Gaus Width: 3.5416



B1L003S, U13-ch86, adc3

calib_packv5_042523_0143.root, FC#13, port D2

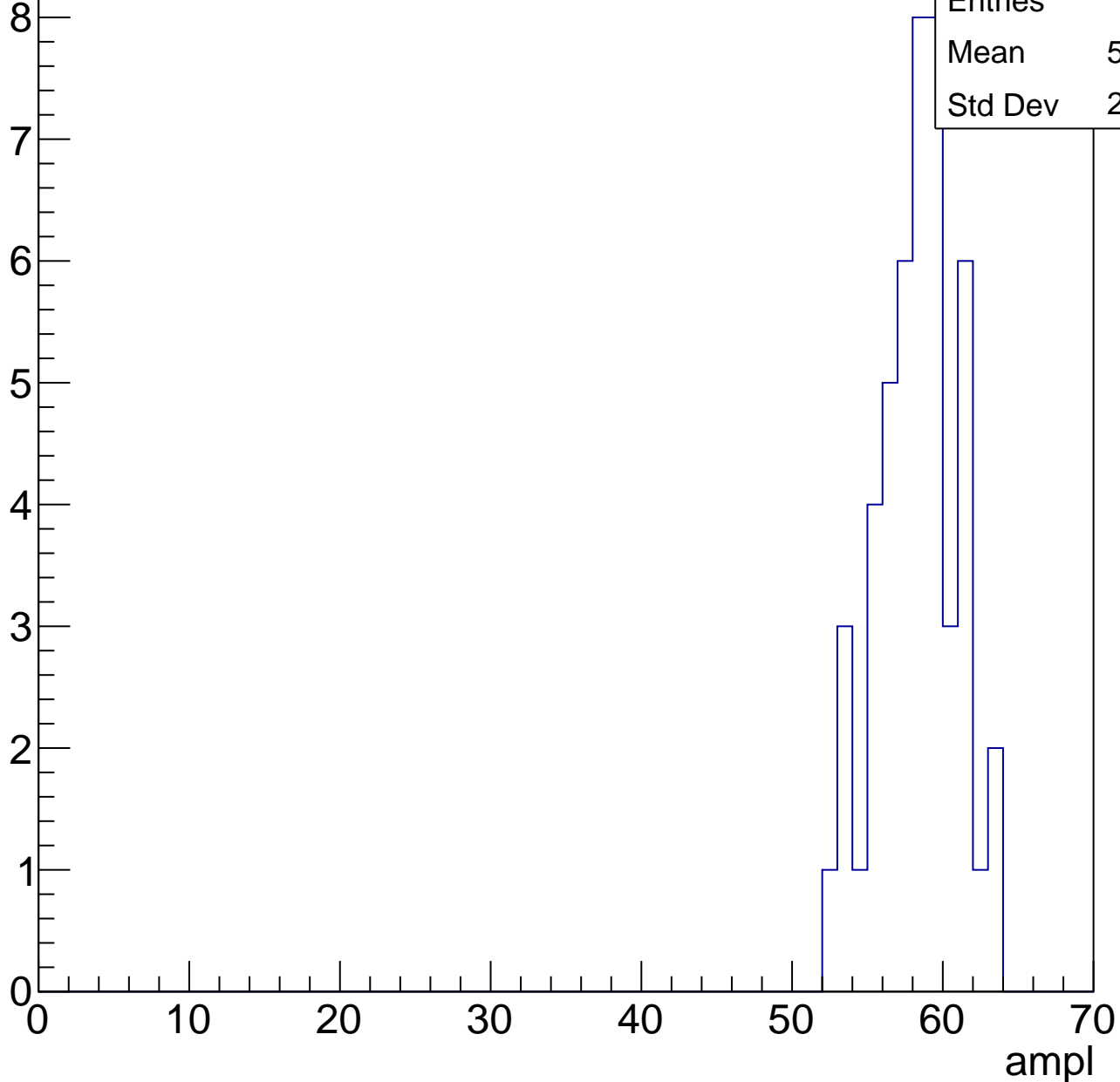
Entry



B1L003S, U13-ch86, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

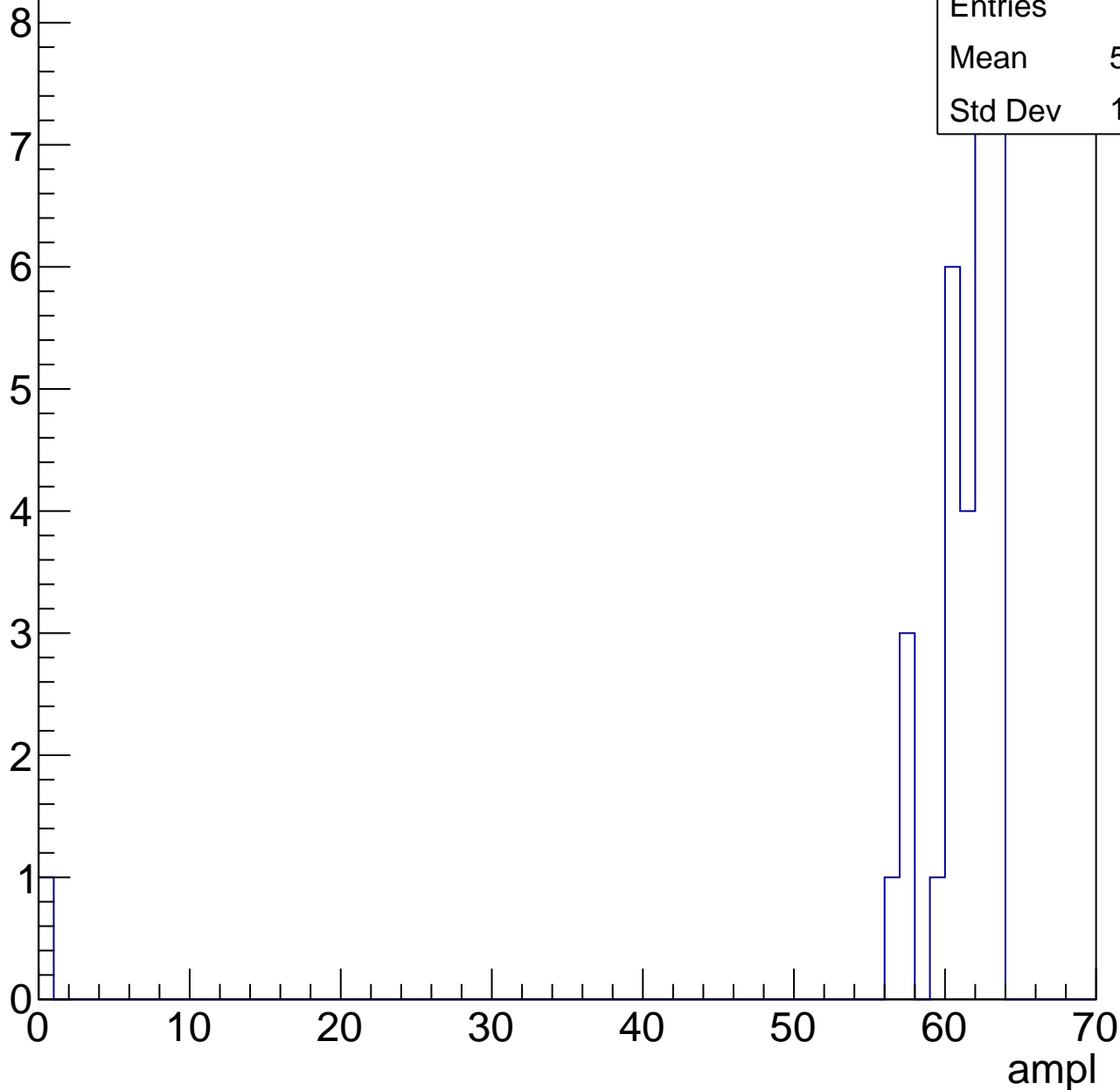


B1L003S, U13-ch86, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	32
Mean	59.06
Std Dev	10.79



B1L003S, U13-ch86, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch86, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch87, adc0

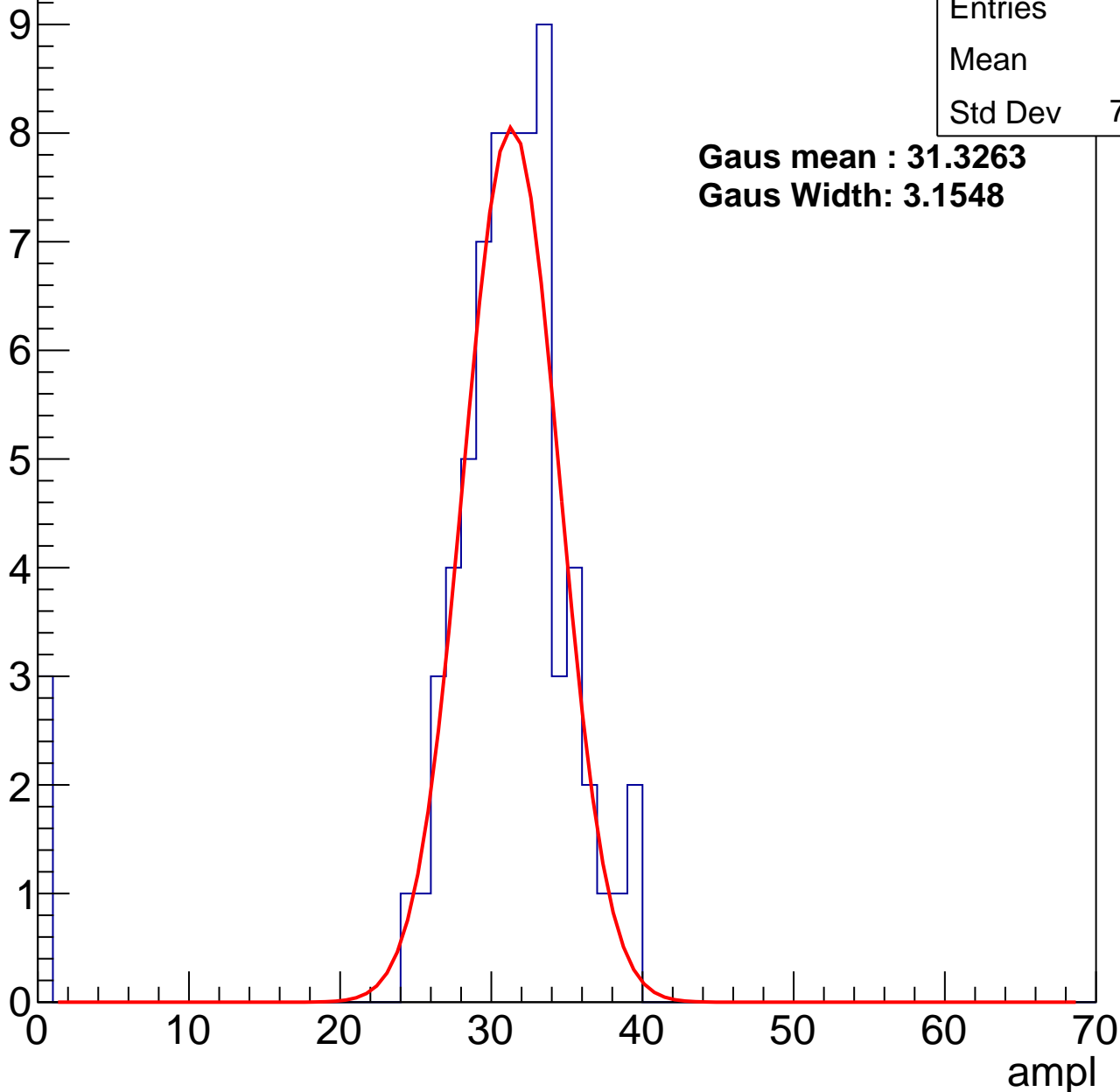
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.8
Std Dev	7.056

Gaus mean : 31.3263

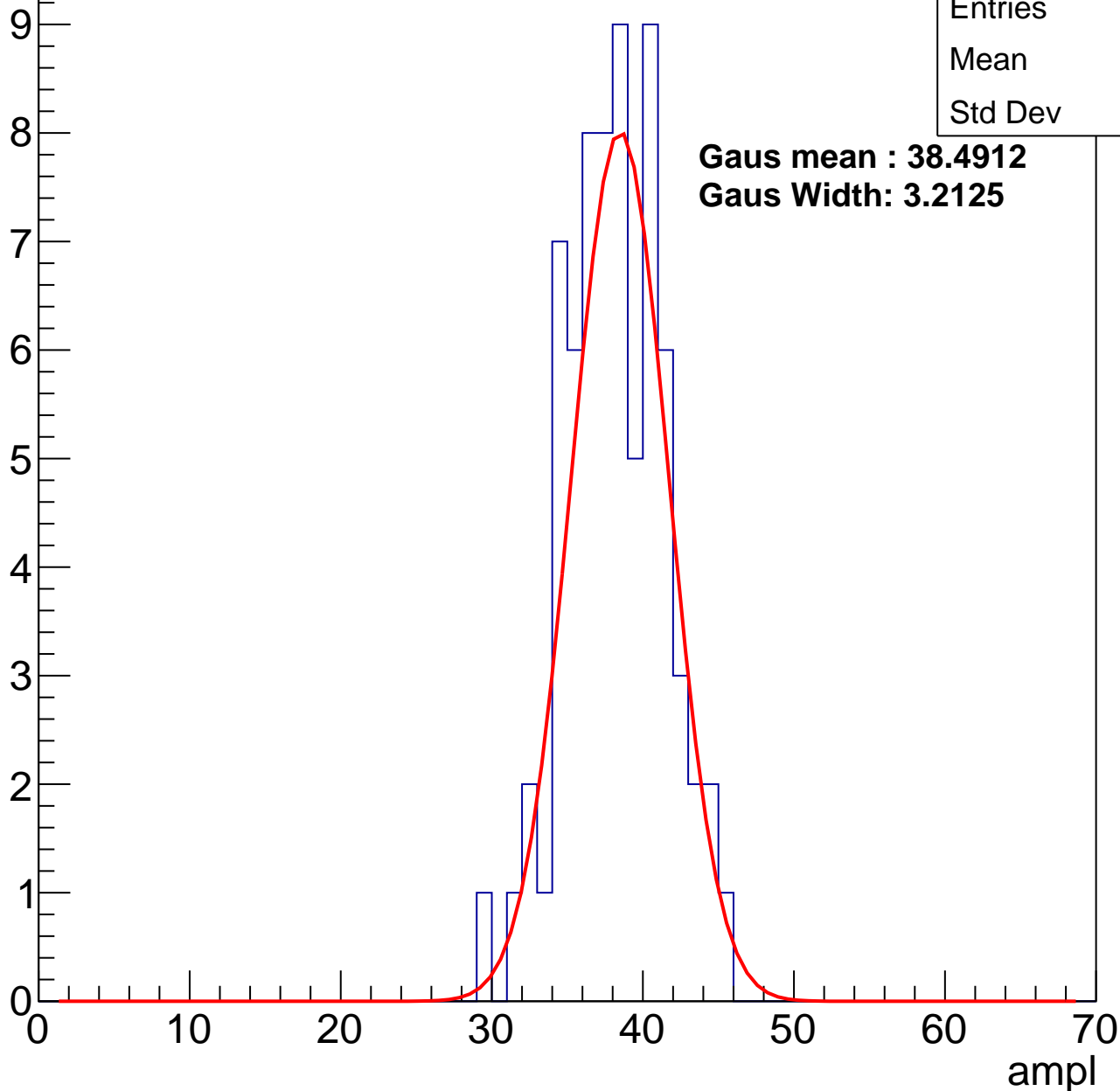
Gaus Width: 3.1548



B1L003S, U13-ch87, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch87, adc2

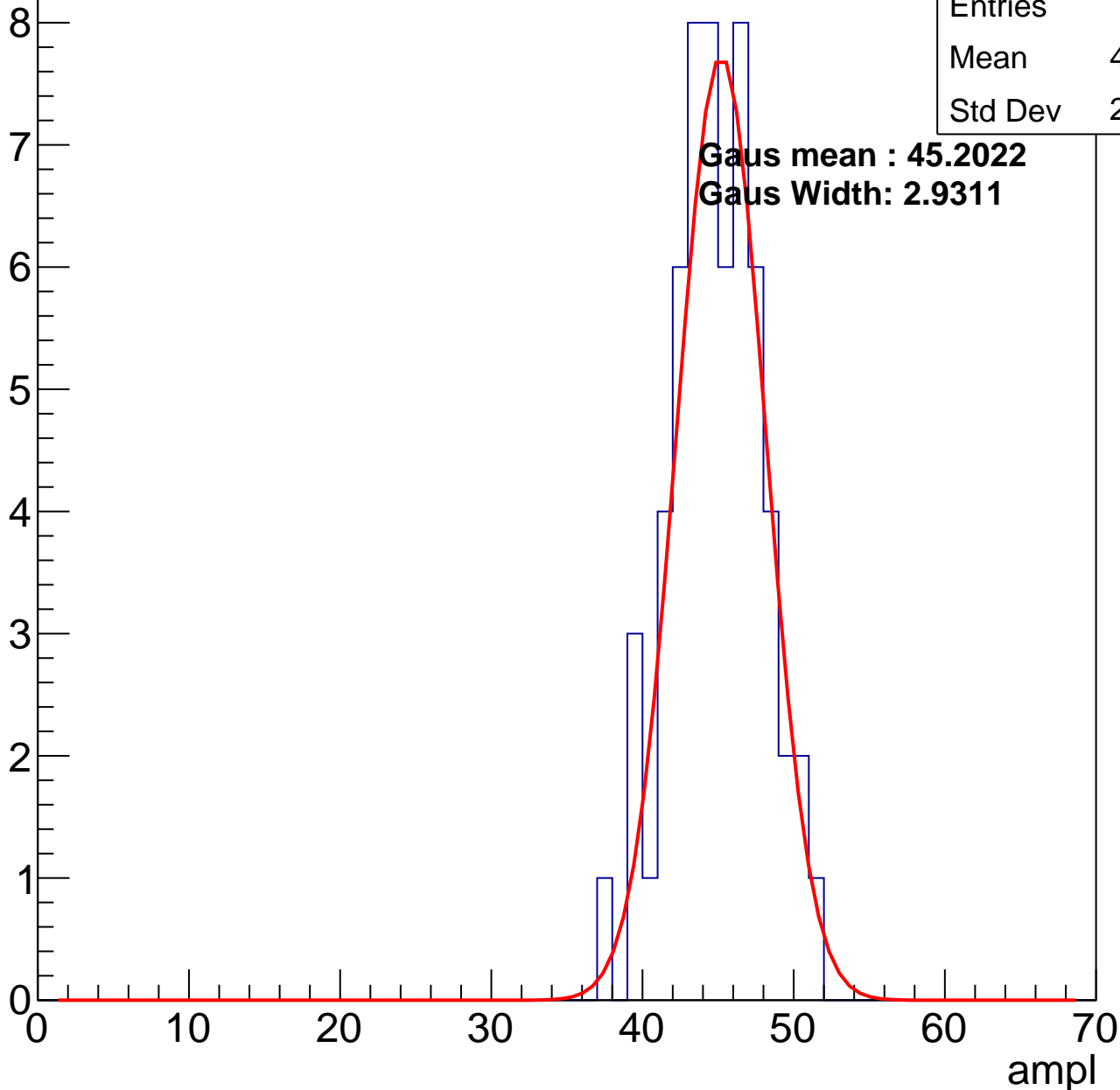
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	44.45
Std Dev	2.952

Gaus mean : 45.2022

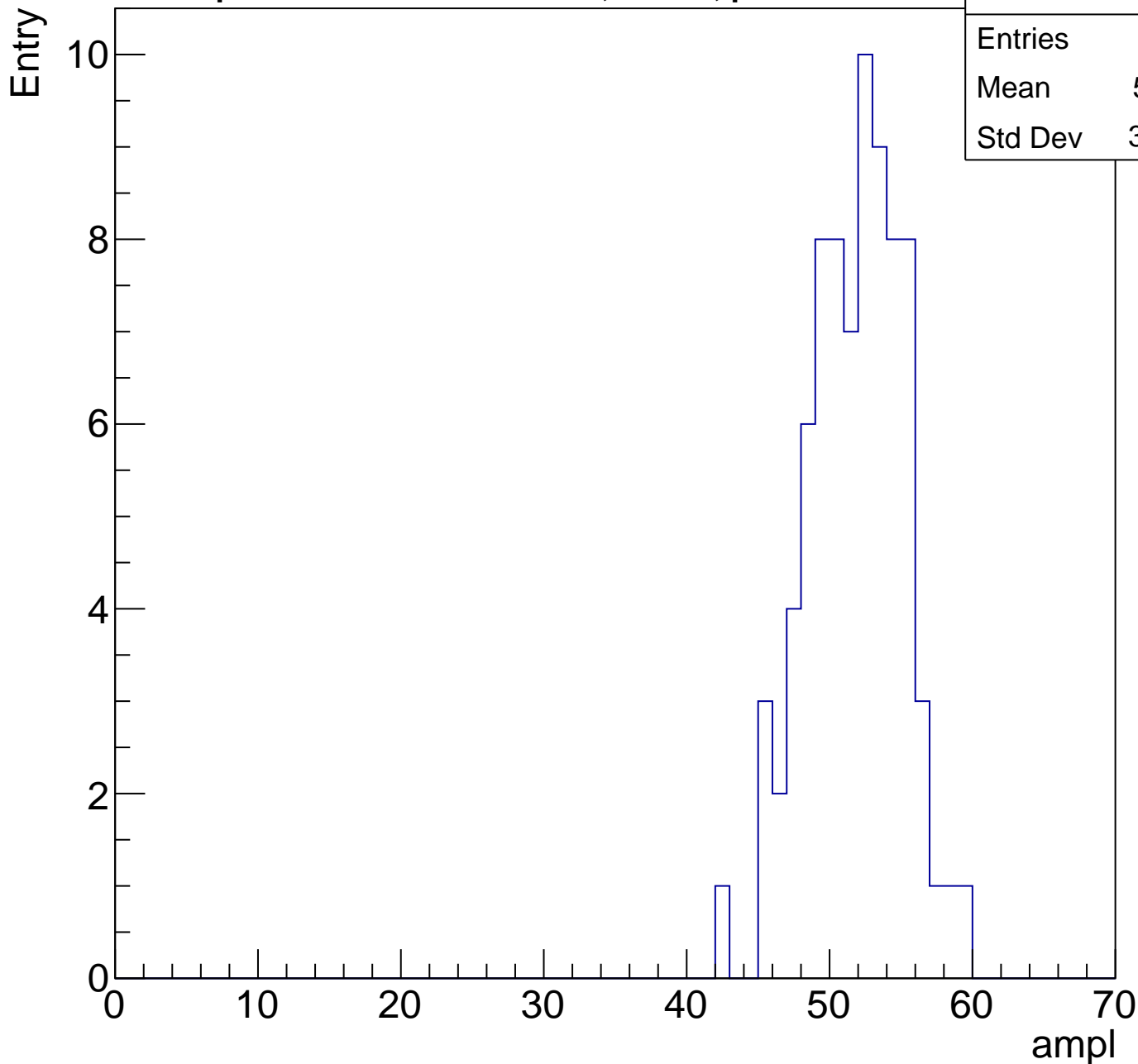
Gaus Width: 2.9311



B1L003S, U13-ch87, adc3

calib_packv5_042523_0143.root, FC#13, port D2

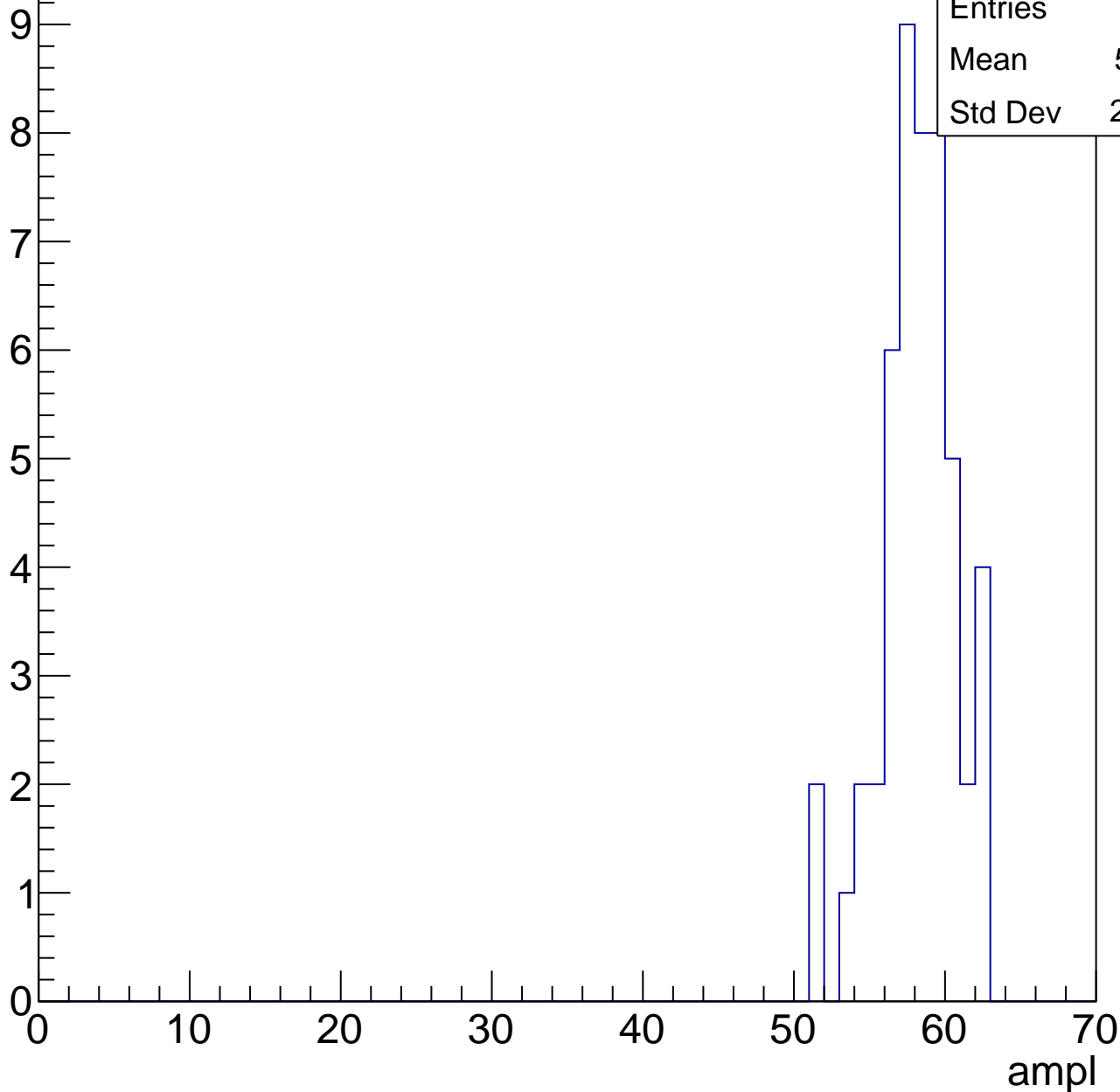
Entries	80
Mean	51.31
Std Dev	3.277



B1L003S, U13-ch87, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



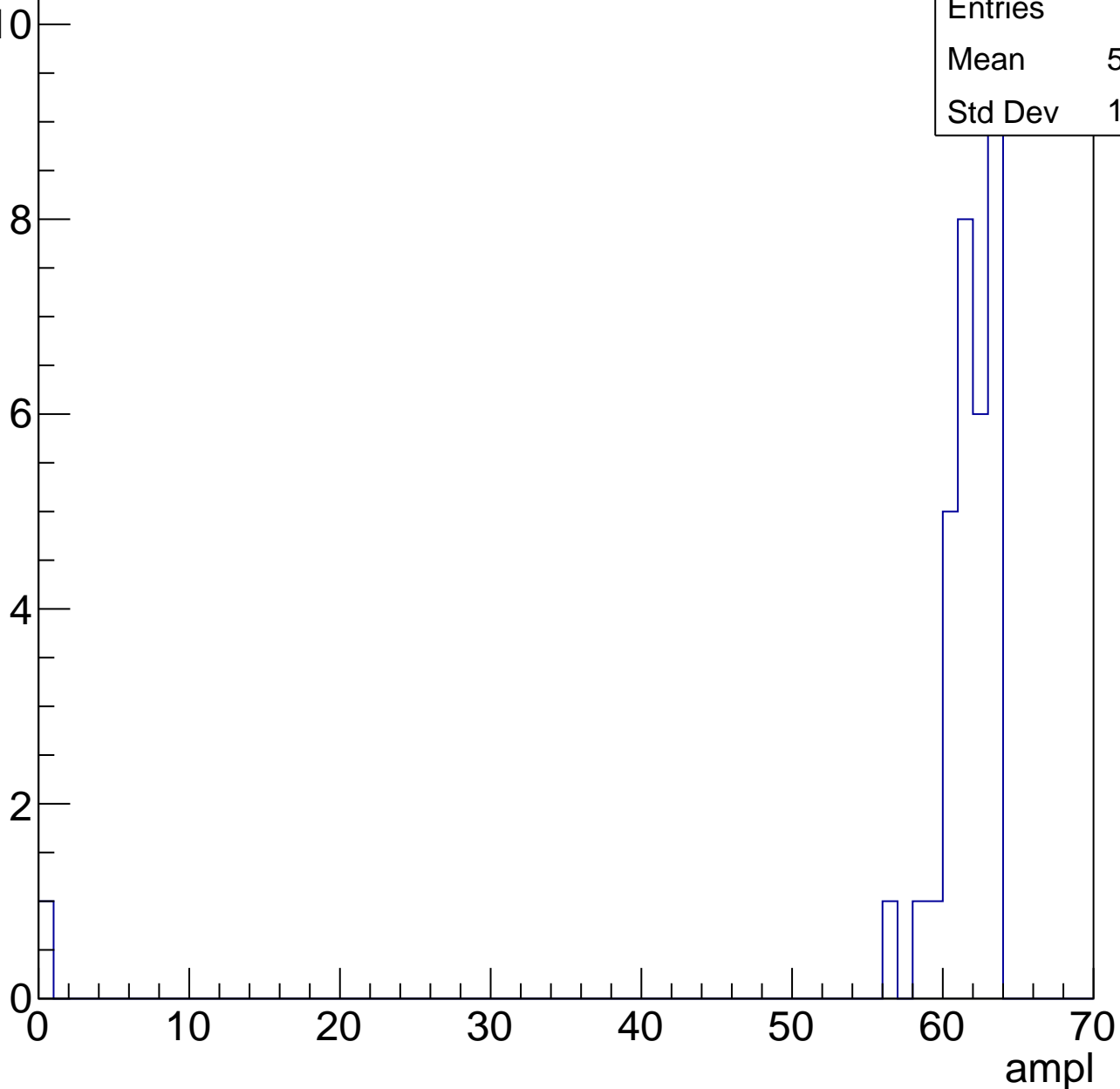
Entries	49
Mean	57.71
Std Dev	2.523

B1L003S, U13-ch87, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	59.48
Std Dev	10.64



B1L003S, U13-ch87, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch87, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



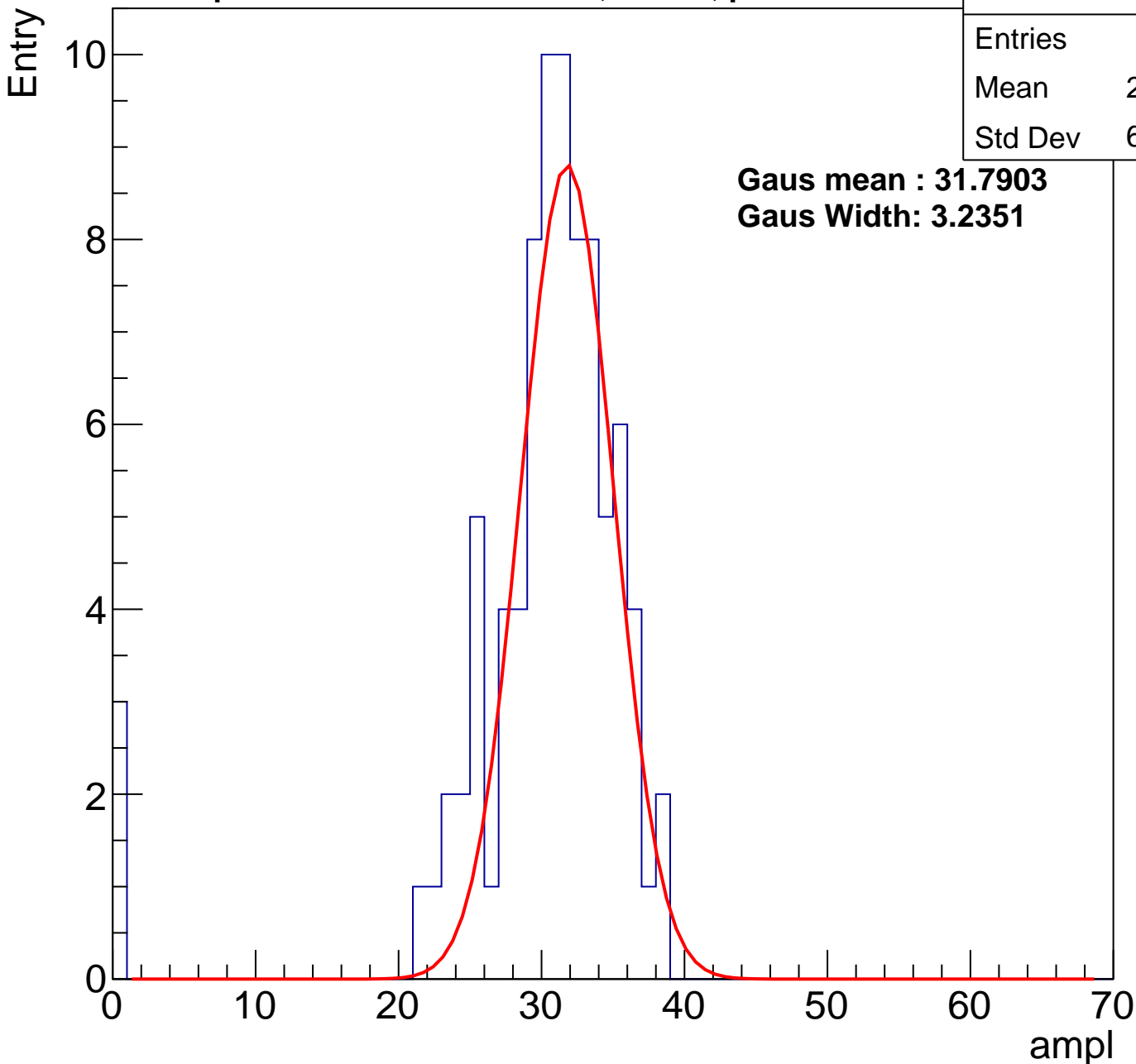
Entries	2
Mean	11
Std Dev	11

B1L003S, U13-ch88, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	29.49
Std Dev	6.738

Gaus mean : 31.7903
Gaus Width: 3.2351



B1L003S, U13-ch88, adc1

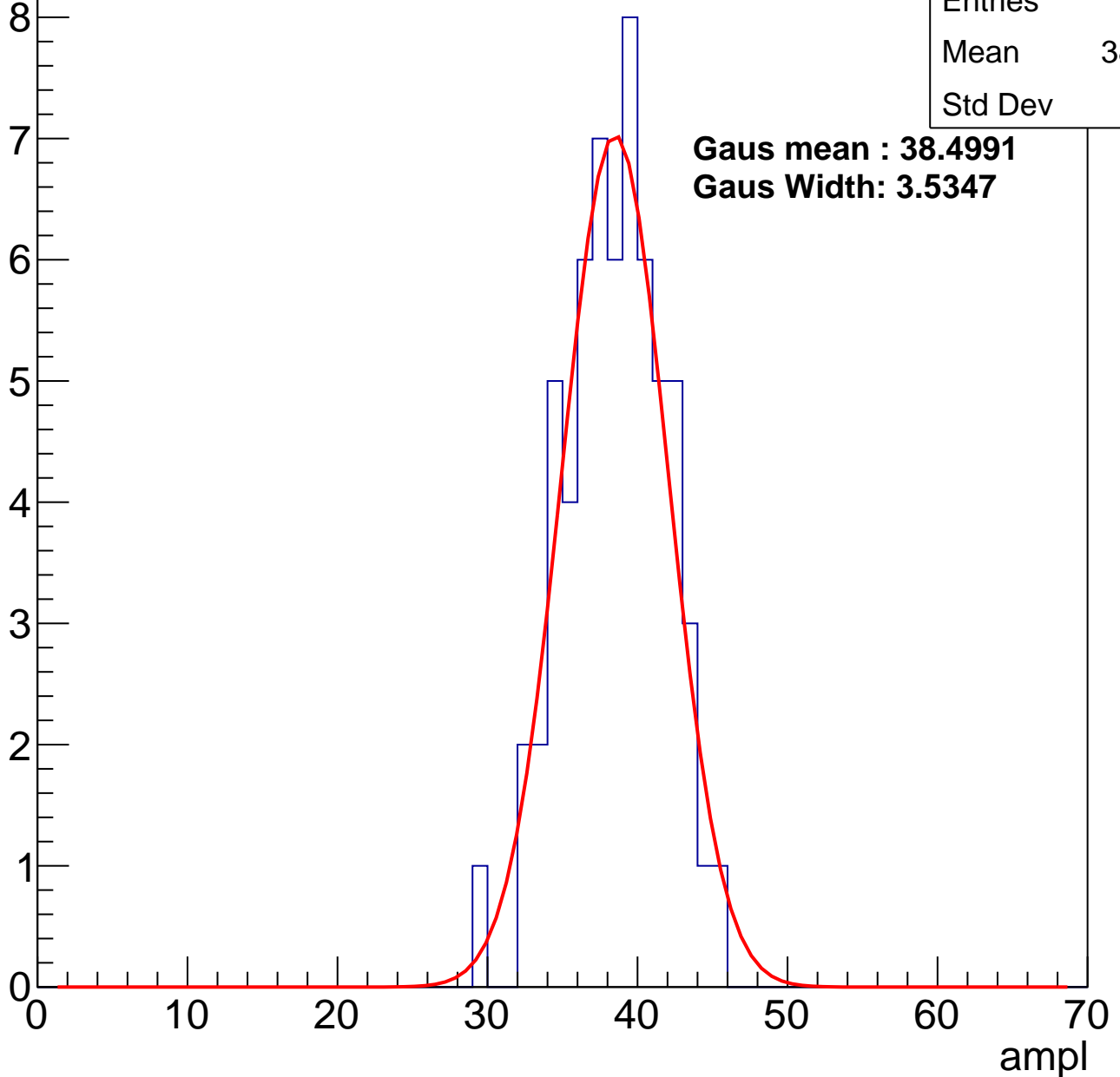
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	38.02
Std Dev	3.28

Gaus mean : 38.4991

Gaus Width: 3.5347



B1L003S, U13-ch88, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	44.16
Std Dev	3.294

Gaus mean : 44.6012

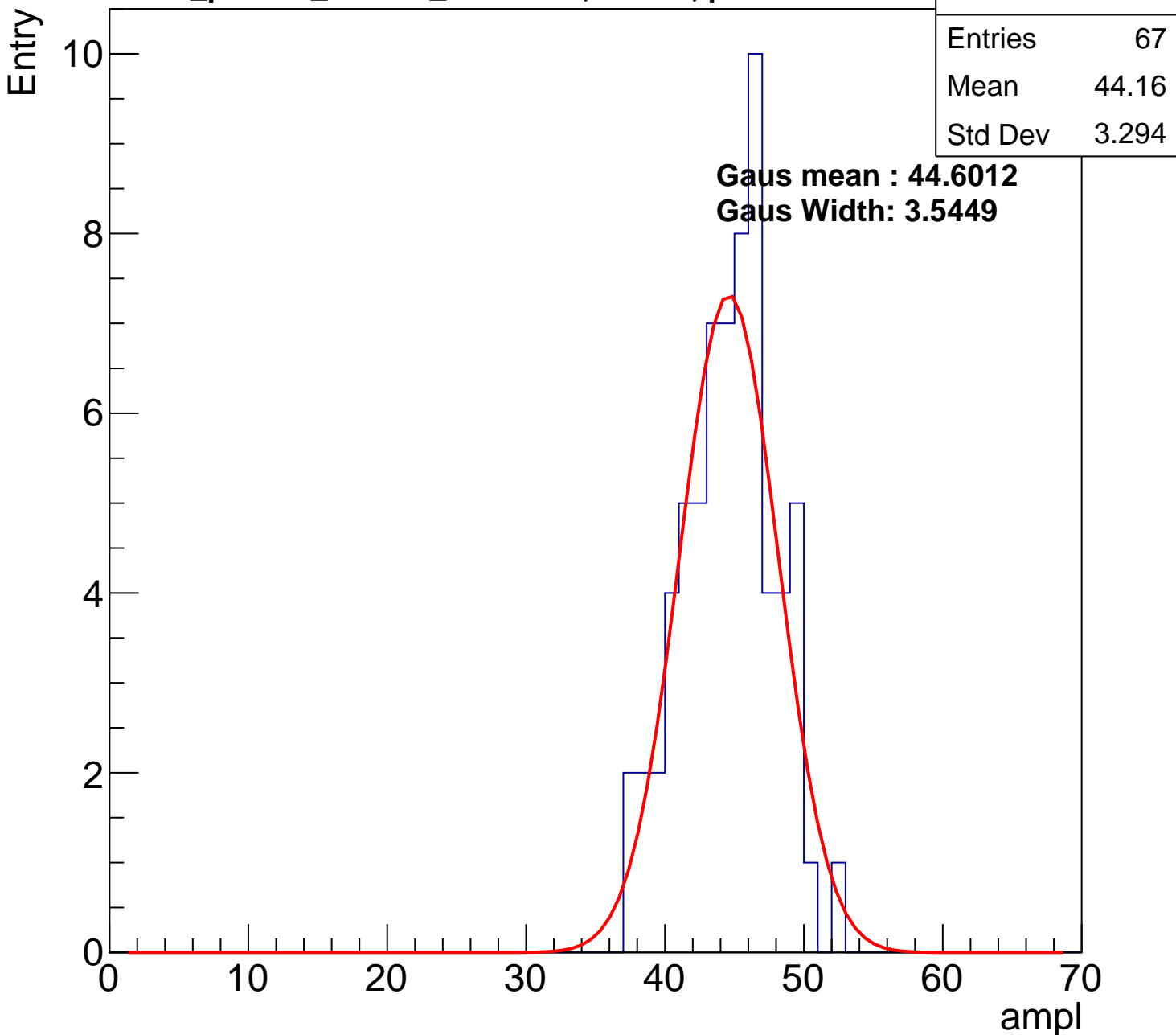
Gaus Width: 3.5449

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

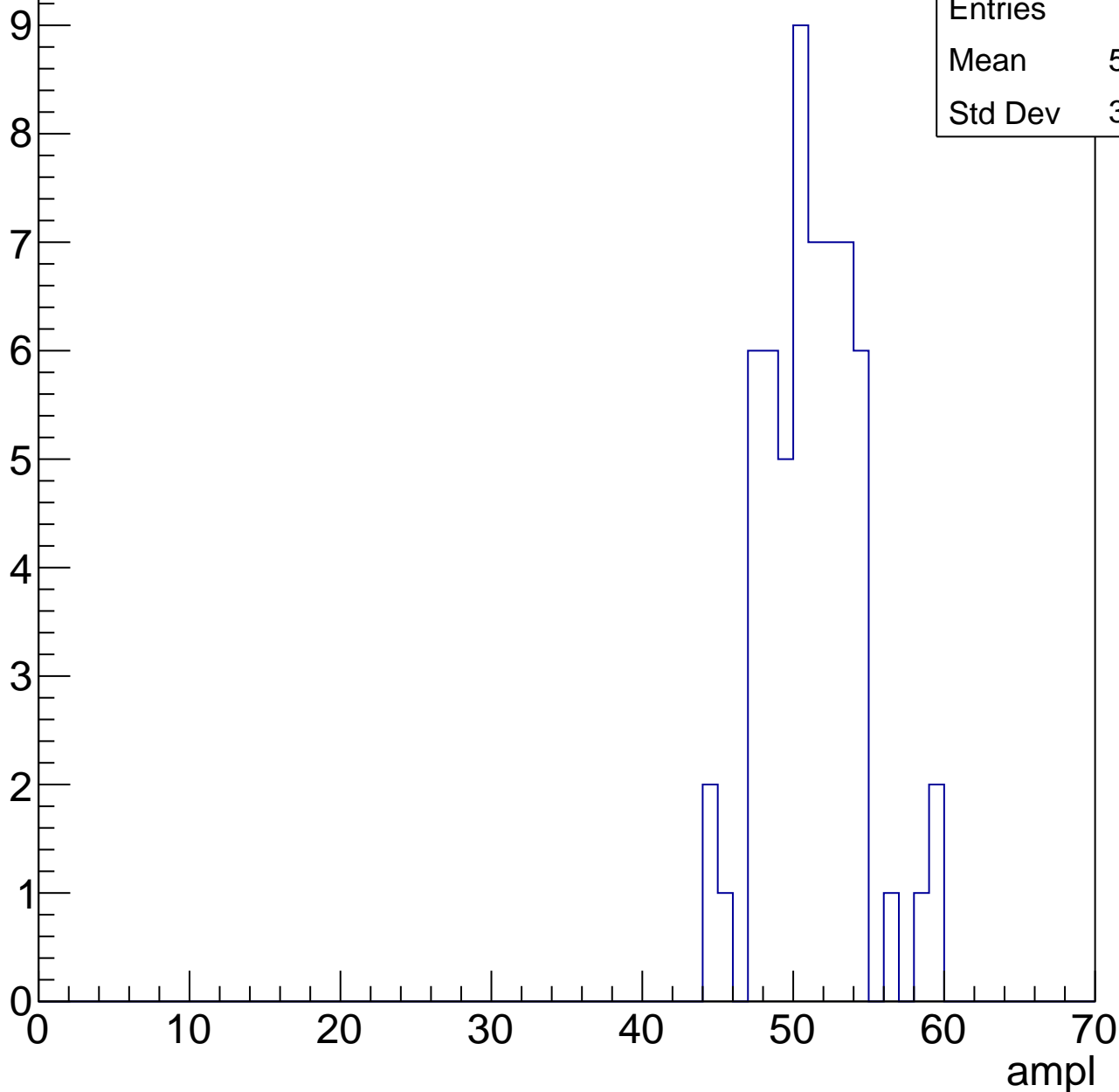


B1L003S, U13-ch88, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

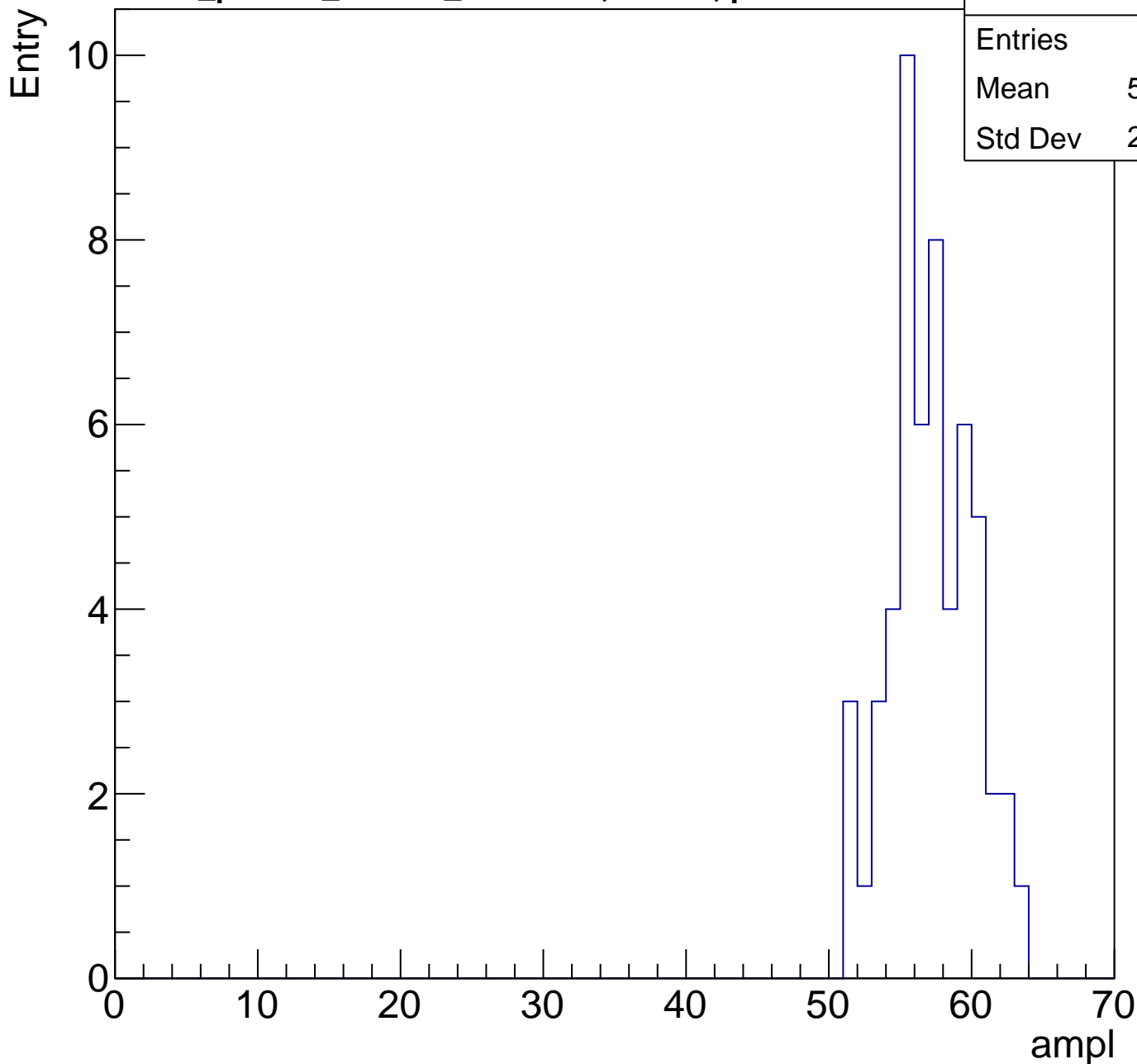
Entries	60
Mean	50.77
Std Dev	3.164



B1L003S, U13-ch88, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	56.67
Std Dev	2.854

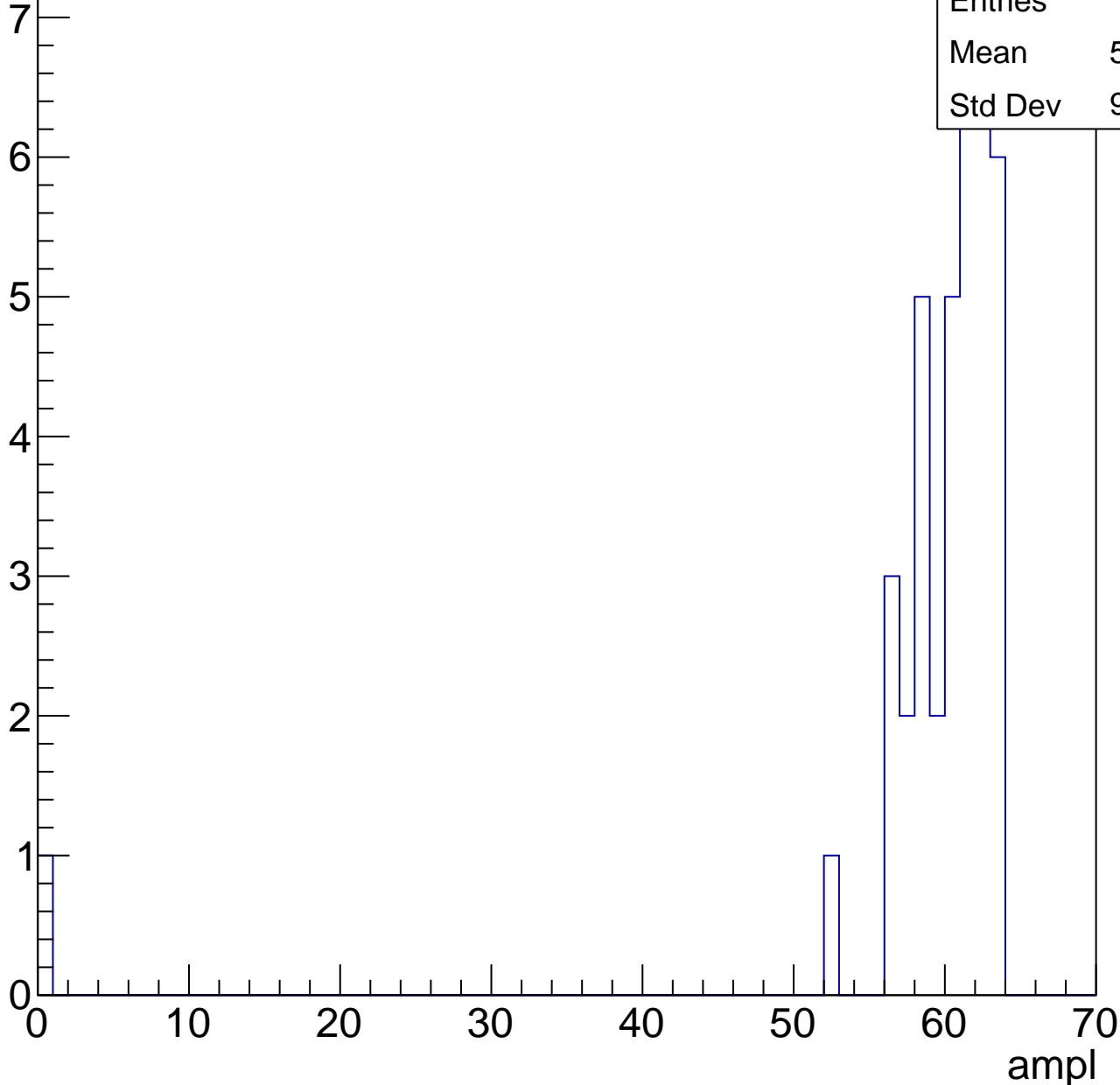


B1L003S, U13-ch88, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.49
Std Dev	9.808



B1L003S, U13-ch88, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B1L003S, U13-ch88, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch89, adc0

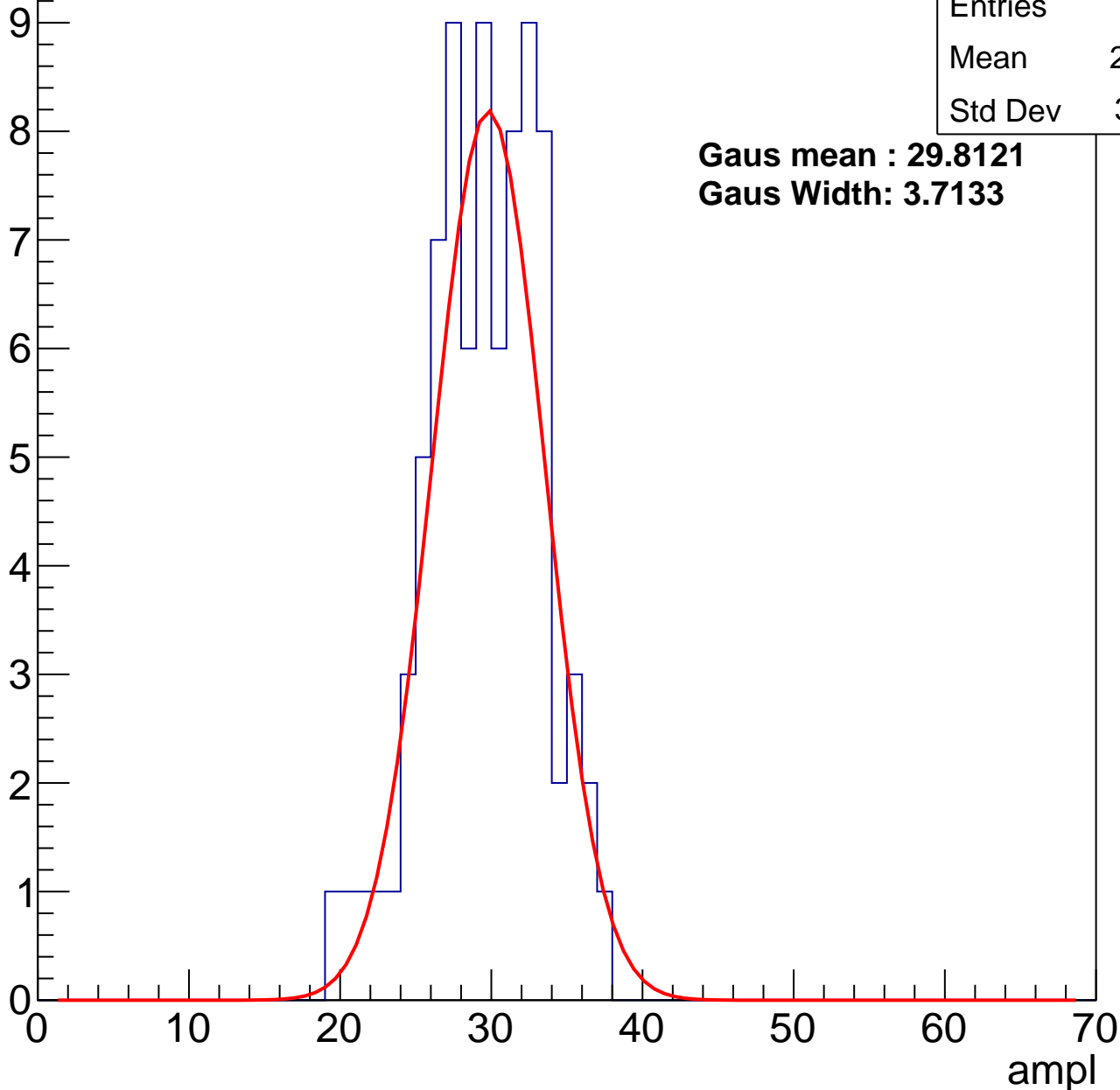
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	29.13
Std Dev	3.731

Gaus mean : 29.8121

Gaus Width: 3.7133



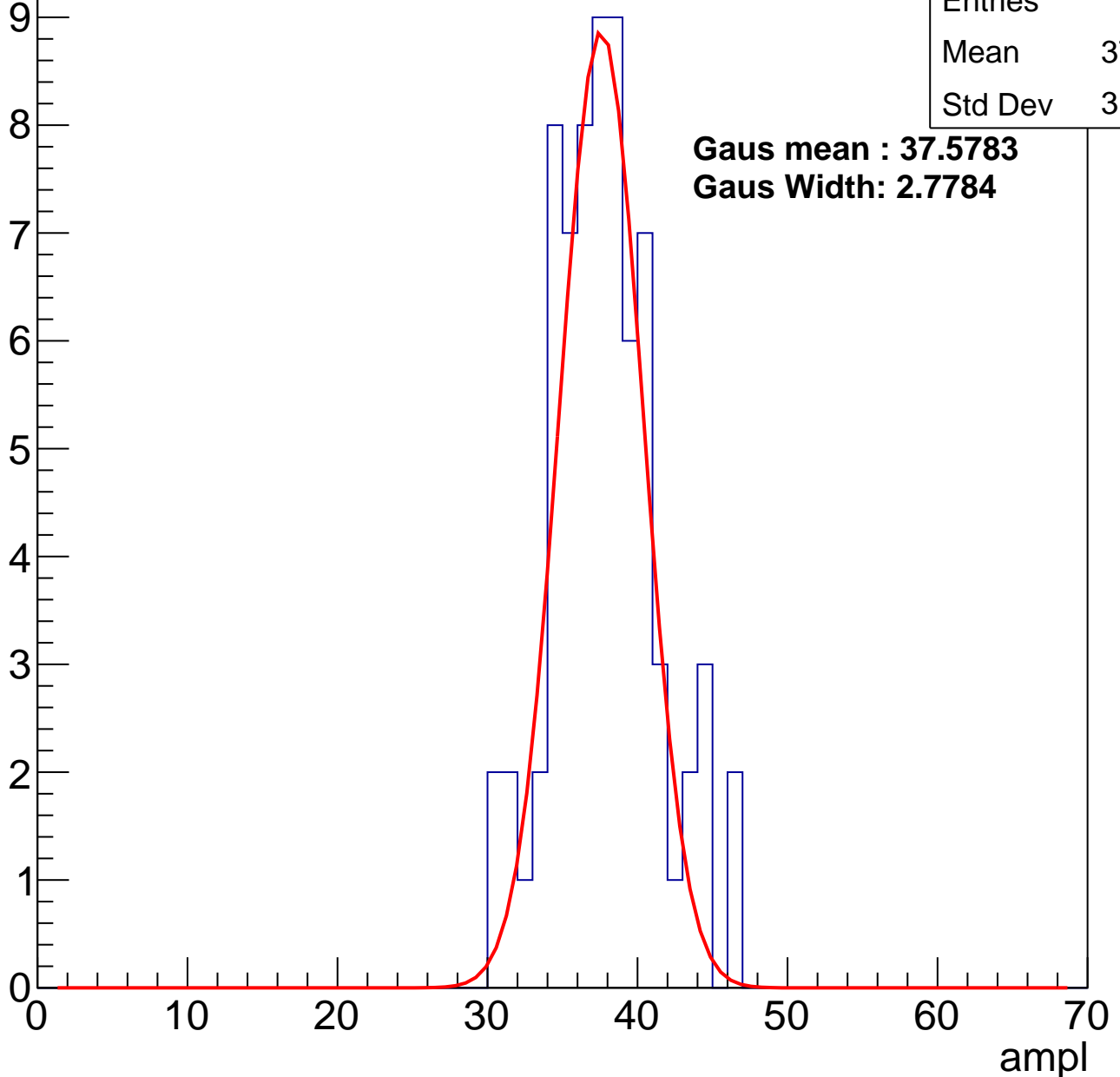
B1L003S, U13-ch89, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.35
Std Dev	3.489

Gaus mean : 37.5783
Gaus Width: 2.7784



B1L003S, U13-ch89, adc2

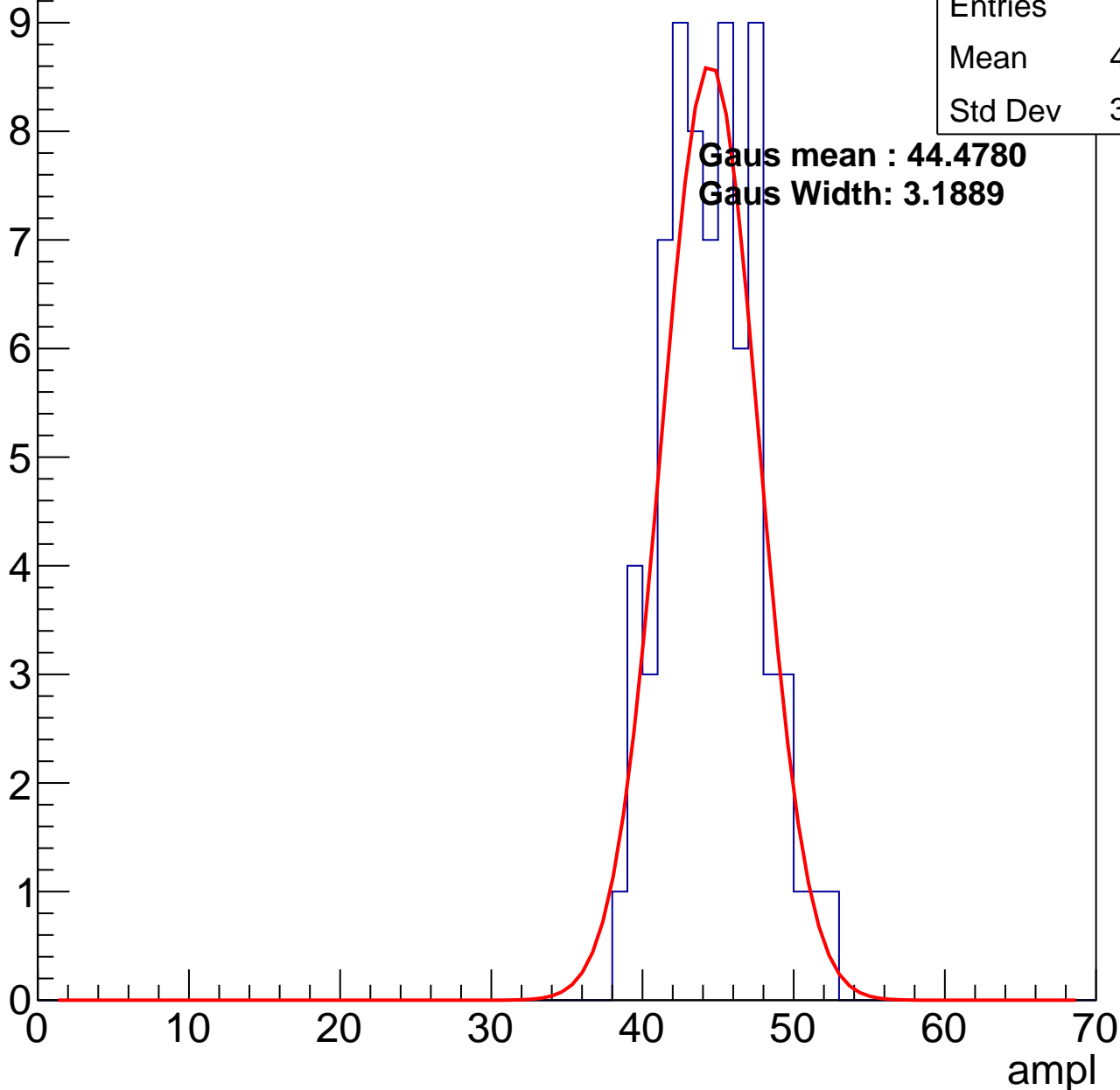
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.15
Std Dev	3.063

Gaus mean : 44.4780

Gaus Width: 3.1889

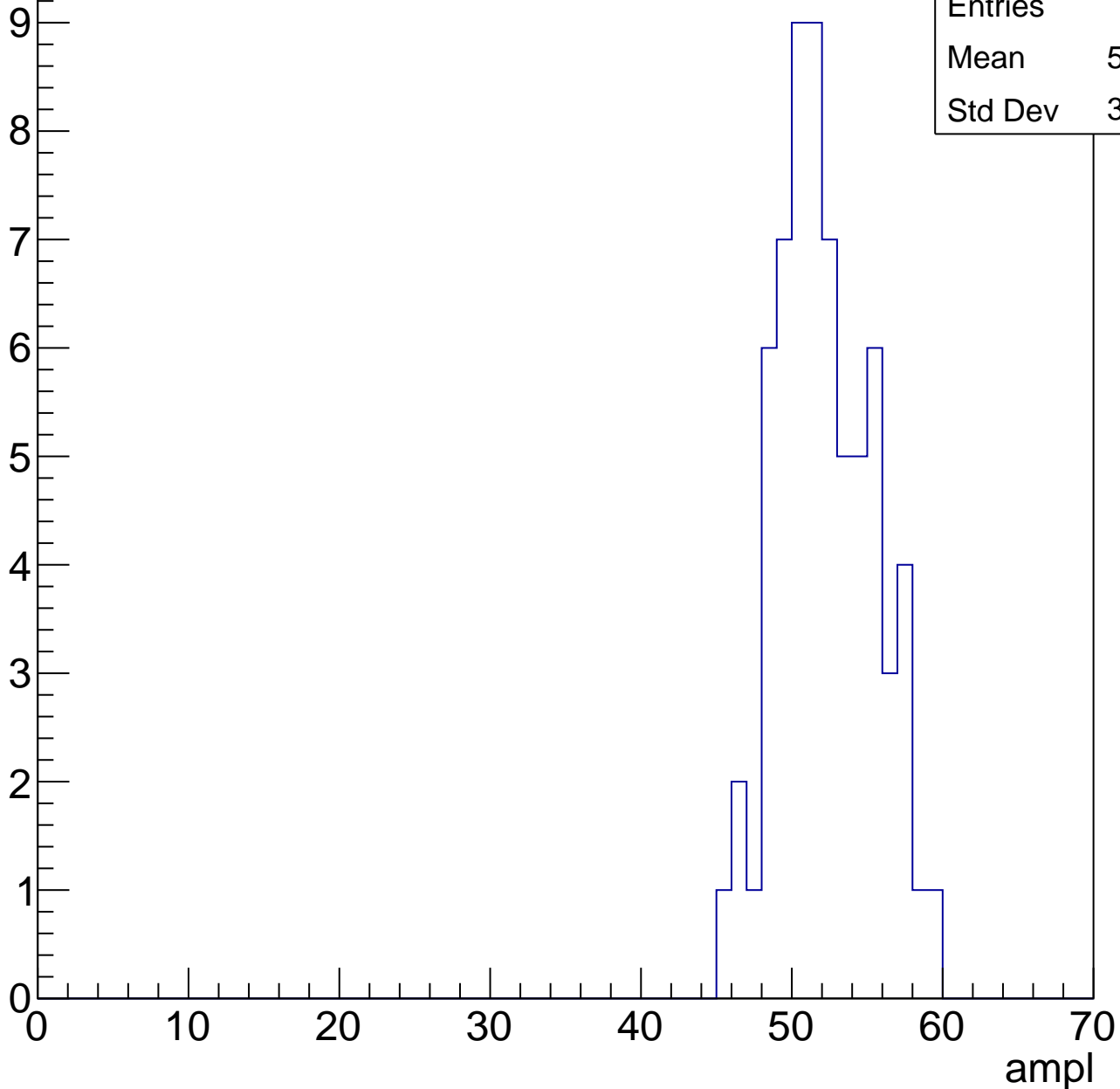


B1L003S, U13-ch89, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	51.73
Std Dev	3.132

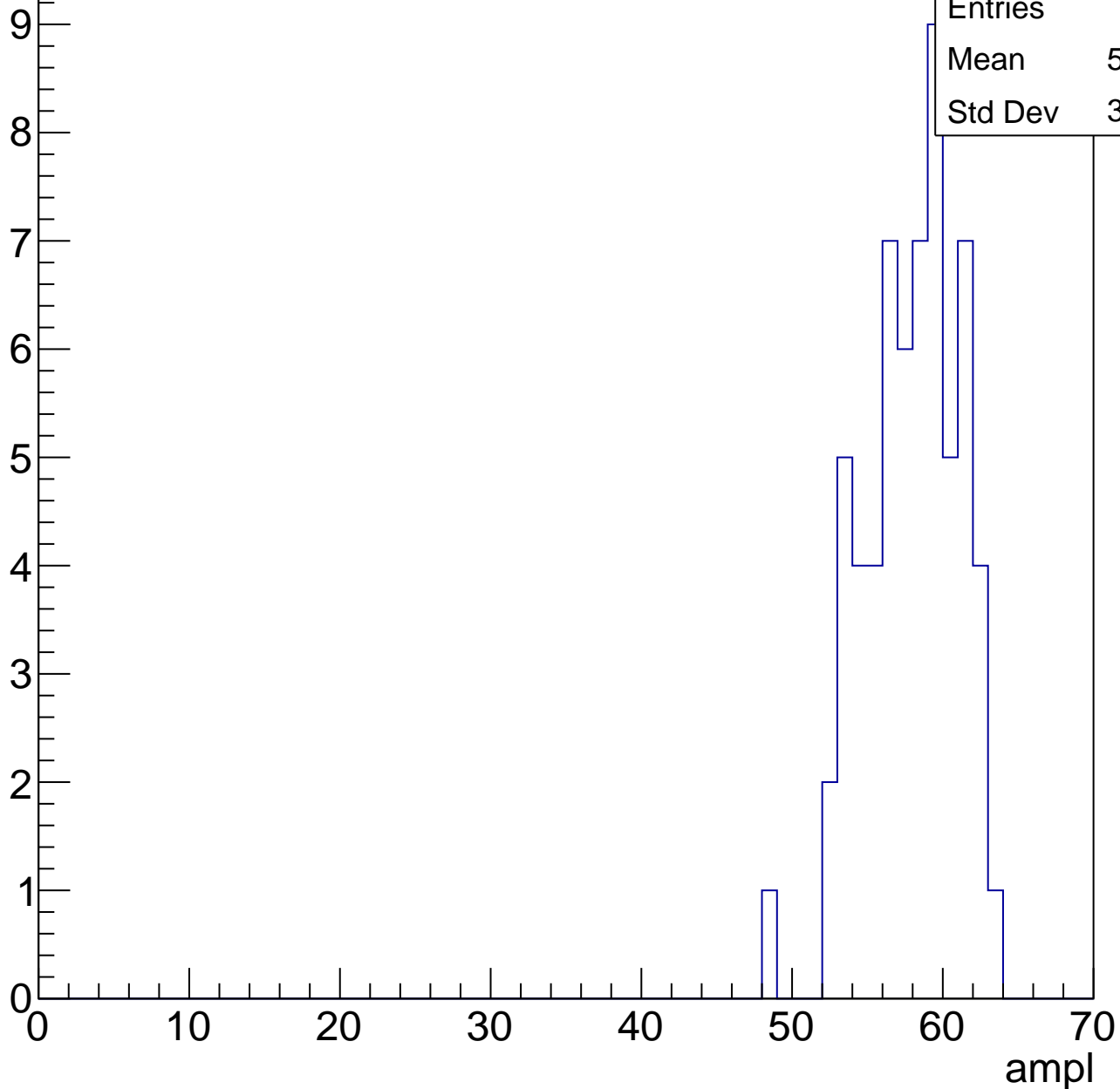


B1L003S, U13-ch89, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	57.45
Std Dev	3.094

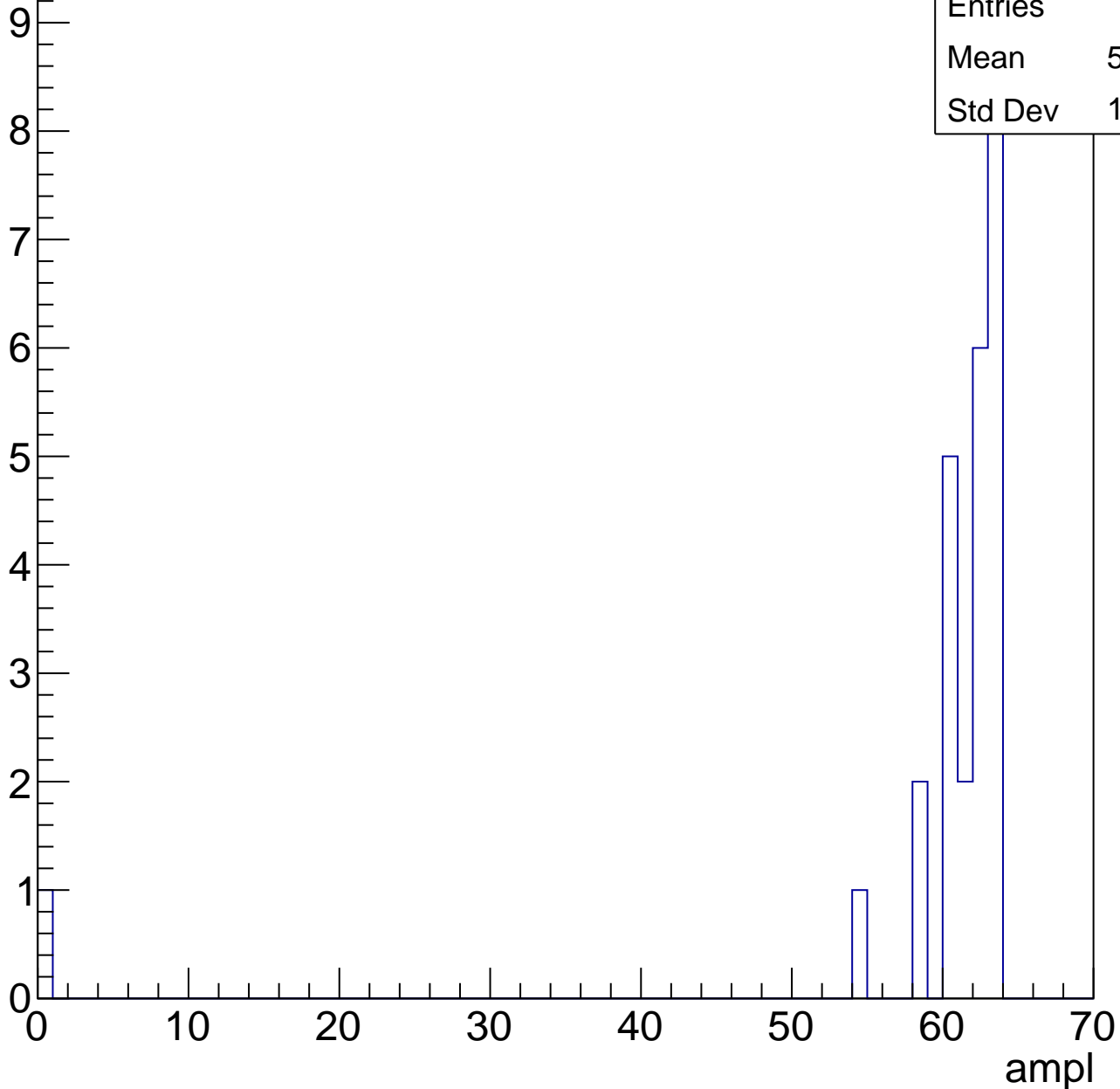


B1L003S, U13-ch89, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	26
Mean	58.88
Std Dev	11.96



B1L003S, U13-ch89, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch89, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch90, adc0

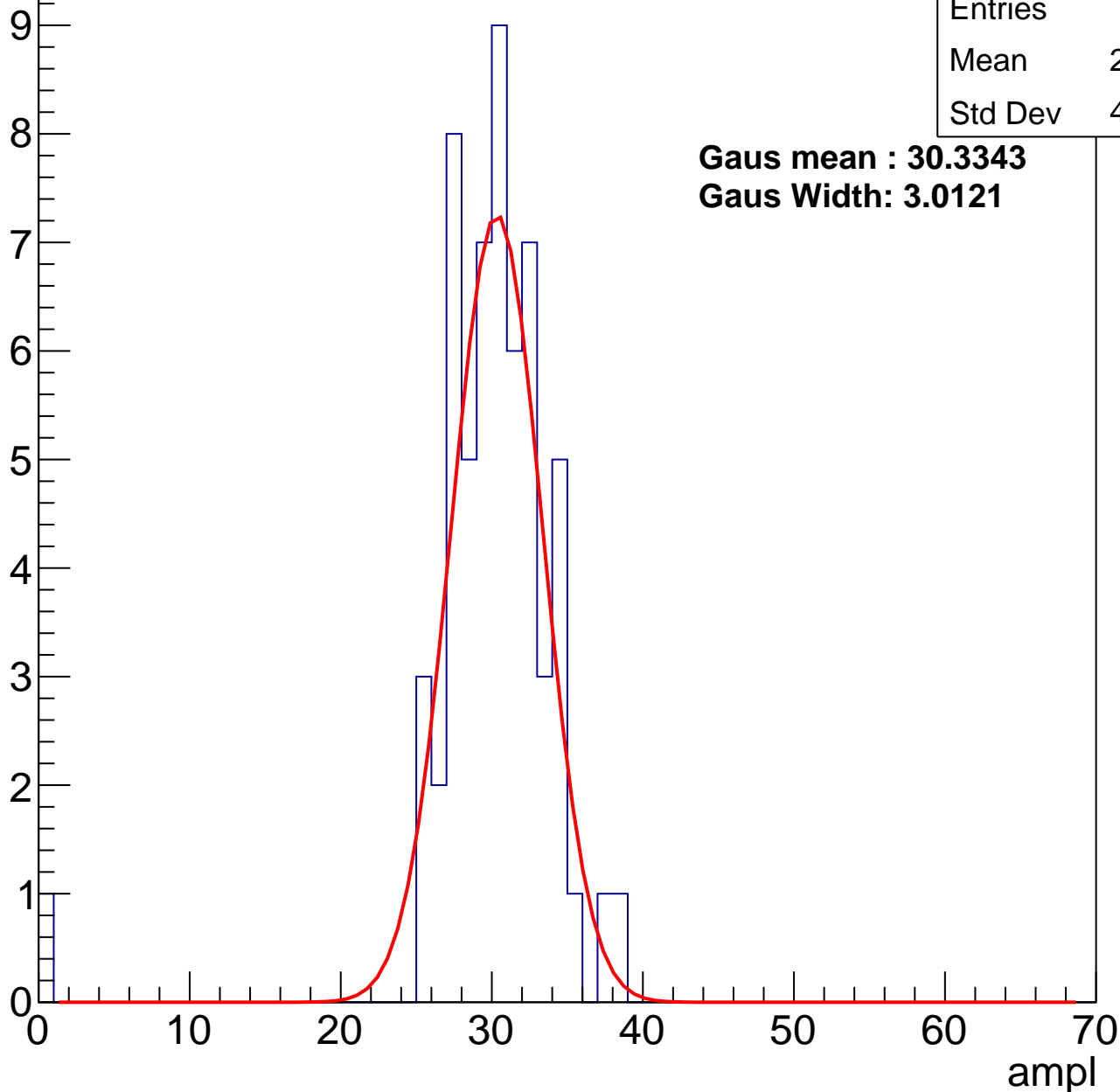
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.58
Std Dev	4.826

Gaus mean : 30.3343

Gaus Width: 3.0121



B1L003S, U13-ch90, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	36.89
Std Dev	3.04

Gaus mean : 37.2012

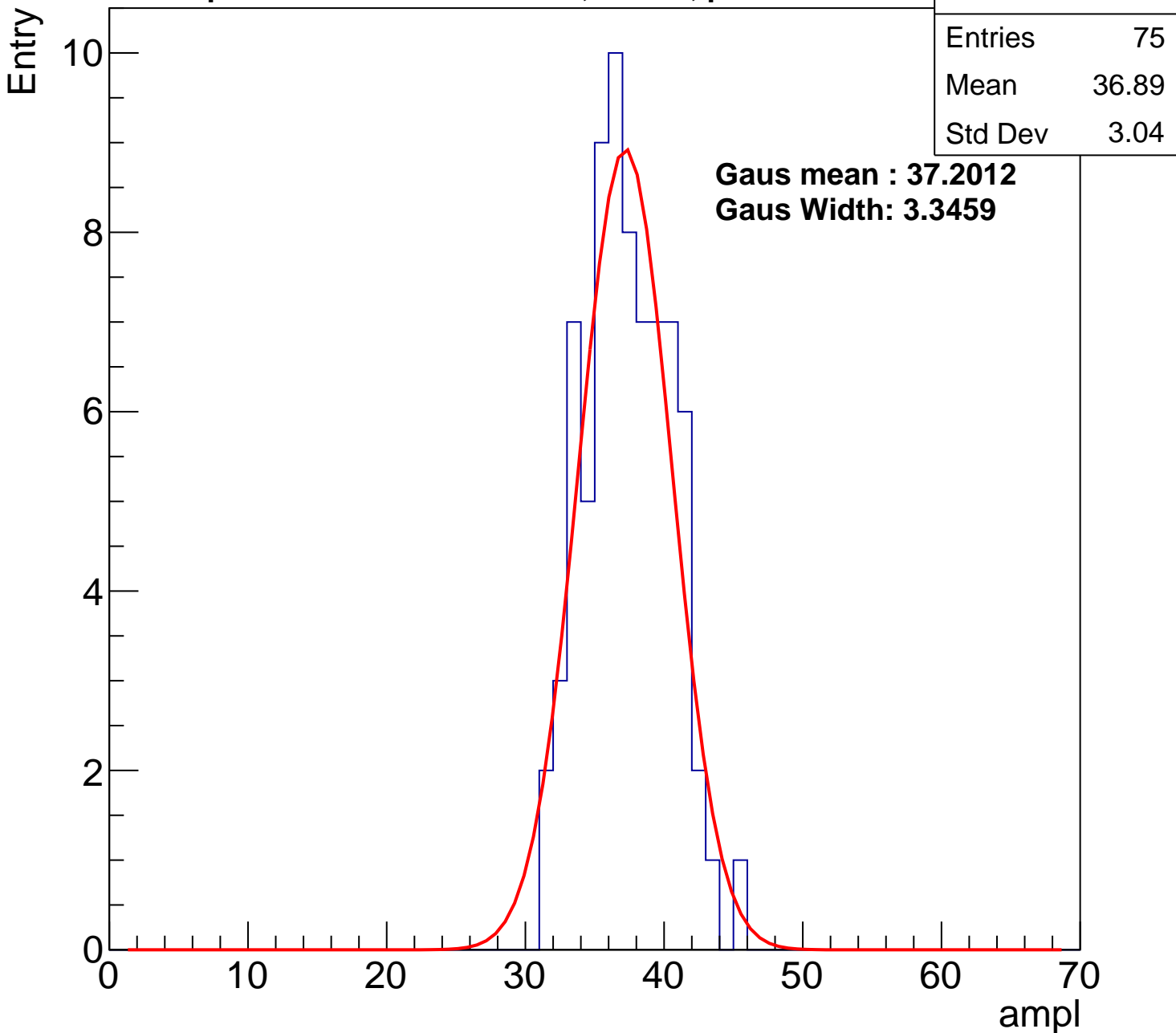
Gaus Width: 3.3459

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch90, adc2

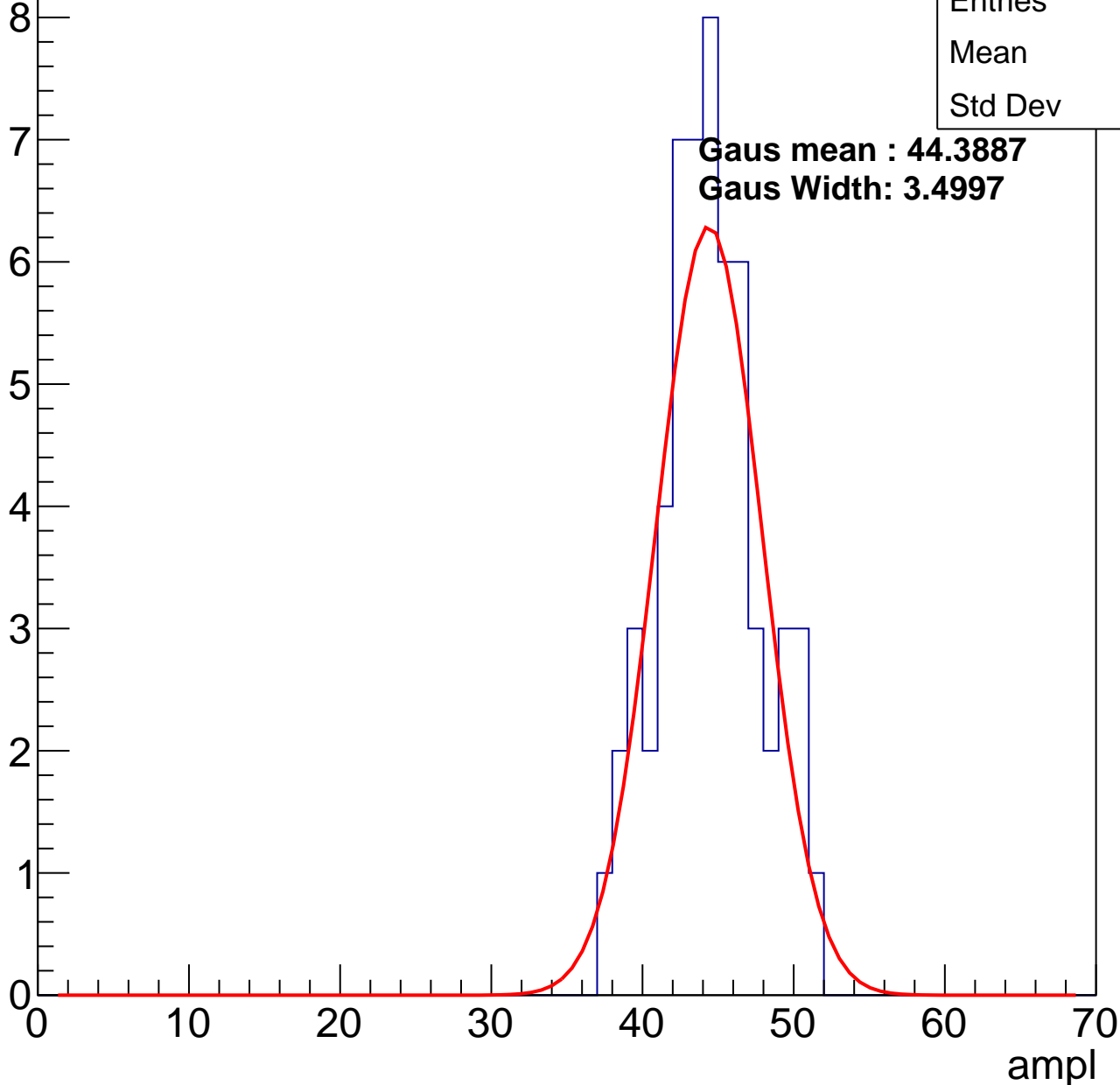
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44
Std Dev	3.27

Gaus mean : 44.3887

Gaus Width: 3.4997

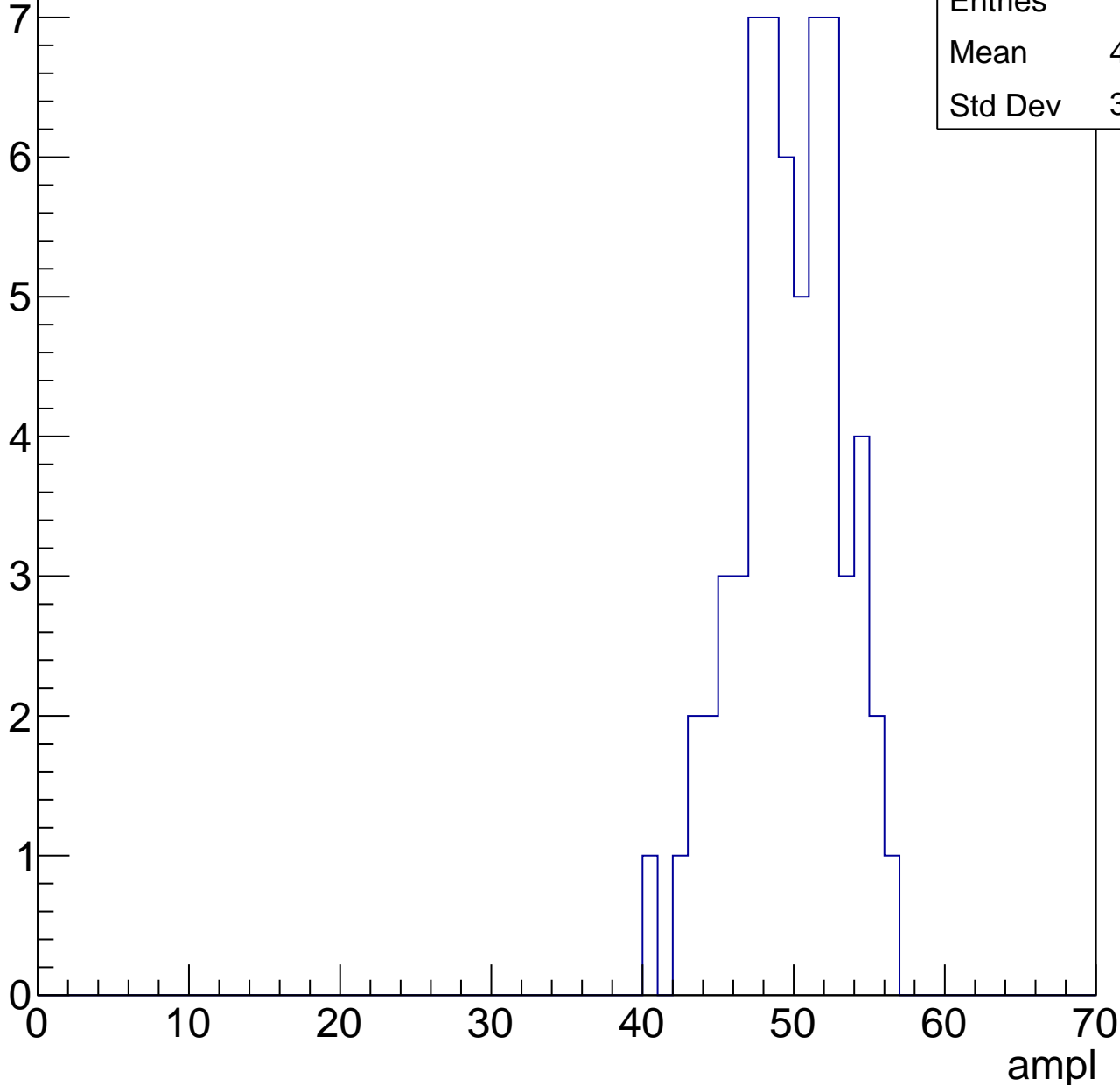


B1L003S, U13-ch90, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

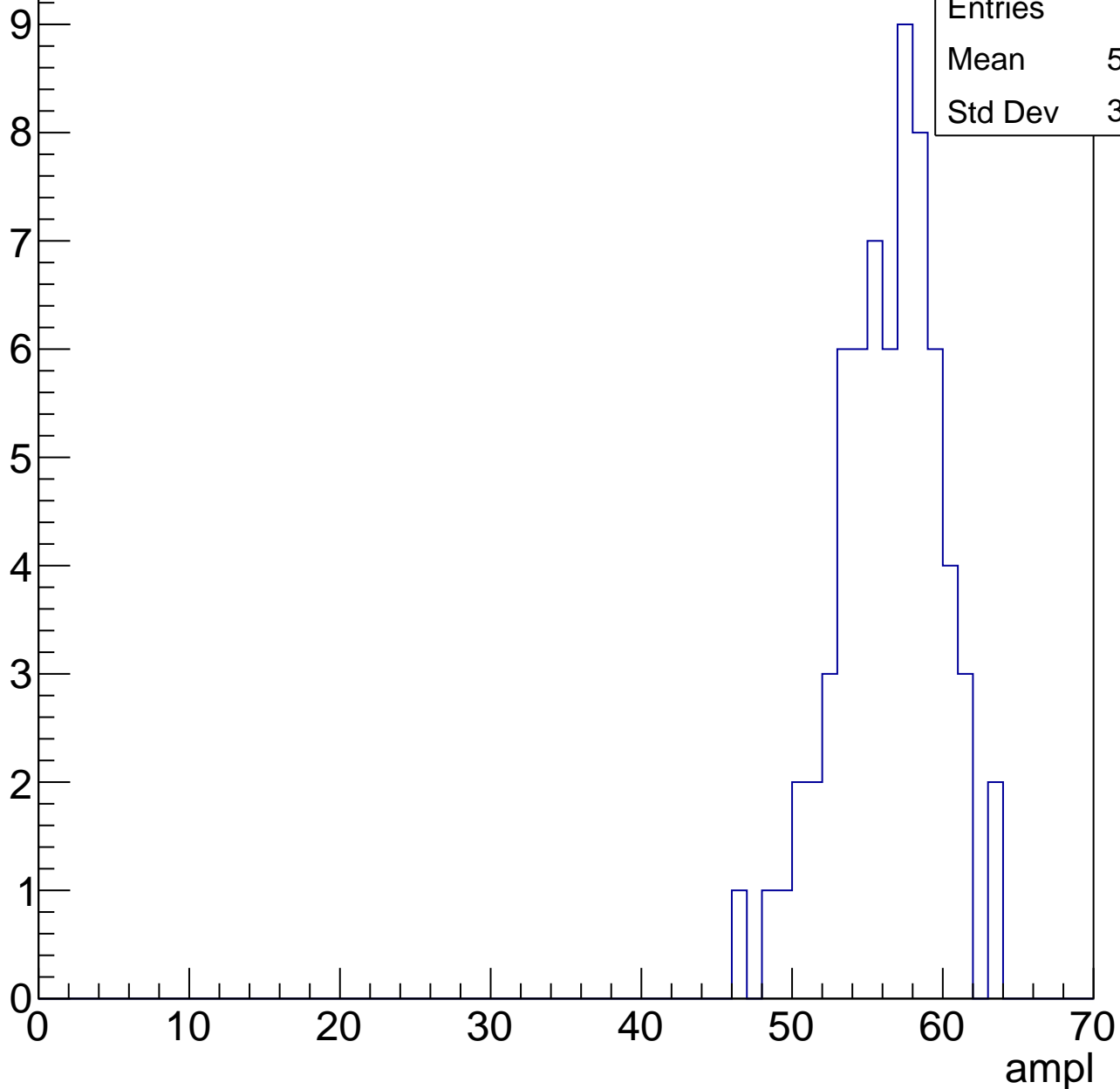
Entries	61
Mean	49.18
Std Dev	3.448



B1L003S, U13-ch90, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



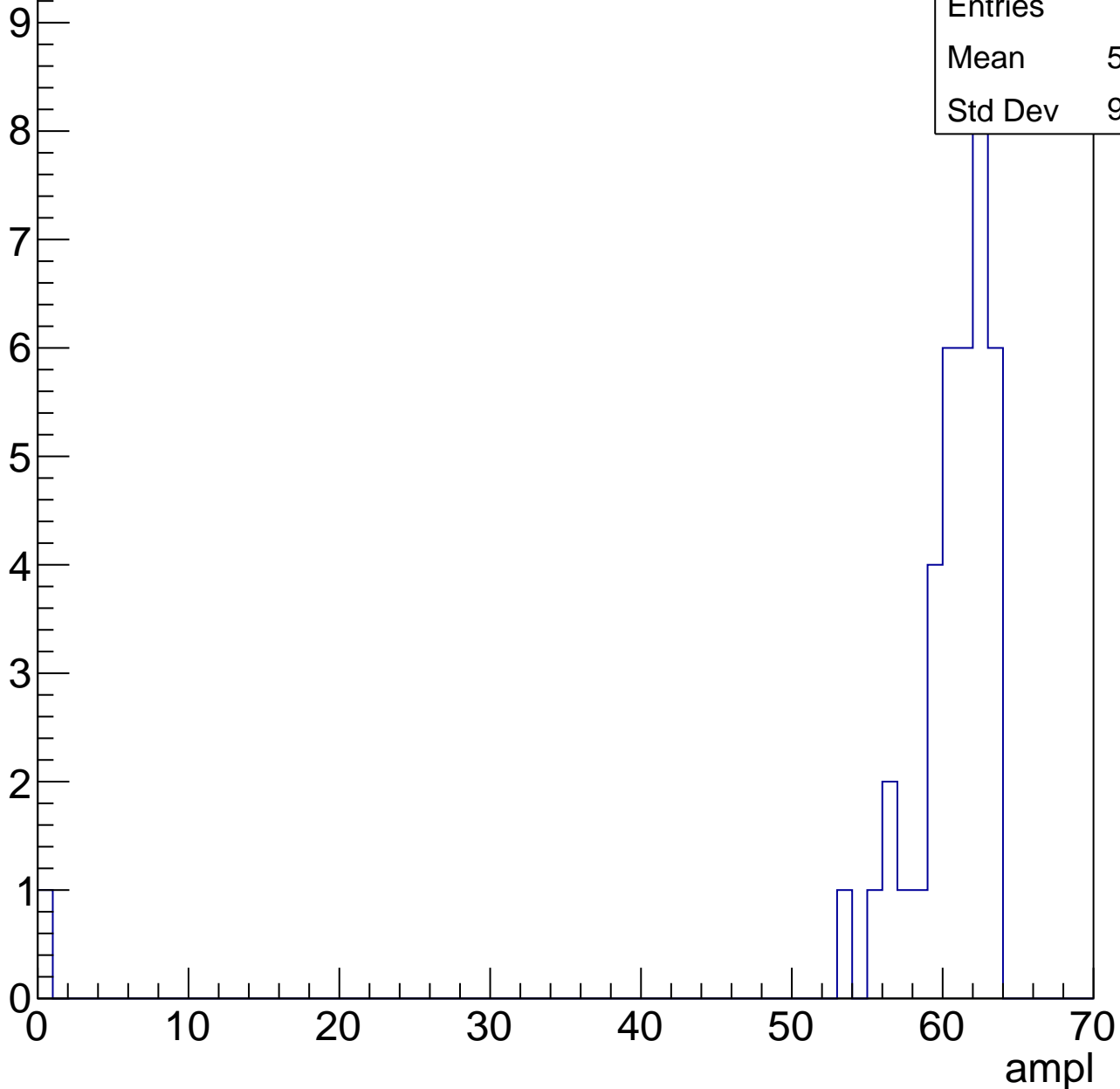
Entries	67
Mean	55.88
Std Dev	3.453

B1L003S, U13-ch90, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	58.76
Std Dev	9.948



B1L003S, U13-ch90, adc6

calib_packv5_042523_0143.root, FC#13, port D2

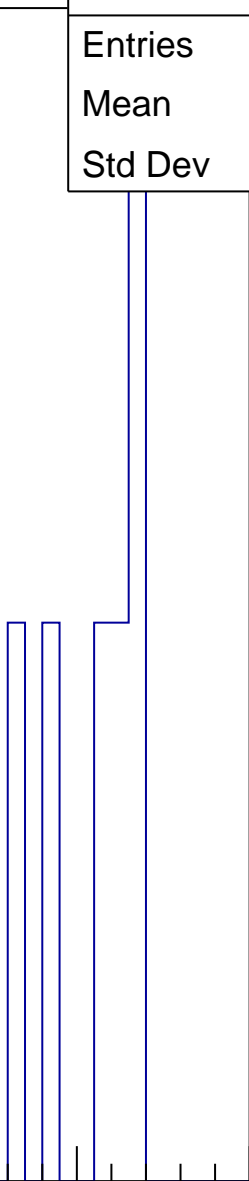
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.5
Std Dev	2.63

0 10 20 30 40 50 60 70

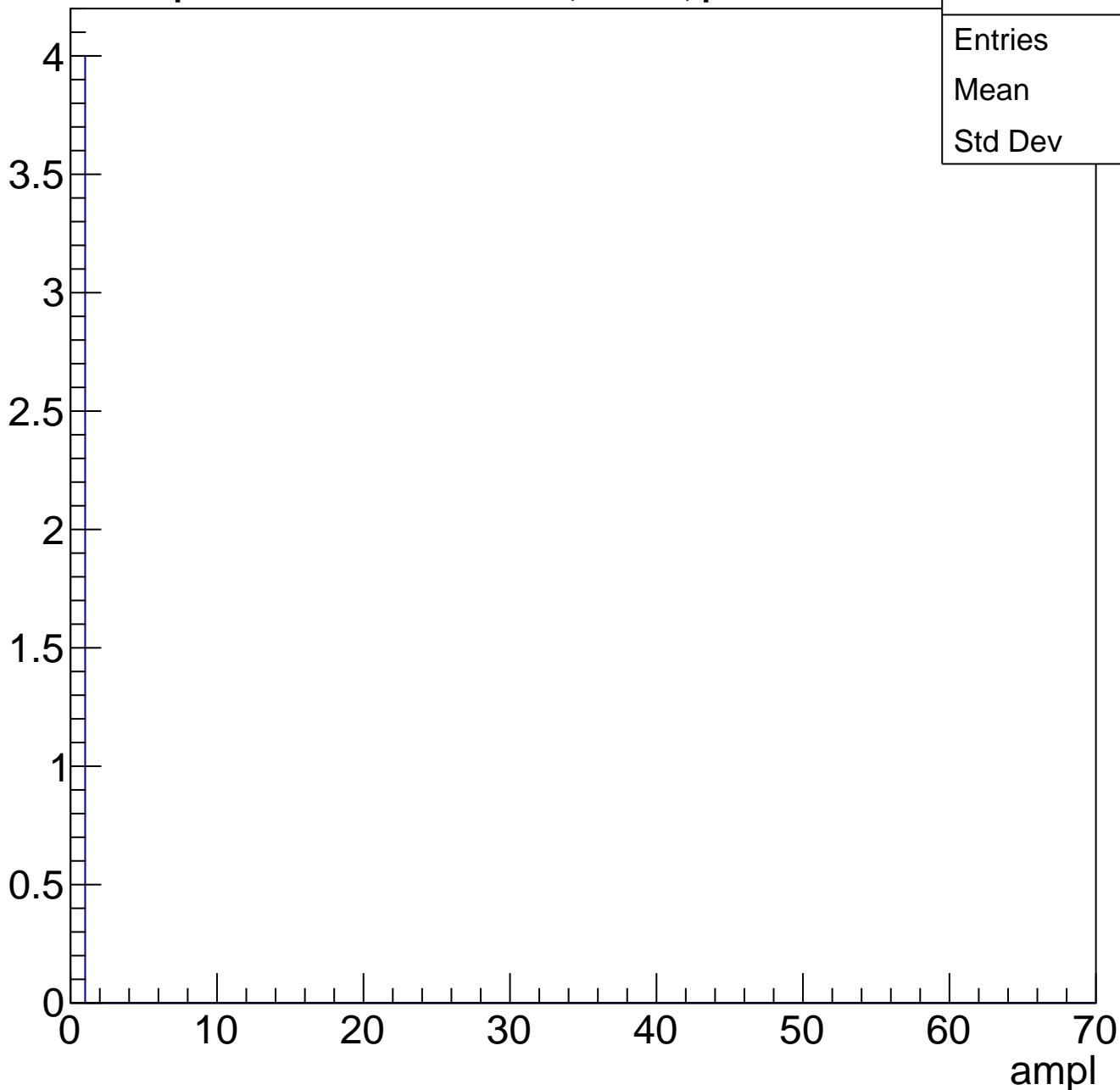
ampl



B1L003S, U13-ch90, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L003S, U13-ch91, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	84
Mean	30.45
Std Dev	4.704

Gaus mean : 32.2555

Gaus Width: 3.9241

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

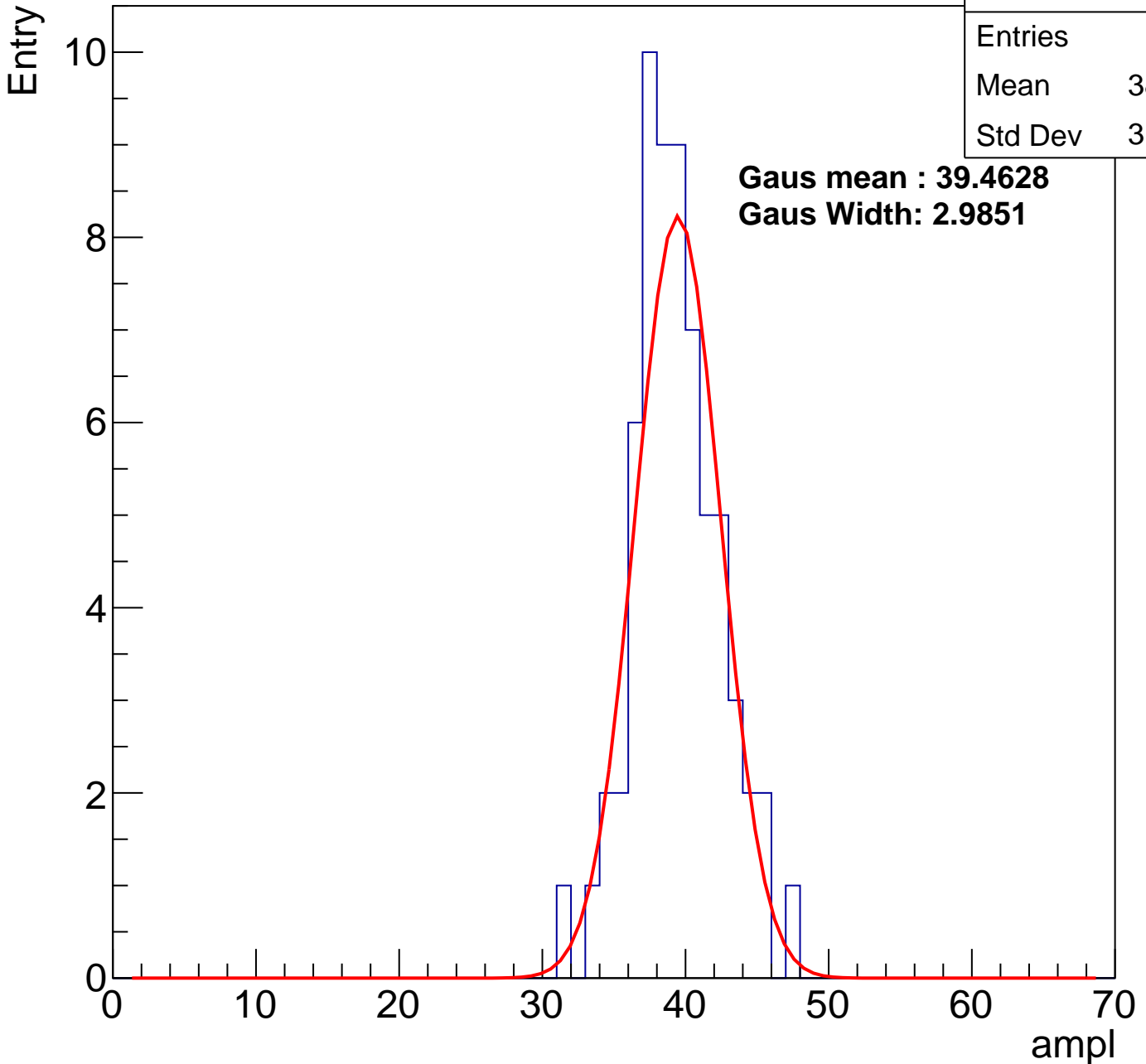
70

B1L003S, U13-ch91, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	38.92
Std Dev	3.014

Gaus mean : 39.4628
Gaus Width: 2.9851



B1L003S, U13-ch91, adc2

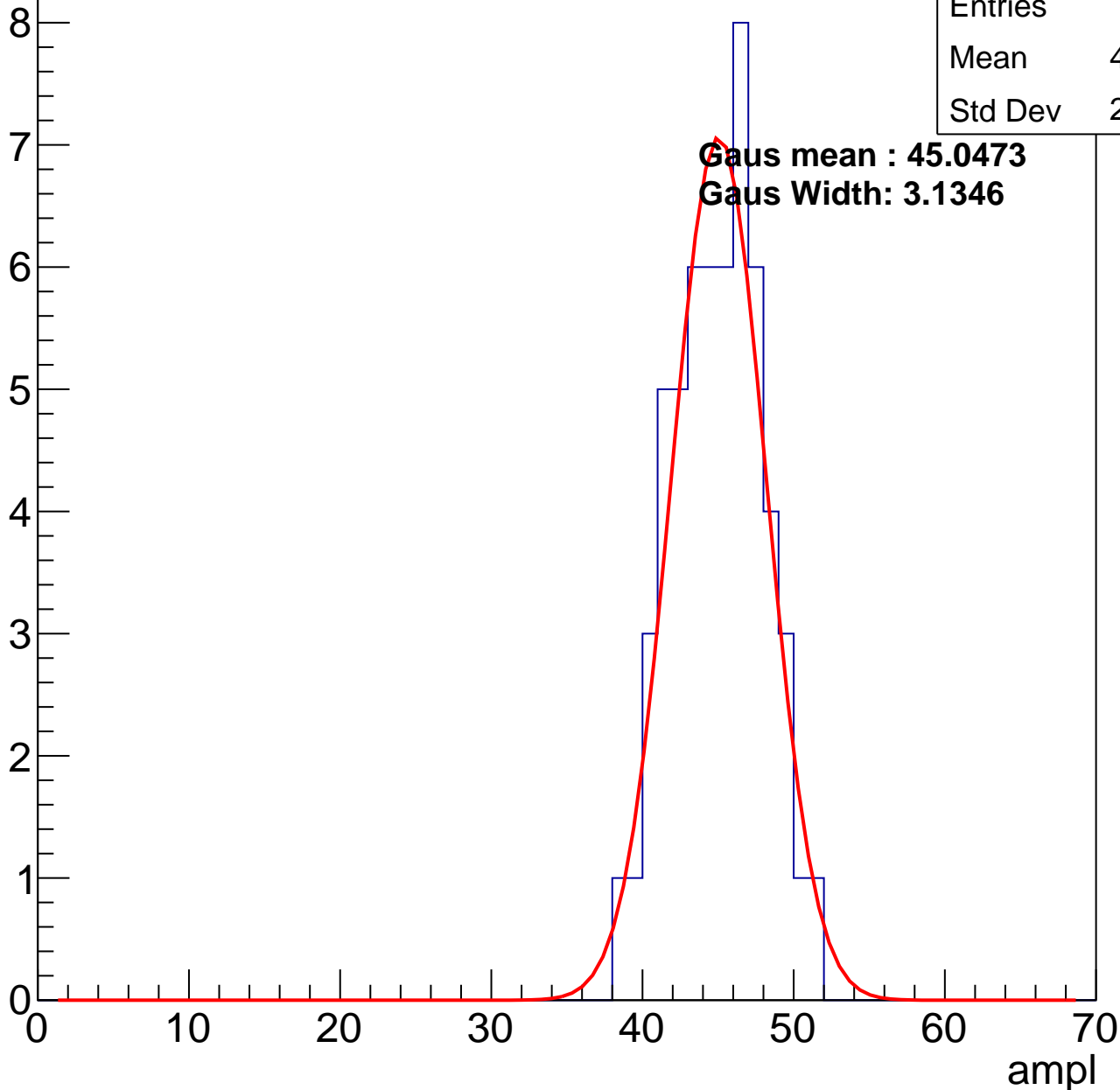
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	44.54
Std Dev	2.927

Gaus mean : 45.0473

Gaus Width: 3.1346

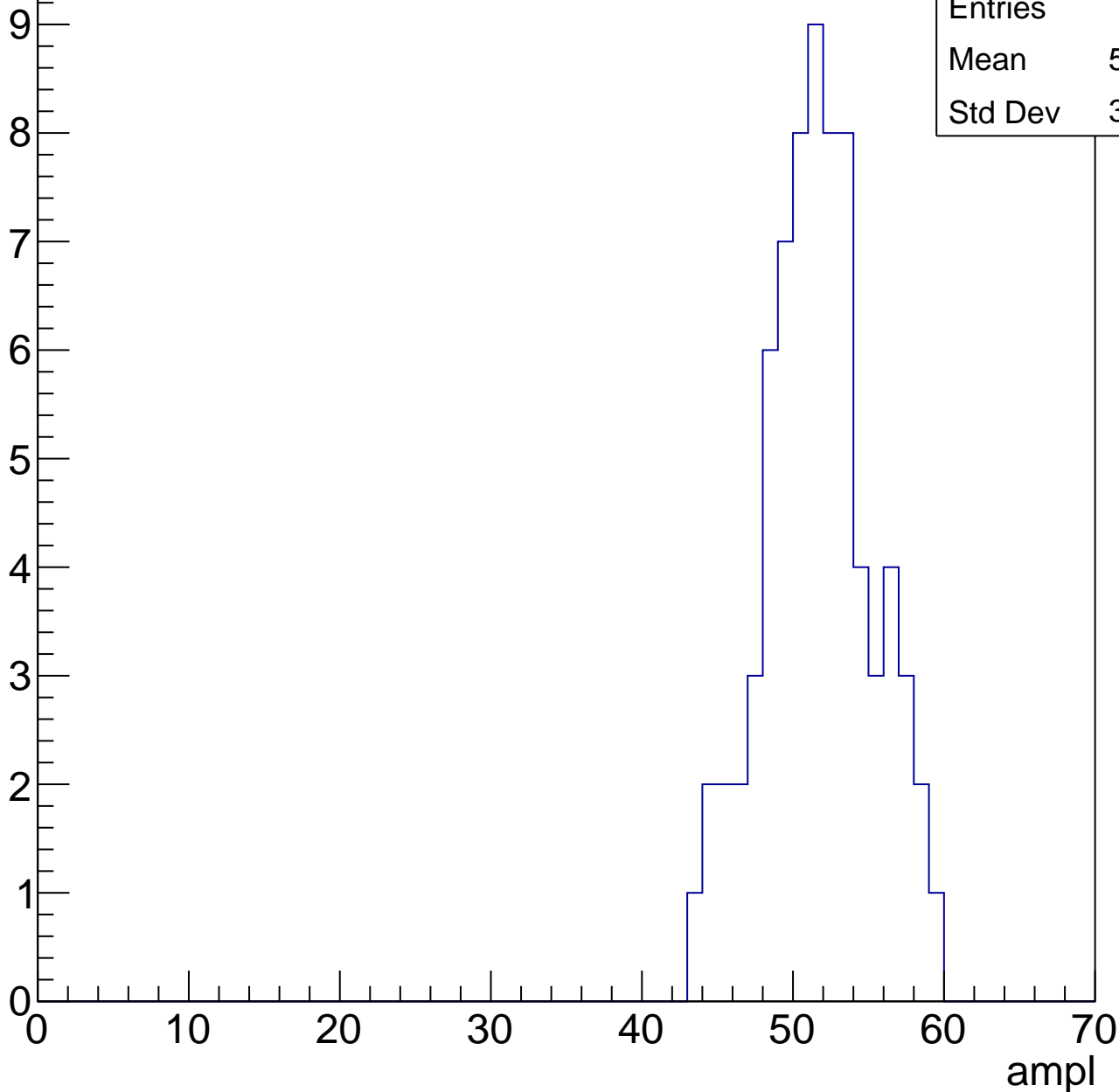


B1L003S, U13-ch91, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	51.16
Std Dev	3.538

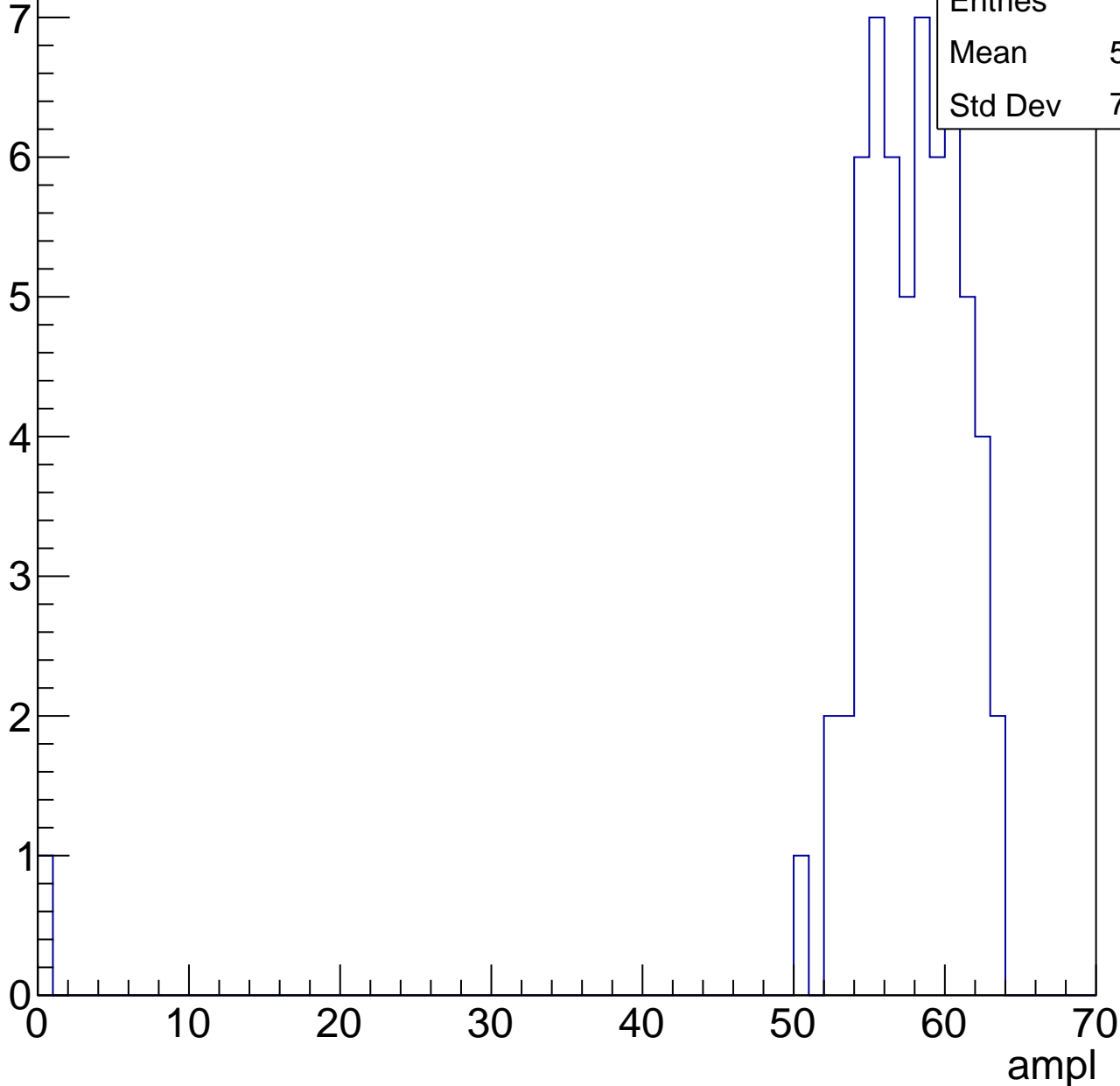


B1L003S, U13-ch91, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	56.54
Std Dev	7.894

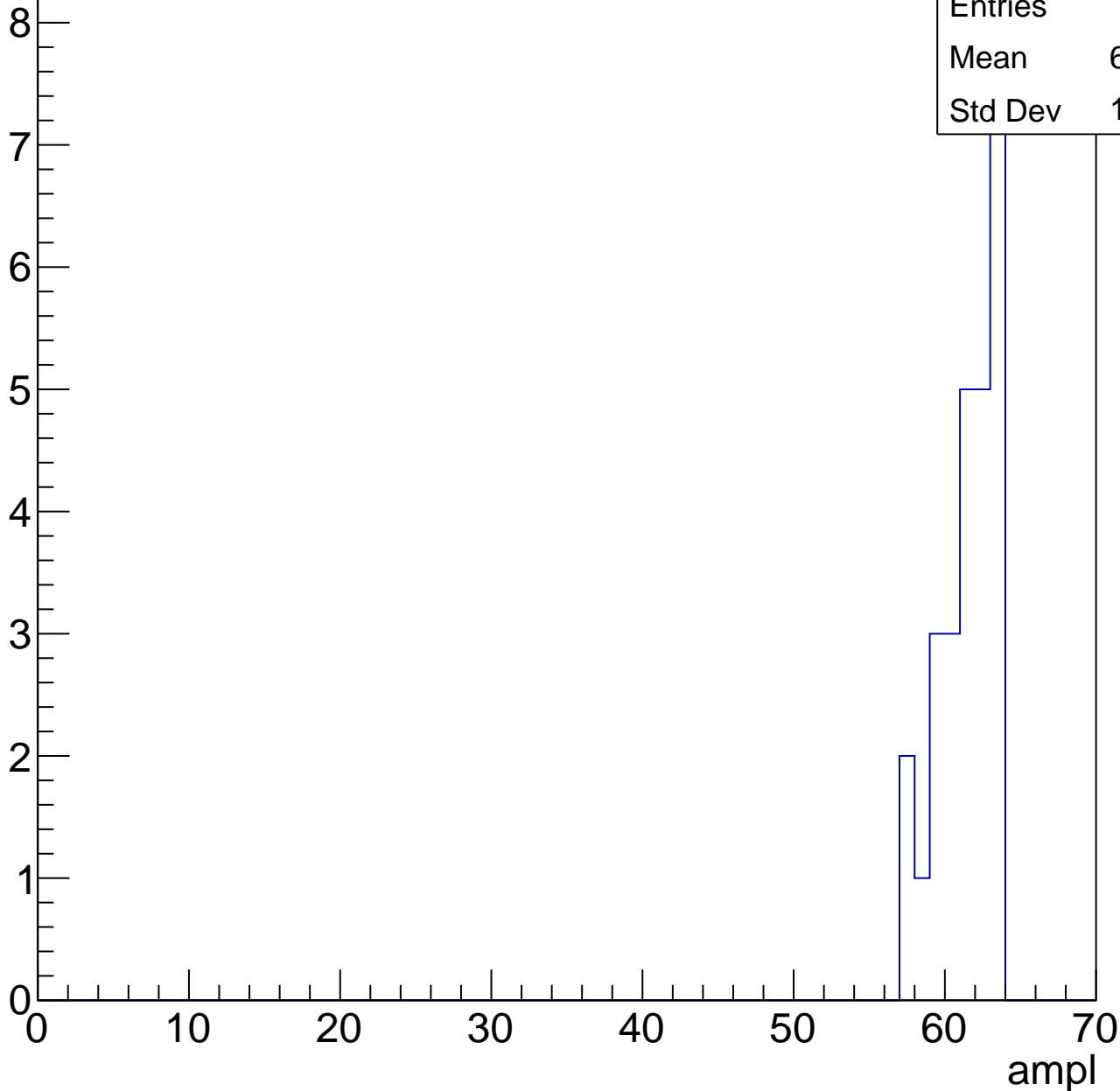


B1L003S, U13-ch91, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

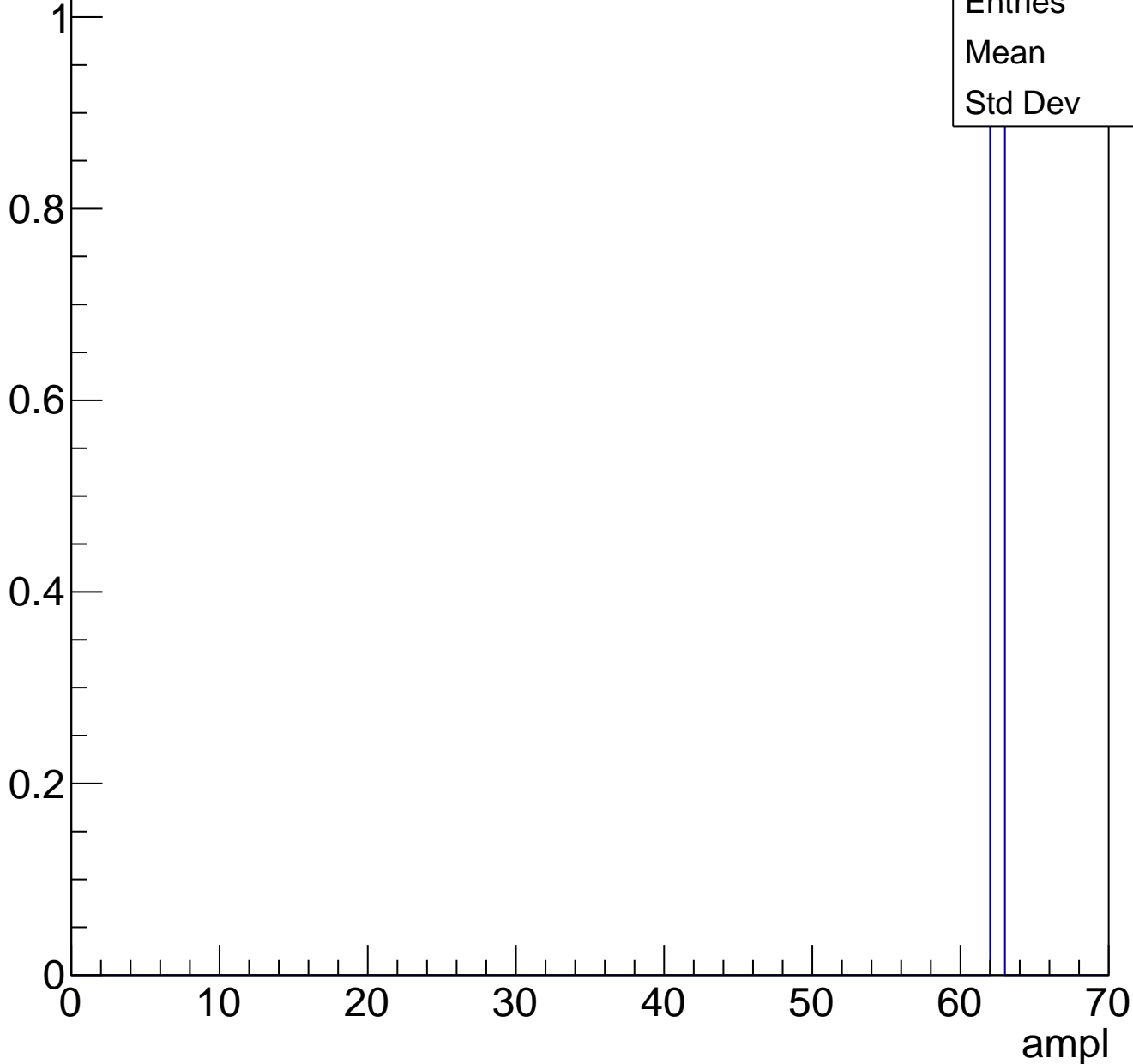
Entries	27
Mean	61.04
Std Dev	1.856



B1L003S, U13-ch91, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch91, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch92, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	28.84
Std Dev	4.689

Gaus mean : 29.6482

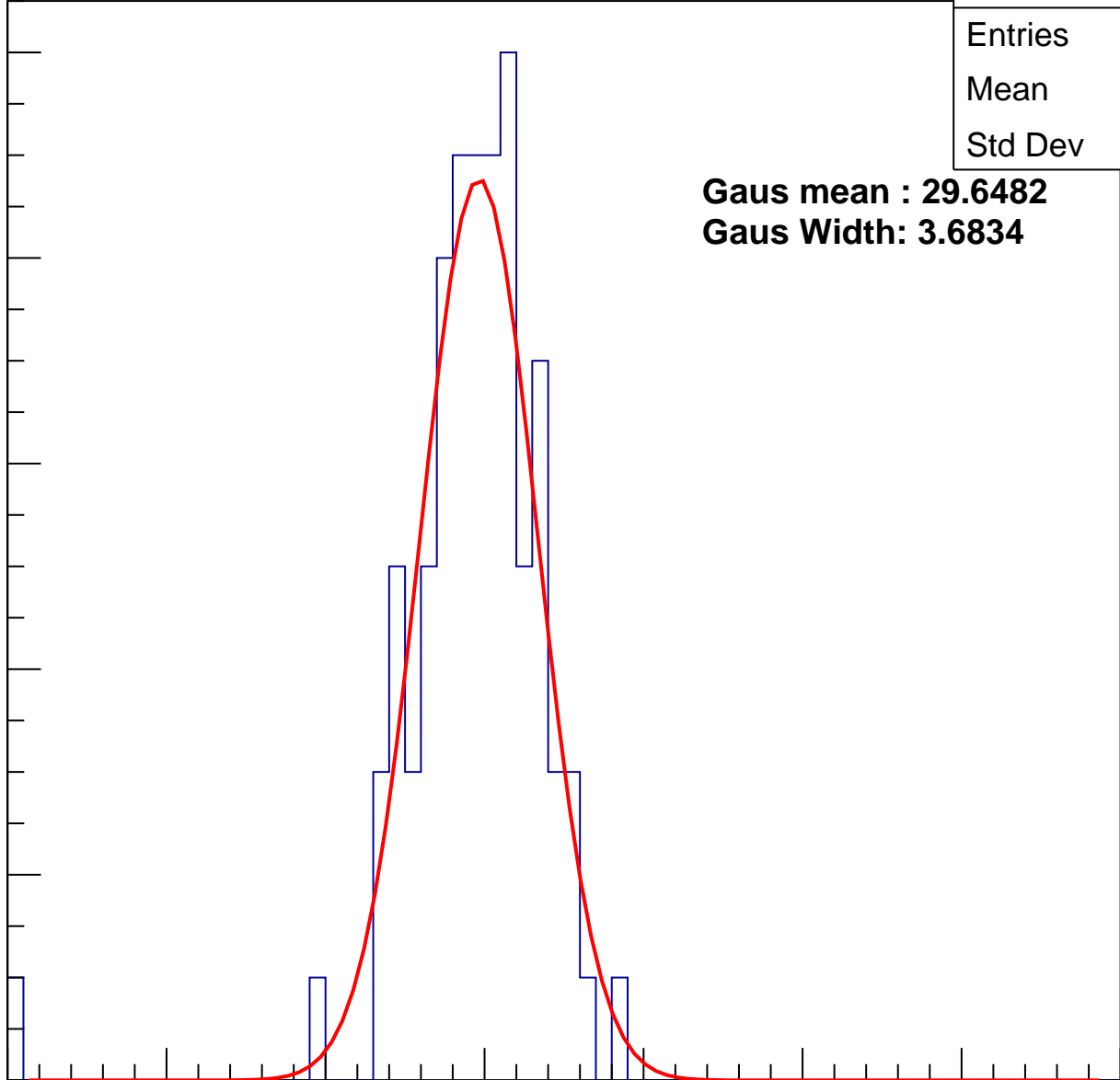
Gaus Width: 3.6834

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch92, adc1

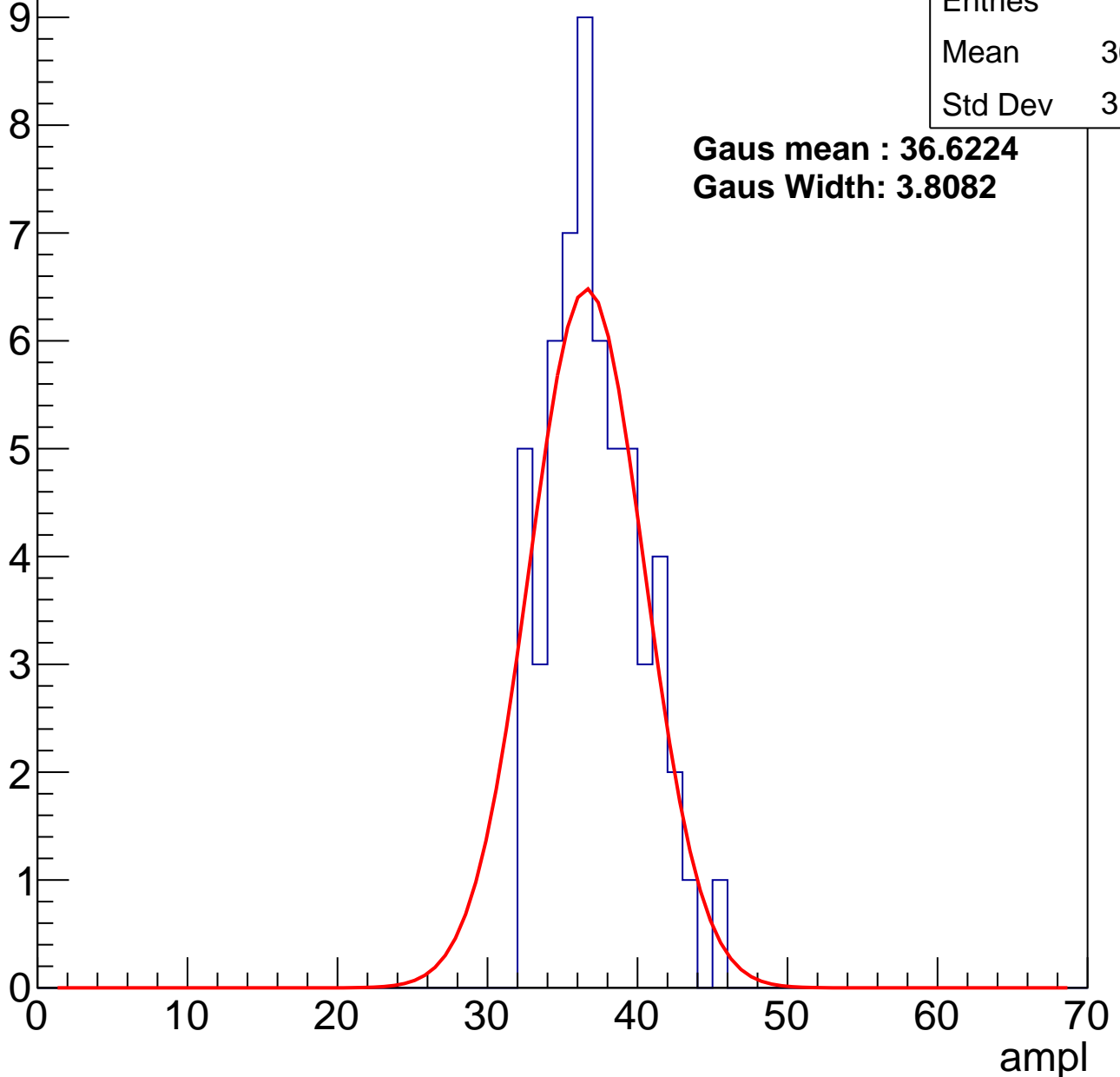
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.75
Std Dev	3.039

Gaus mean : 36.6224

Gaus Width: 3.8082



B1L003S, U13-ch92, adc2

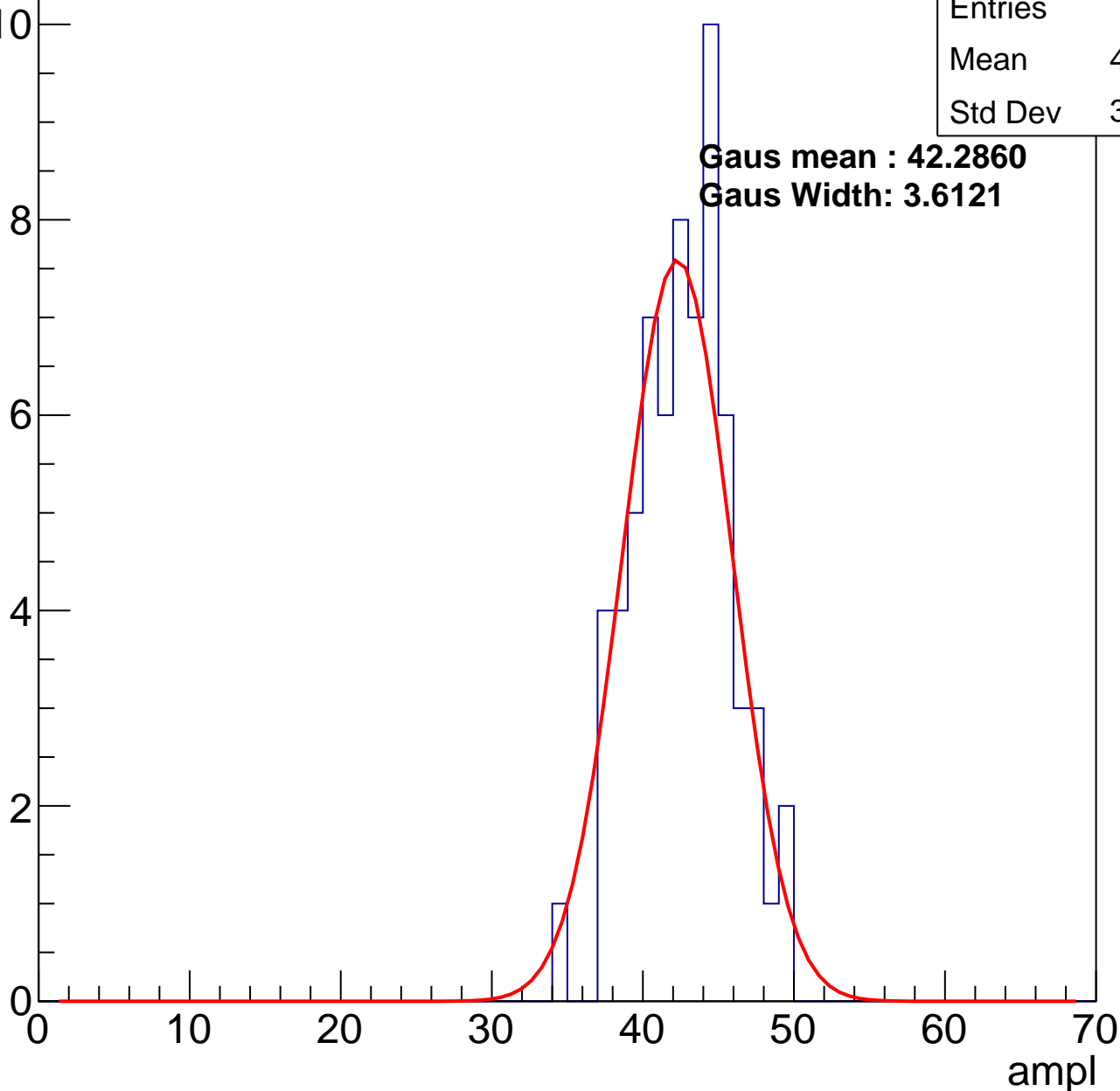
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.19
Std Dev	3.144

Gaus mean : 42.2860

Gaus Width: 3.6121

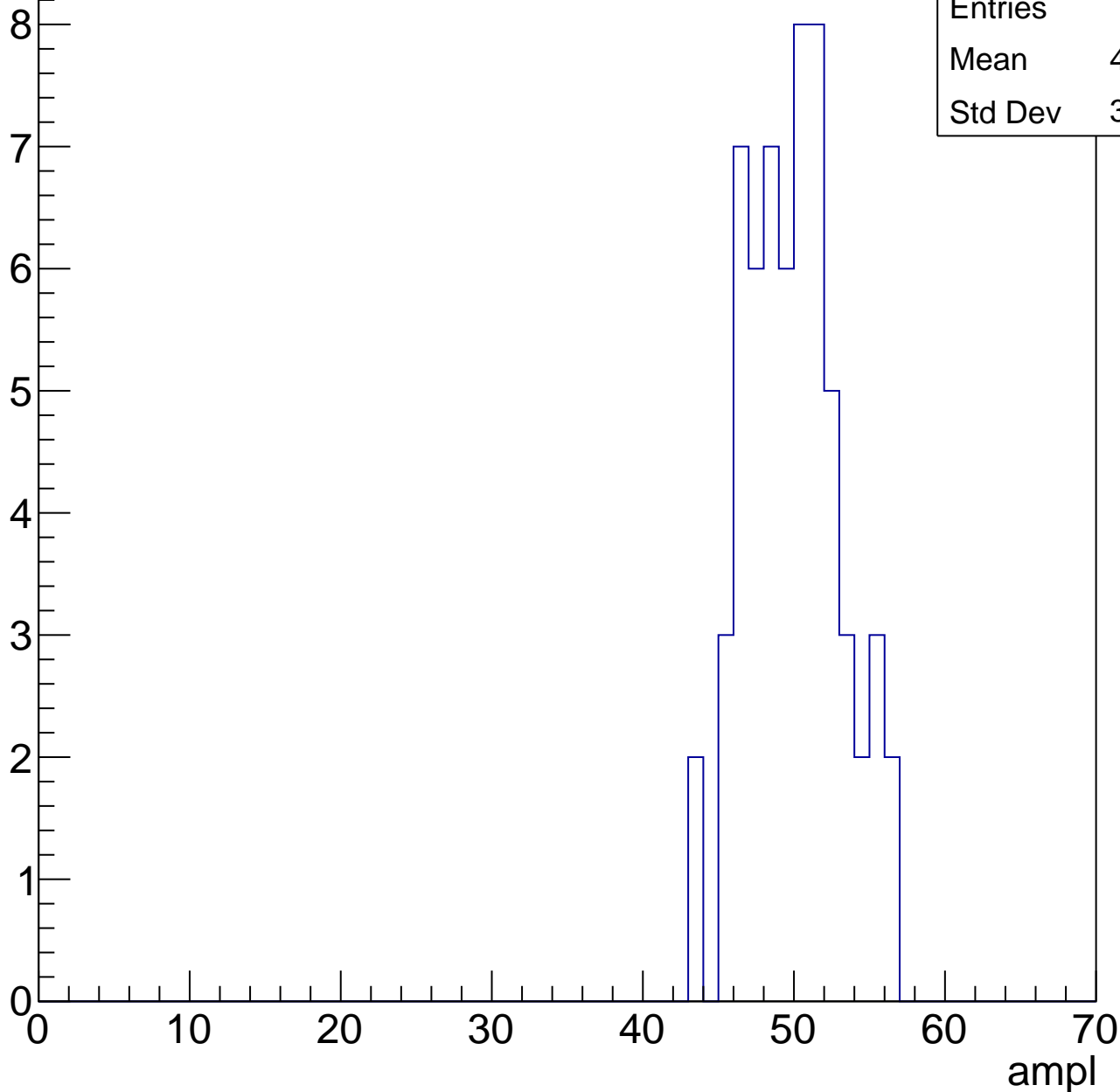


B1L003S, U13-ch92, adc3

calib_packv5_042523_0143.root, FC#13, port D2

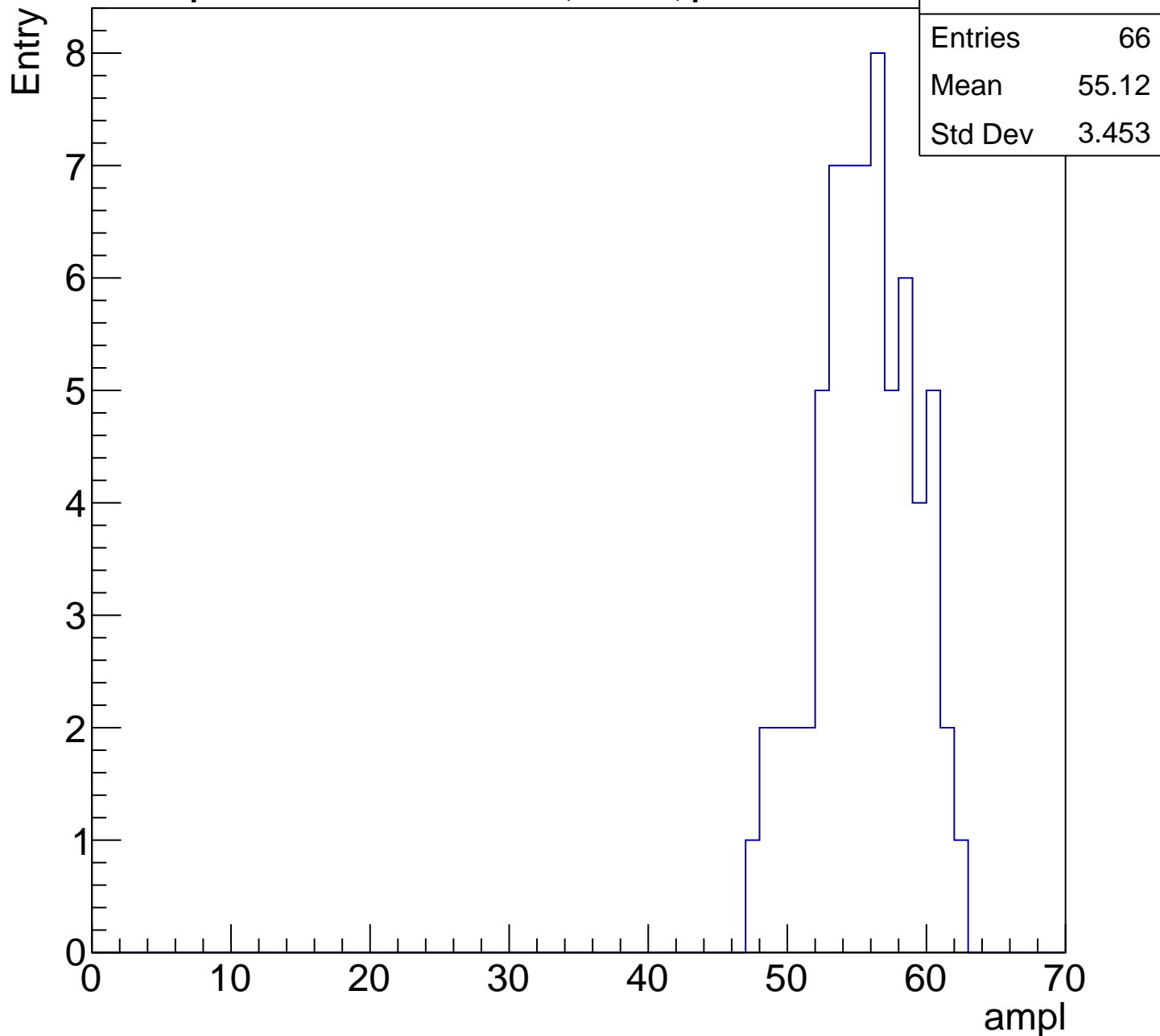
Entry

Entries	62
Mean	49.47
Std Dev	3.083



B1L003S, U13-ch92, adc4

calib_packv5_042523_0143.root, FC#13, port D2

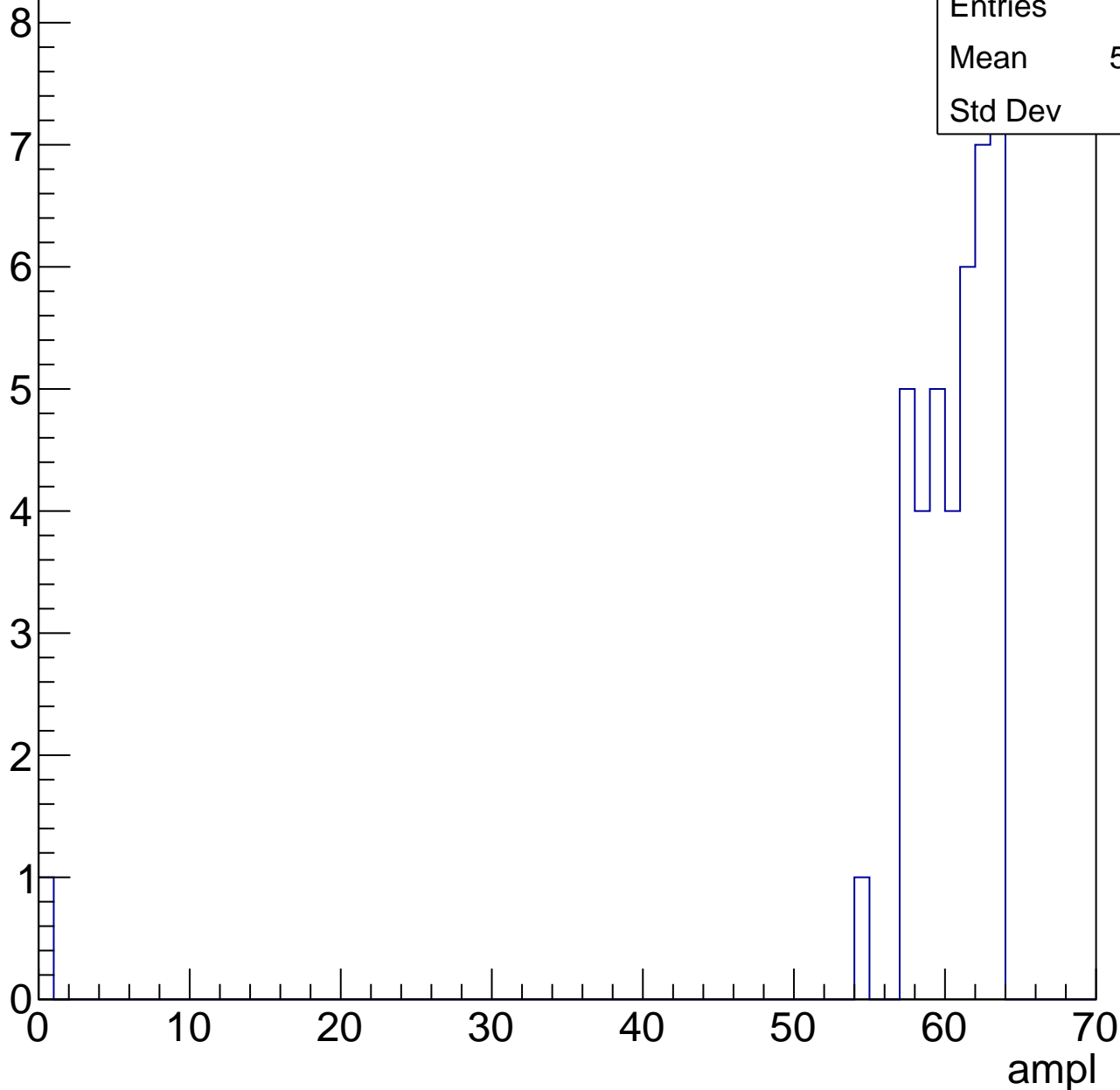


B1L003S, U13-ch92, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.78
Std Dev	9.56



B1L003S, U13-ch92, adc6

calib_packv5_042523_0143.root, FC#13, port D2

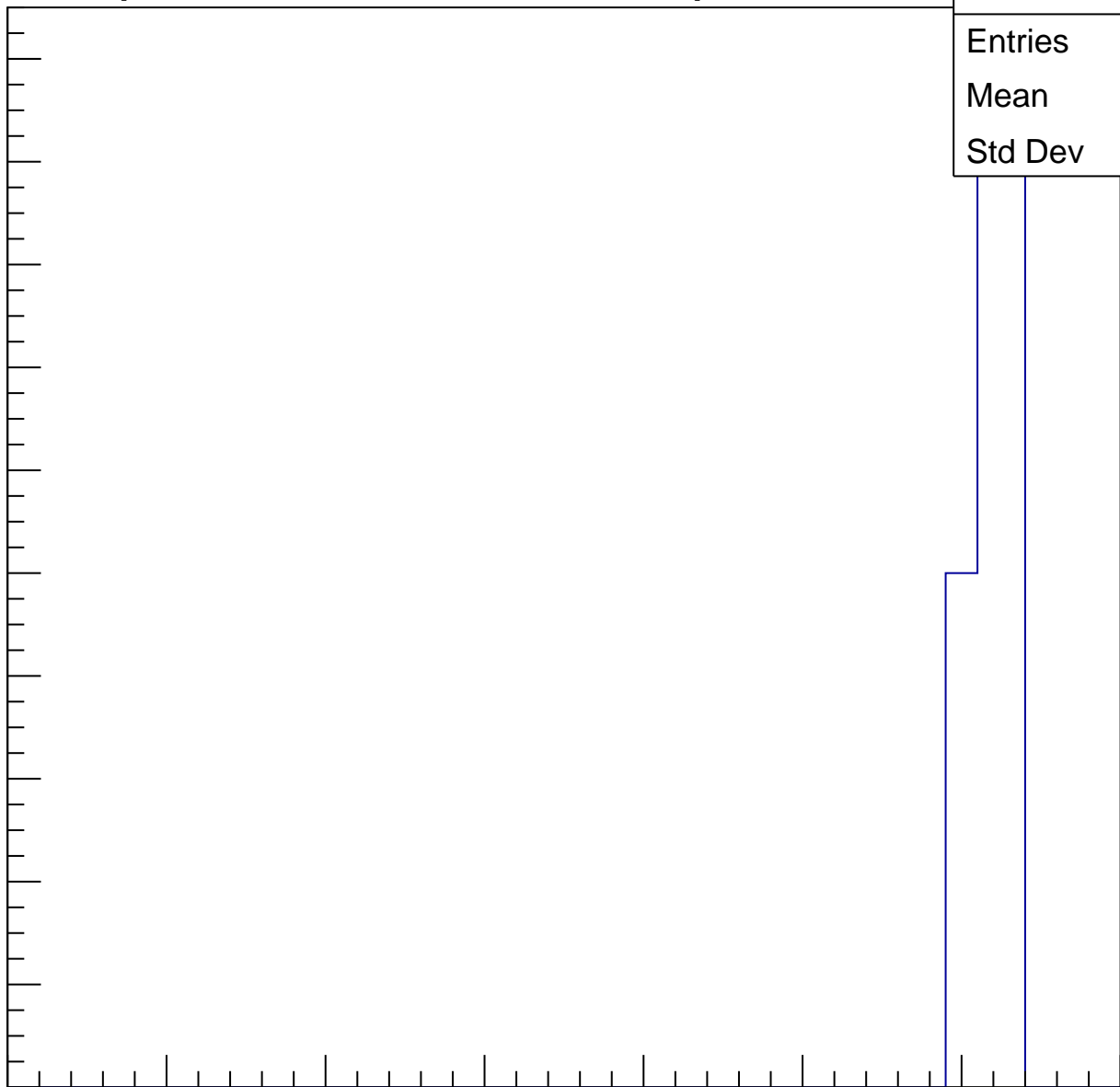
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.38
Std Dev	1.317

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch92, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch93, adc0

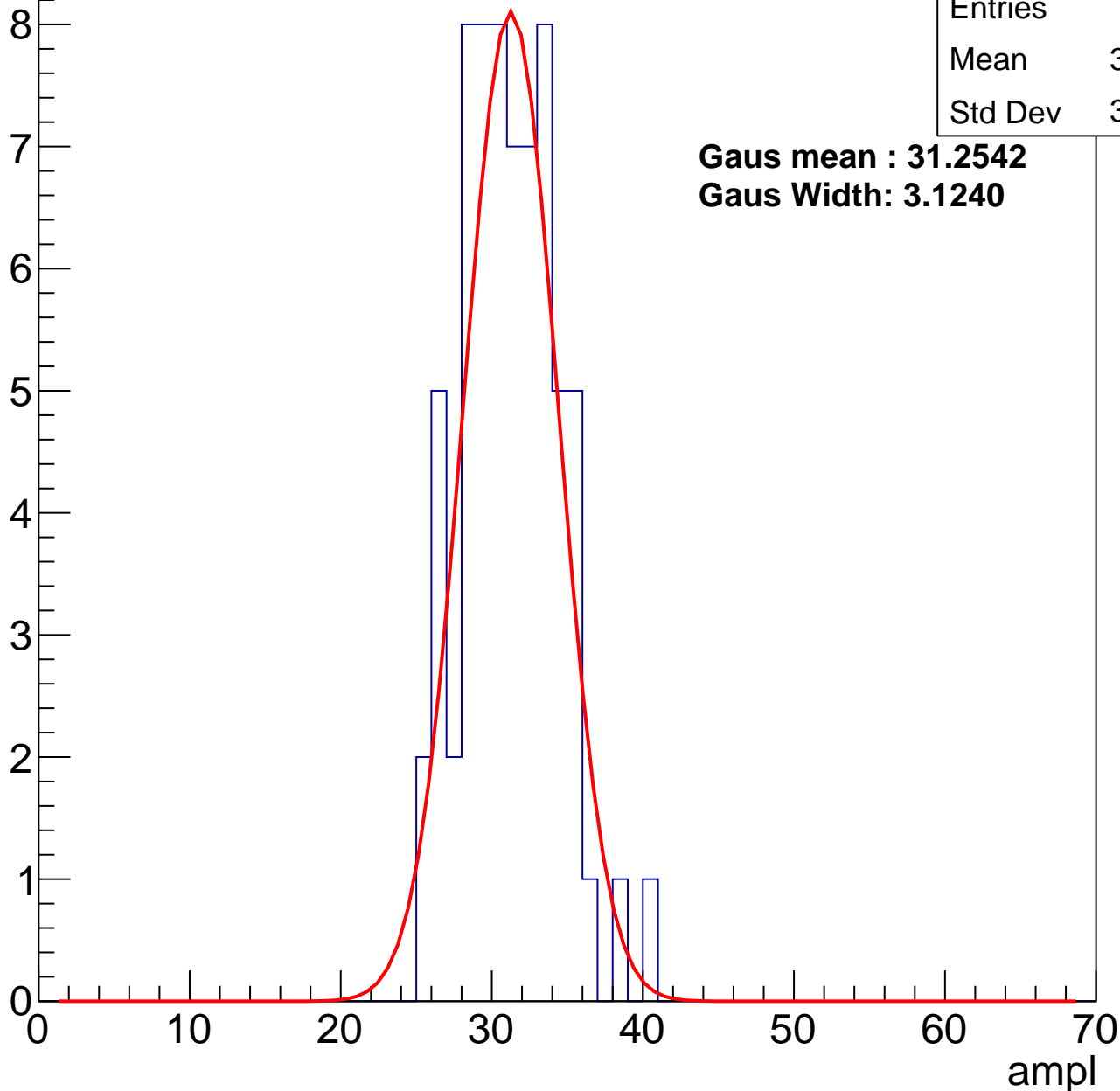
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.79
Std Dev	3.109

Gaus mean : 31.2542

Gaus Width: 3.1240



B1L003S, U13-ch93, adc1

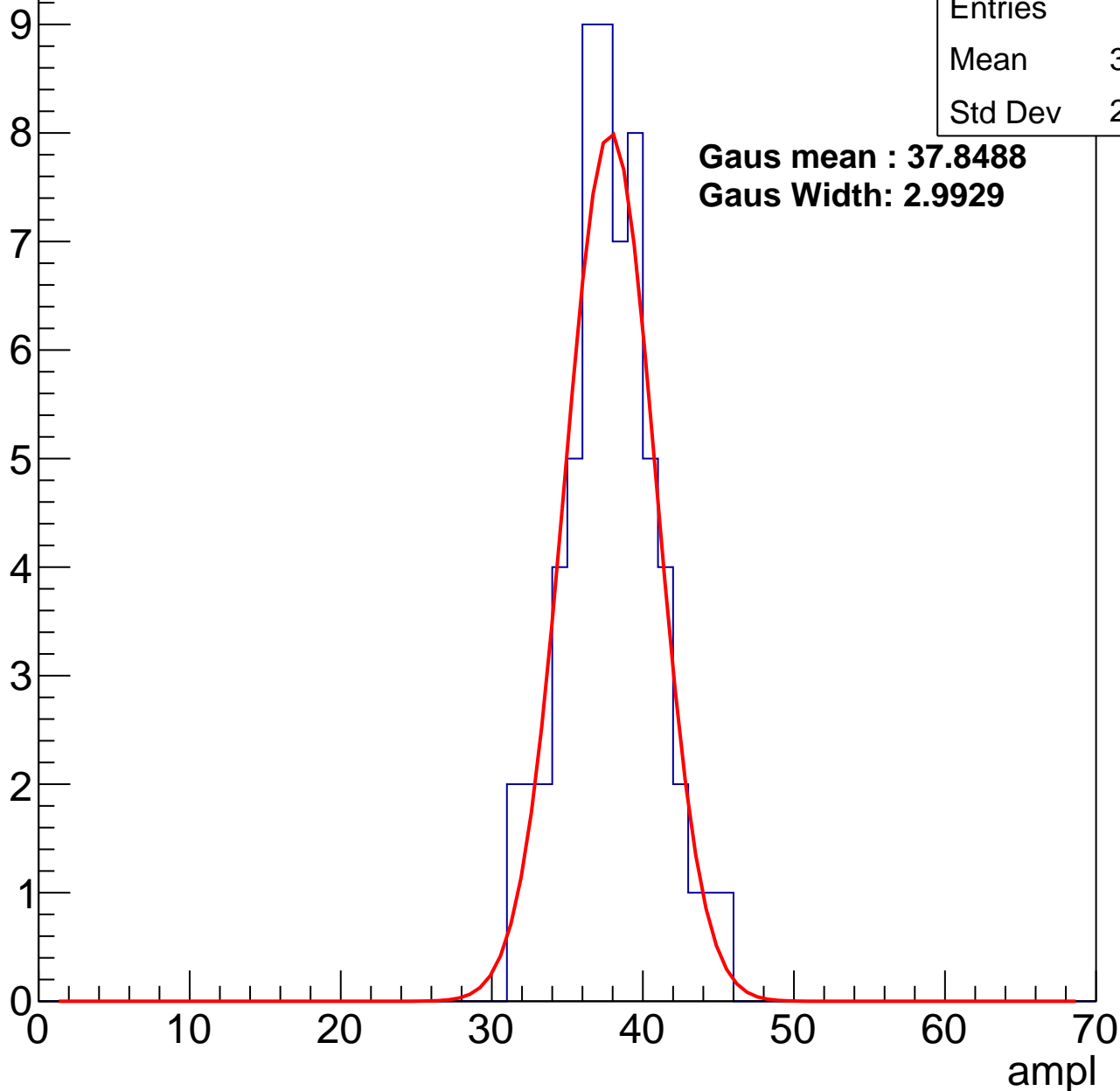
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.39
Std Dev	2.997

Gaus mean : 37.8488

Gaus Width: 2.9929



B1L003S, U13-ch93, adc2

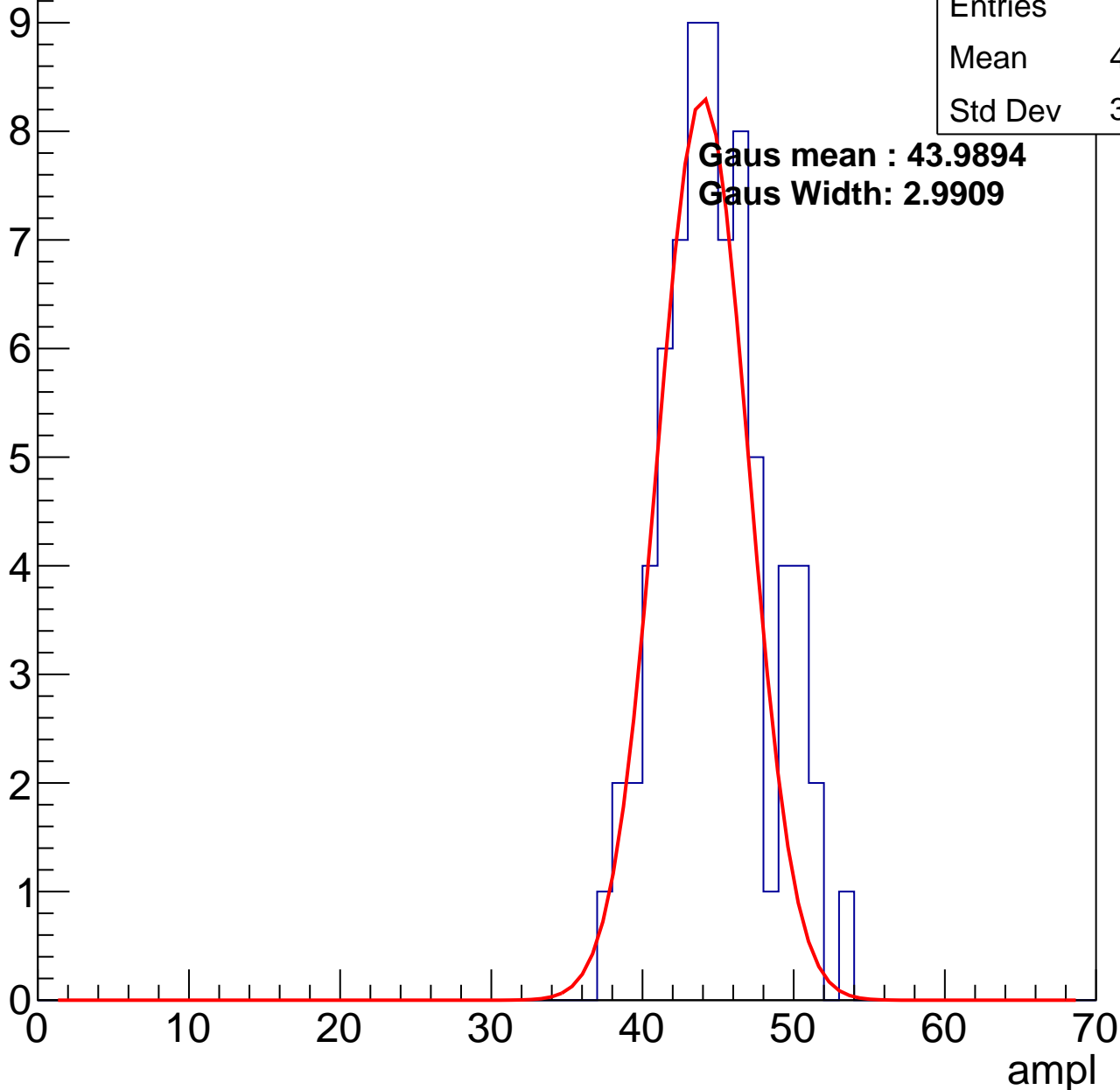
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.32
Std Dev	3.419

Gaus mean : 43.9894

Gaus Width: 2.9909

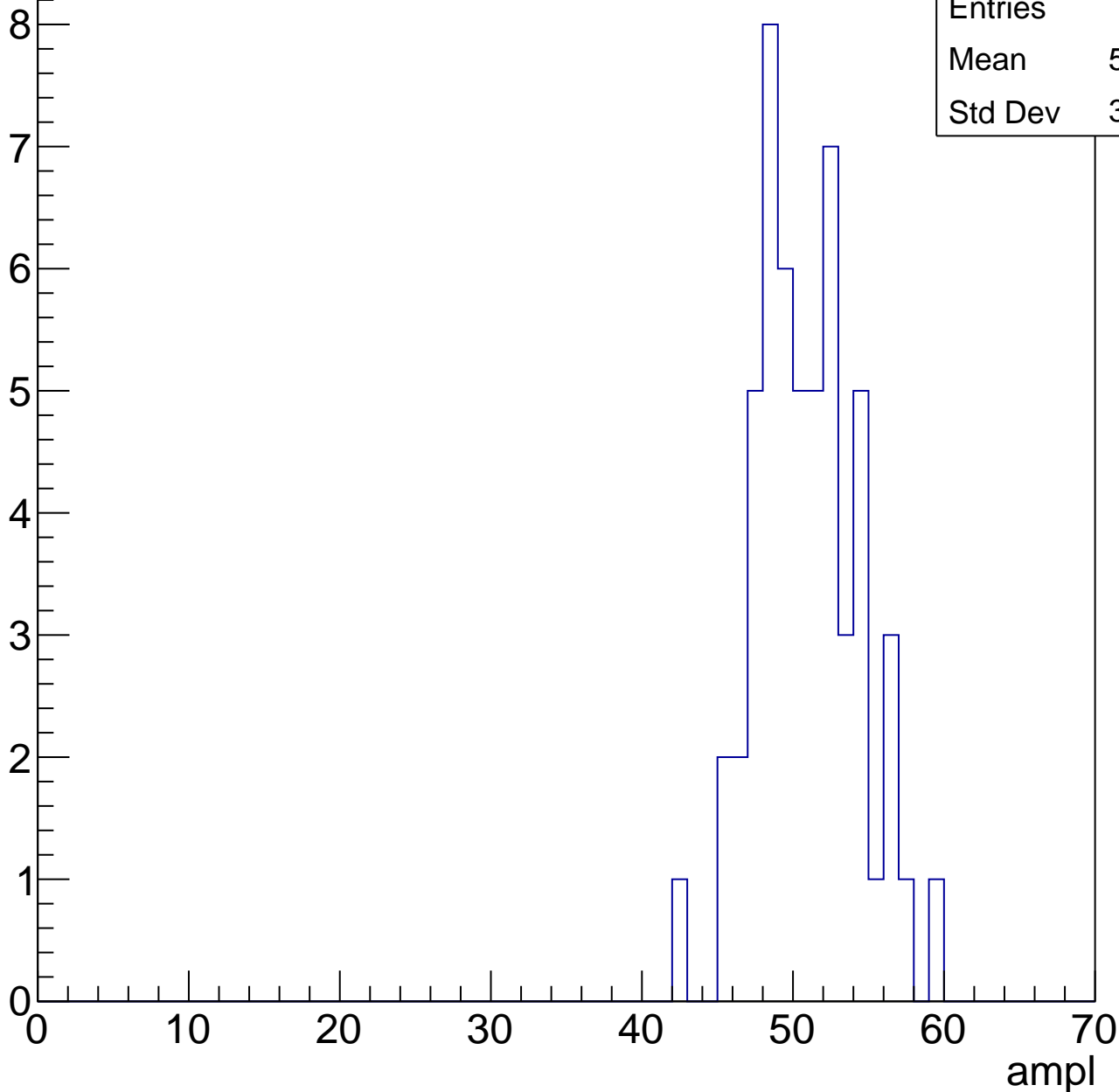


B1L003S, U13-ch93, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	50.44
Std Dev	3.362

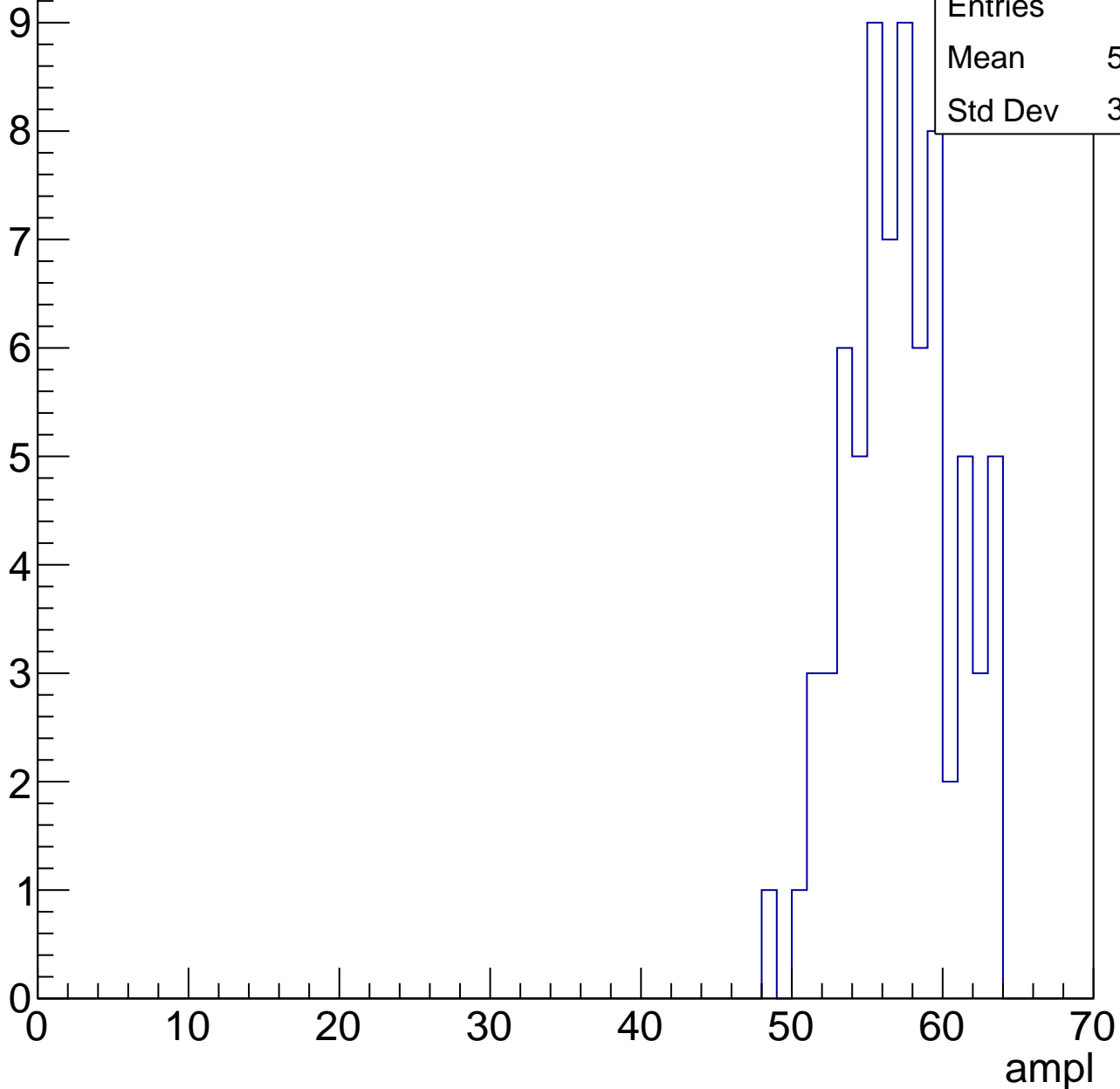


B1L003S, U13-ch93, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	56.73
Std Dev	3.477

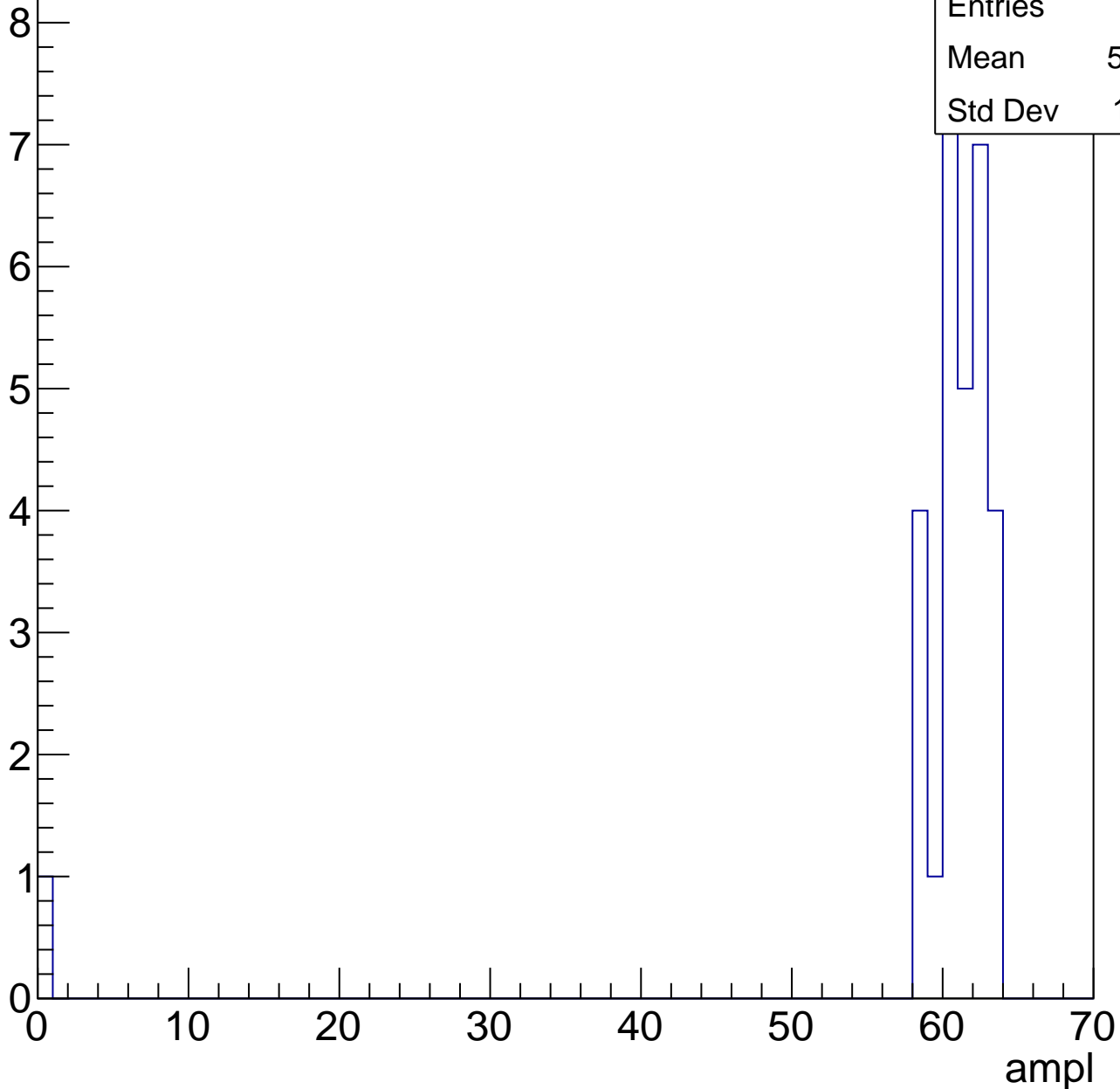


B1L003S, U13-ch93, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	30
Mean	58.73
Std Dev	11.01



B1L003S, U13-ch93, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U13-ch93, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch94, adc0

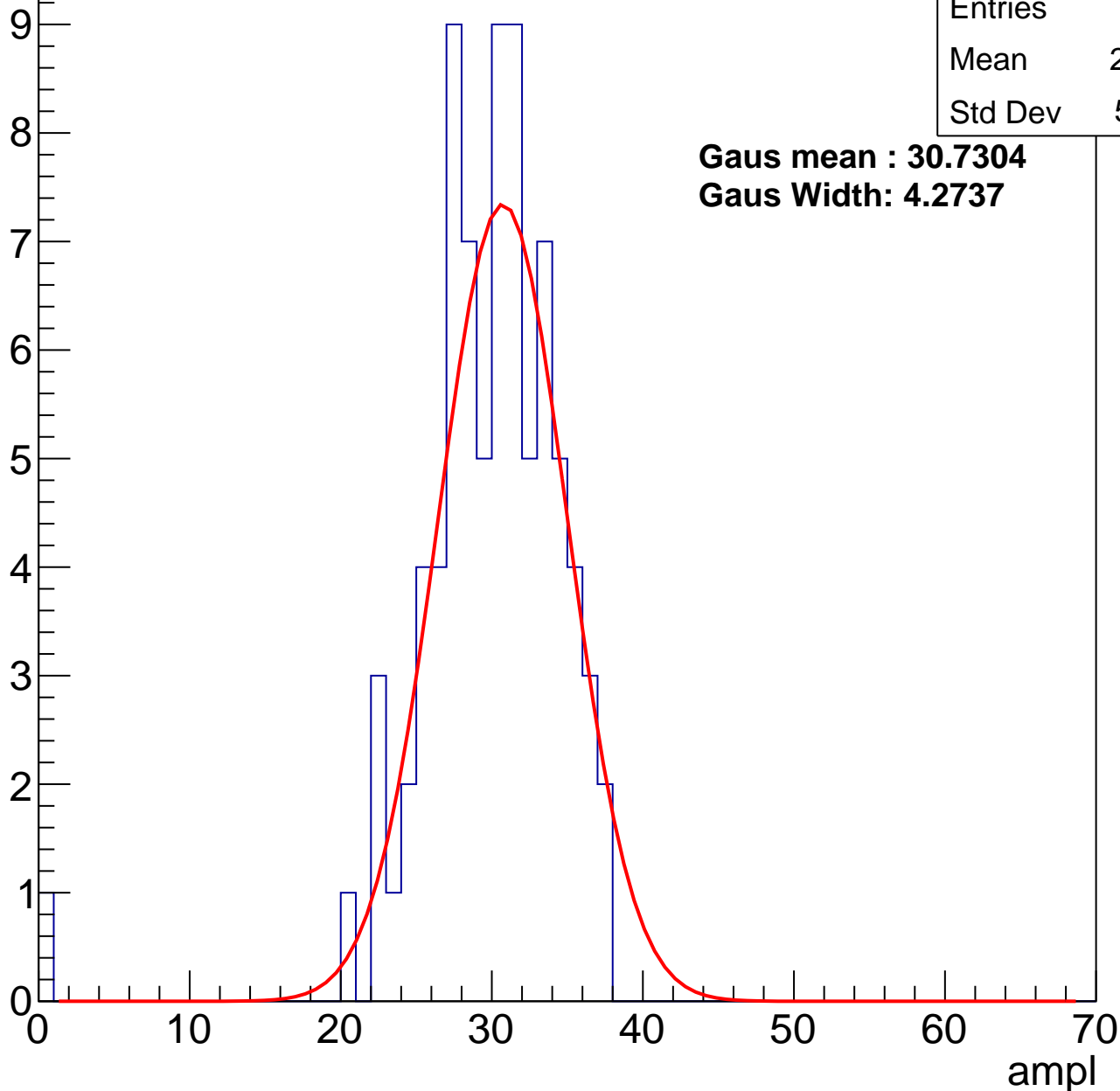
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	29.35
Std Dev	5.011

Gaus mean : 30.7304

Gaus Width: 4.2737



B1L003S, U13-ch94, adc1

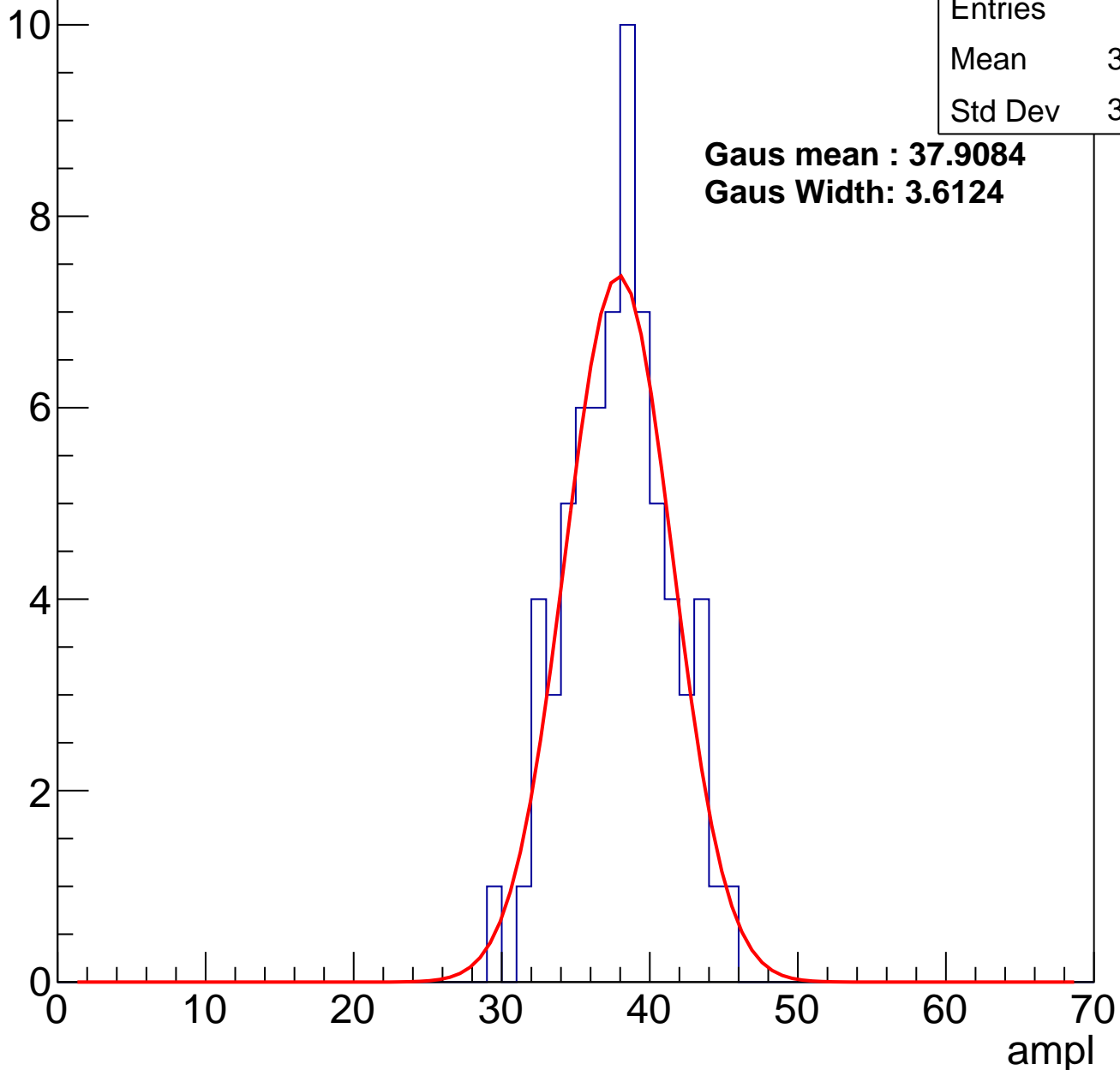
calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	37.44
Std Dev	3.406

Gaus mean : 37.9084

Gaus Width: 3.6124

Entry



B1L003S, U13-ch94, adc2

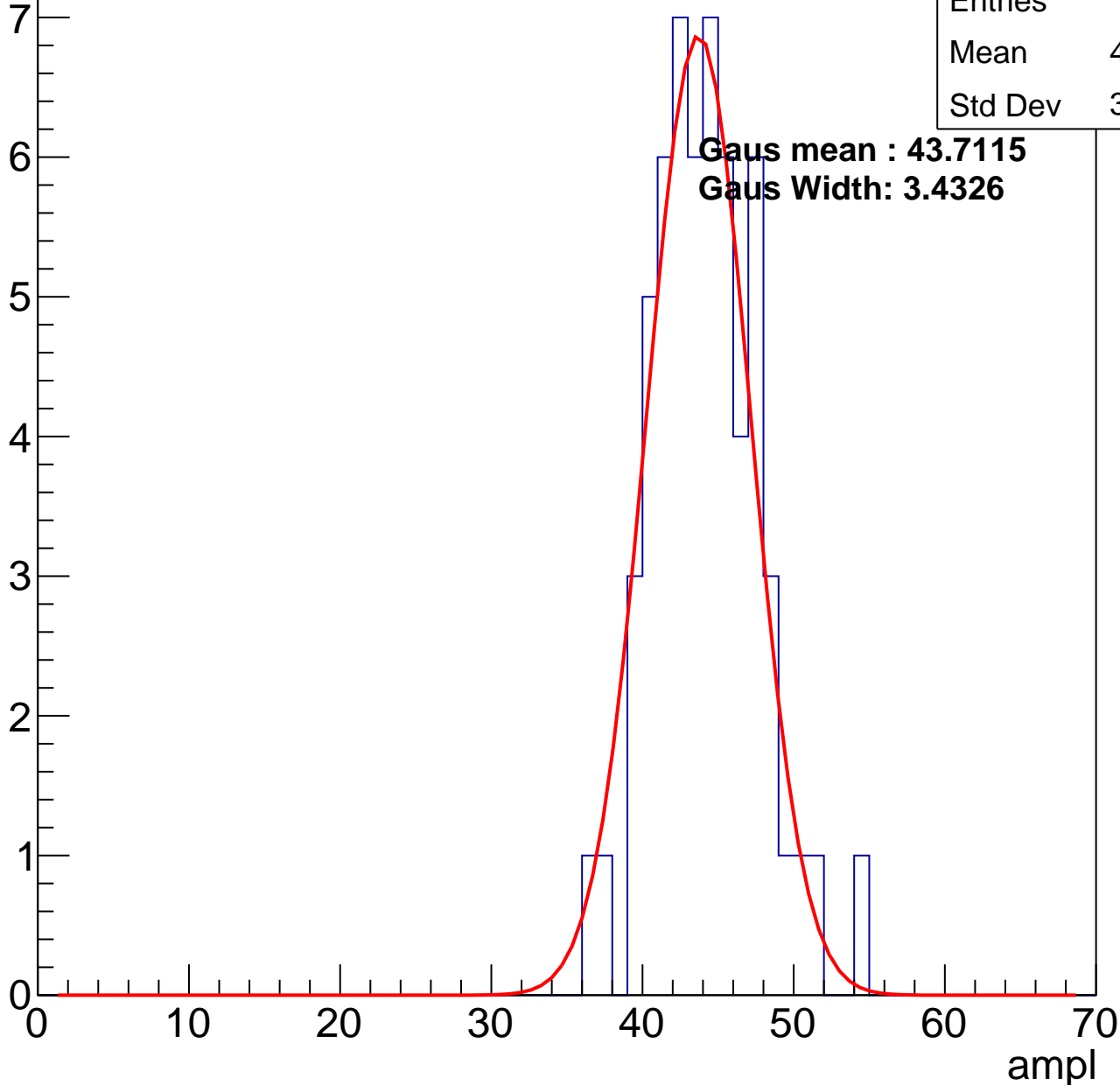
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.73
Std Dev	3.409

Gaus mean : 43.7115

Gaus Width: 3.4326

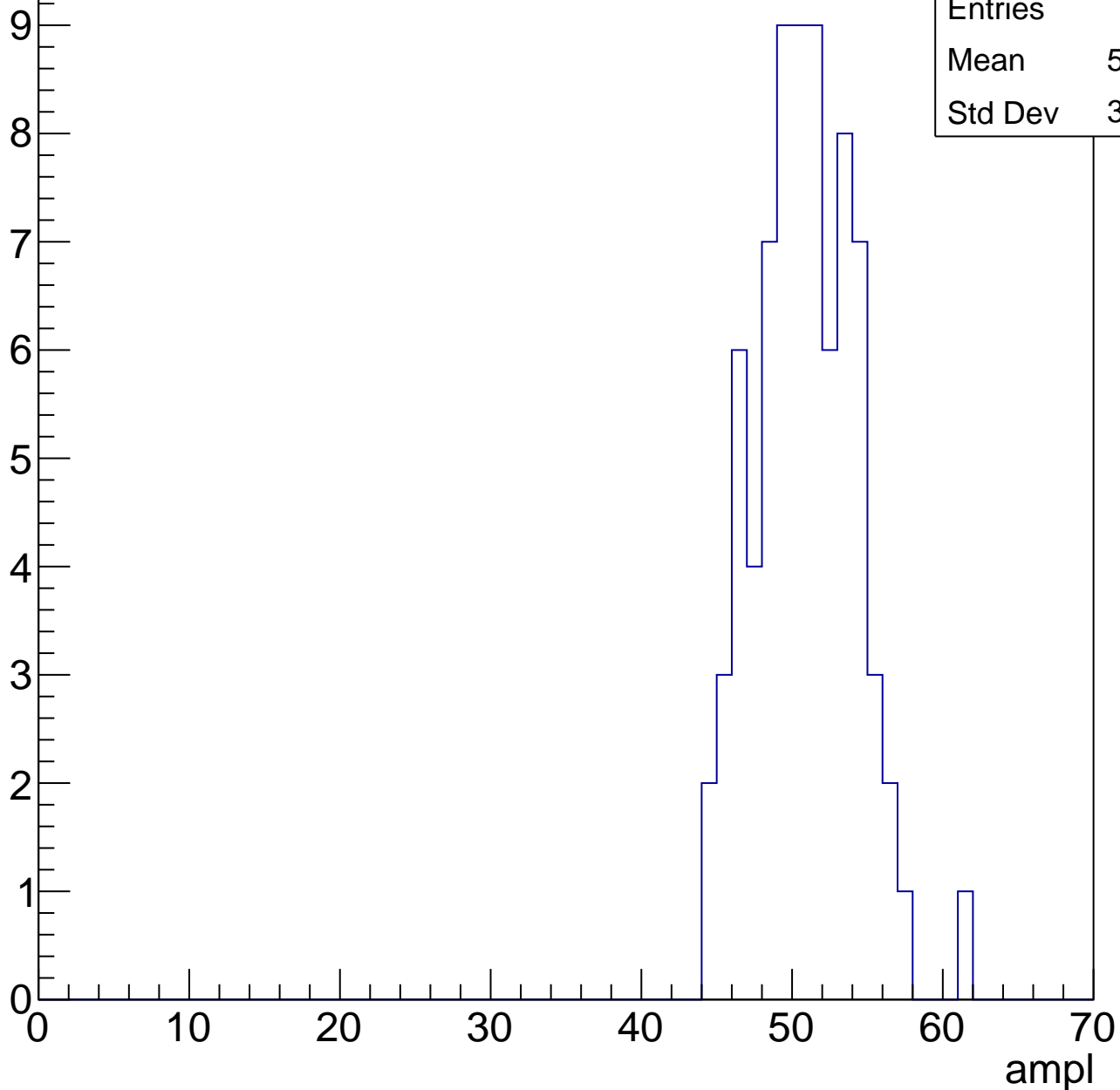


B1L003S, U13-ch94, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	50.42
Std Dev	3.296

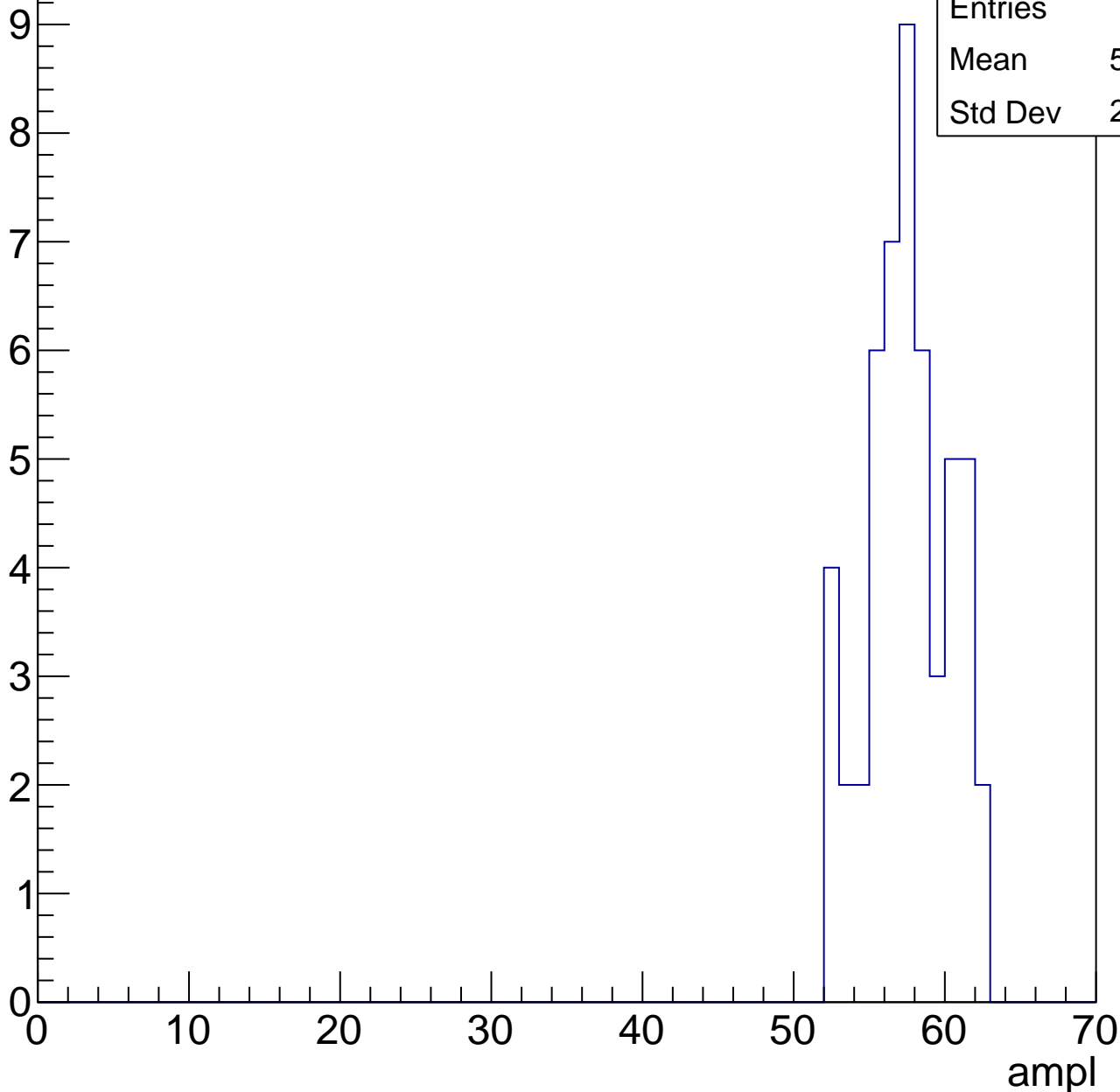


B1L003S, U13-ch94, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

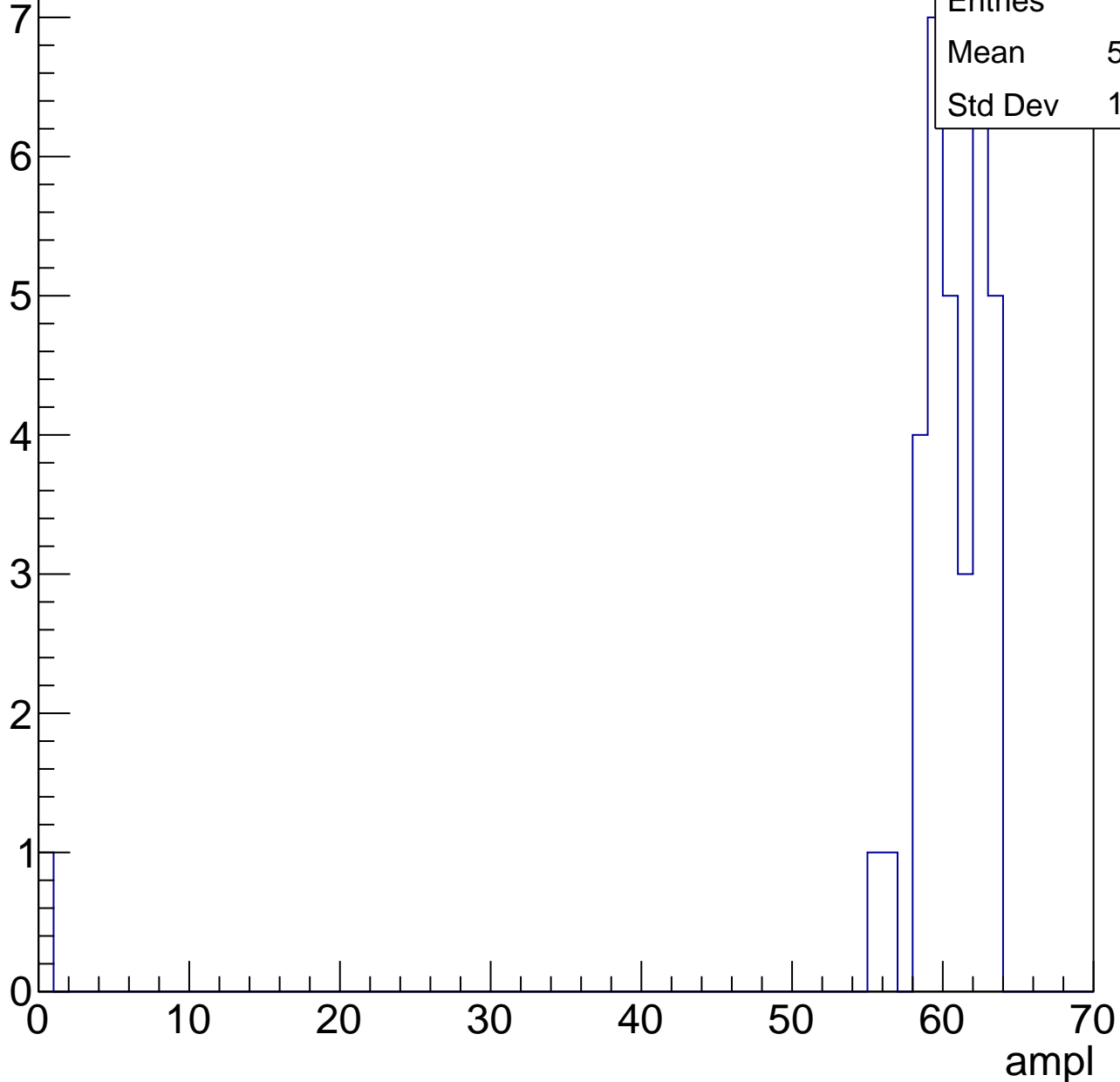
Entries	51
Mean	57.08
Std Dev	2.707



B1L003S, U13-ch94, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

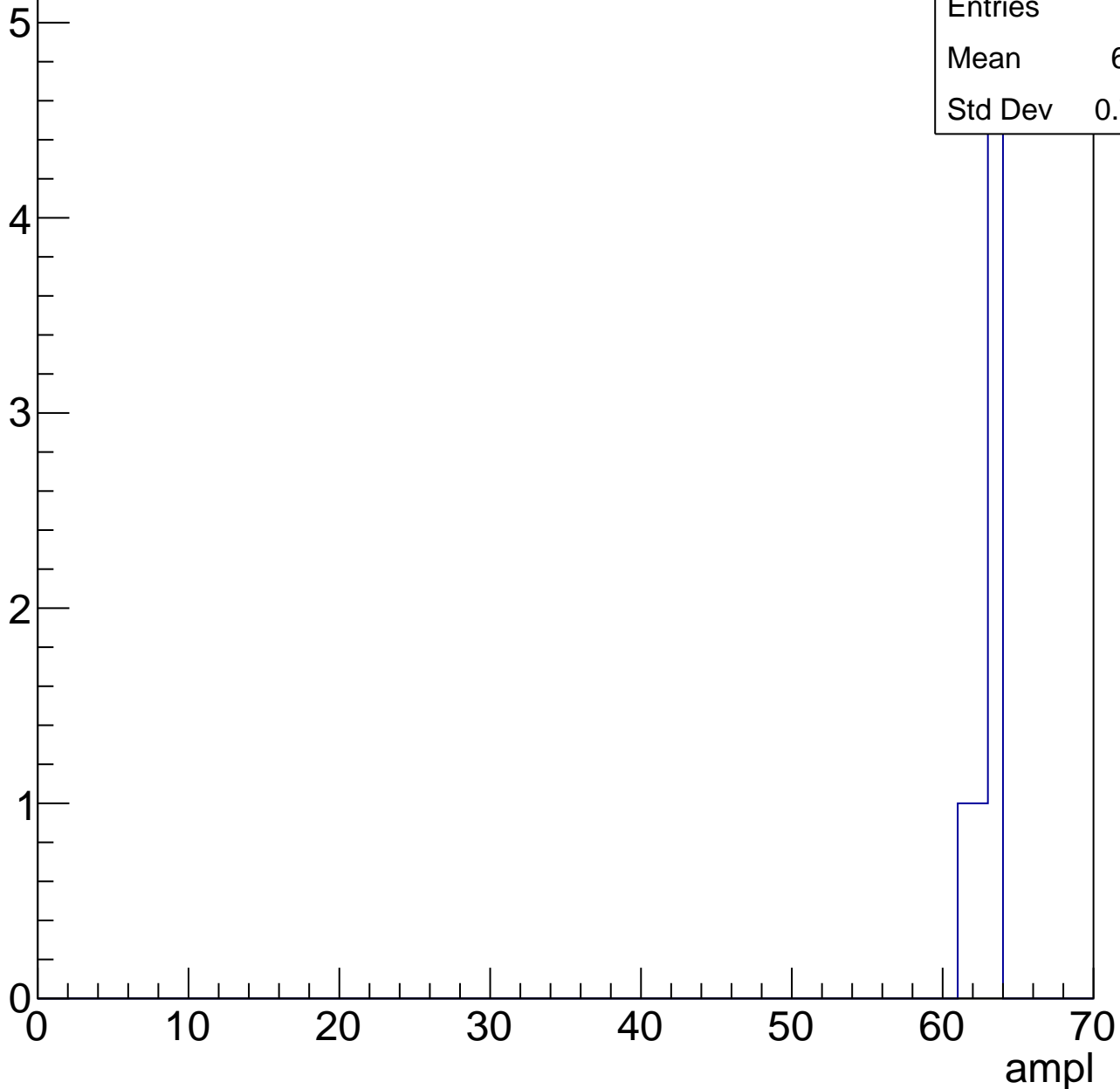


B1L003S, U13-ch94, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B1L003S, U13-ch94, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch95, adc0

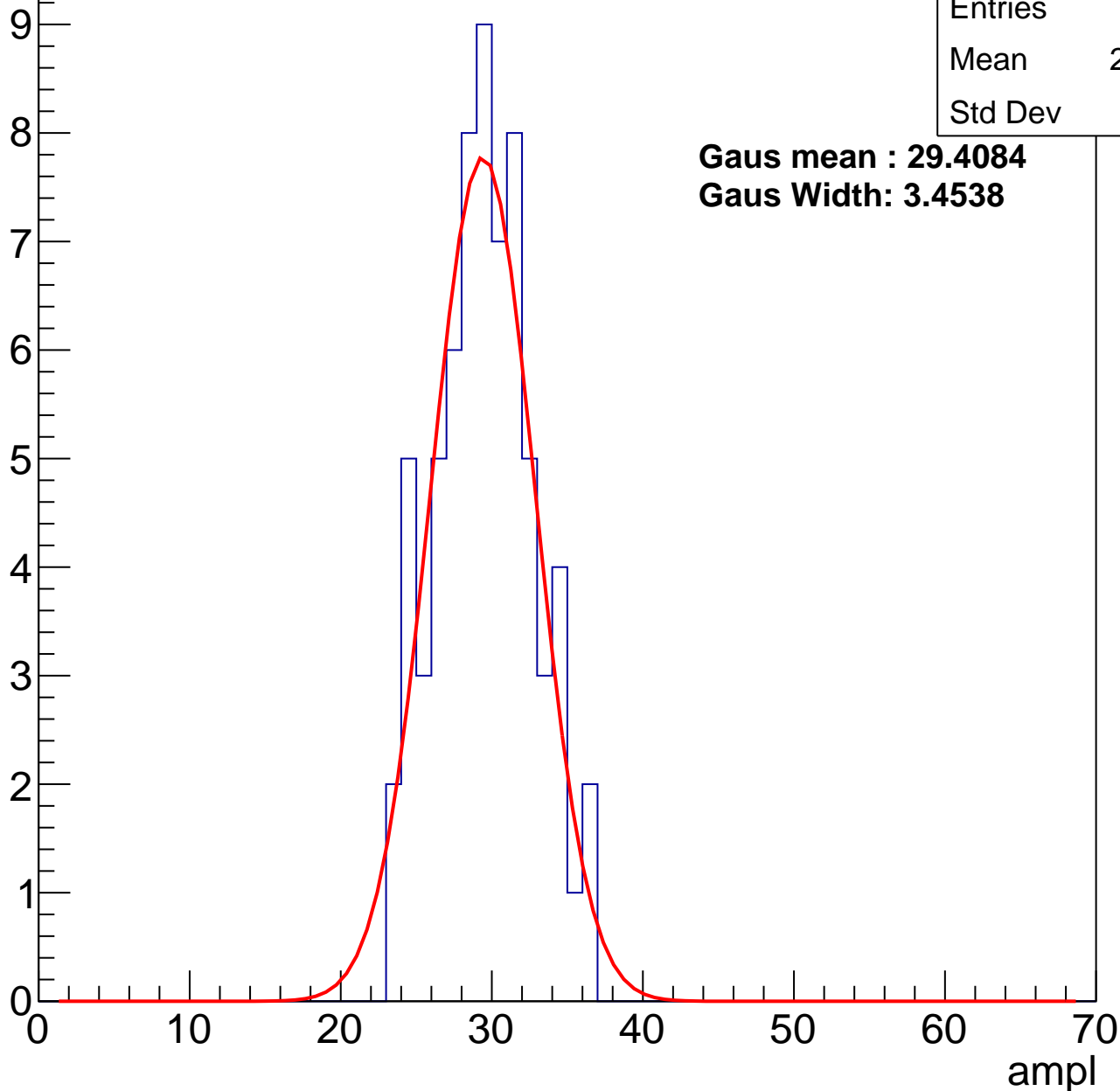
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.09
Std Dev	3.18

Gaus mean : 29.4084

Gaus Width: 3.4538

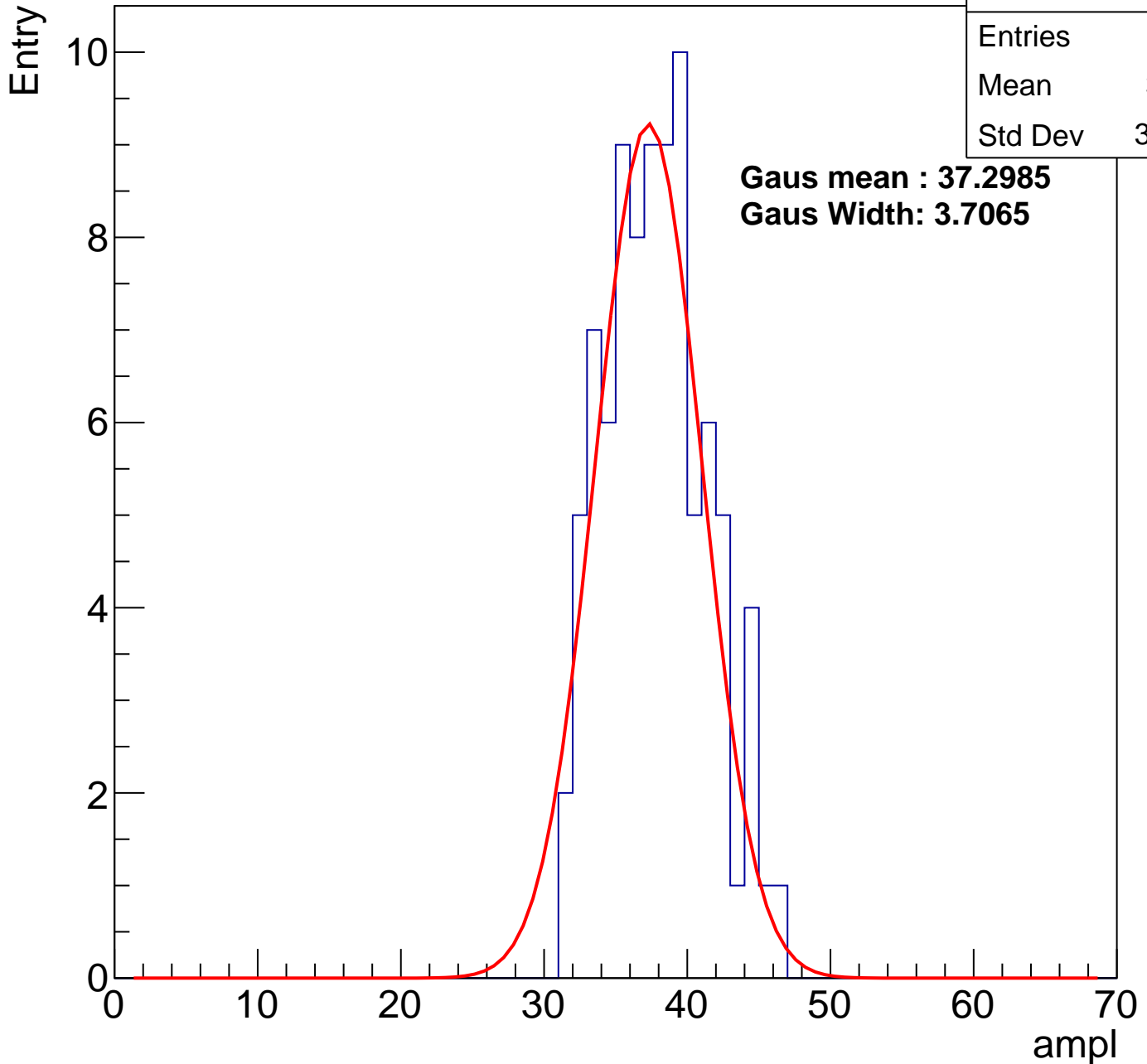


B1L003S, U13-ch95, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	88
Mean	37.4
Std Dev	3.511

Gaus mean : 37.2985
Gaus Width: 3.7065



B1L003S, U13-ch95, adc2

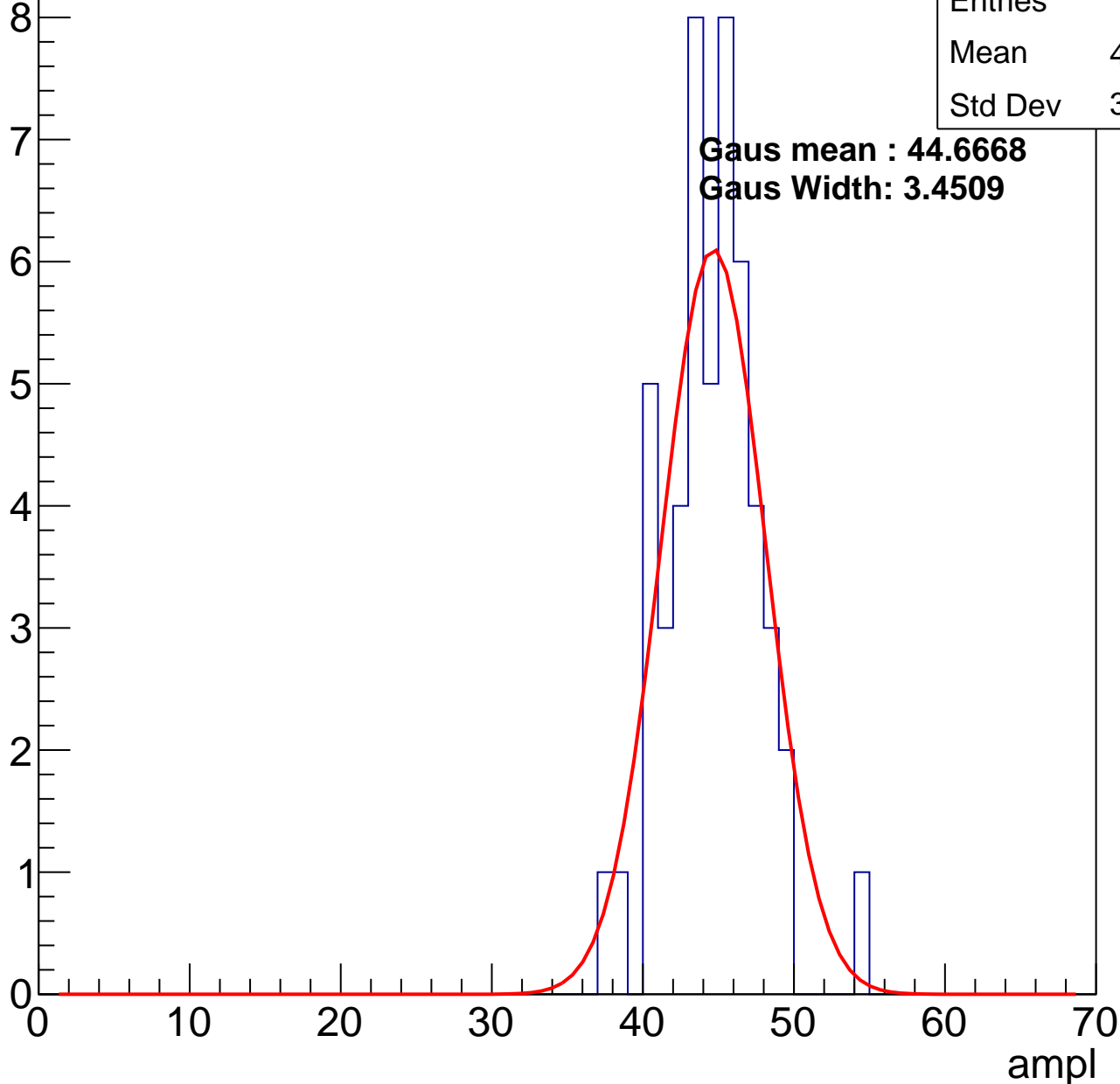
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	44.12
Std Dev	3.072

Gaus mean : 44.6668

Gaus Width: 3.4509

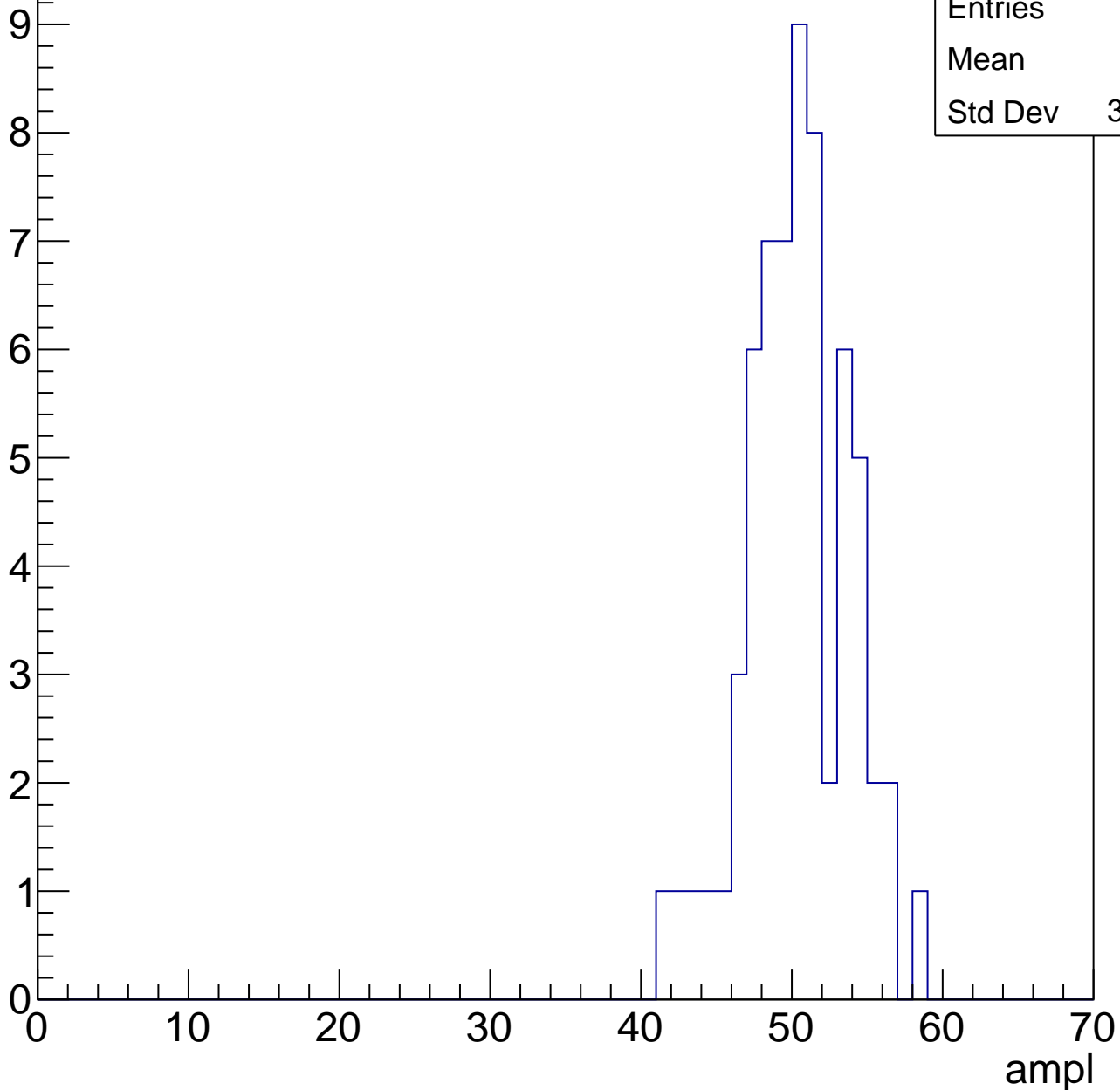


B1L003S, U13-ch95, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.9
Std Dev	3.398



B1L003S, U13-ch95, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

Entries 74

Mean 56.42

Std Dev 3.111

8

6

4

2

0

0

10

20

30

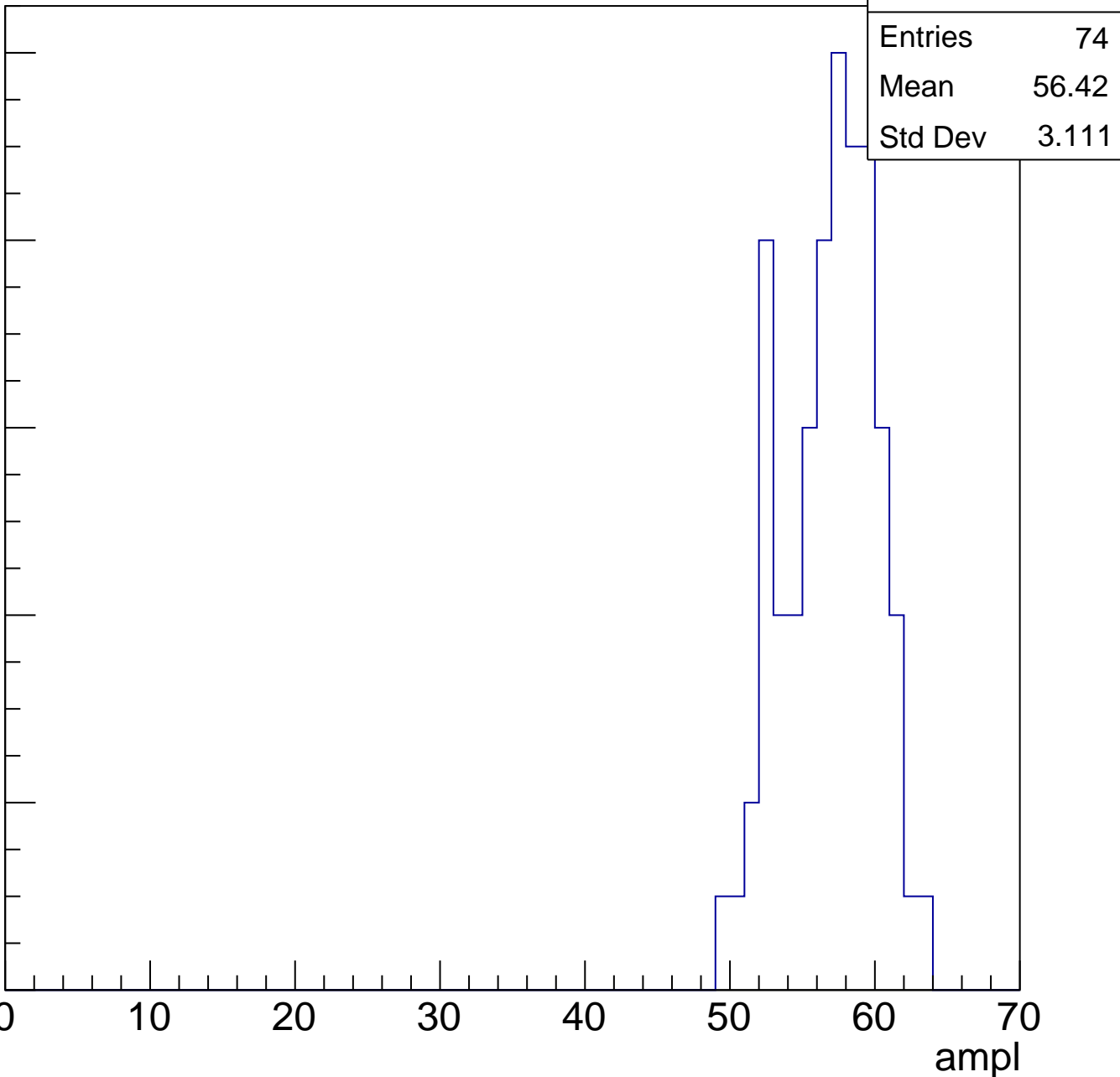
40

50

60

70

ampl

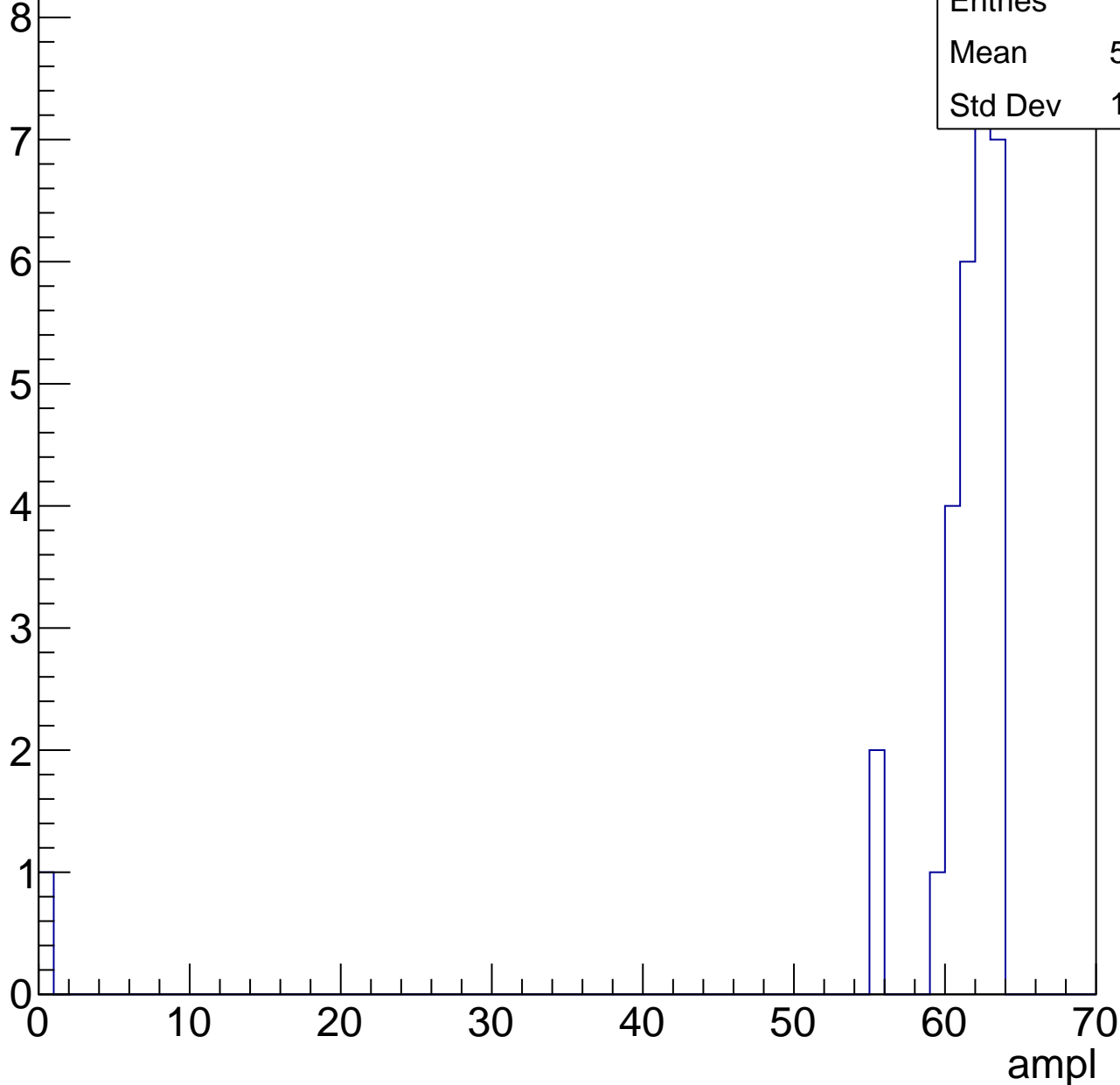


B1L003S, U13-ch95, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	29
Mean	59.03
Std Dev	11.33



B1L003S, U13-ch95, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L003S, U13-ch95, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch96, adc0

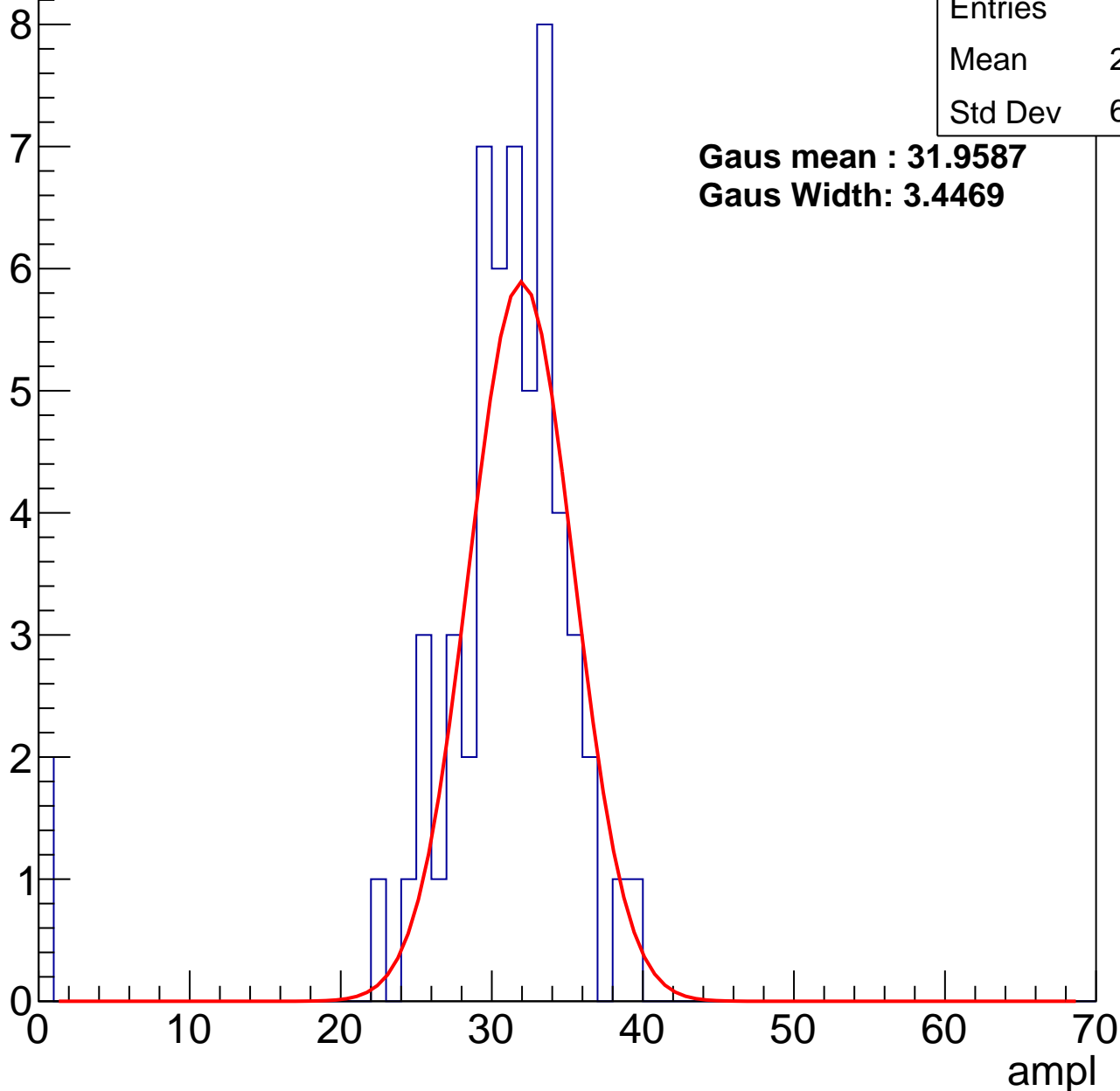
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.79
Std Dev	6.603

Gaus mean : 31.9587

Gaus Width: 3.4469



B1L003S, U13-ch96, adc1

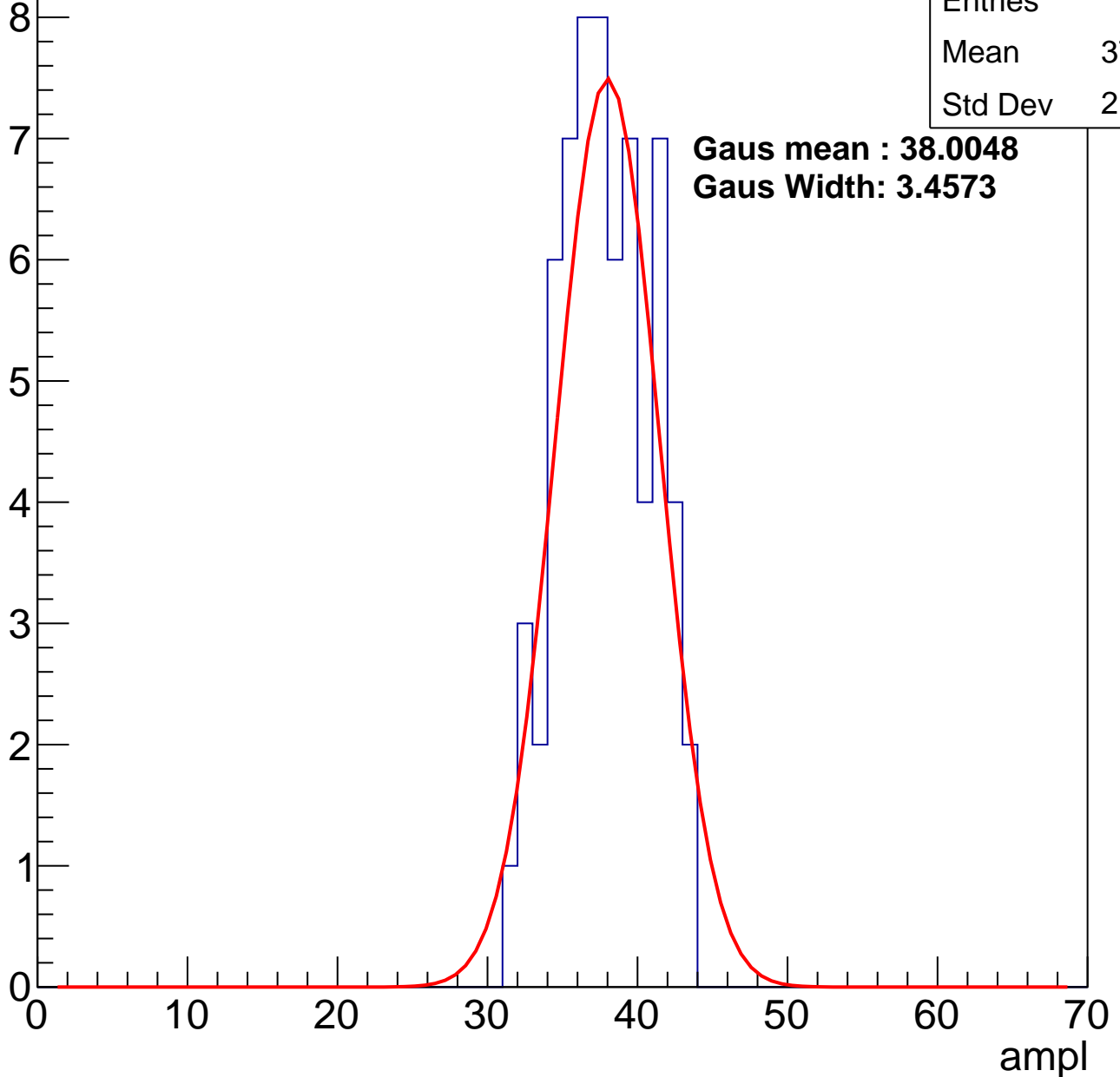
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	37.35
Std Dev	2.984

Gaus mean : 38.0048

Gaus Width: 3.4573



B1L003S, U13-ch96, adc2

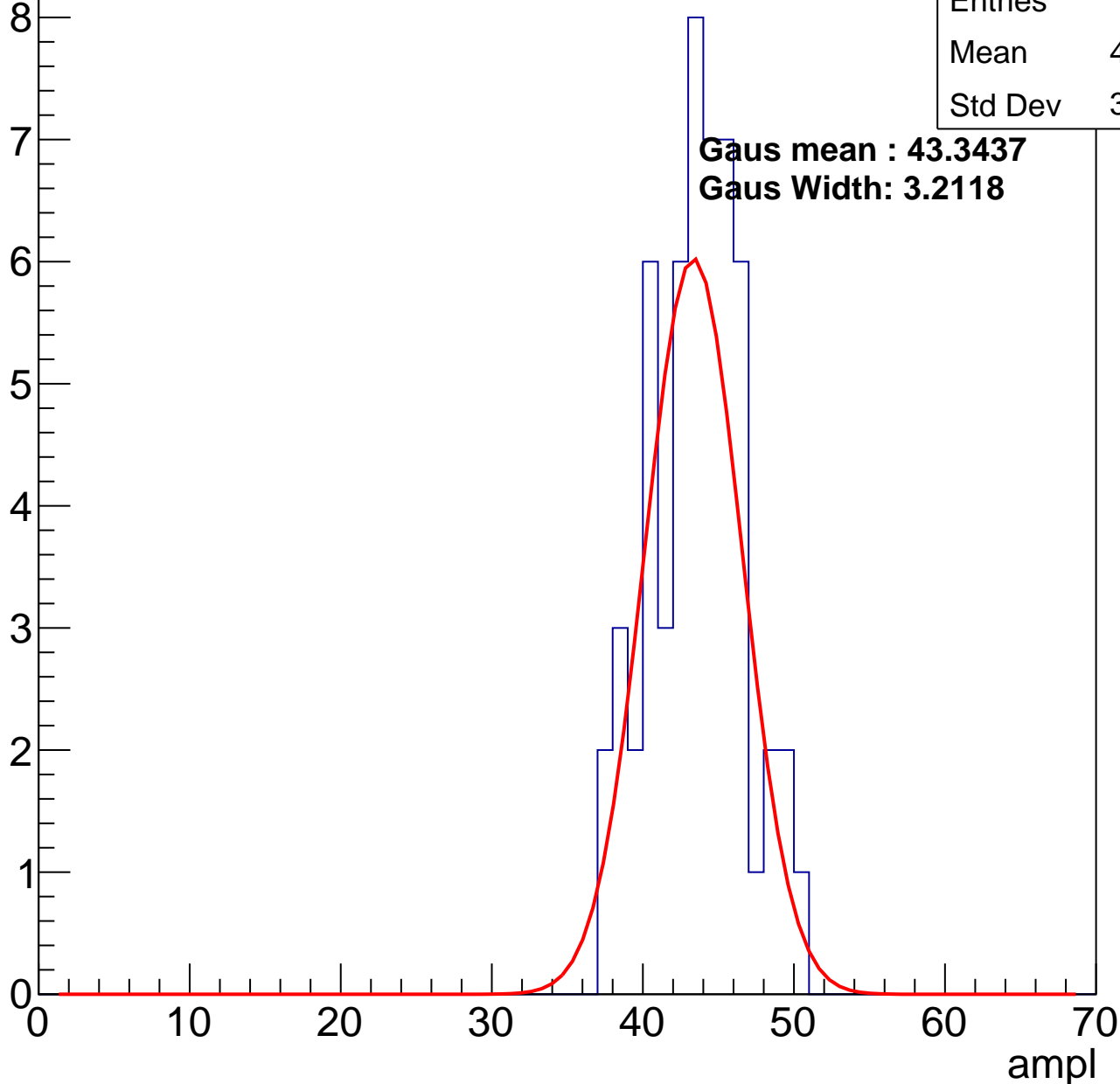
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.12
Std Dev	3.065

Gaus mean : 43.3437

Gaus Width: 3.2118

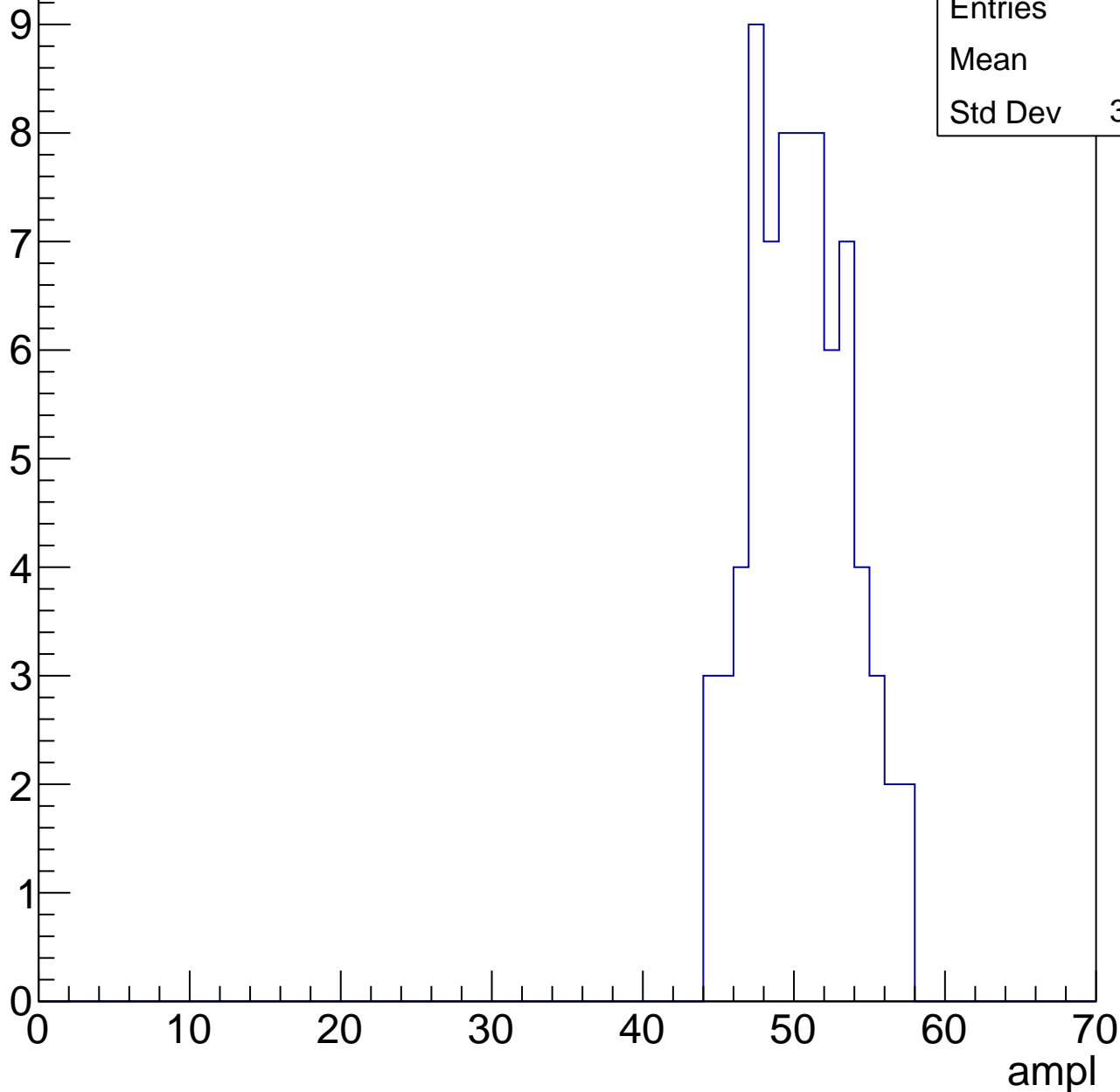


B1L003S, U13-ch96, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	50
Std Dev	3.222

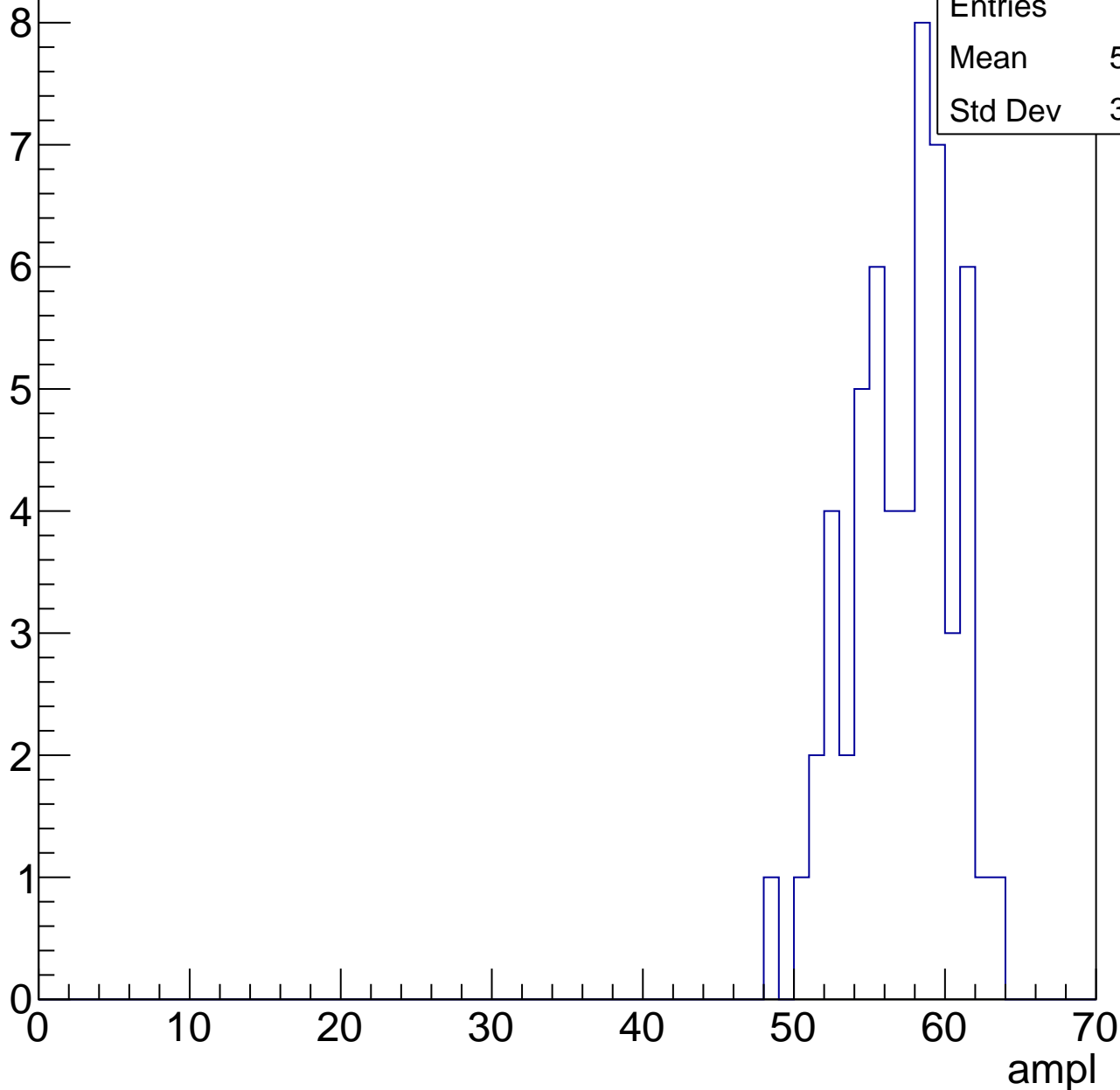


B1L003S, U13-ch96, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	56.62
Std Dev	3.355



B1L003S, U13-ch96, adc5

calib_packv5_042523_0143.root, FC#13, port D2

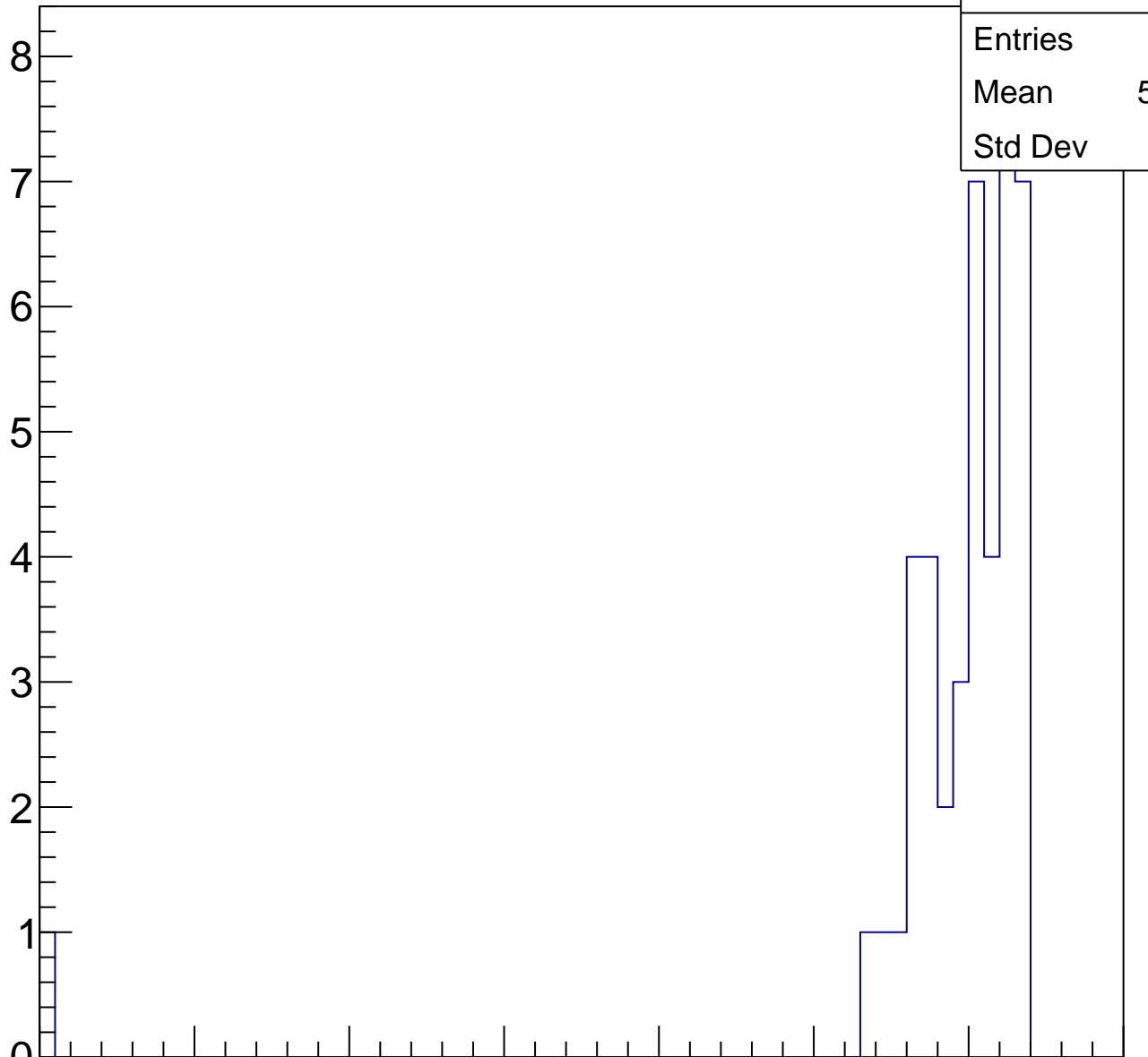
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.33
Std Dev	9.4

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch96, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

B1L003S, U13-ch96, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch97, adc0

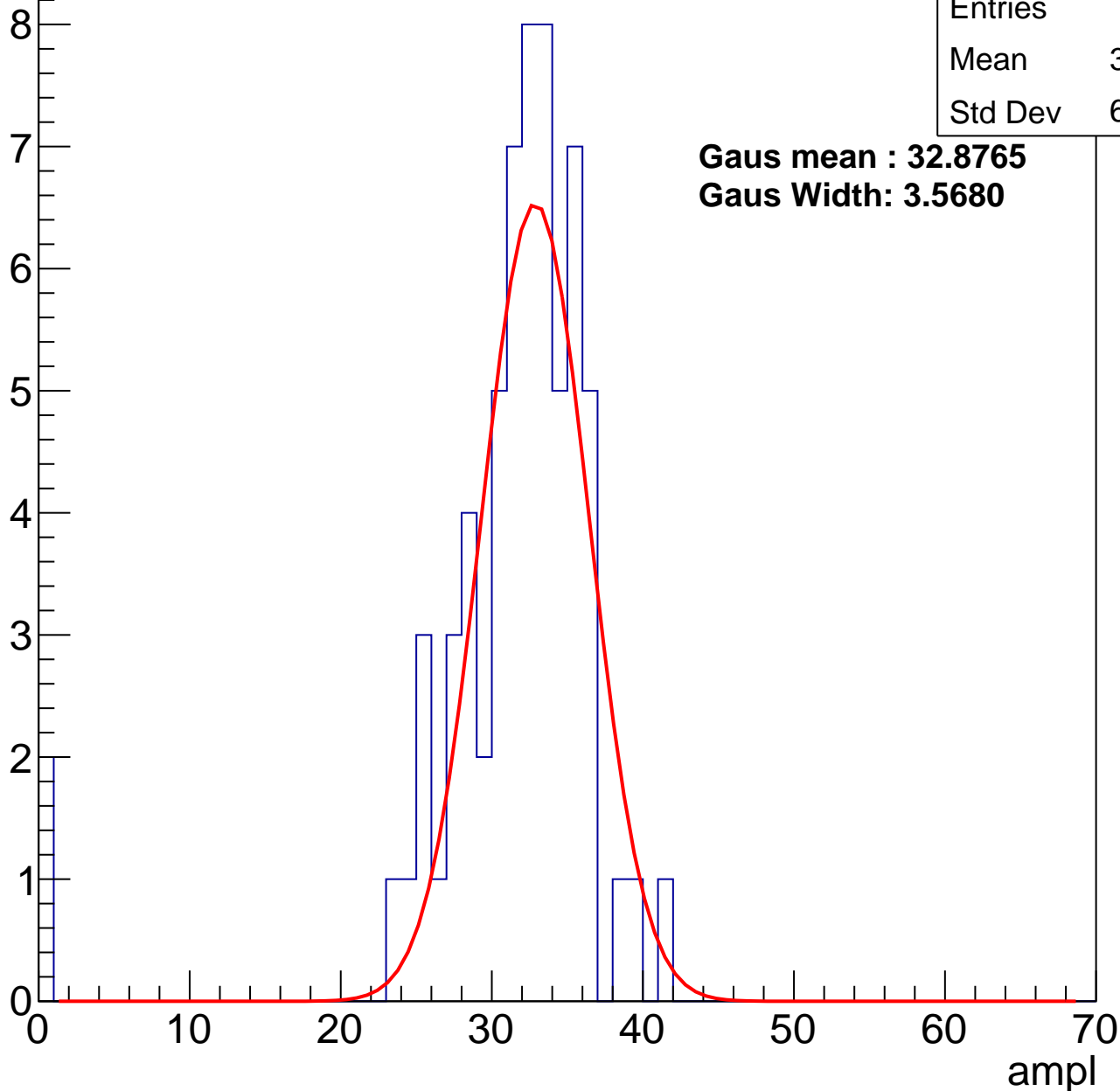
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.75
Std Dev	6.559

Gaus mean : 32.8765

Gaus Width: 3.5680



B1L003S, U13-ch97, adc1

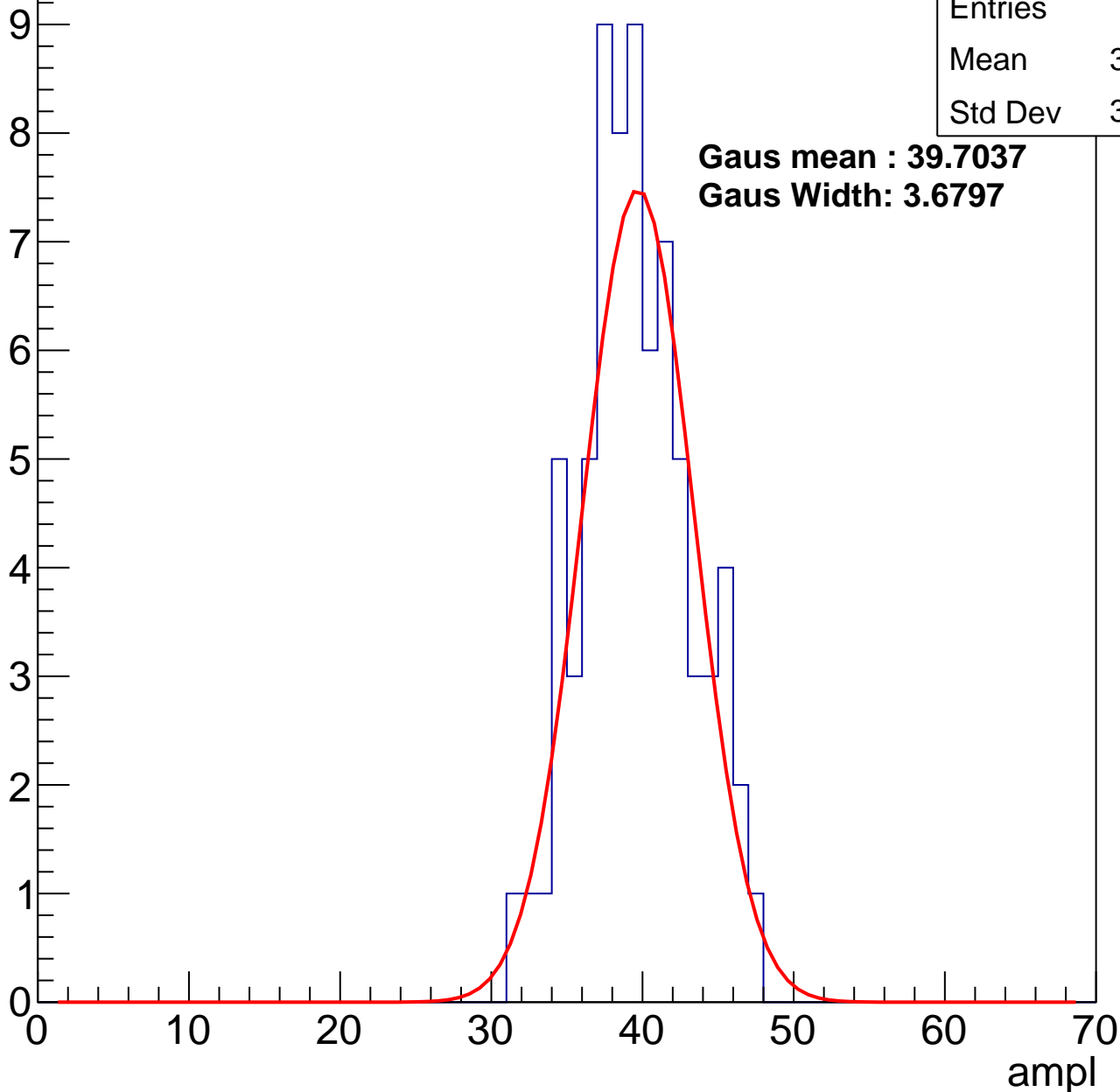
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	39.12
Std Dev	3.546

Gaus mean : 39.7037

Gaus Width: 3.6797



B1L003S, U13-ch97, adc2

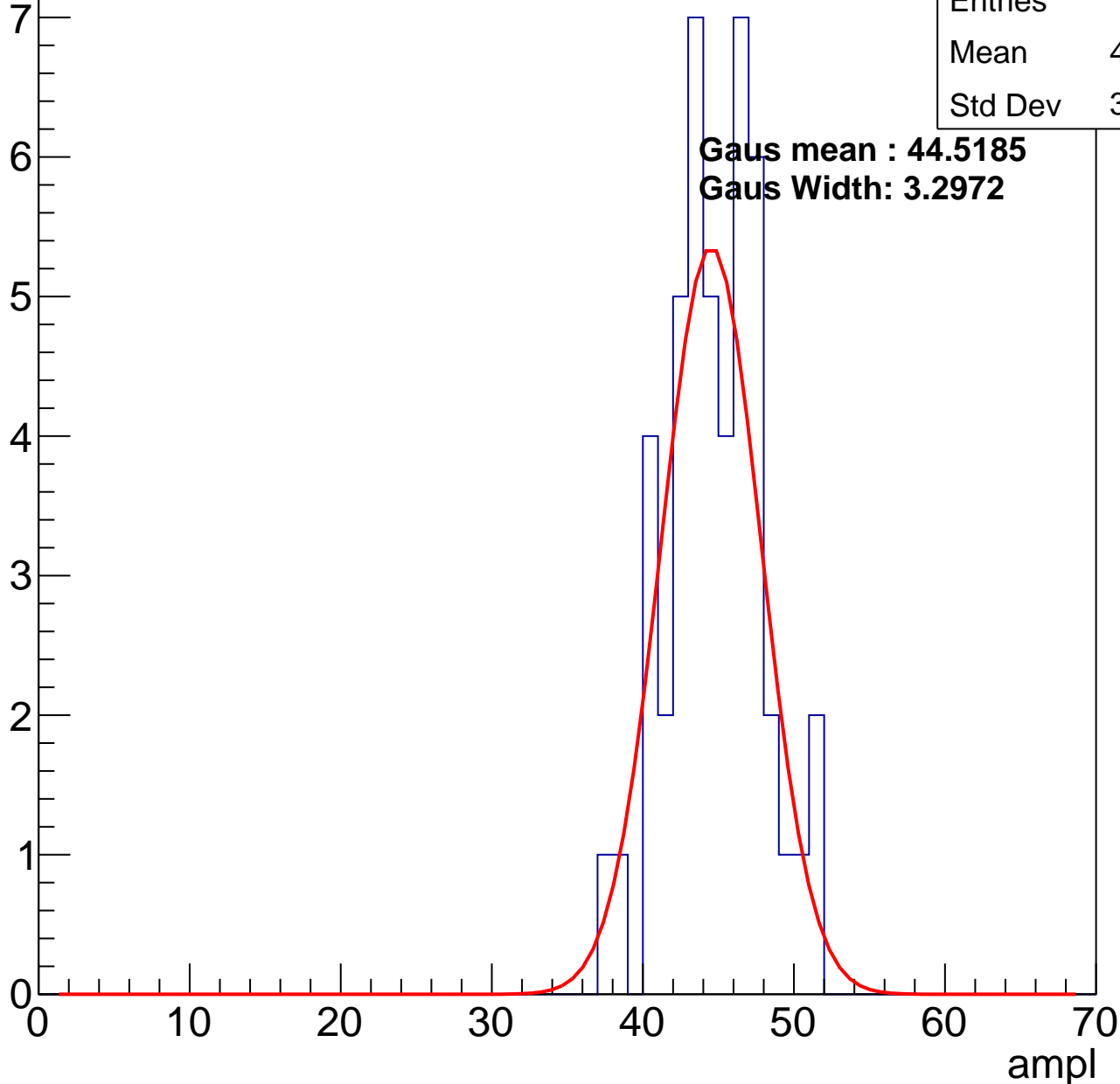
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	44.35
Std Dev	3.112

Gaus mean : 44.5185

Gaus Width: 3.2972

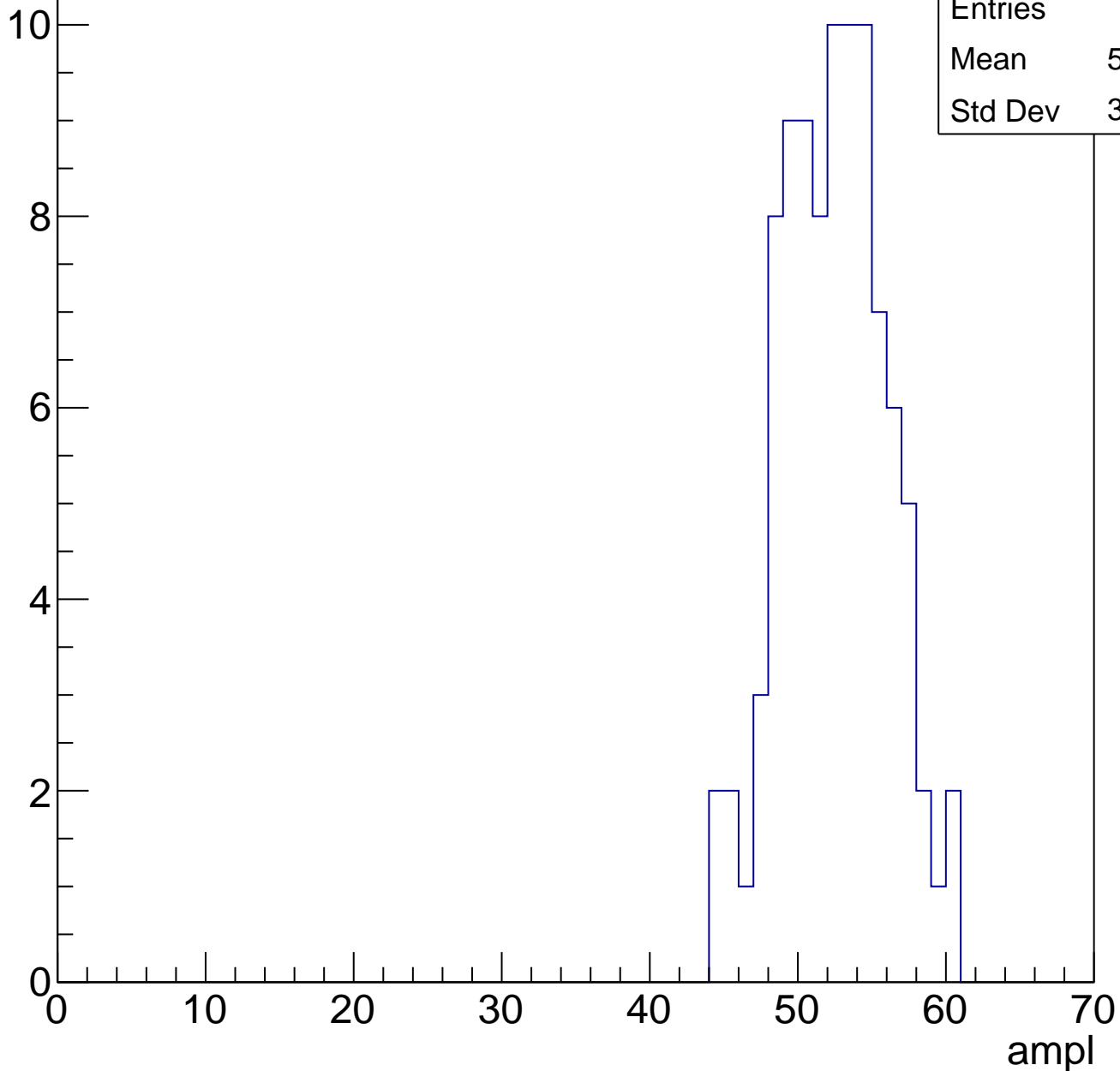


B1L003S, U13-ch97, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	95
Mean	51.99
Std Dev	3.514

Entry

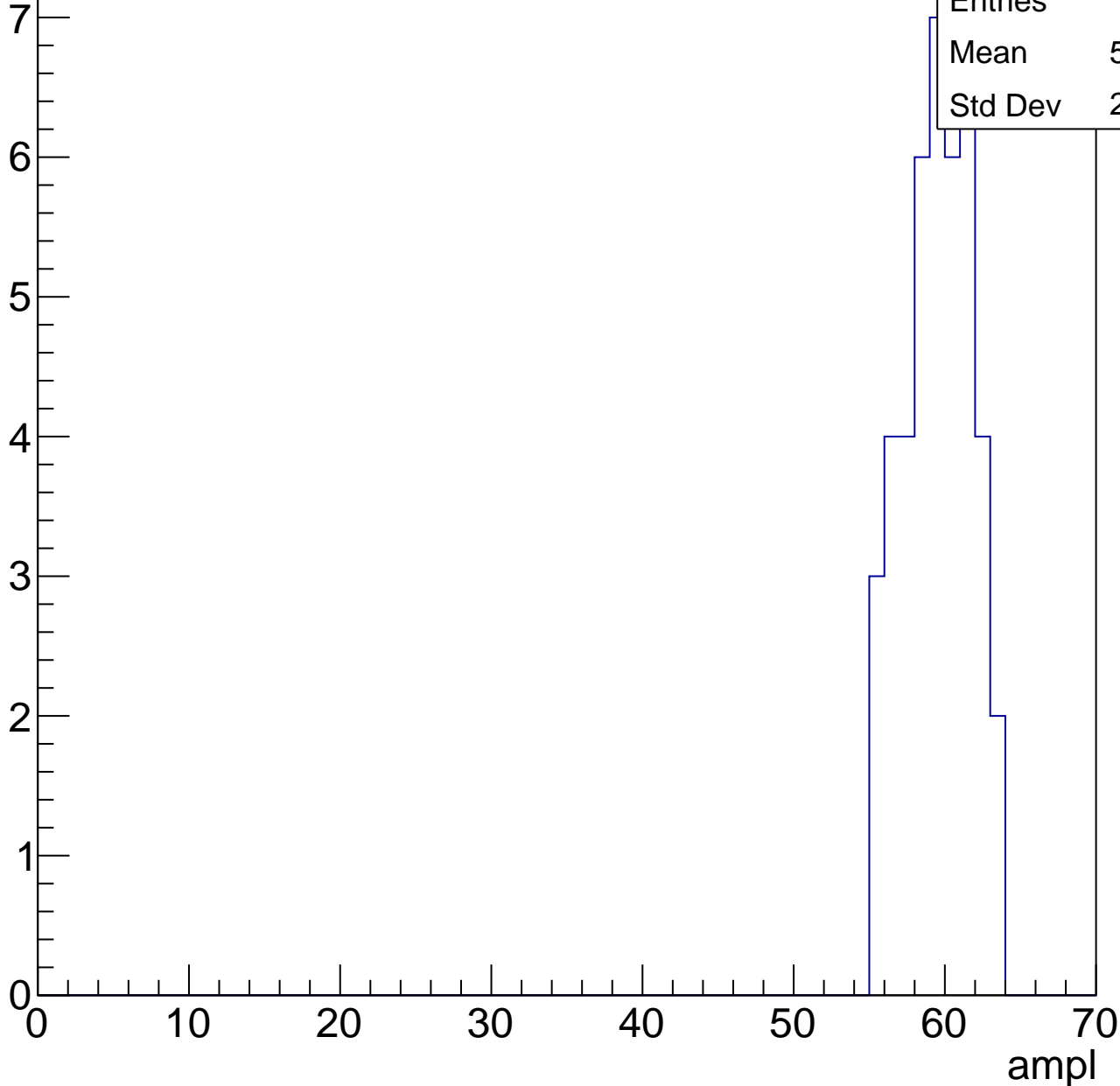


B1L003S, U13-ch97, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	59.05
Std Dev	2.199

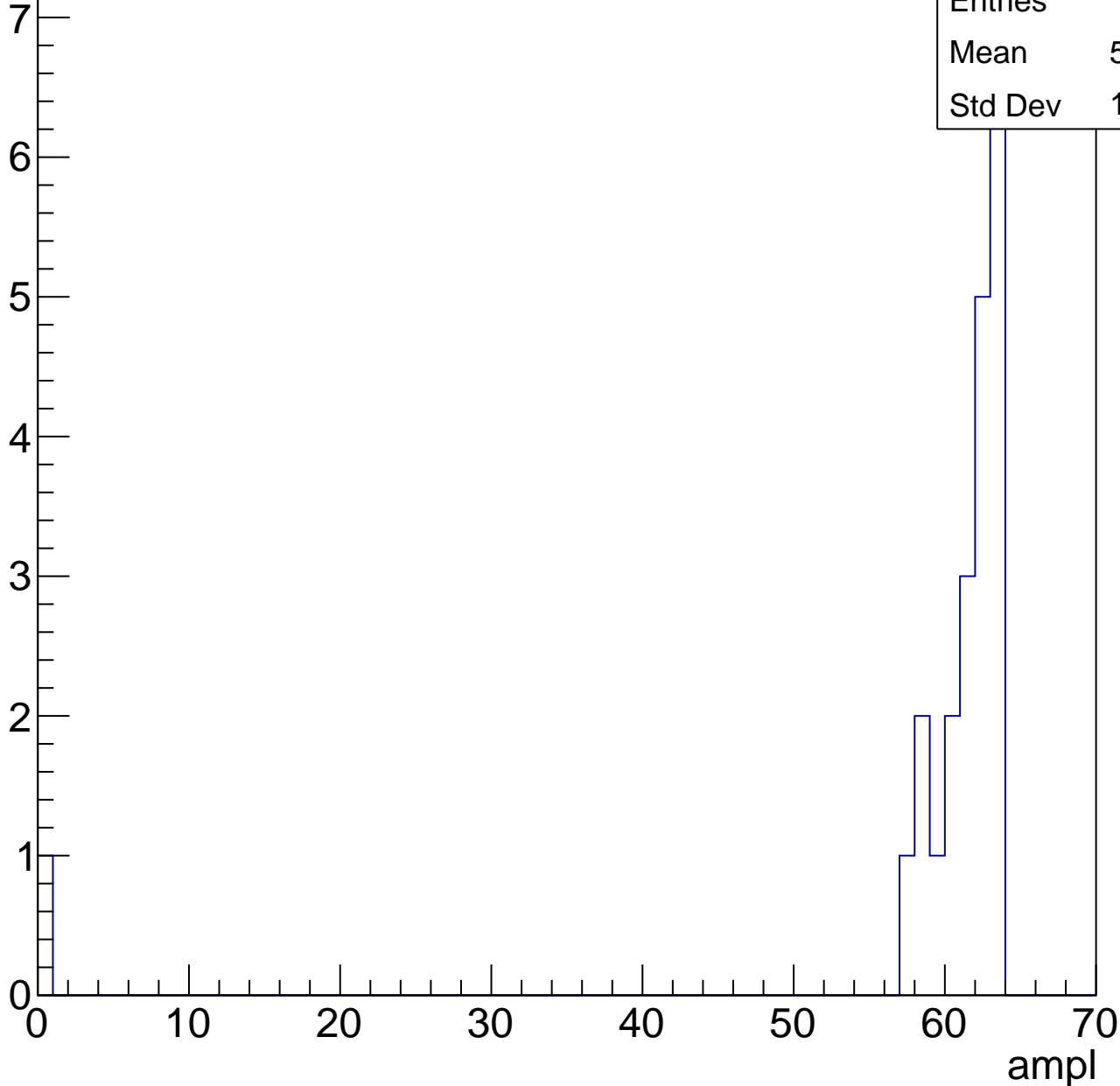


B1L003S, U13-ch97, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

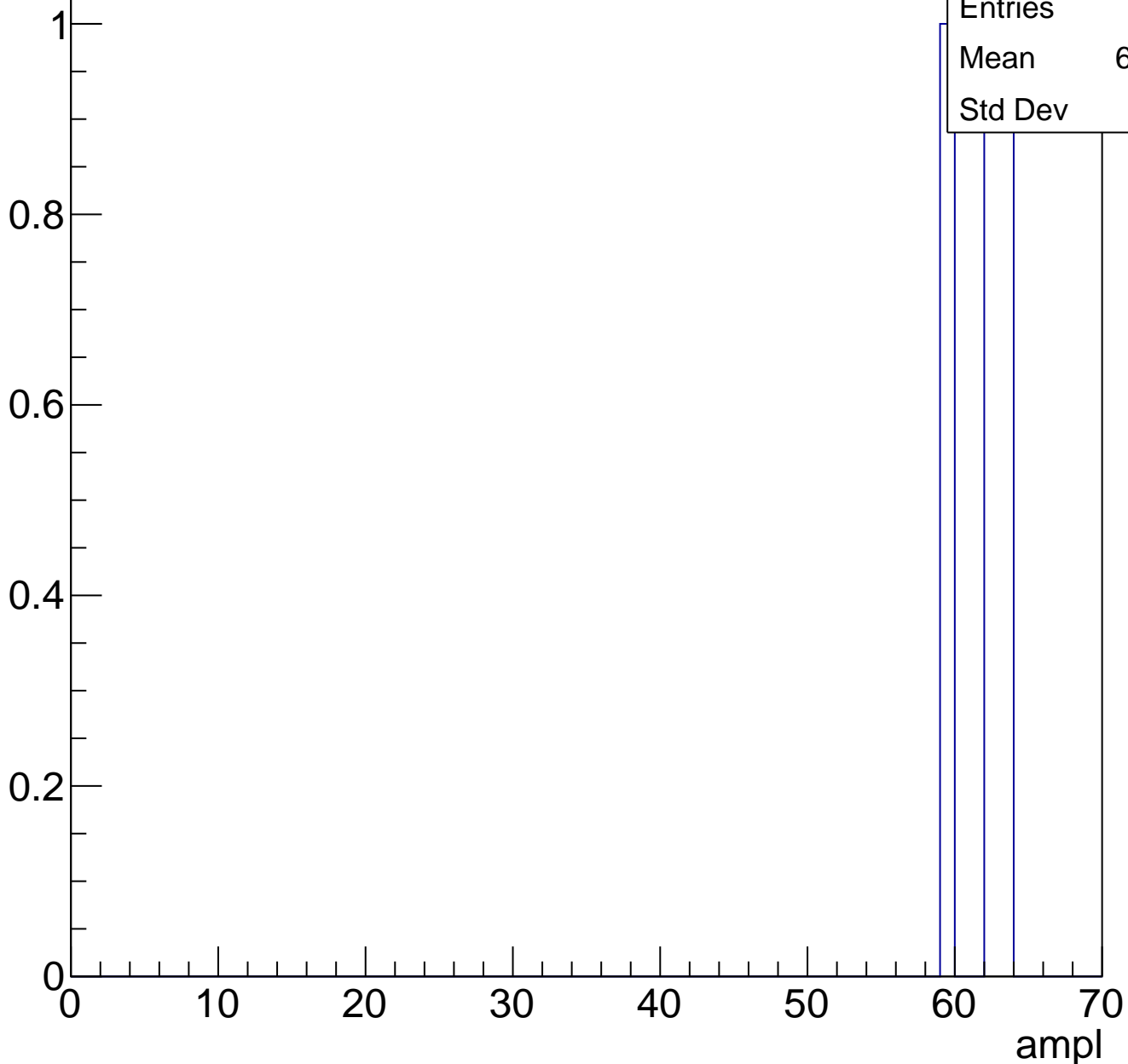
Entries	22
Mean	58.45
Std Dev	12.88



B1L003S, U13-ch97, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch97, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch98, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	29.58
Std Dev	4.873

Gaus mean : 30.0917

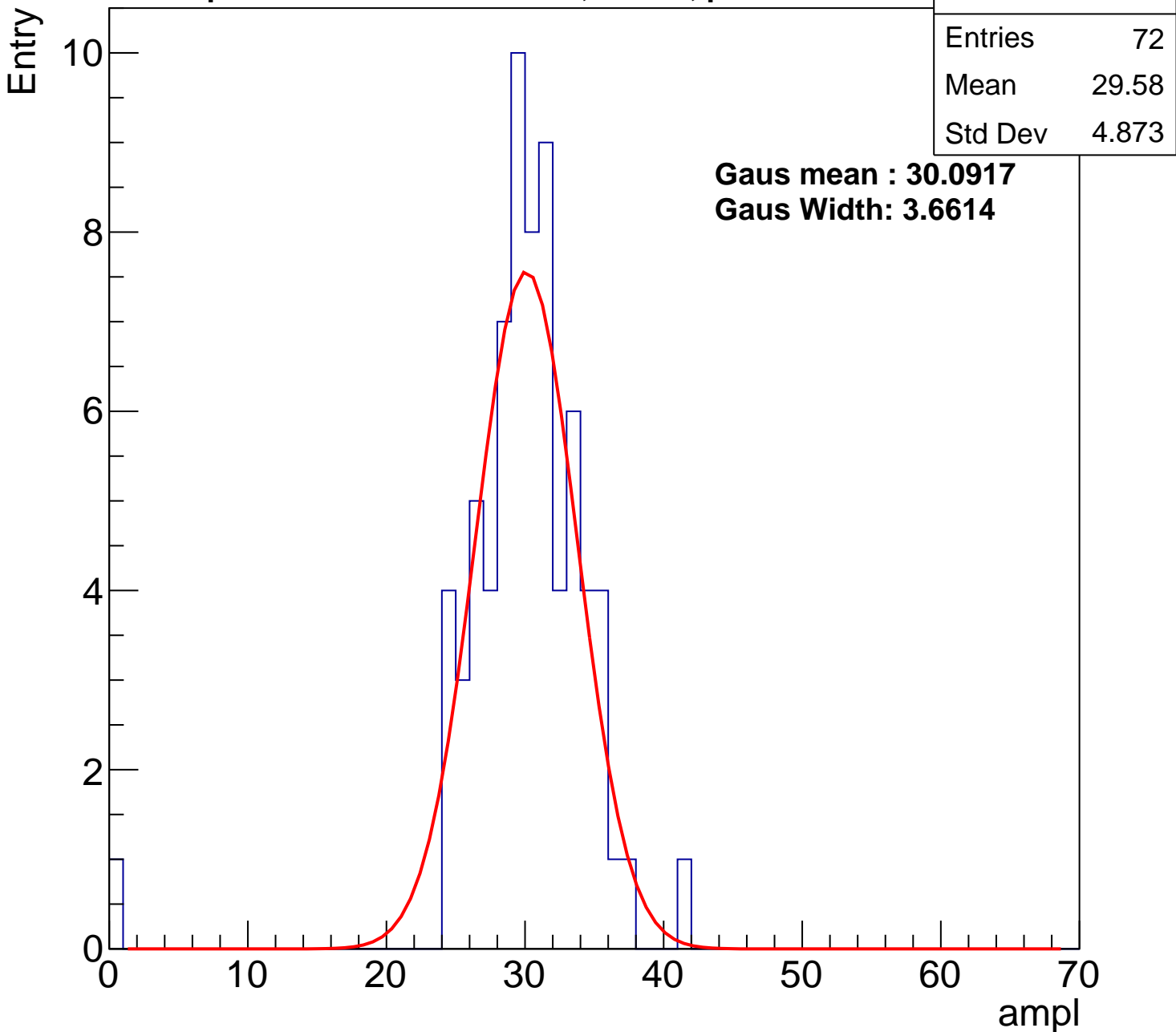
Gaus Width: 3.6614

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch98, adc1

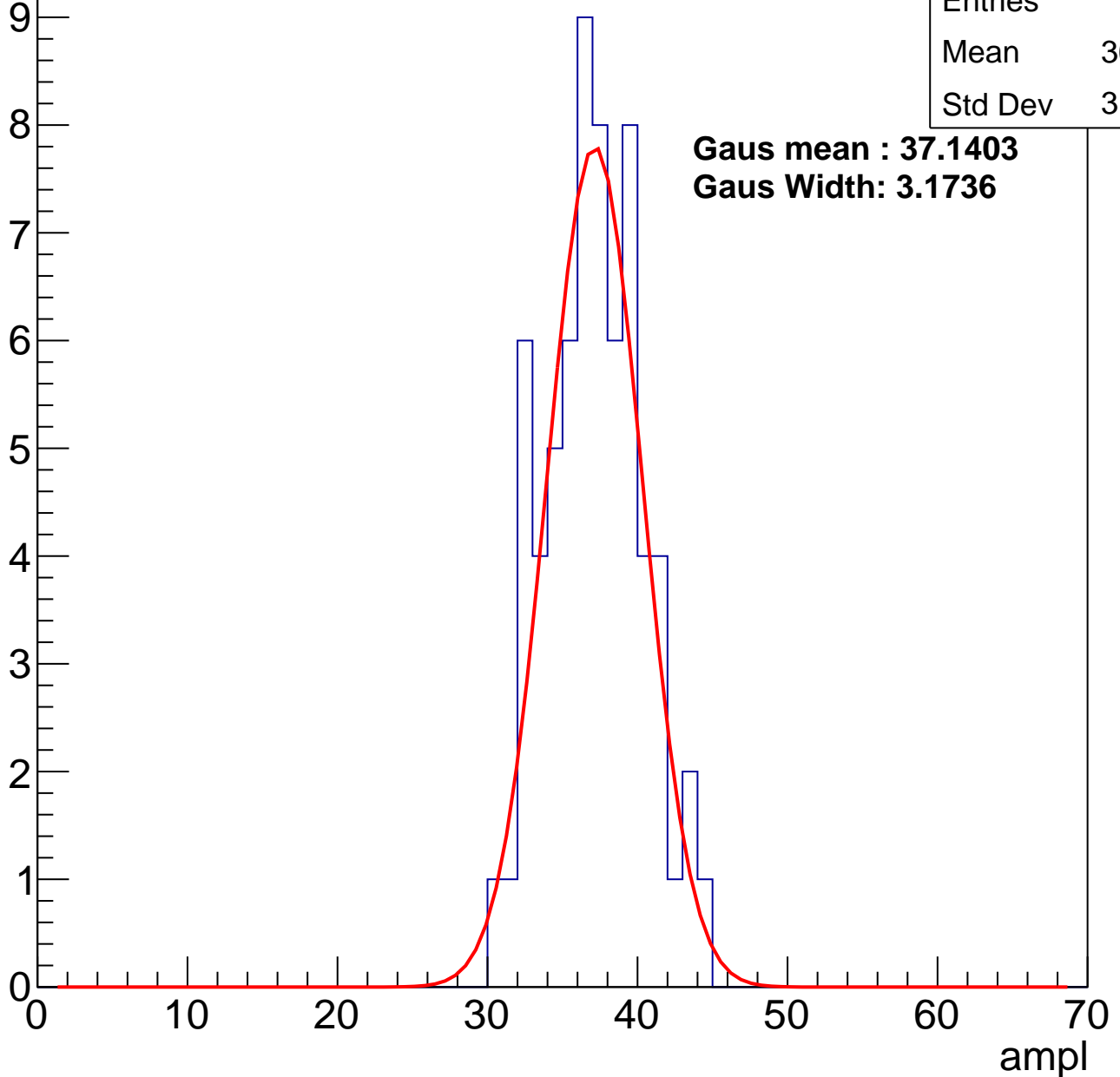
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.68
Std Dev	3.149

Gaus mean : 37.1403

Gaus Width: 3.1736



B1L003S, U13-ch98, adc2

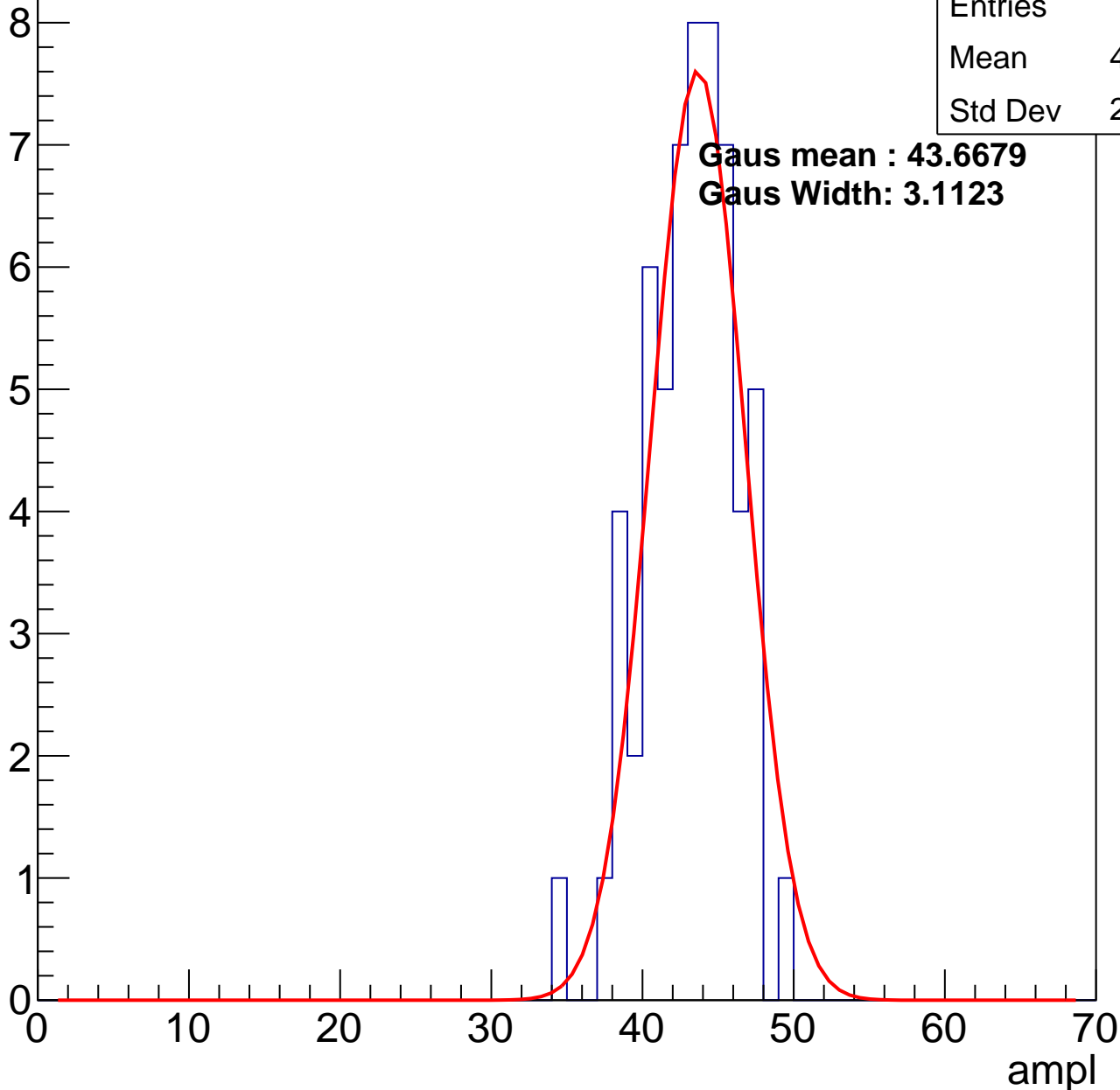
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.69
Std Dev	2.947

Gaus mean : 43.6679

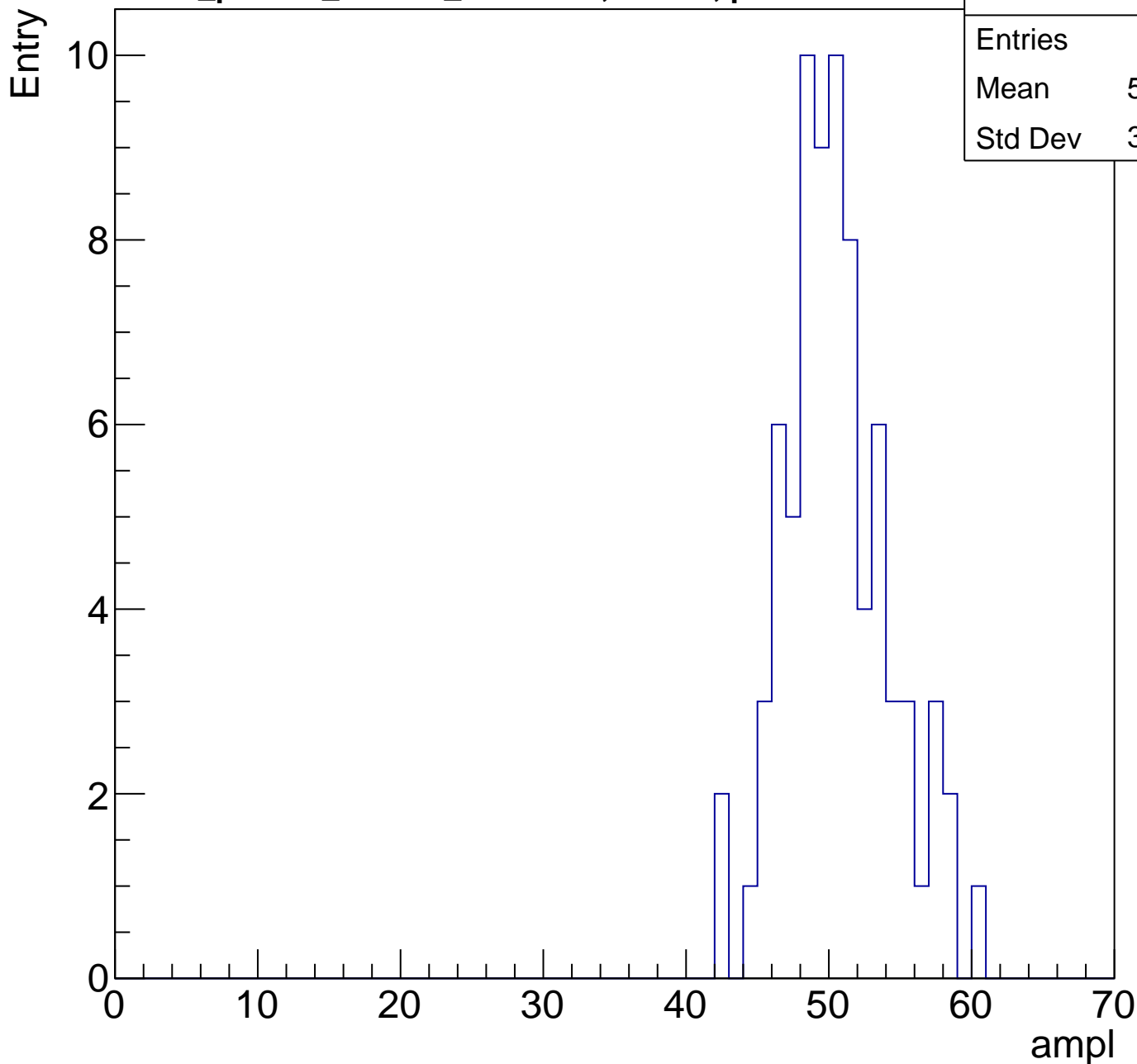
Gaus Width: 3.1123



B1L003S, U13-ch98, adc3

calib_packv5_042523_0143.root, FC#13, port D2

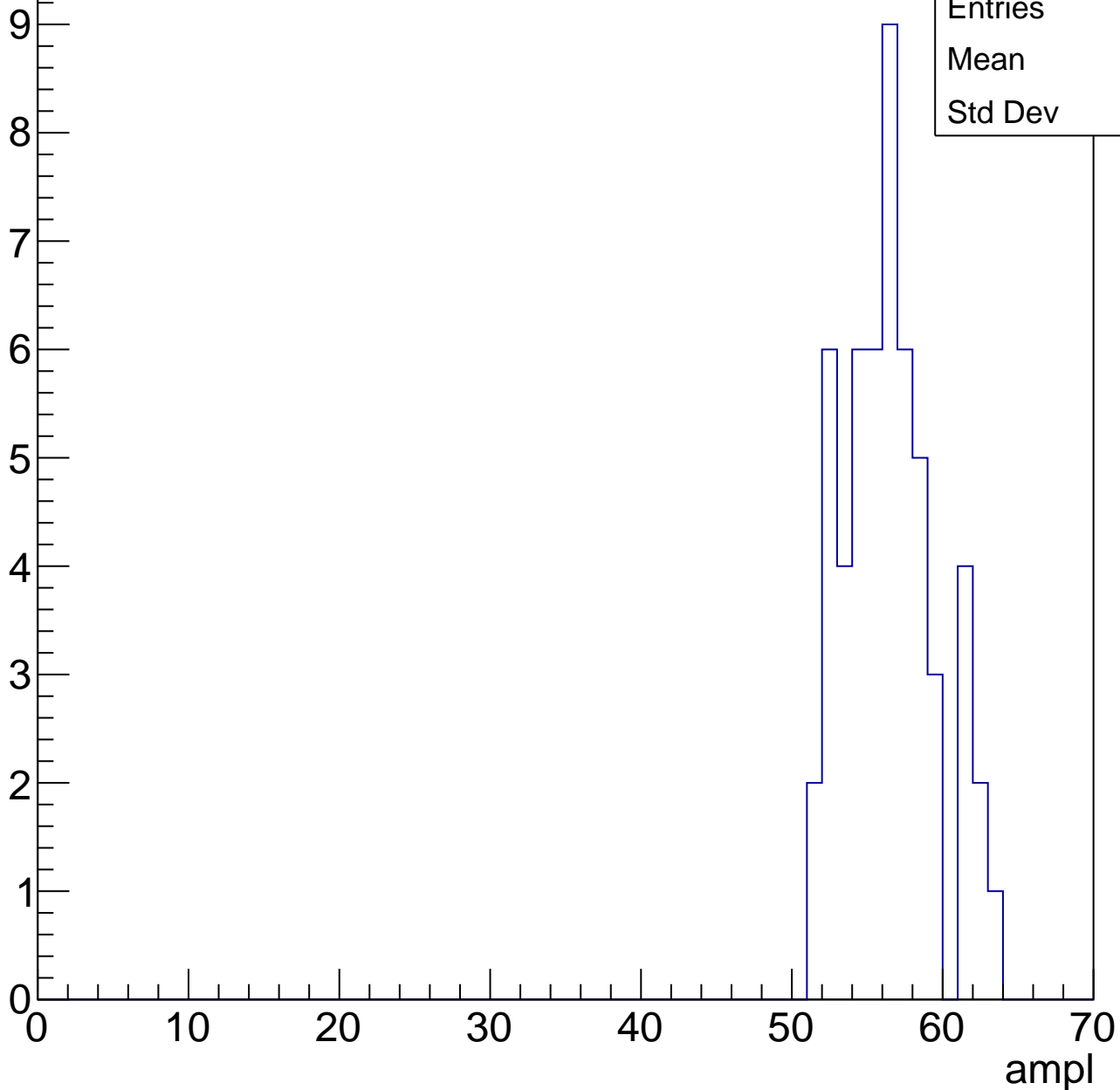
Entries	77
Mean	50.12
Std Dev	3.675



B1L003S, U13-ch98, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

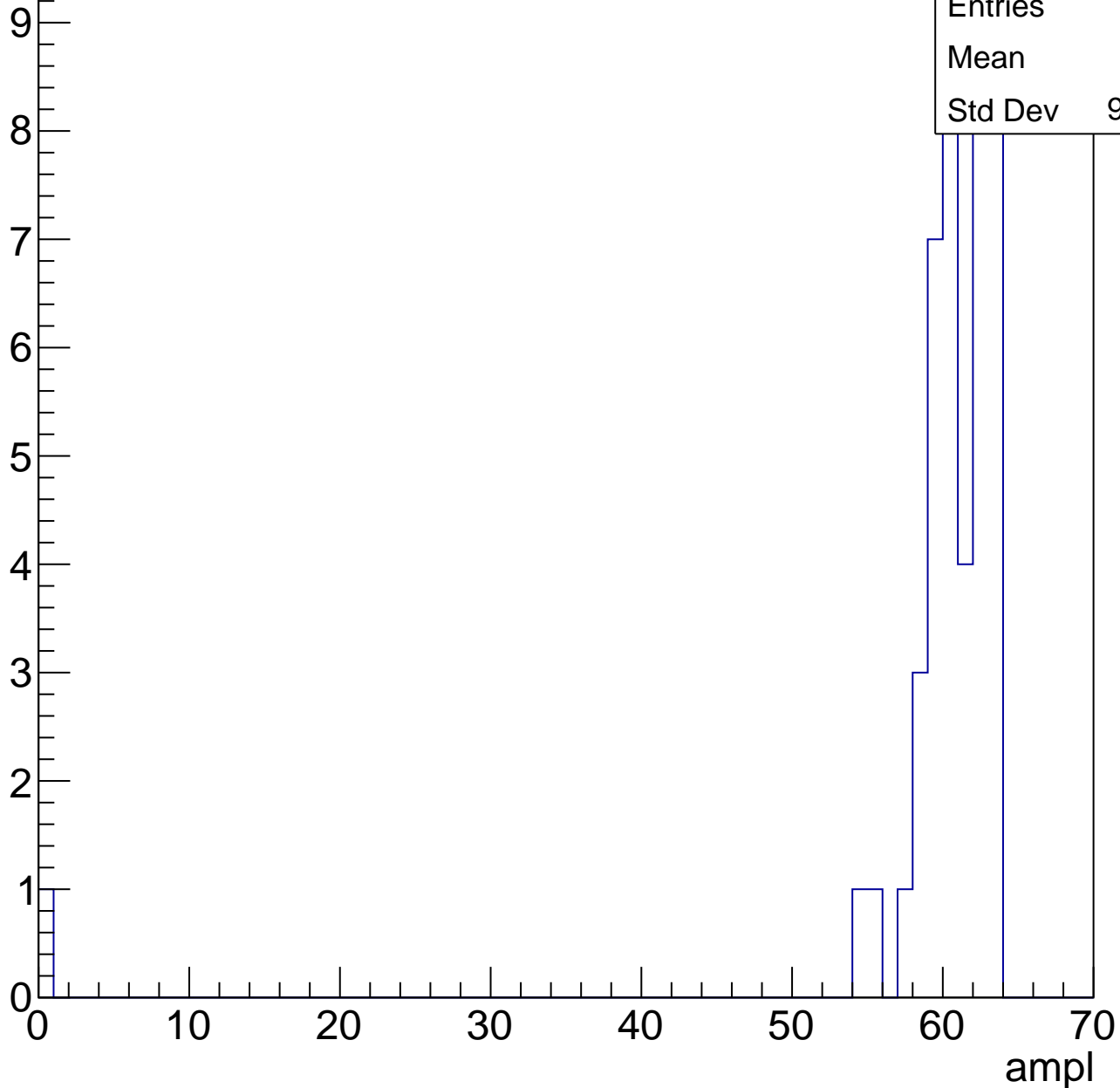


Entries	54
Mean	56
Std Dev	3

B1L003S, U13-ch98, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch98, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

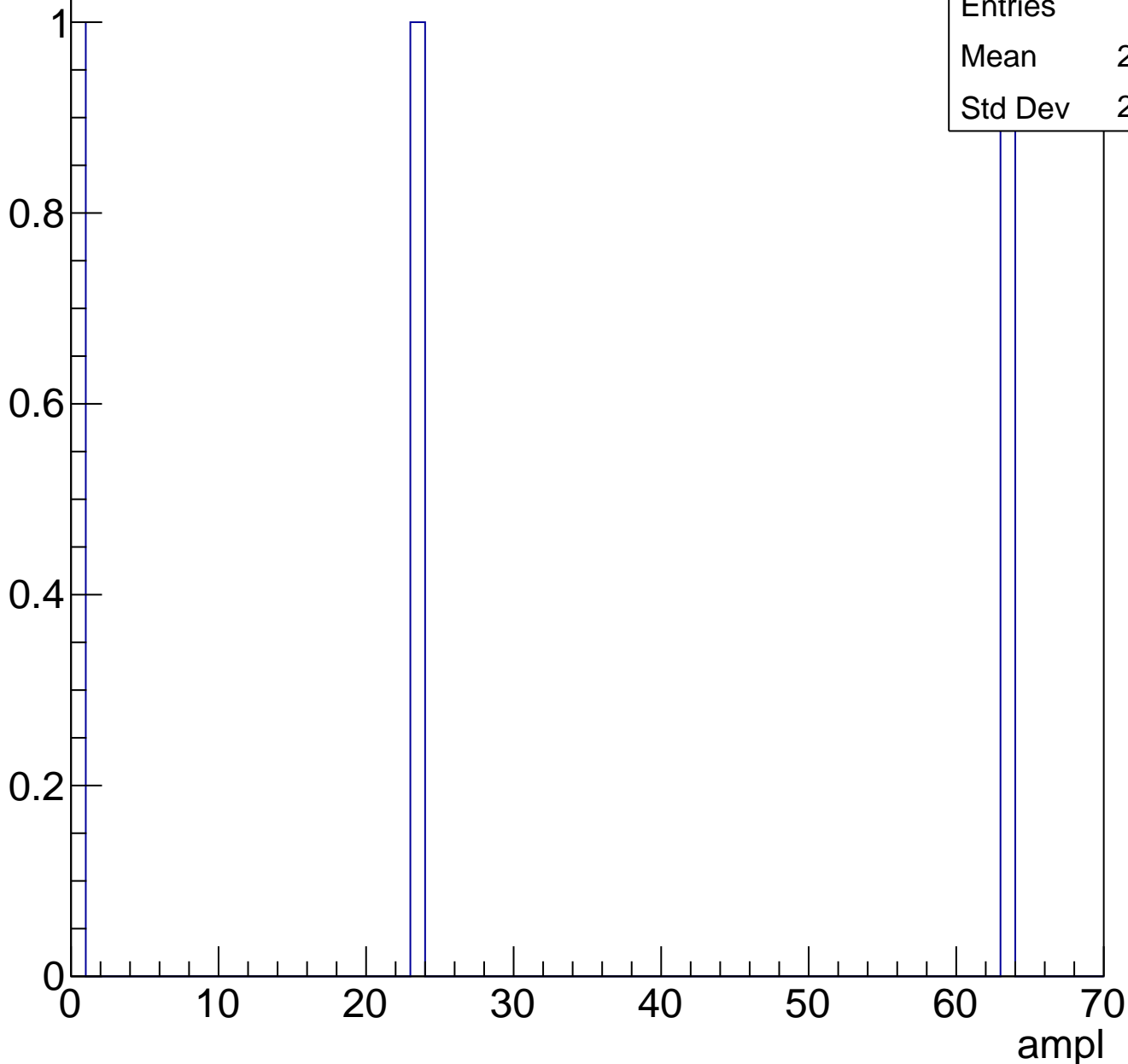
Entries	2
Mean	61
Std Dev	0

ampl

B1L003S, U13-ch98, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B1L003S, U13-ch99, adc0

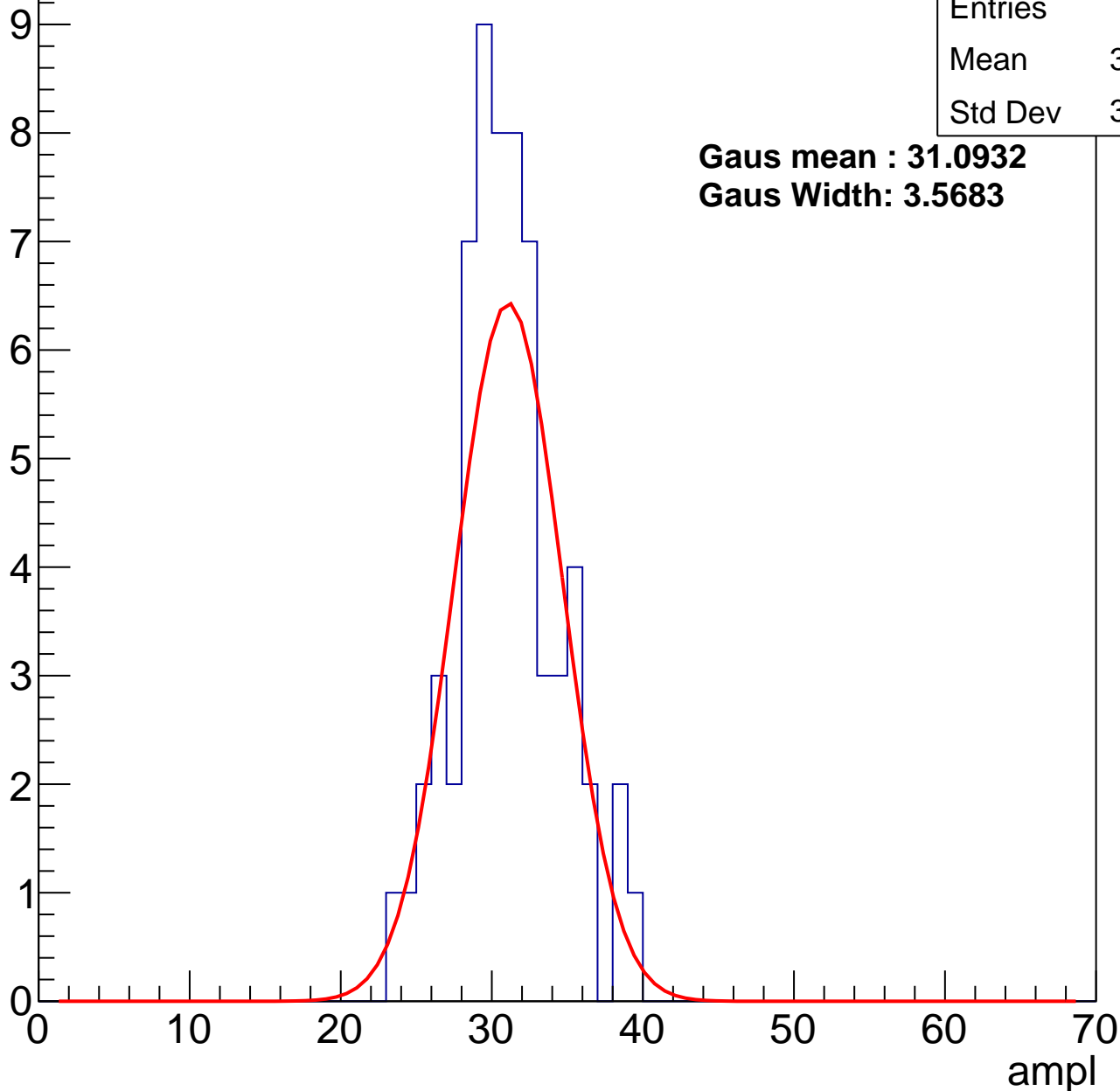
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.57
Std Dev	3.356

Gaus mean : 31.0932

Gaus Width: 3.5683



B1L003S, U13-ch99, adc1

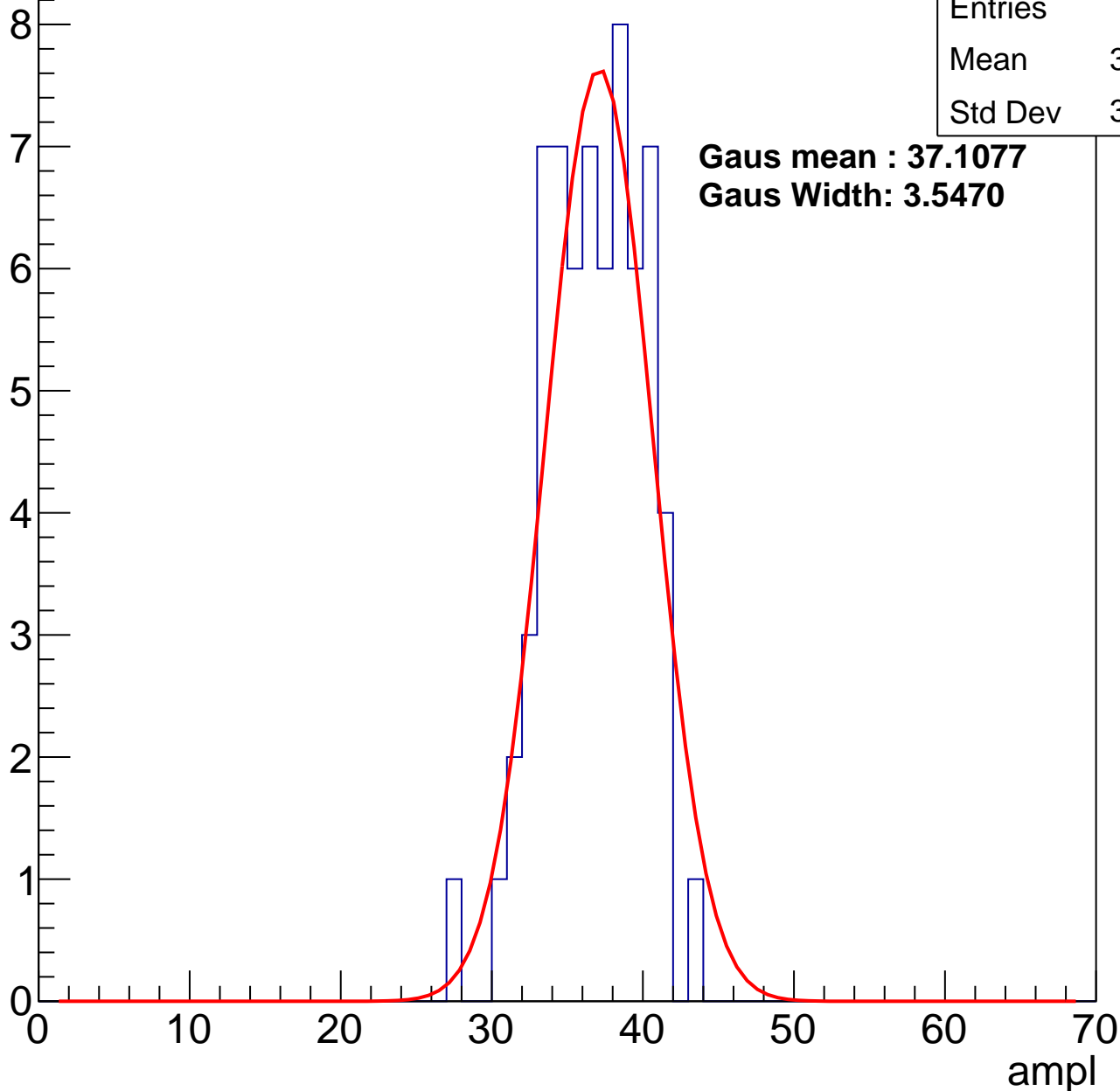
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.26
Std Dev	3.164

Gaus mean : 37.1077

Gaus Width: 3.5470



B1L003S, U13-ch99, adc2

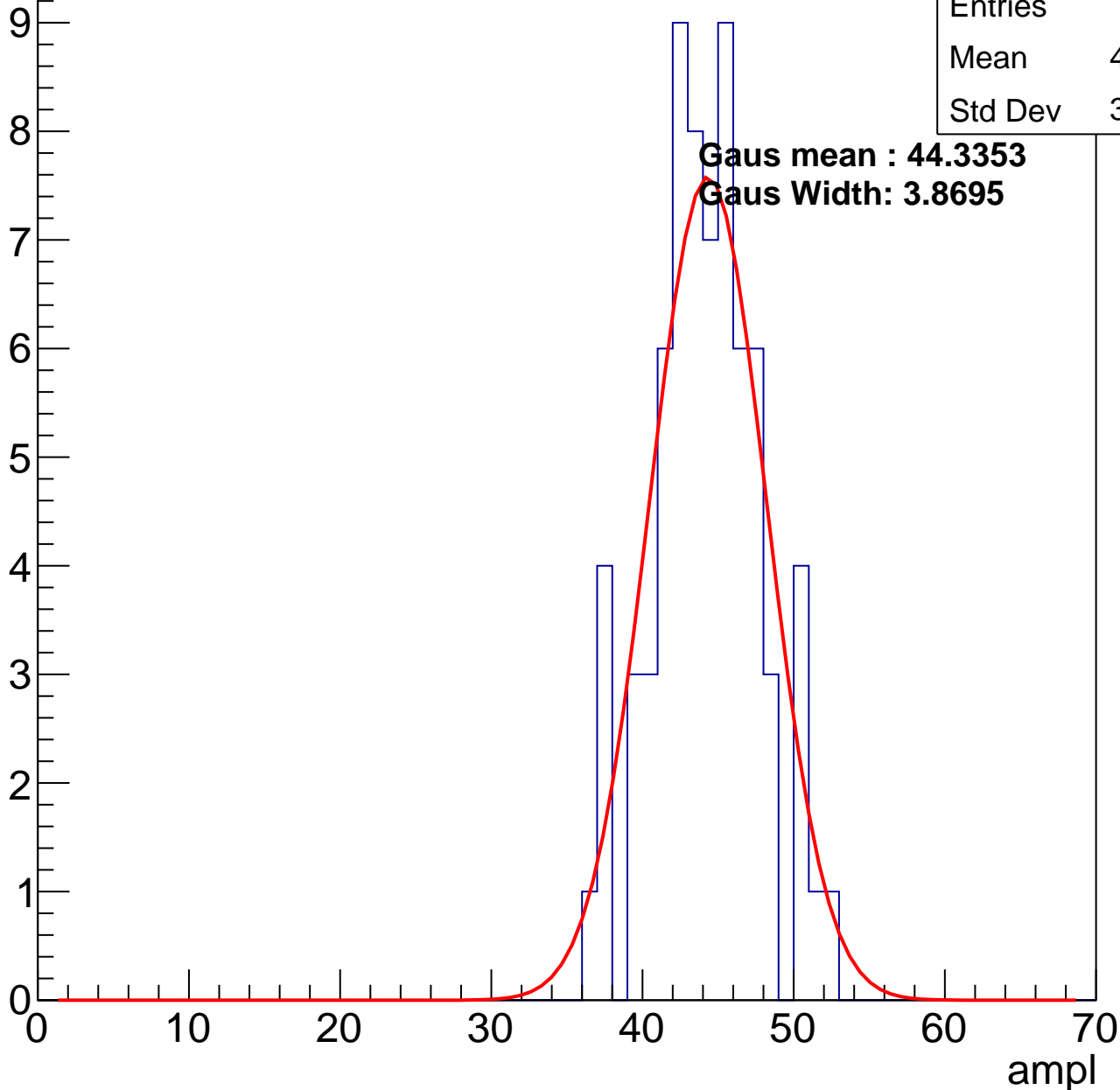
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.76
Std Dev	3.498

Gaus mean : 44.3353

Gaus Width: 3.8695

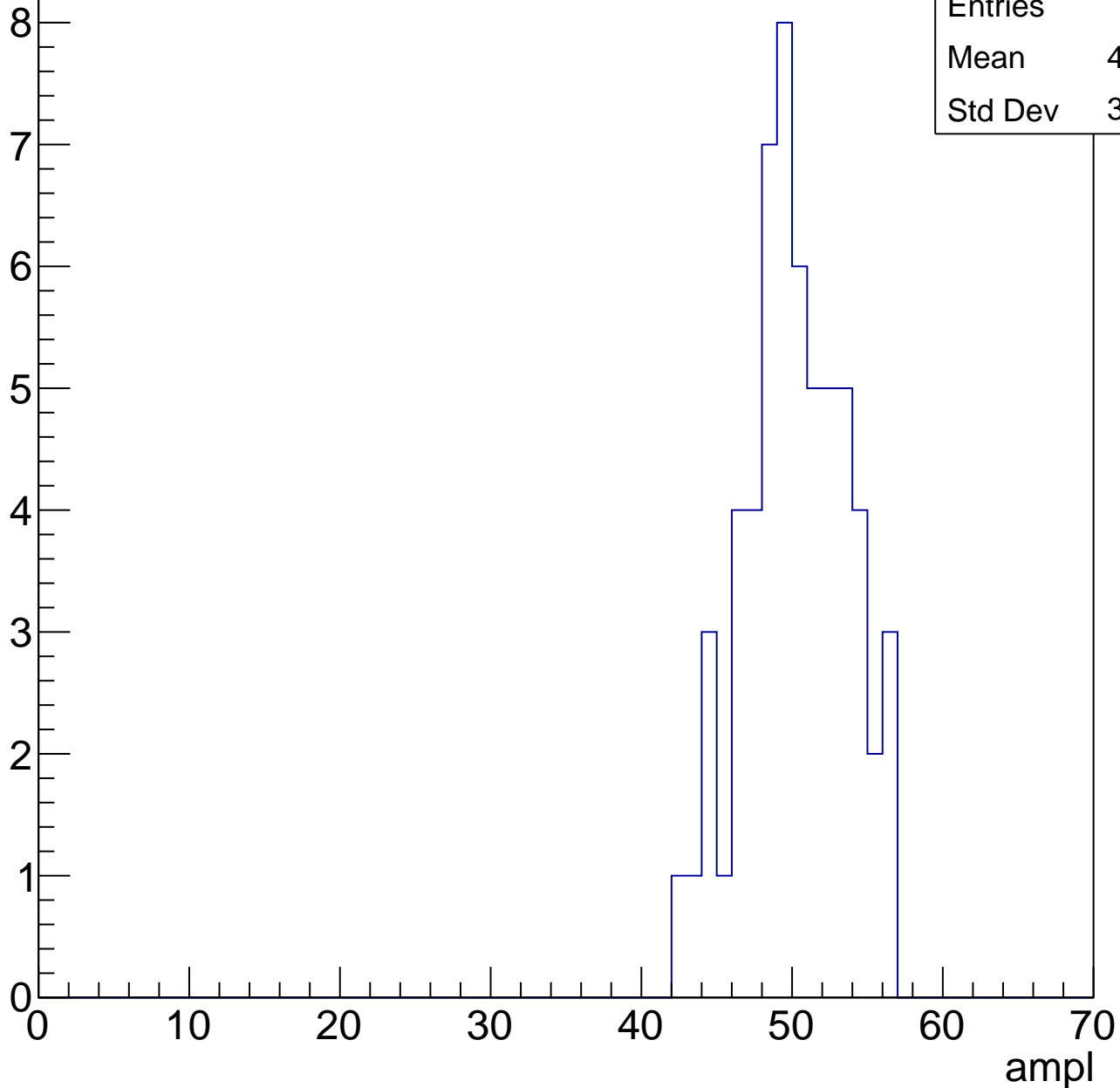


B1L003S, U13-ch99, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	49.76
Std Dev	3.372

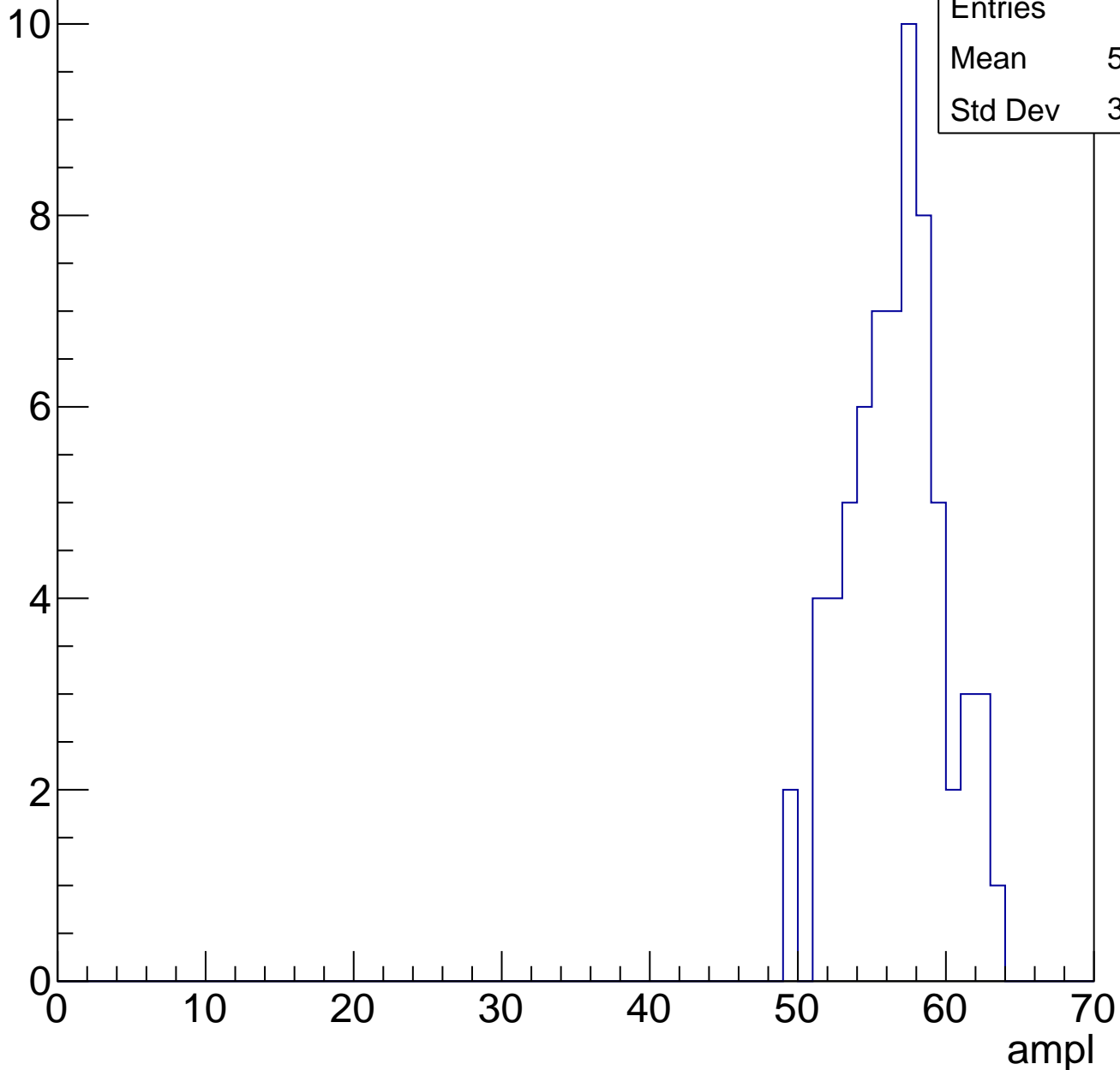


B1L003S, U13-ch99, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	56.07
Std Dev	3.206

Entry

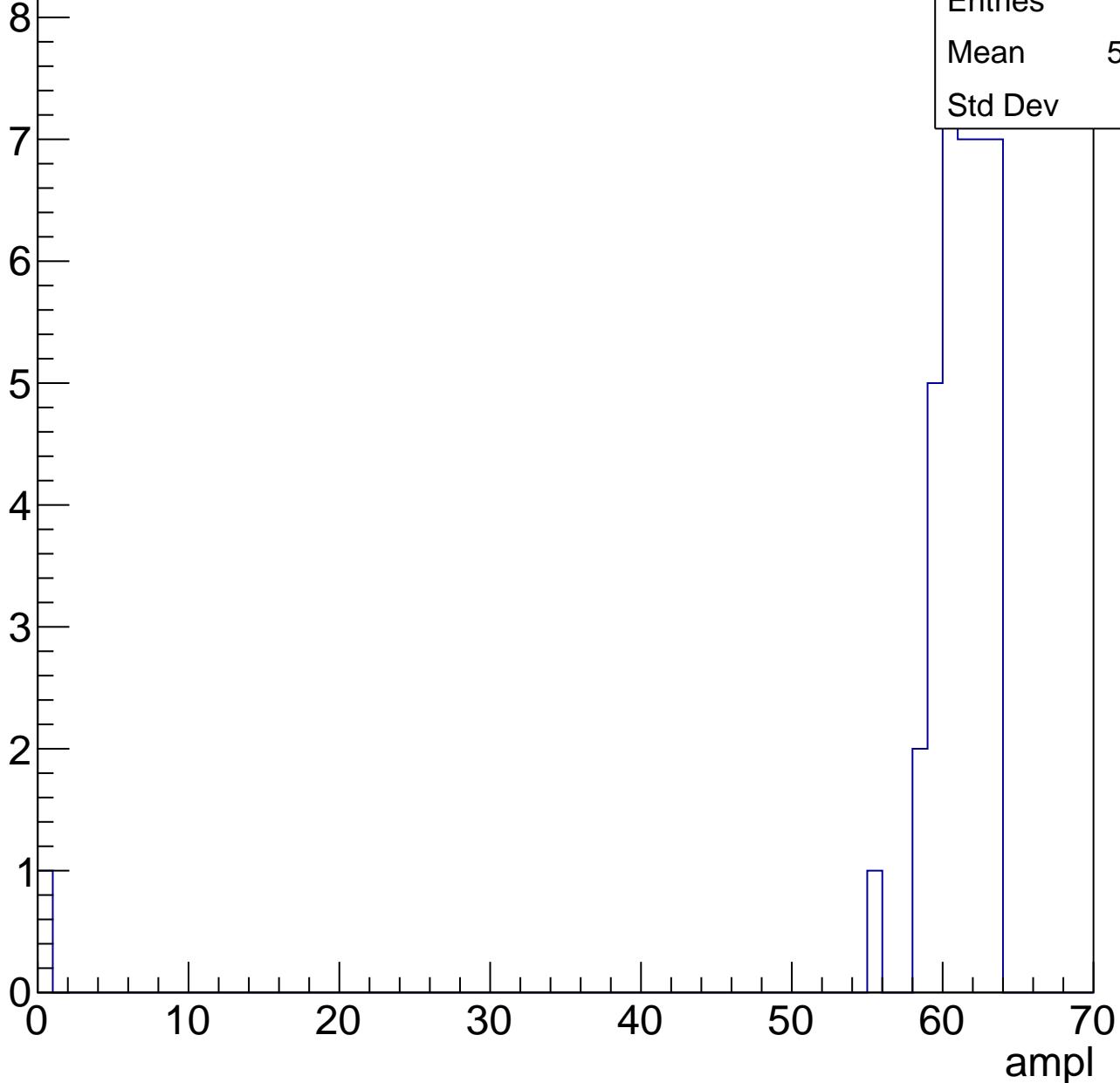


B1L003S, U13-ch99, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59.16
Std Dev	9.88



B1L003S, U13-ch99, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch99, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch100, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	29.94
Std Dev	4.861

Gaus mean : 31.4025

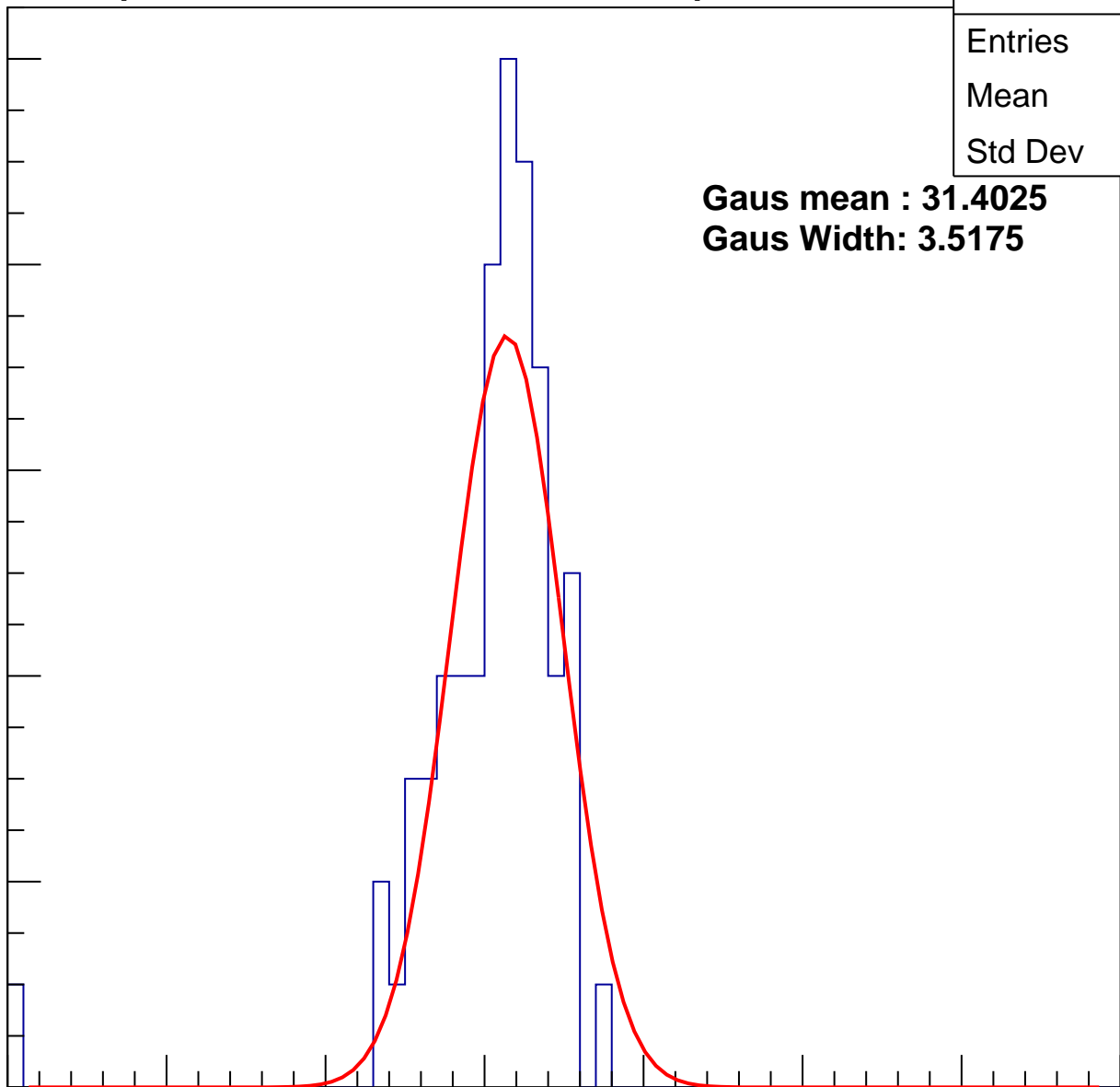
Gaus Width: 3.5175

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch100, adc1

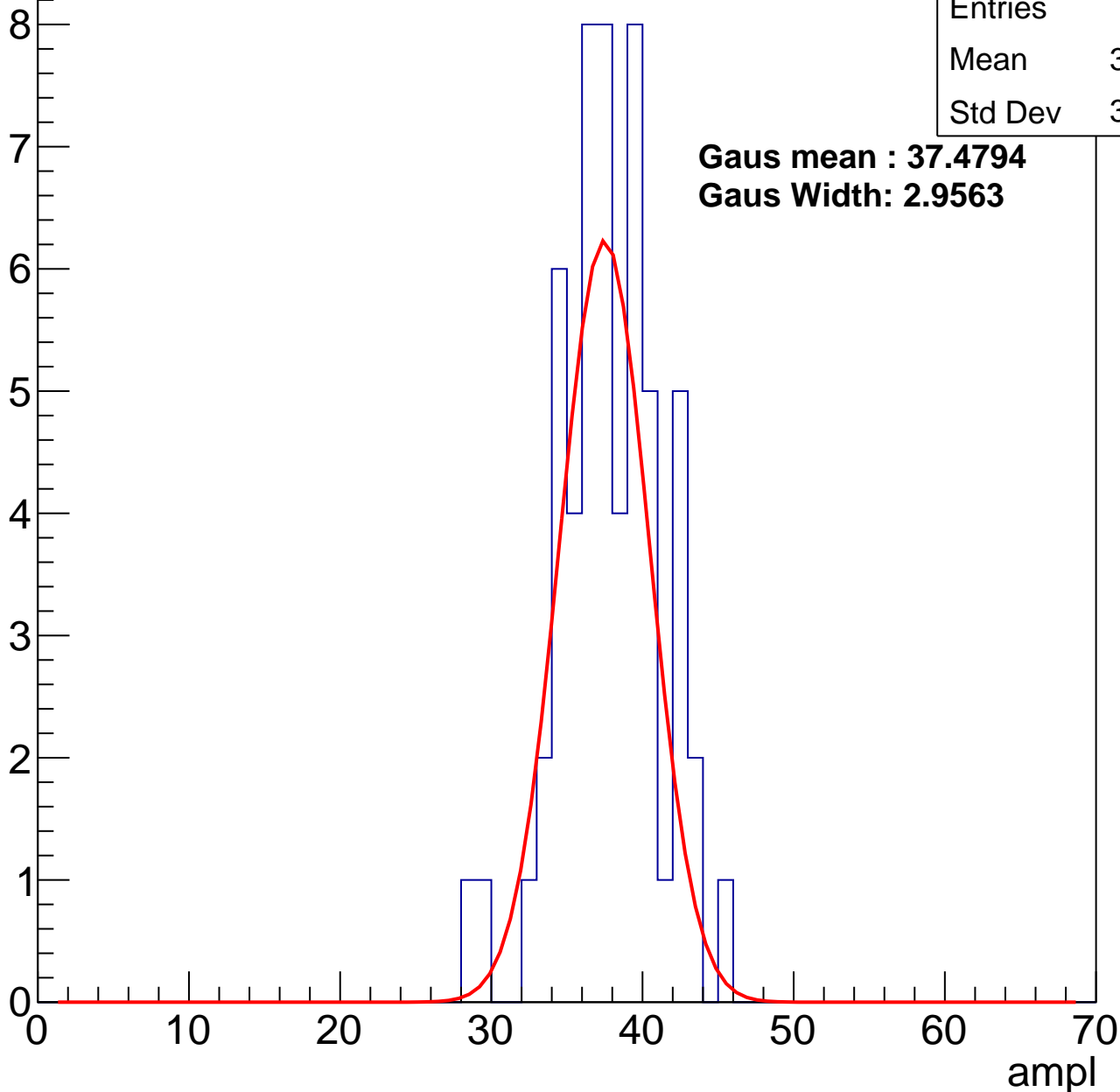
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	37.35
Std Dev	3.327

Gaus mean : 37.4794

Gaus Width: 2.9563



B1L003S, U13-ch100, adc2

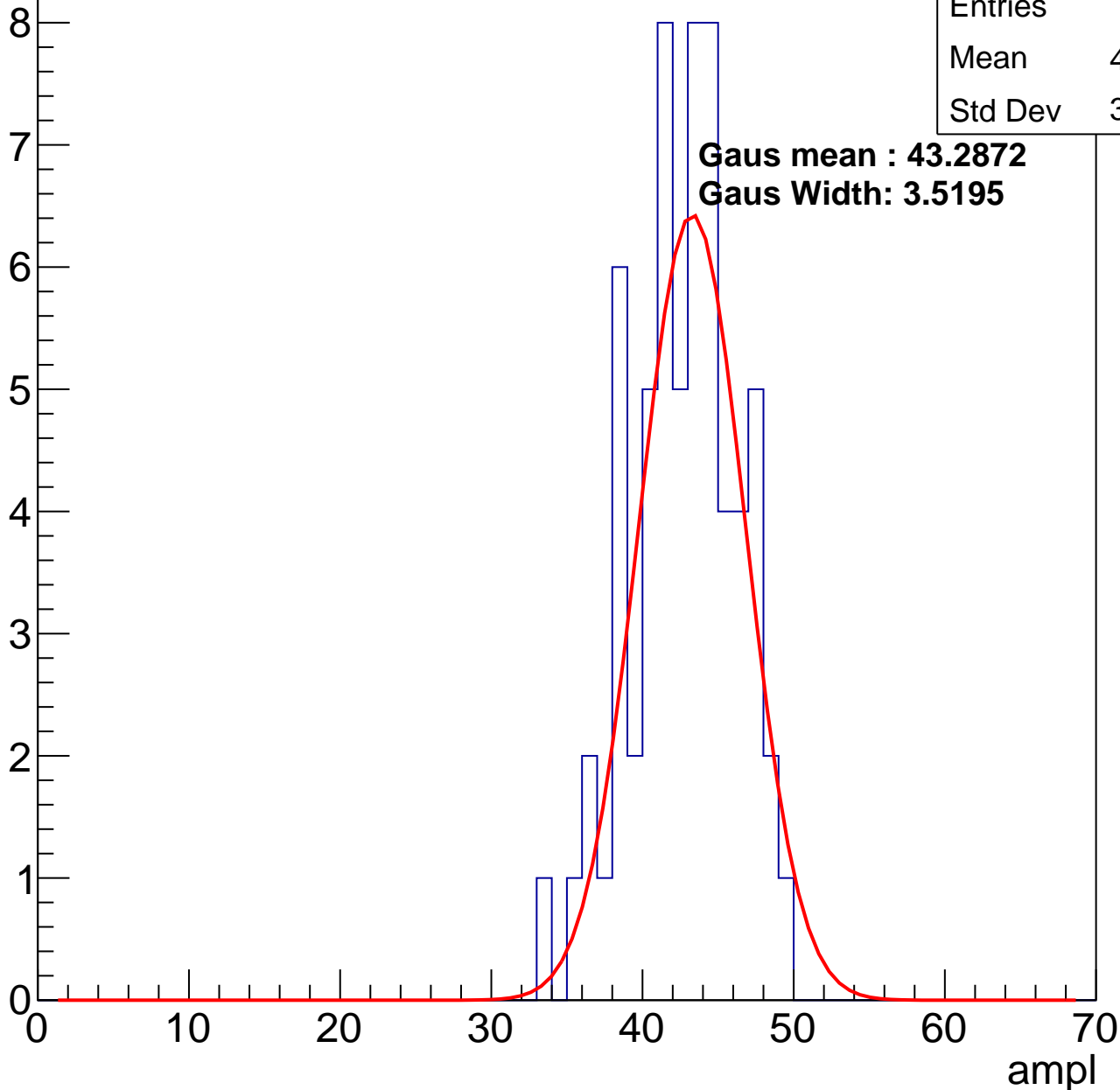
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.24
Std Dev	3.454

Gaus mean : 43.2872

Gaus Width: 3.5195

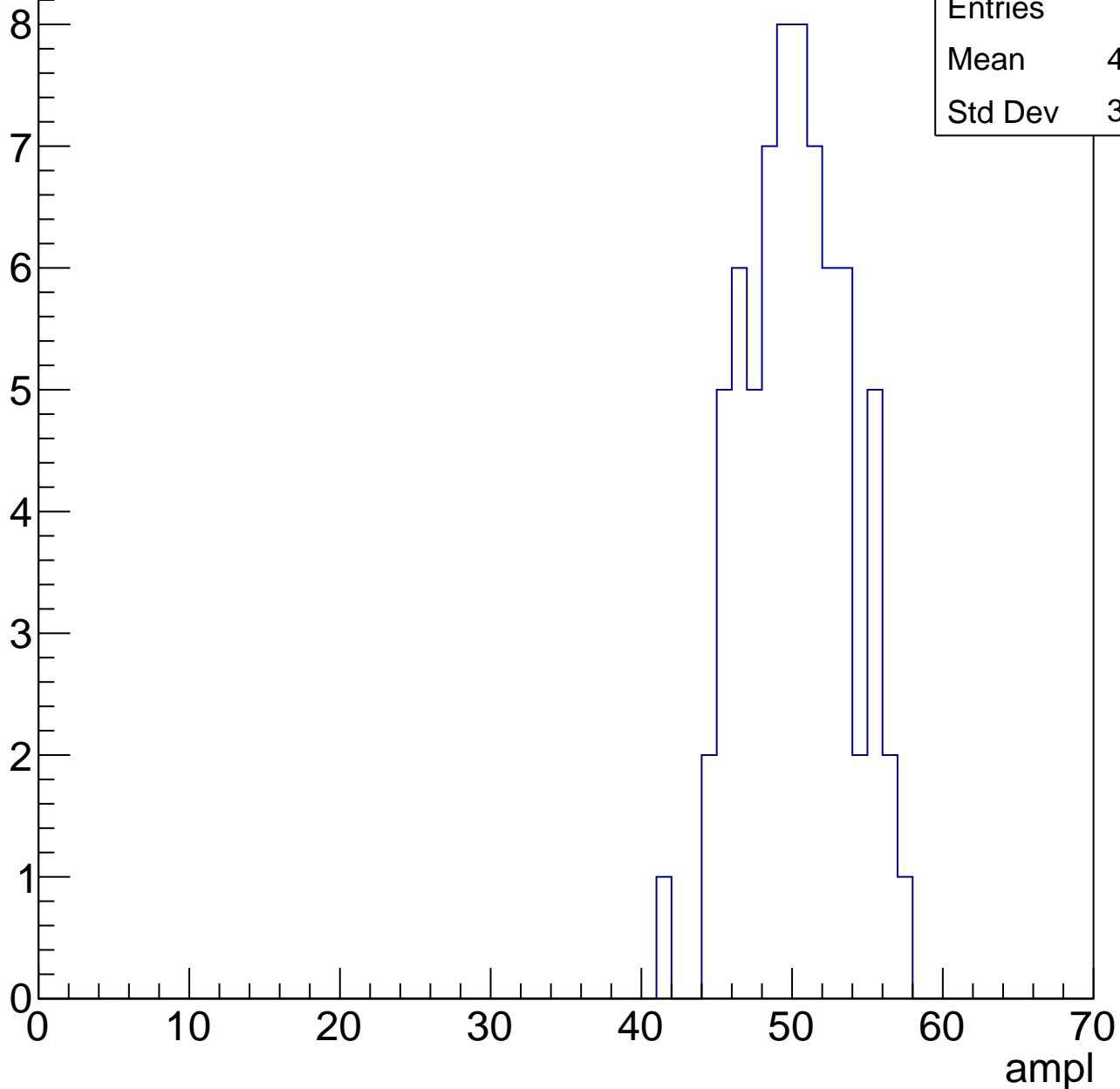


B1L003S, U13-ch100, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.75
Std Dev	3.385

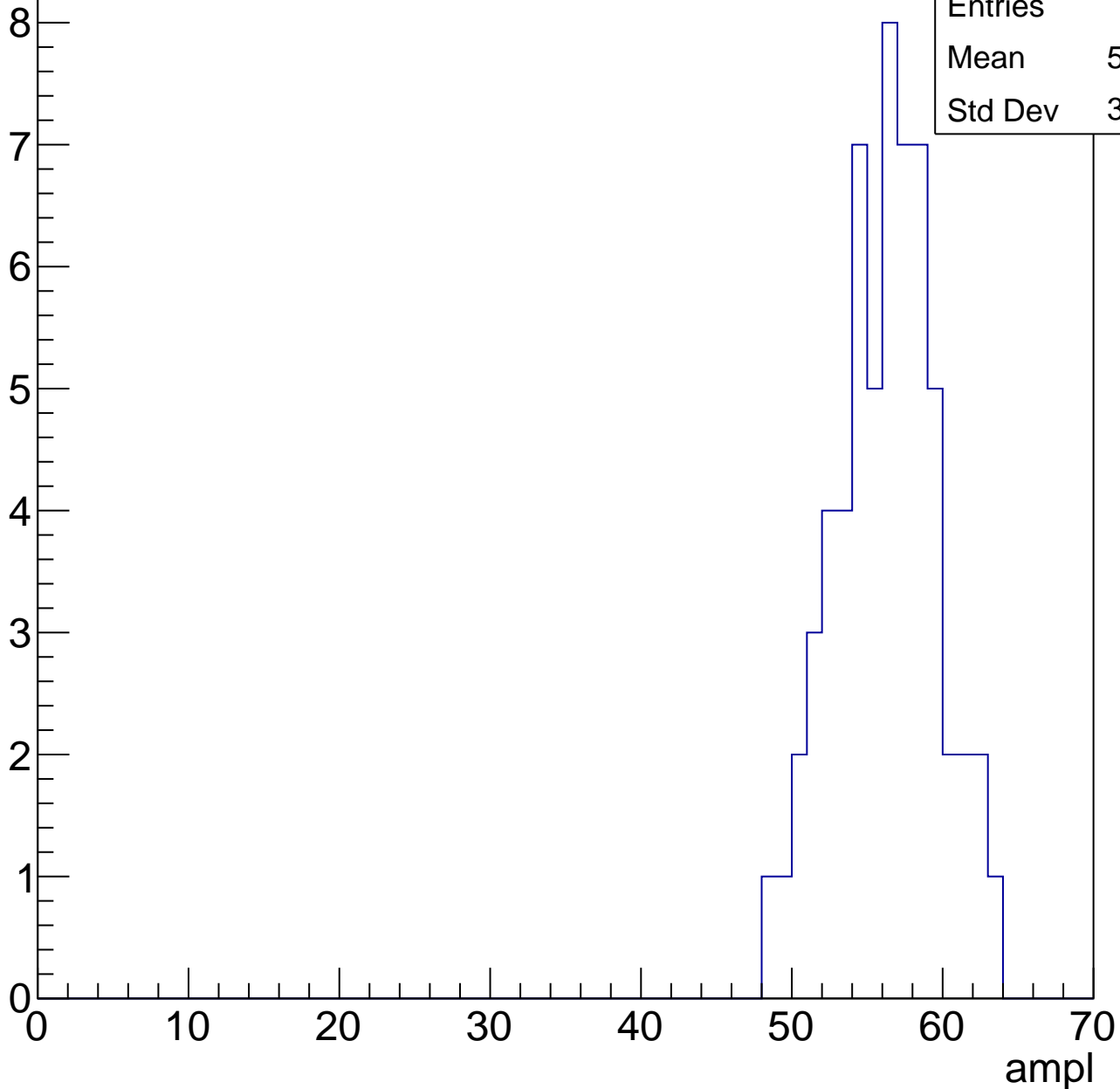


B1L003S, U13-ch100, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

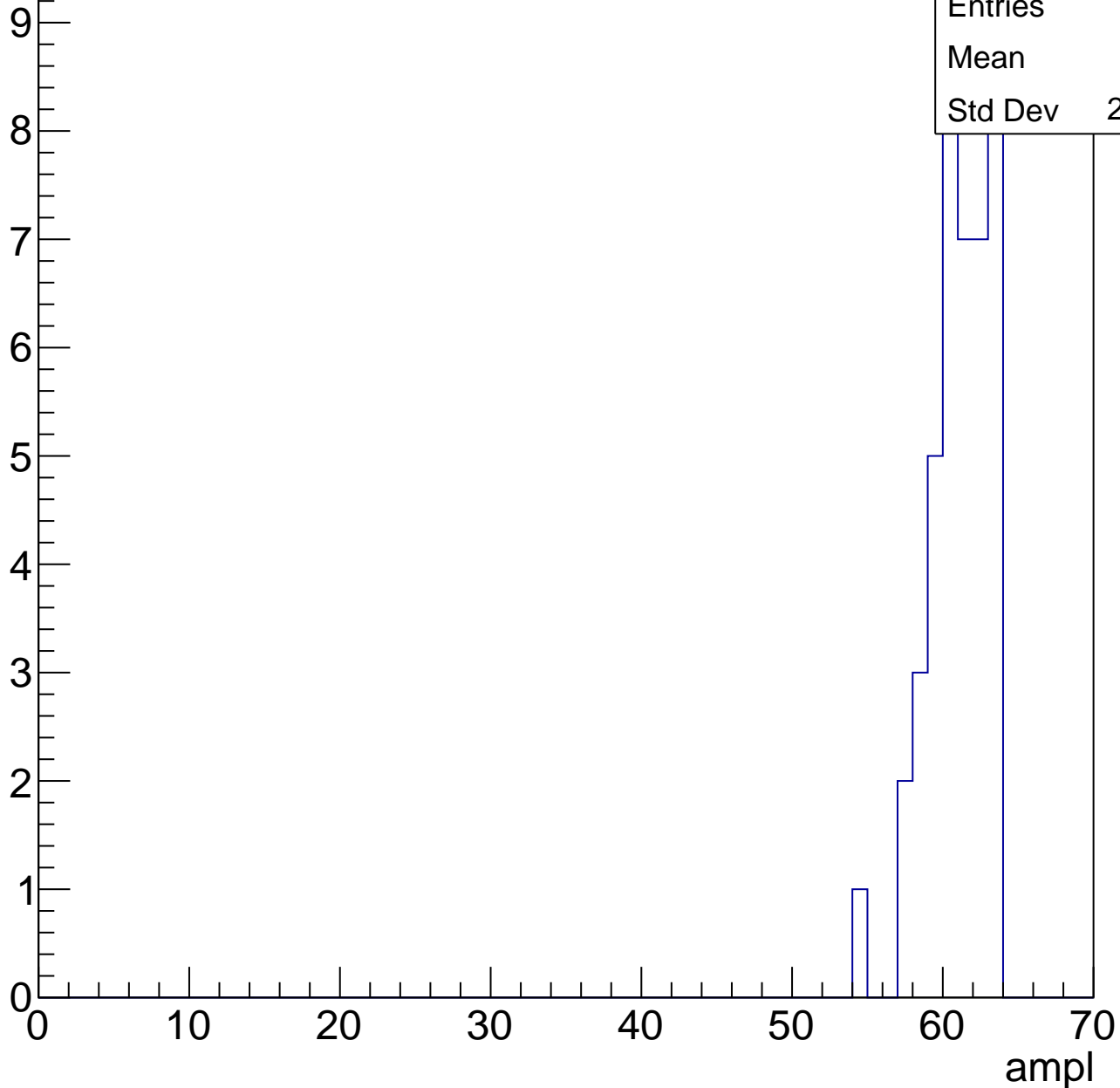
Entries	61
Mean	55.74
Std Dev	3.319



B1L003S, U13-ch100, adc5

calib_packv5_042523_0143.root, FC#13, port D2

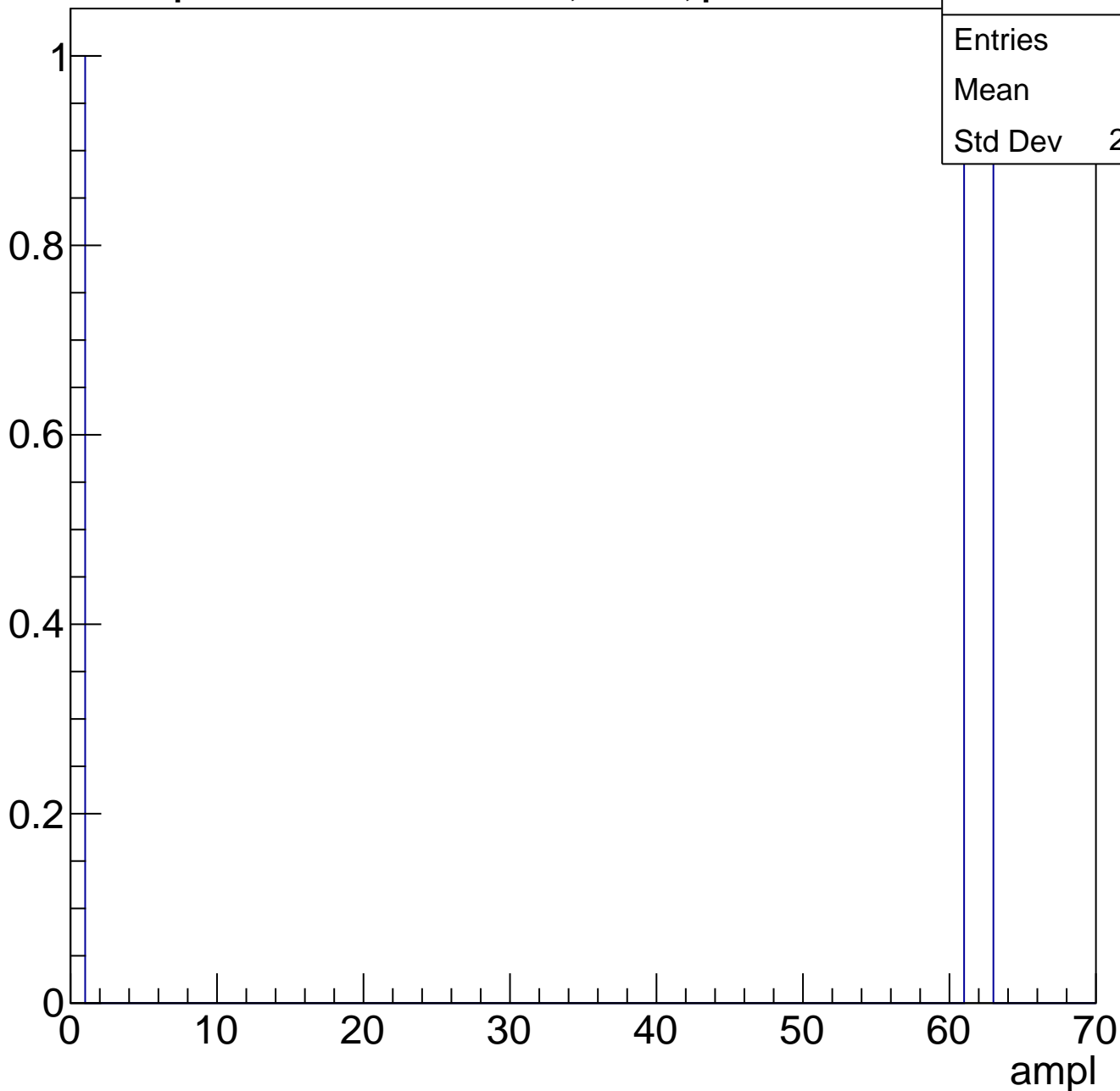
Entry



B1L003S, U13-ch100, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch100, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U13-ch101, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	29.94
Std Dev	4.8

Gaus mean : 30.8878

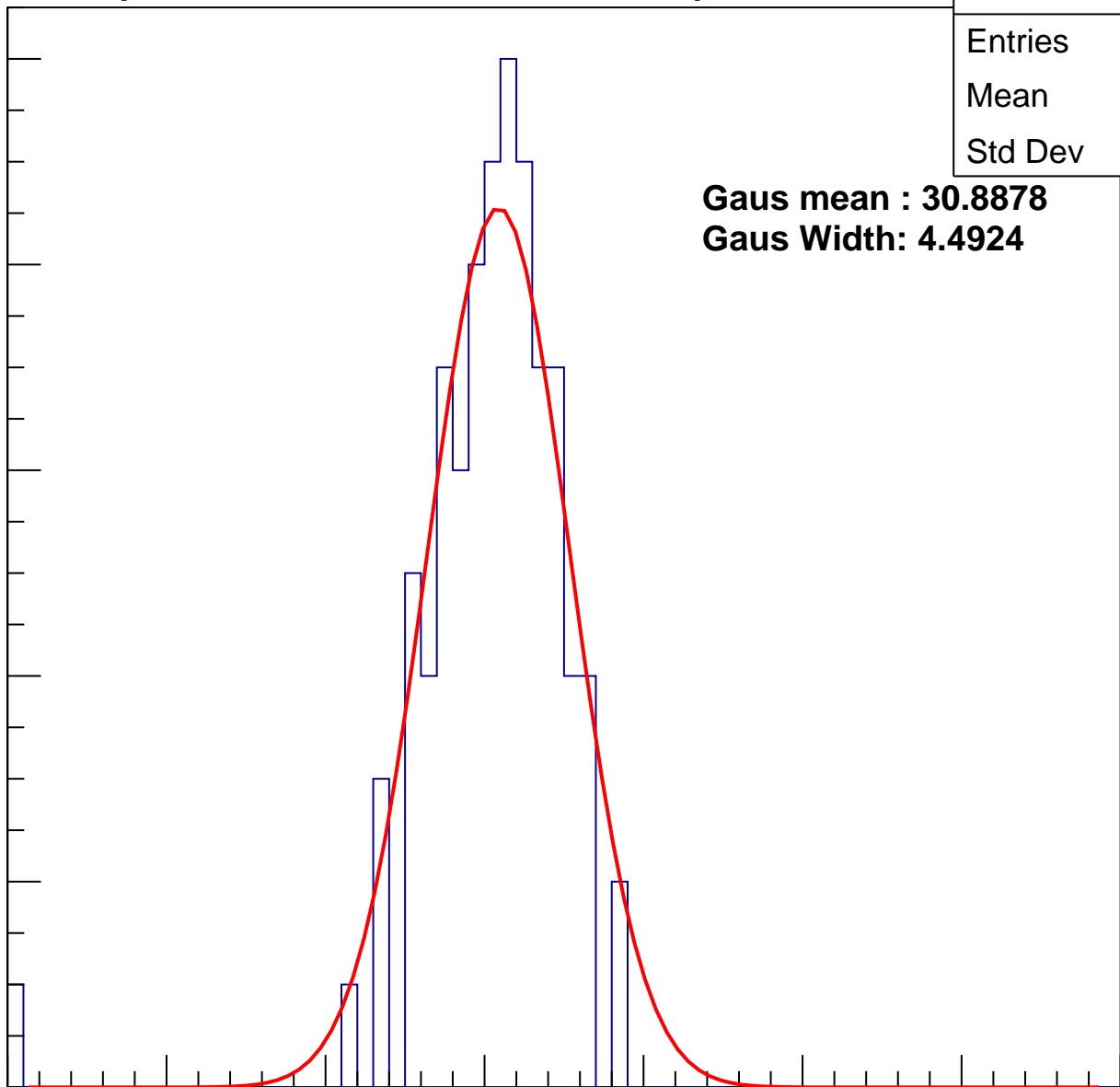
Gaus Width: 4.4924

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch101, adc1

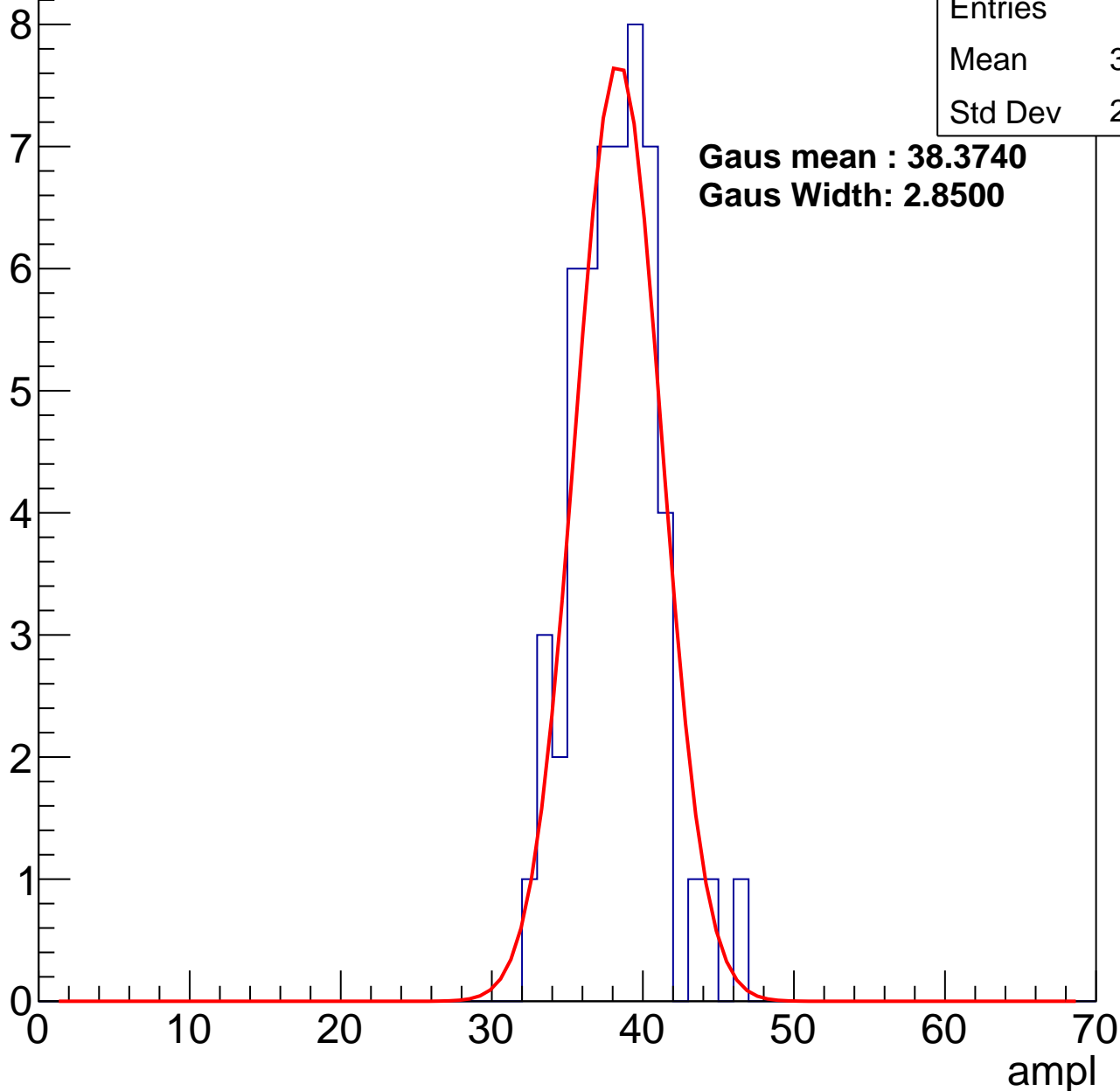
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	37.76
Std Dev	2.795

Gaus mean : 38.3740

Gaus Width: 2.8500



B1L003S, U13-ch101, adc2

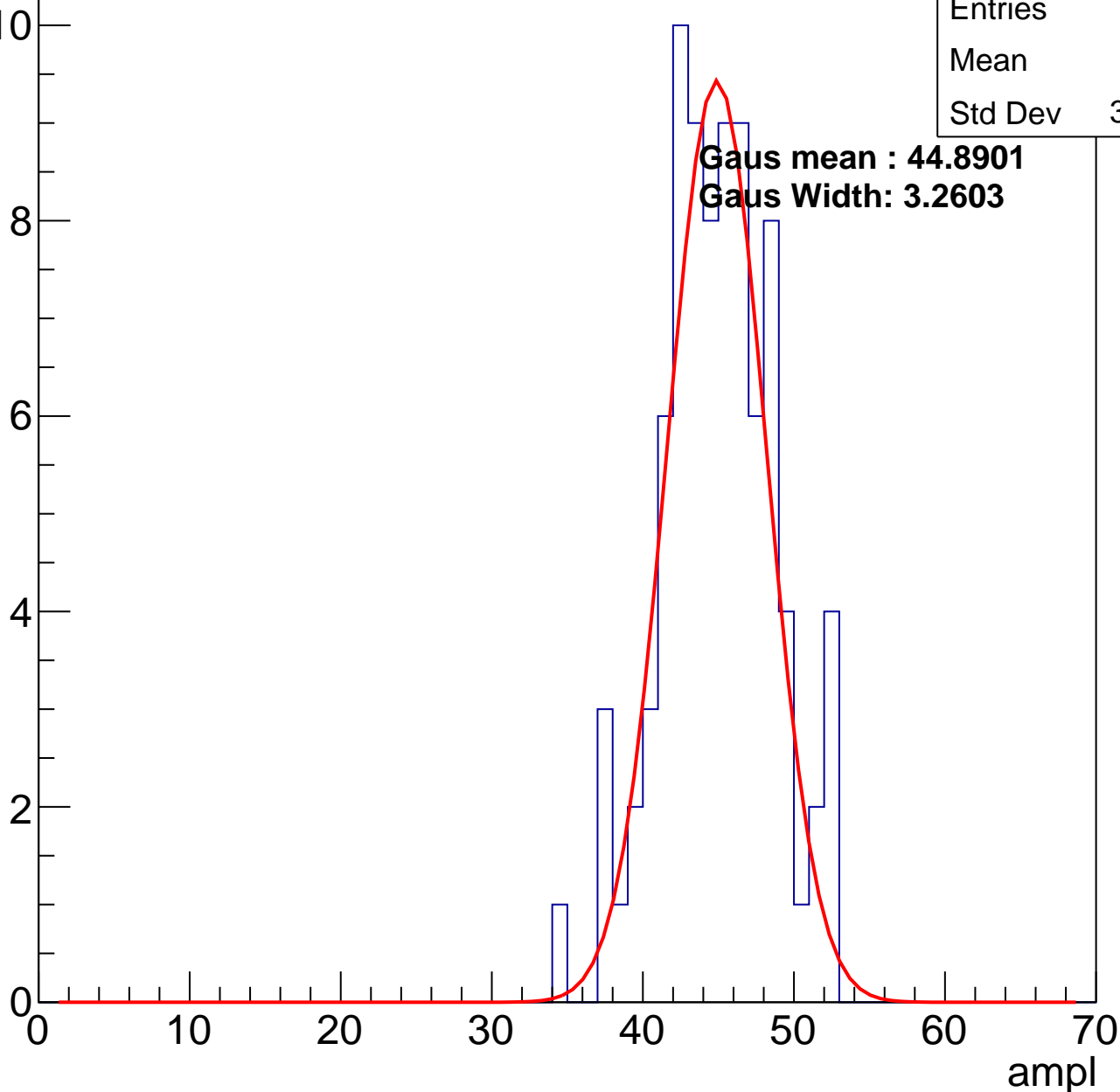
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	44.5
Std Dev	3.703

Gaus mean : 44.8901

Gaus Width: 3.2603

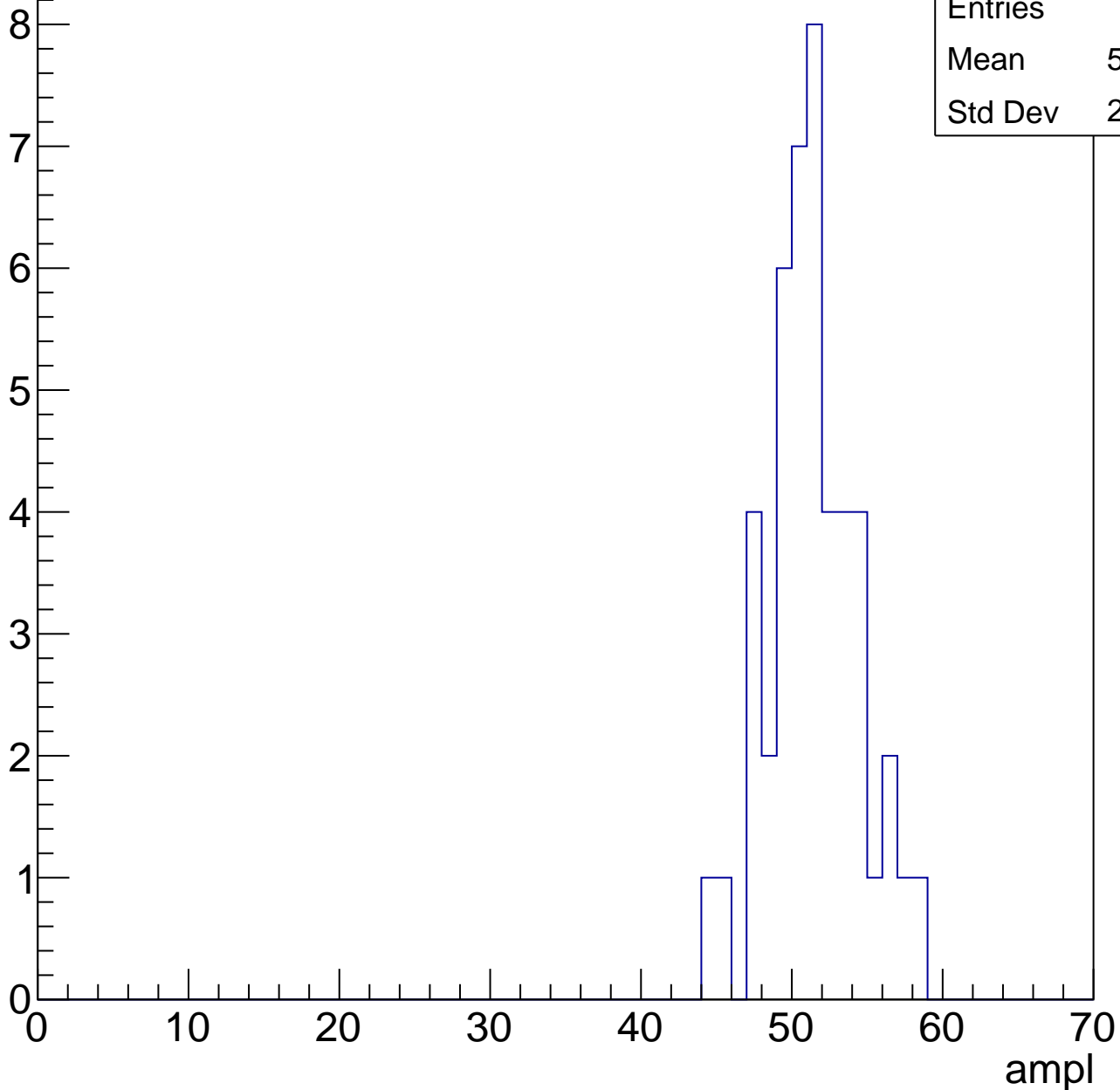


B1L003S, U13-ch101, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	50.93
Std Dev	2.966



B1L003S, U13-ch101, adc4

calib_packv5_042523_0143.root, FC#13, port D2

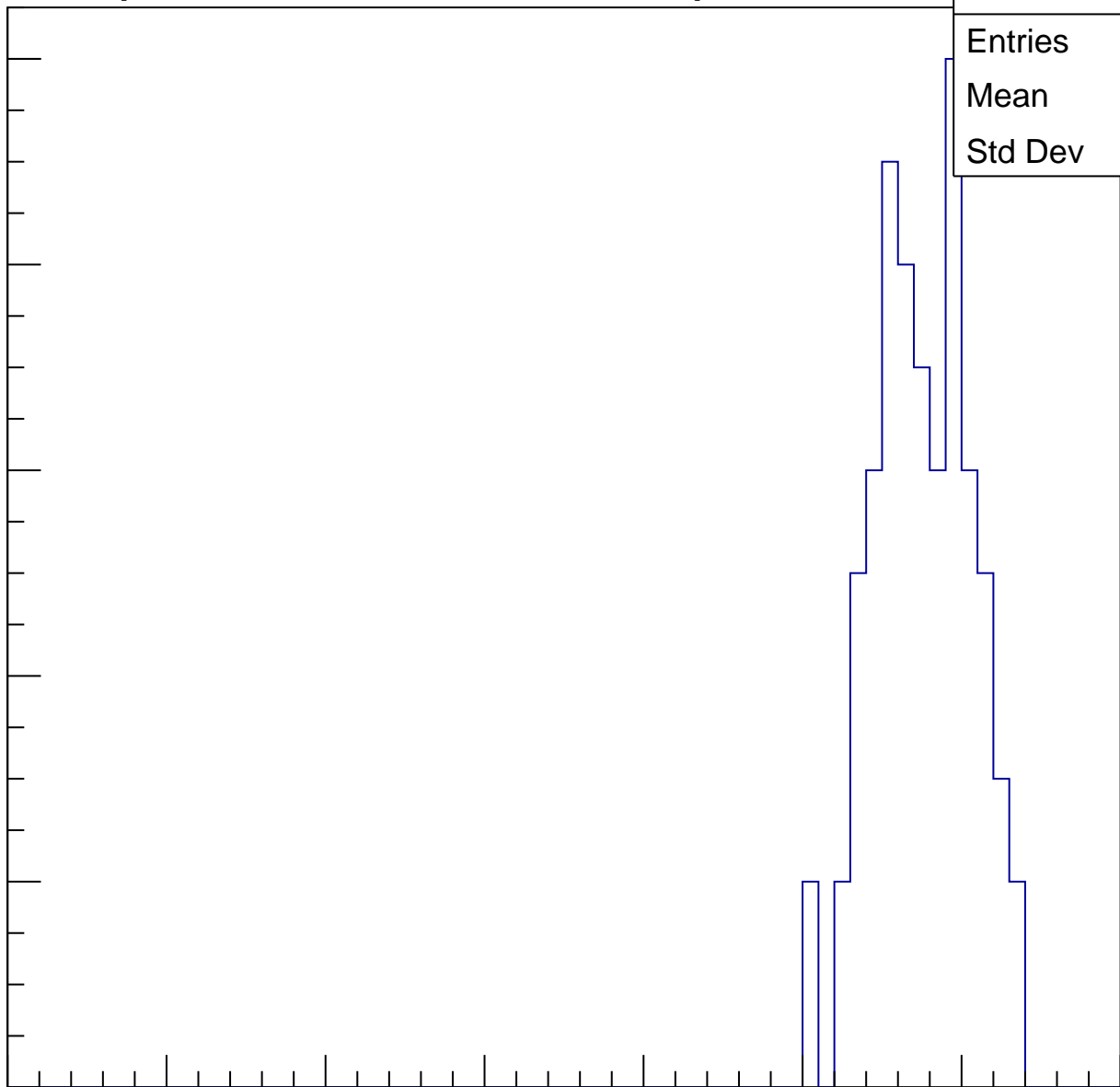
Entries	71
Mean	57.04
Std Dev	3.032

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

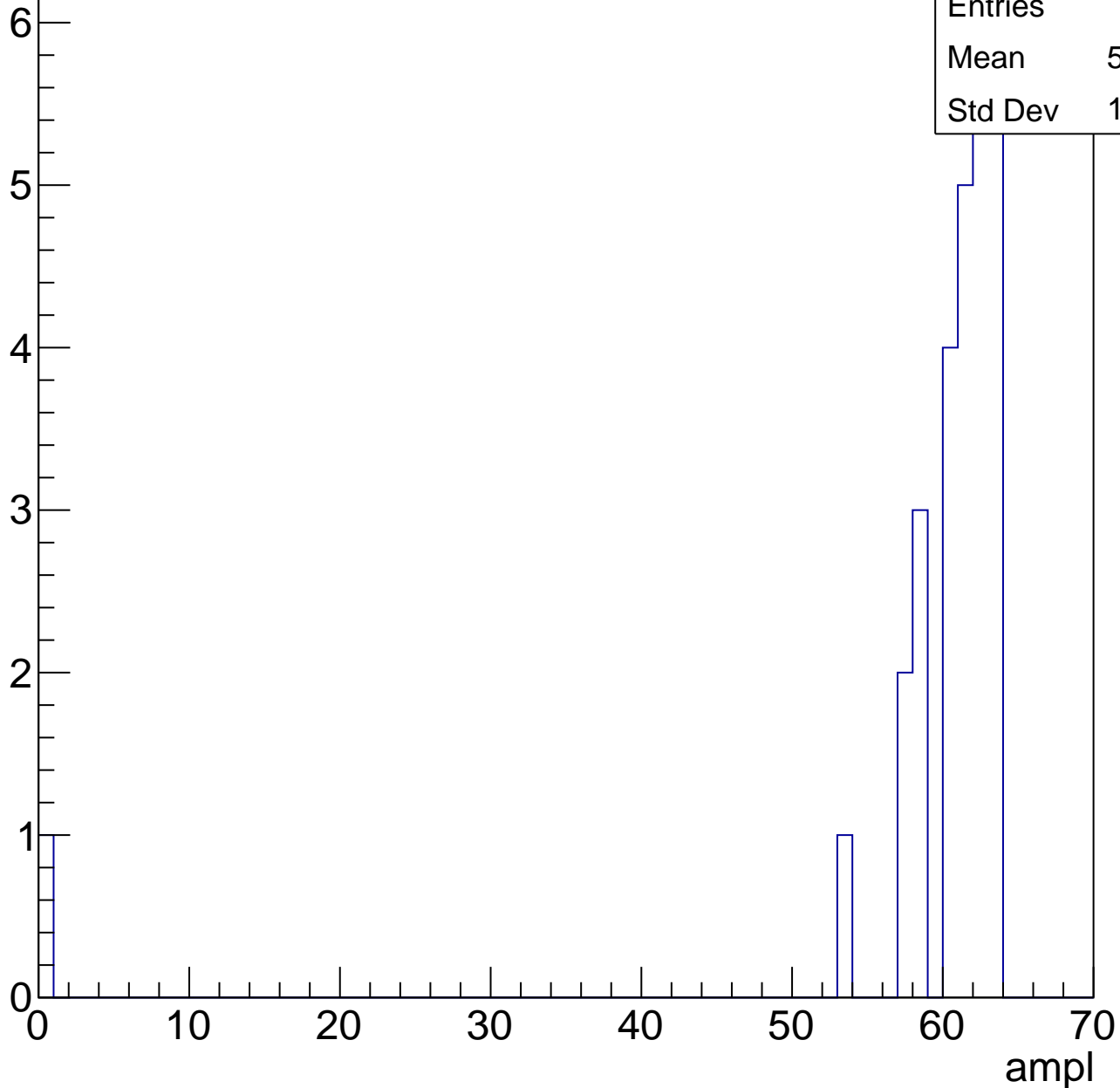


B1L003S, U13-ch101, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	28
Mean	58.43
Std Dev	11.48



B1L003S, U13-ch101, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch101, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch102, adc0

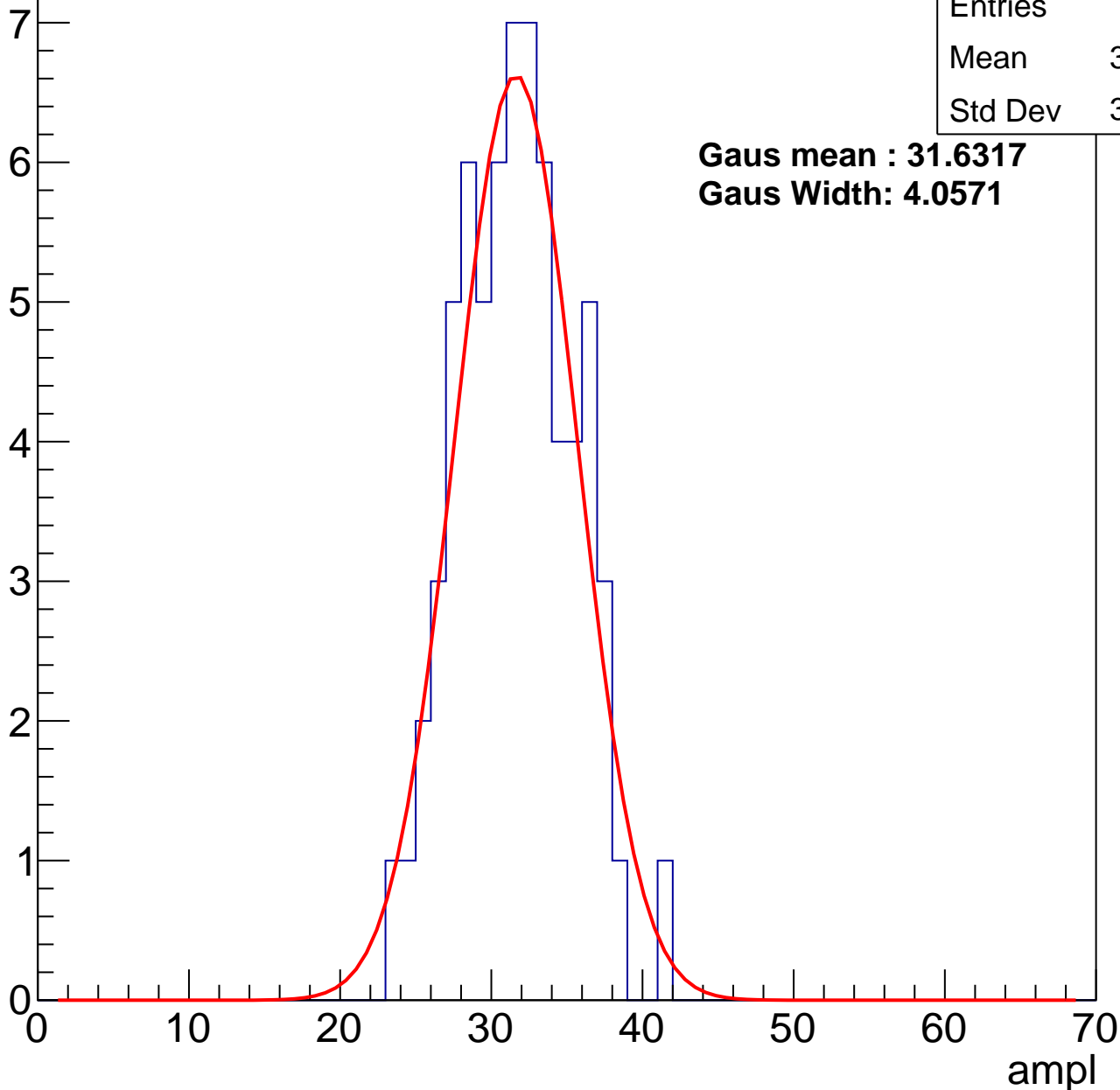
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	31.16
Std Dev	3.728

Gaus mean : 31.6317

Gaus Width: 4.0571



B1L003S, U13-ch102, adc1

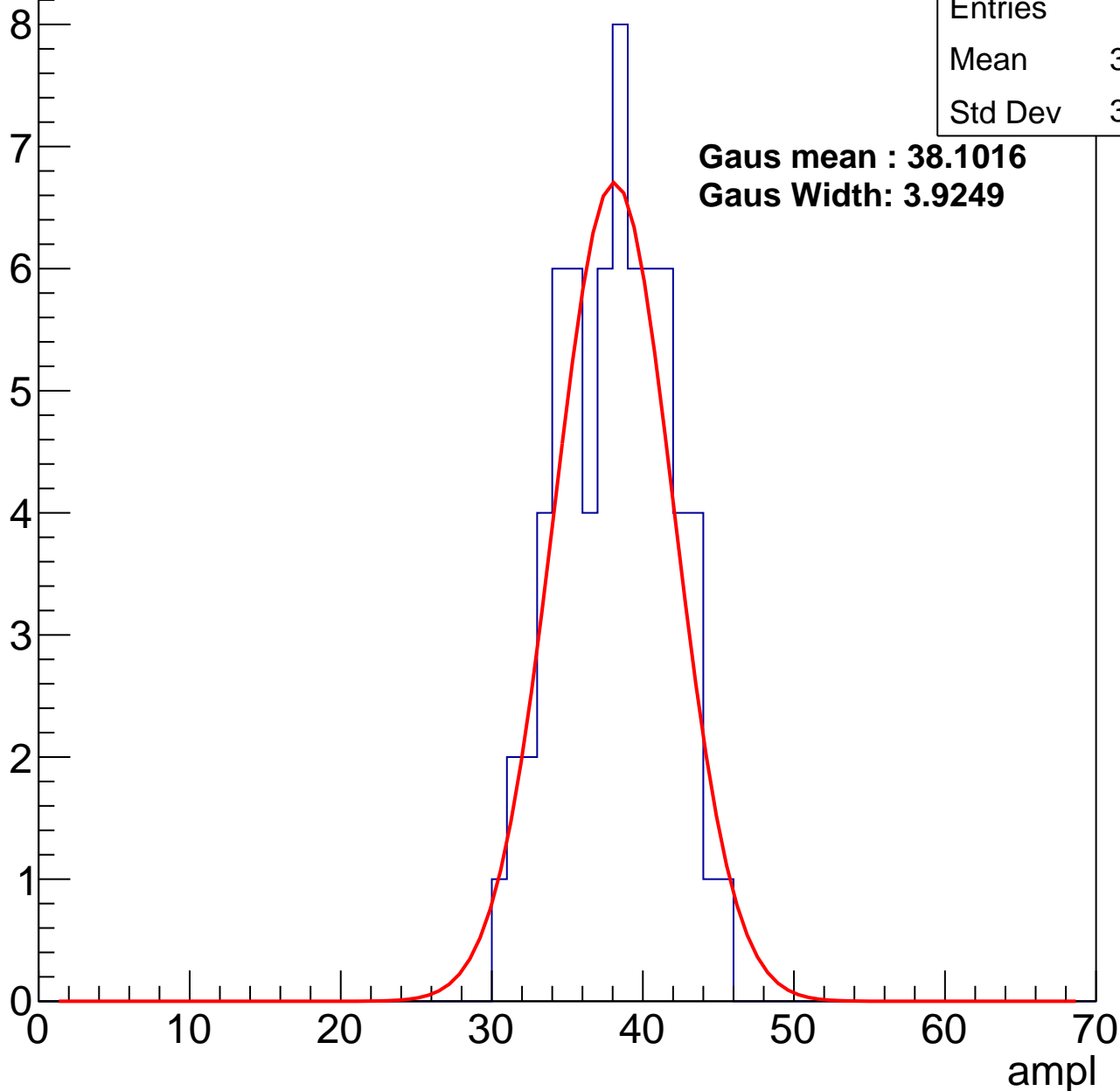
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.63
Std Dev	3.519

Gaus mean : 38.1016

Gaus Width: 3.9249



B1L003S, U13-ch102, adc2

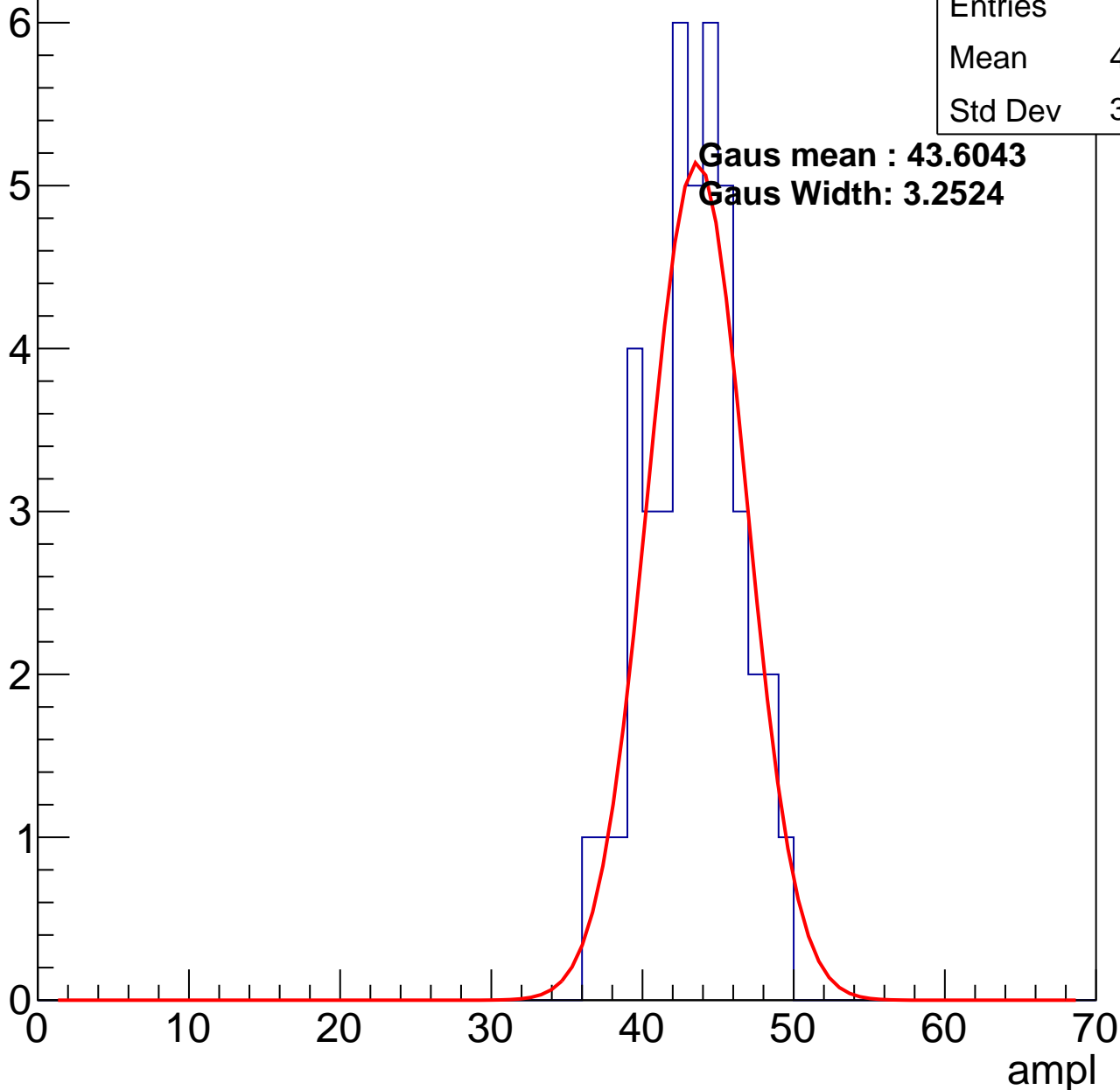
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	42.86
Std Dev	3.008

Gaus mean : 43.6043

Gaus Width: 3.2524



B1L003S, U13-ch102, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	48.6
Std Dev	2.881

Entry

10

8

6

4

2

0

0

10

20

30

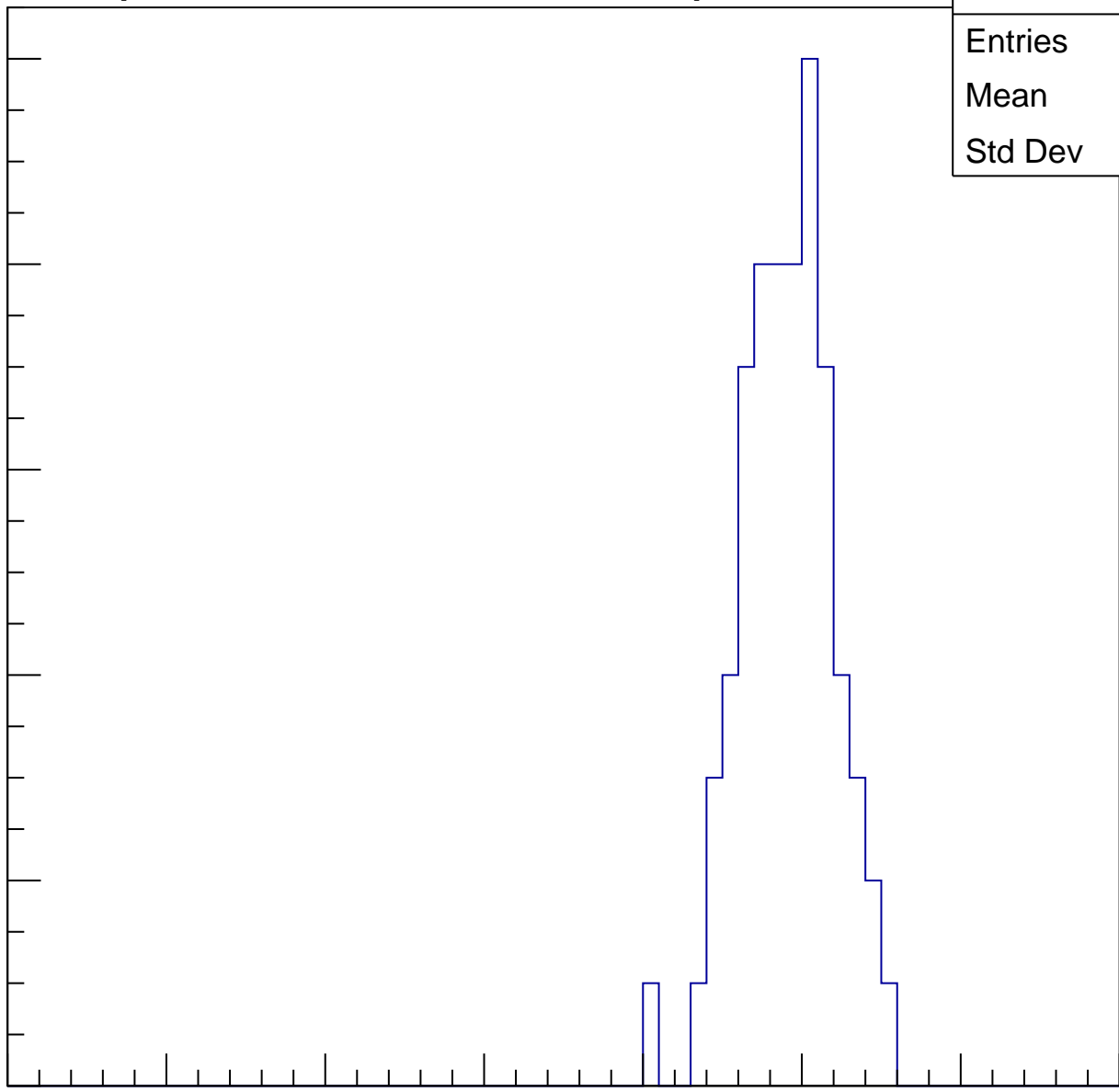
40

50

60

70

ampl

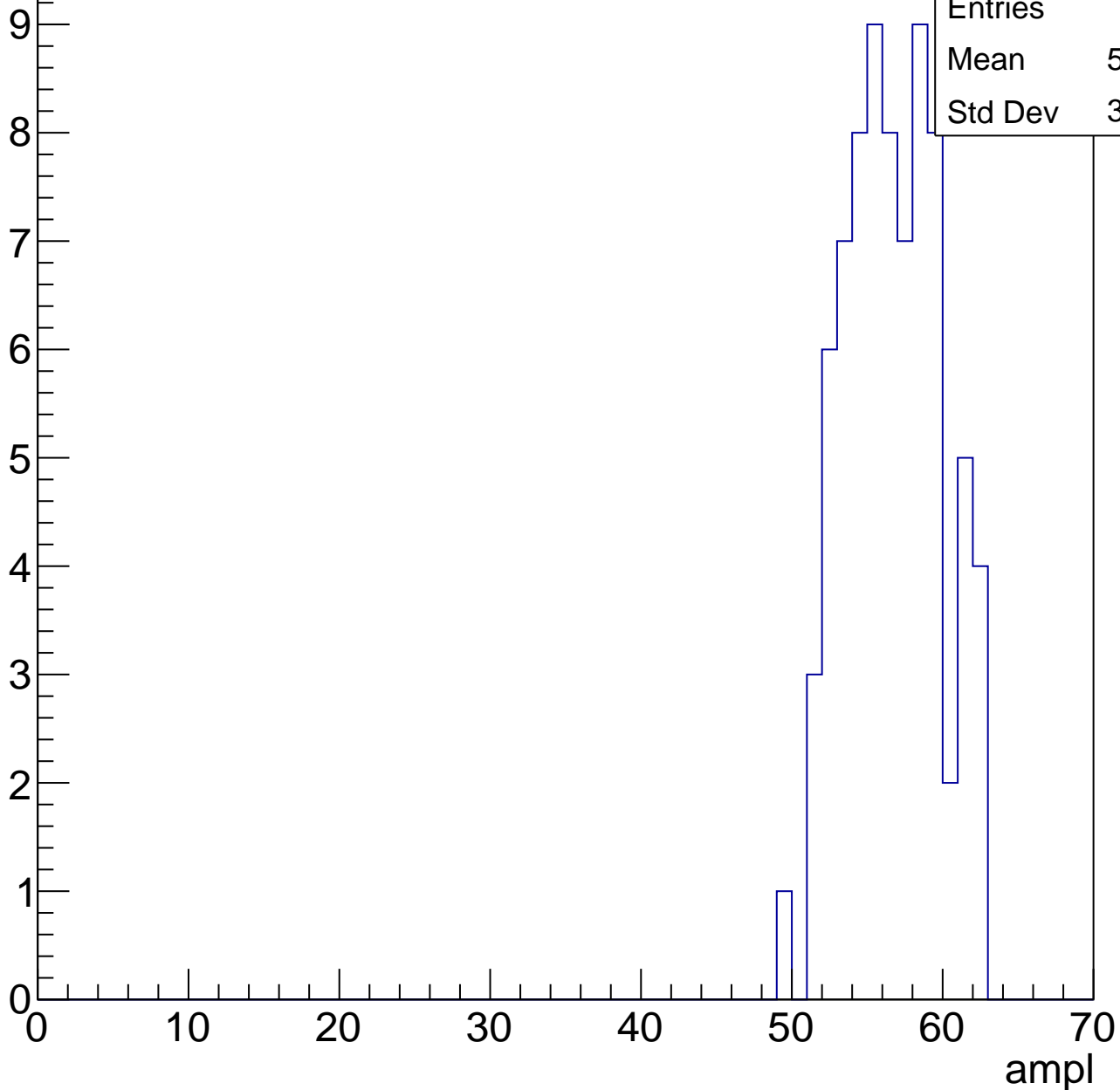


B1L003S, U13-ch102, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	56.18
Std Dev	3.095

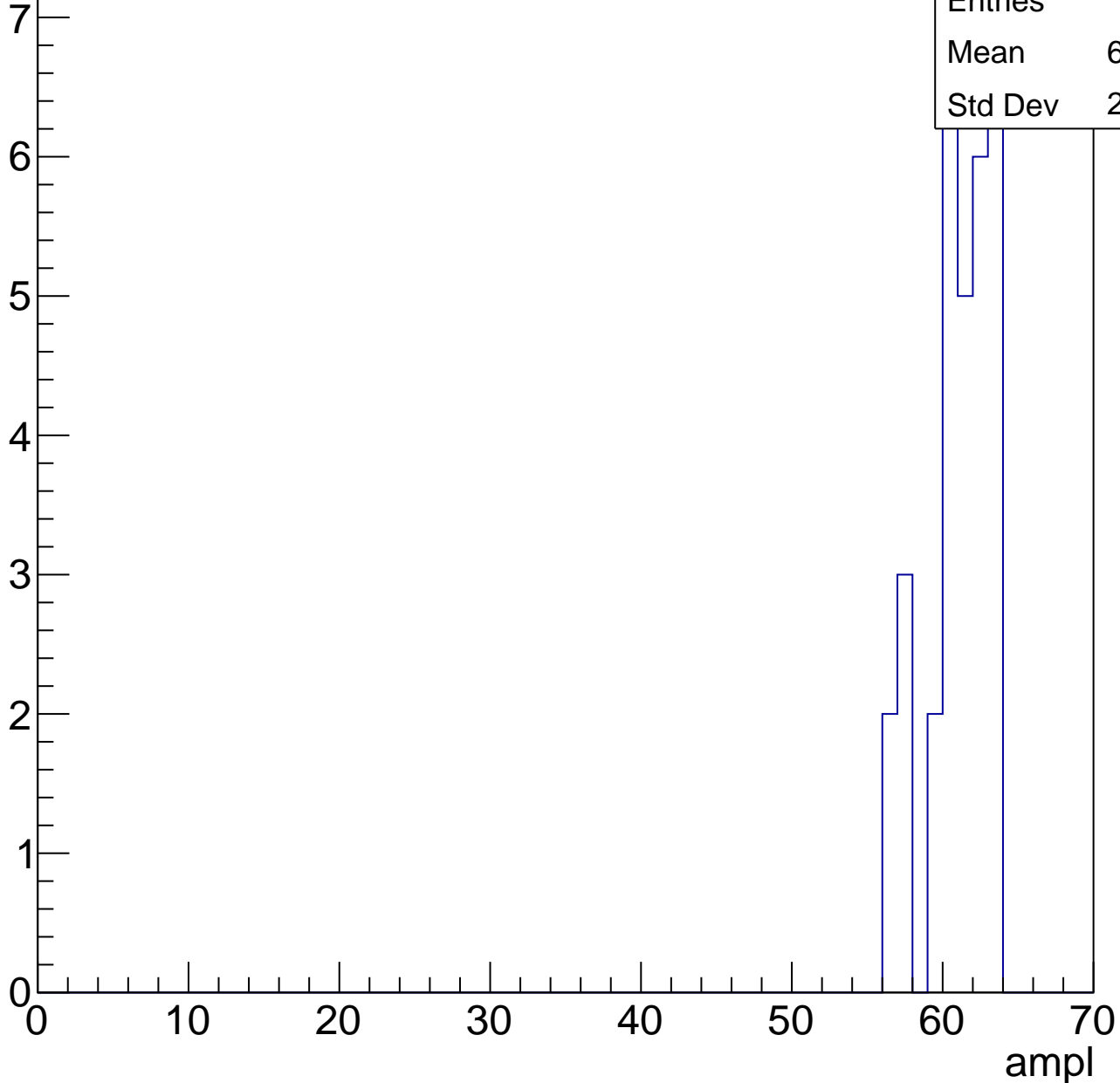


B1L003S, U13-ch102, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

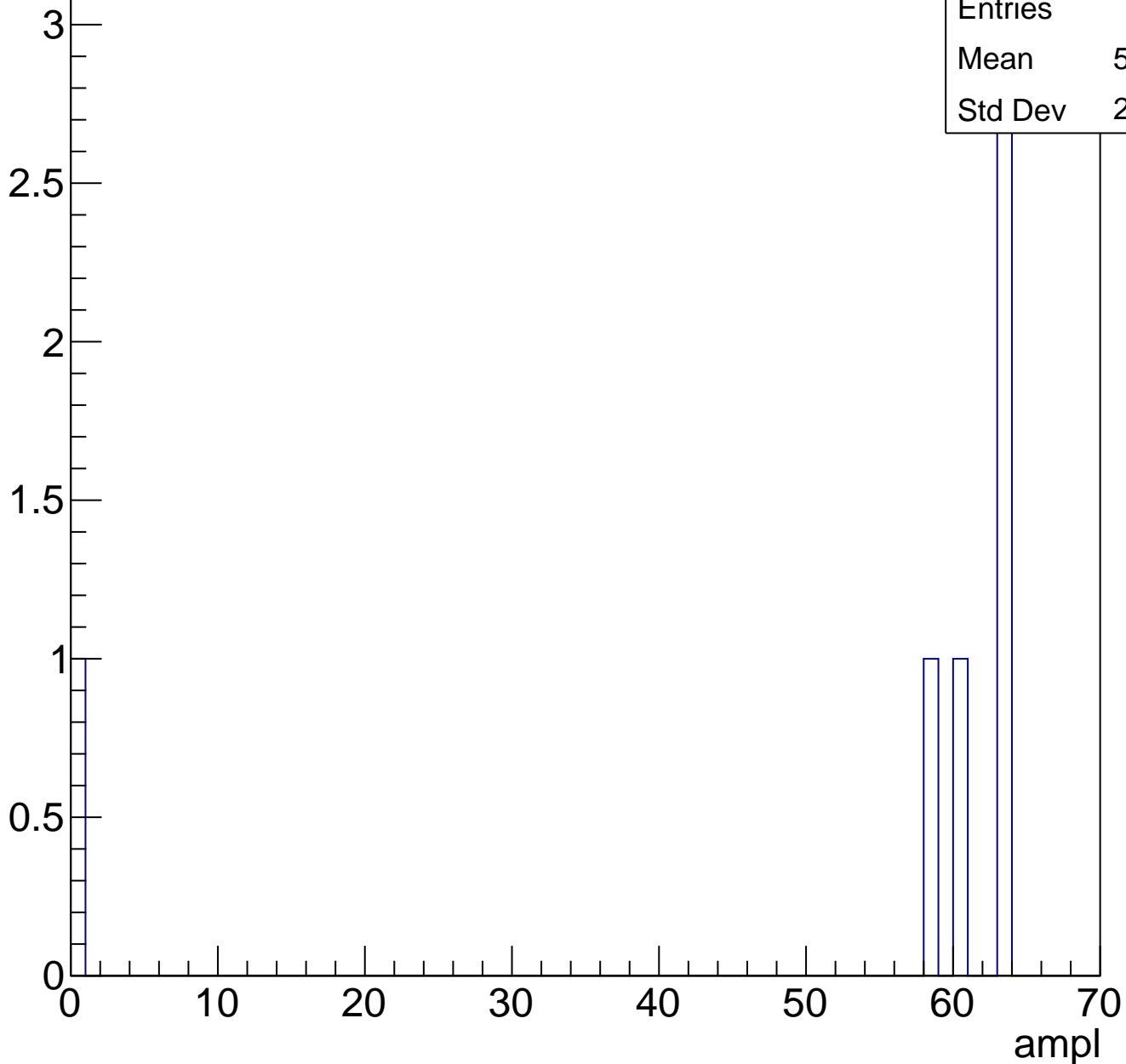
Entries	32
Mean	60.59
Std Dev	2.104



B1L003S, U13-ch102, adc6

calib_packv5_042523_0143.root, FC#13, port D2

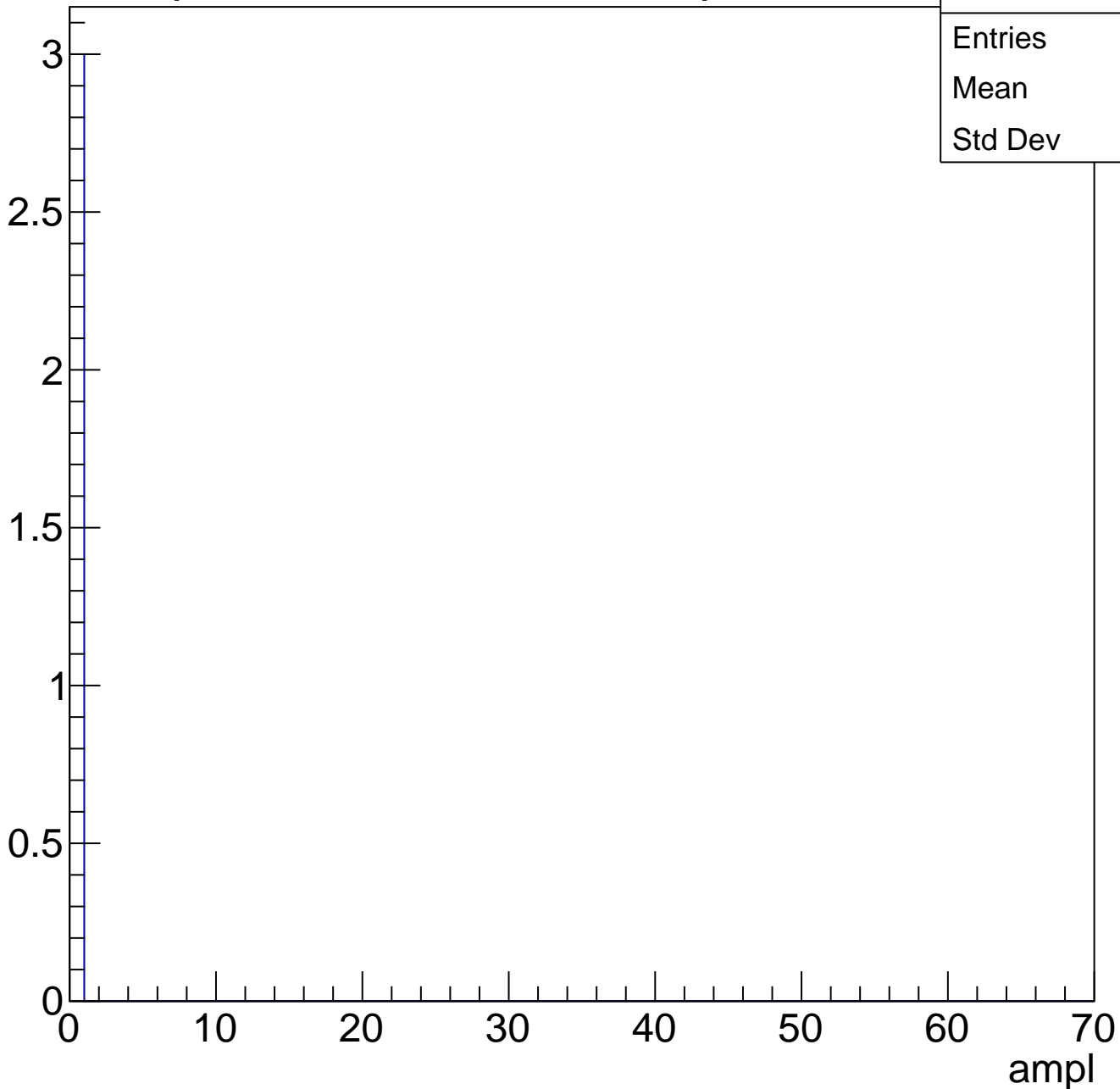
Entry



B1L003S, U13-ch102, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U13-ch103, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	32.05
Std Dev	3.588

Gaus mean : 33.0260

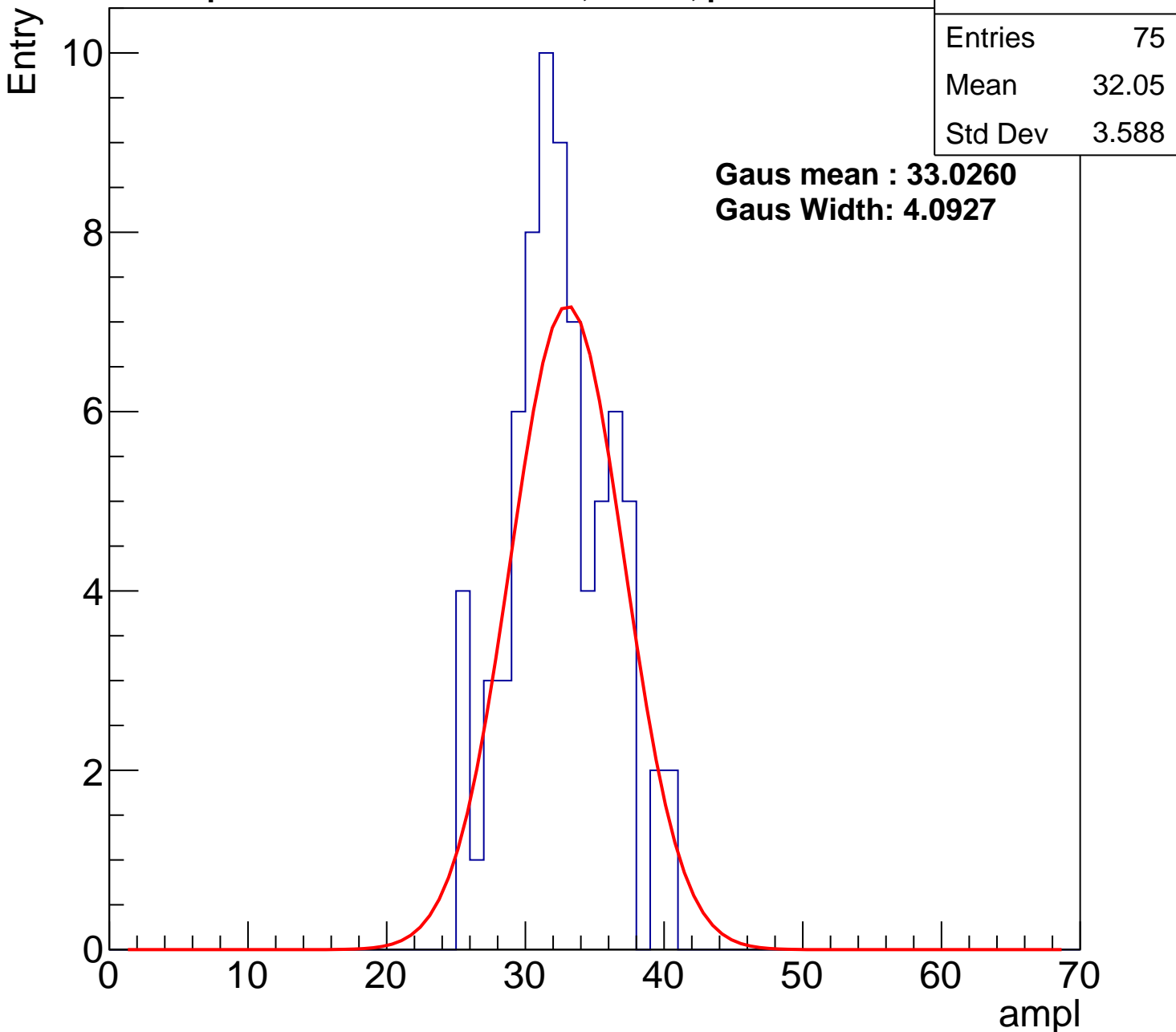
Gaus Width: 4.0927

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch103, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	39.25
Std Dev	3.675

Gaus mean : 39.4268

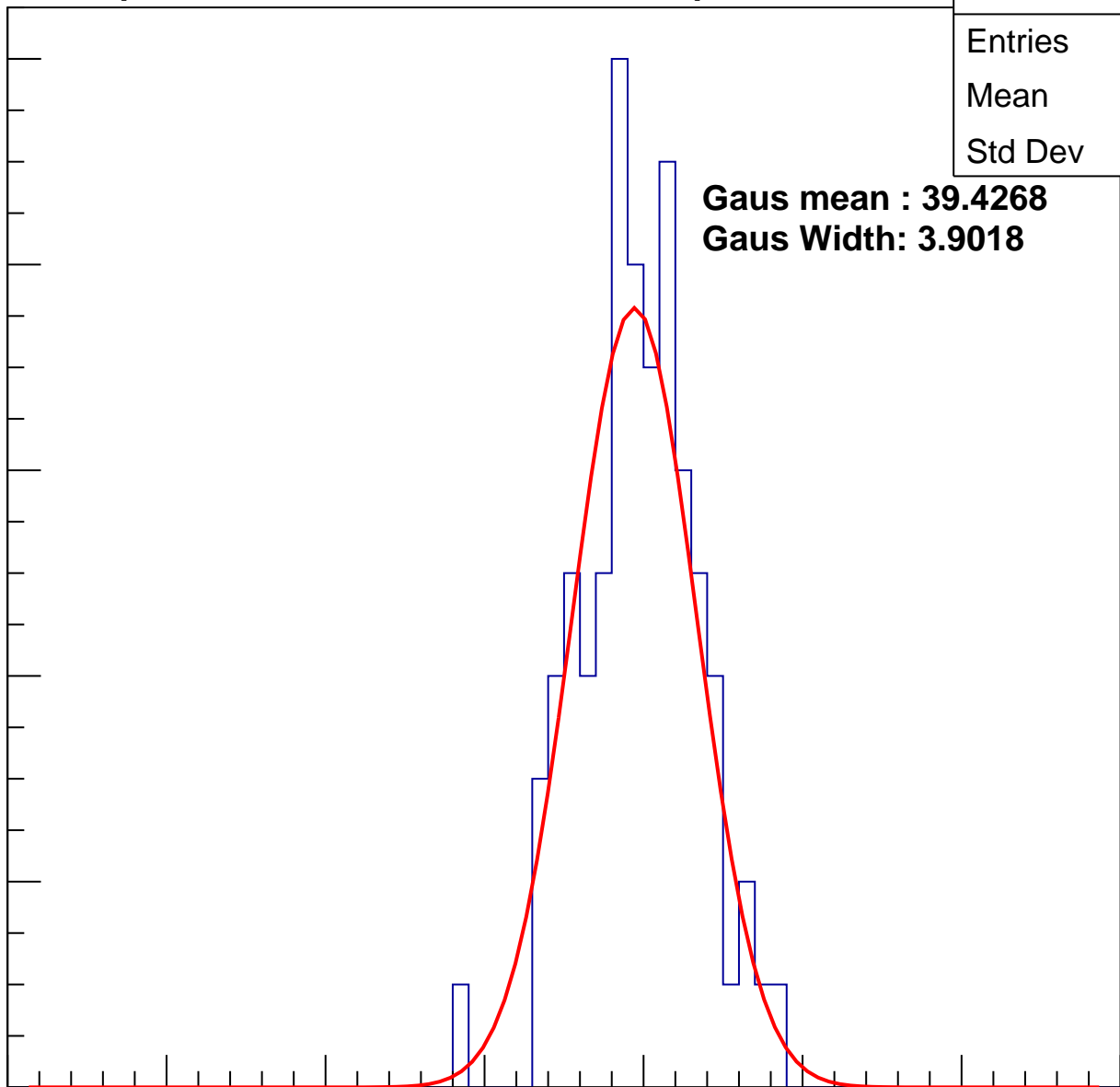
Gaus Width: 3.9018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch103, adc2

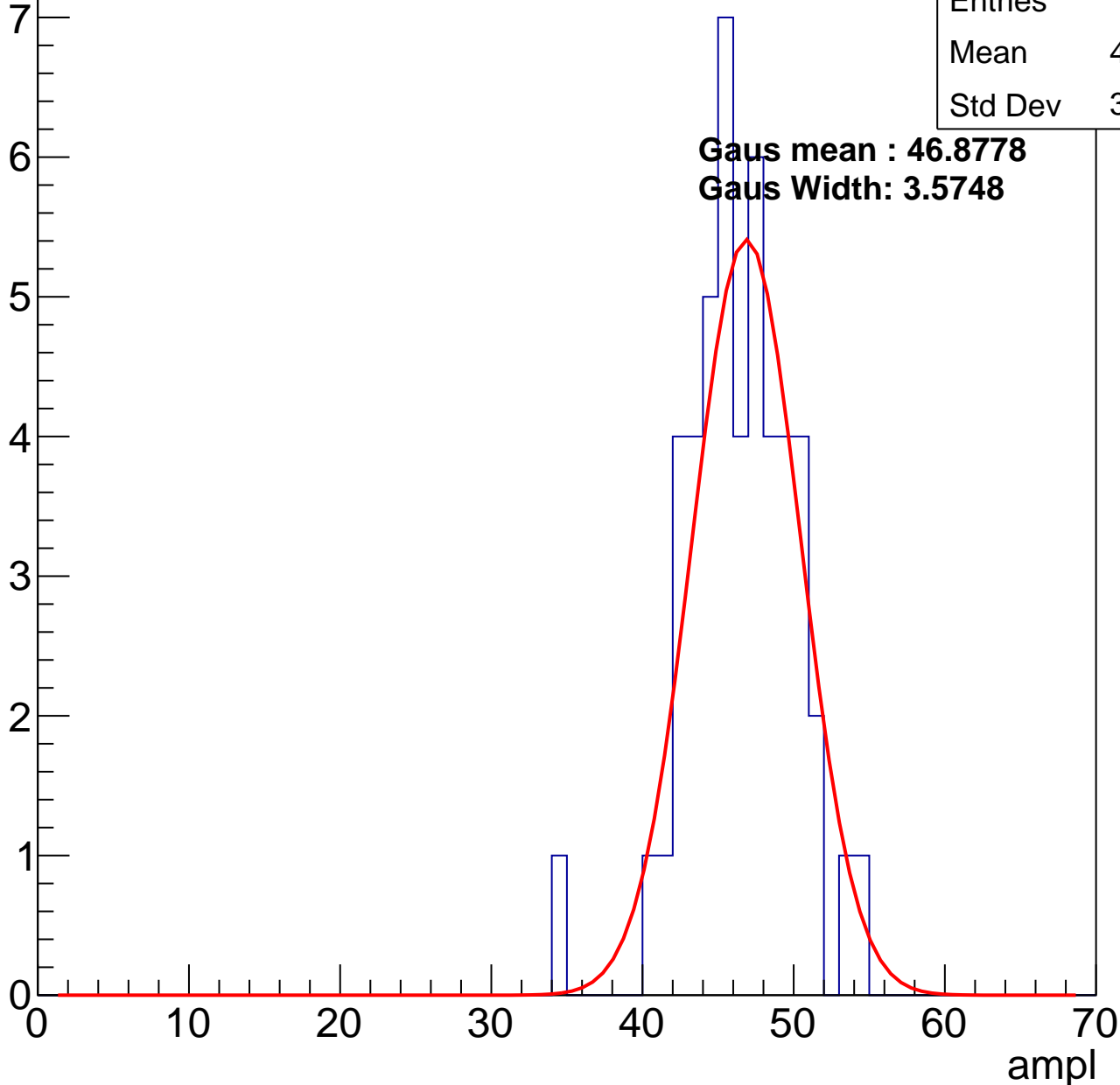
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	45.98
Std Dev	3.548

Gaus mean : 46.8778

Gaus Width: 3.5748

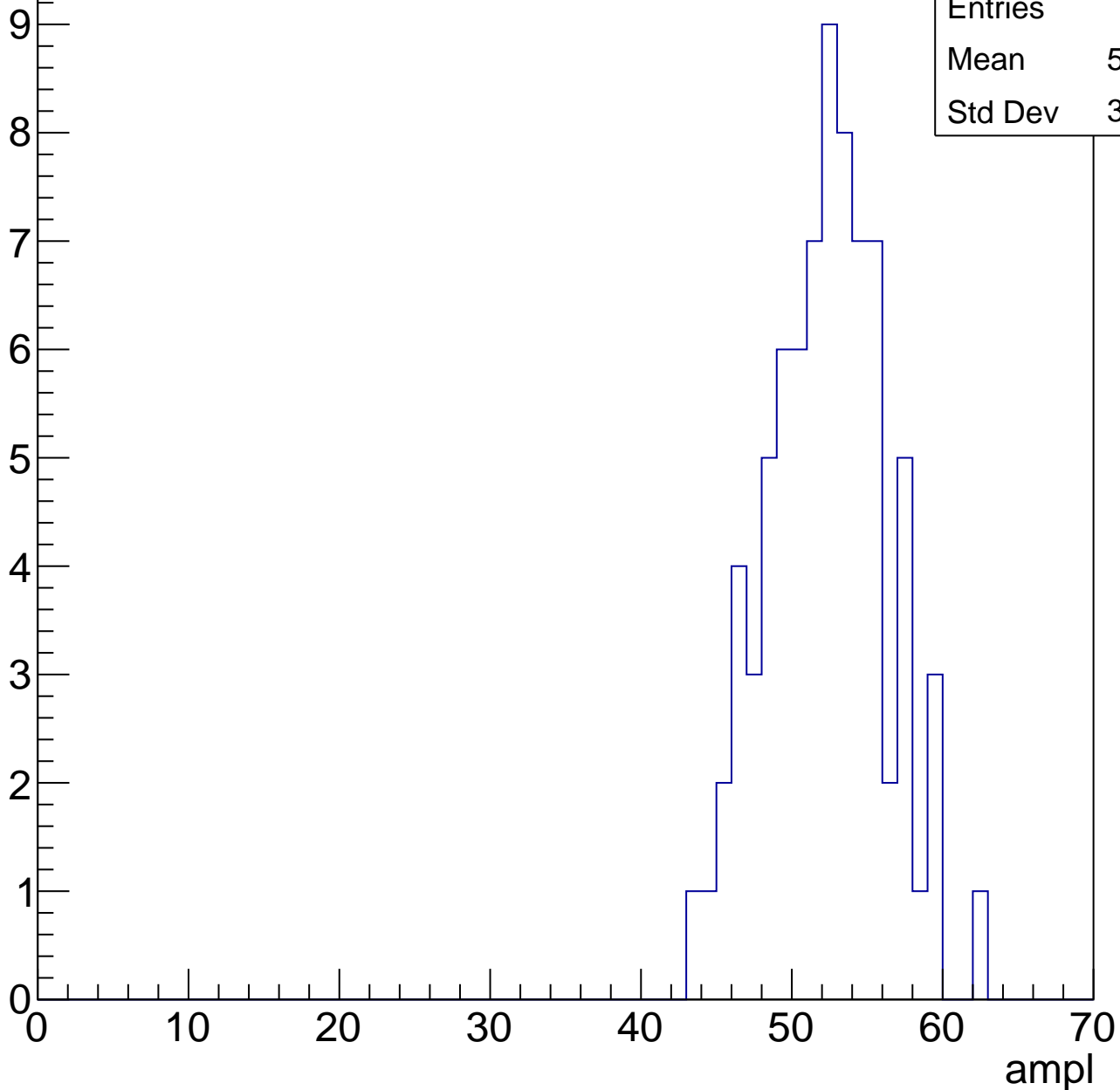


B1L003S, U13-ch103, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

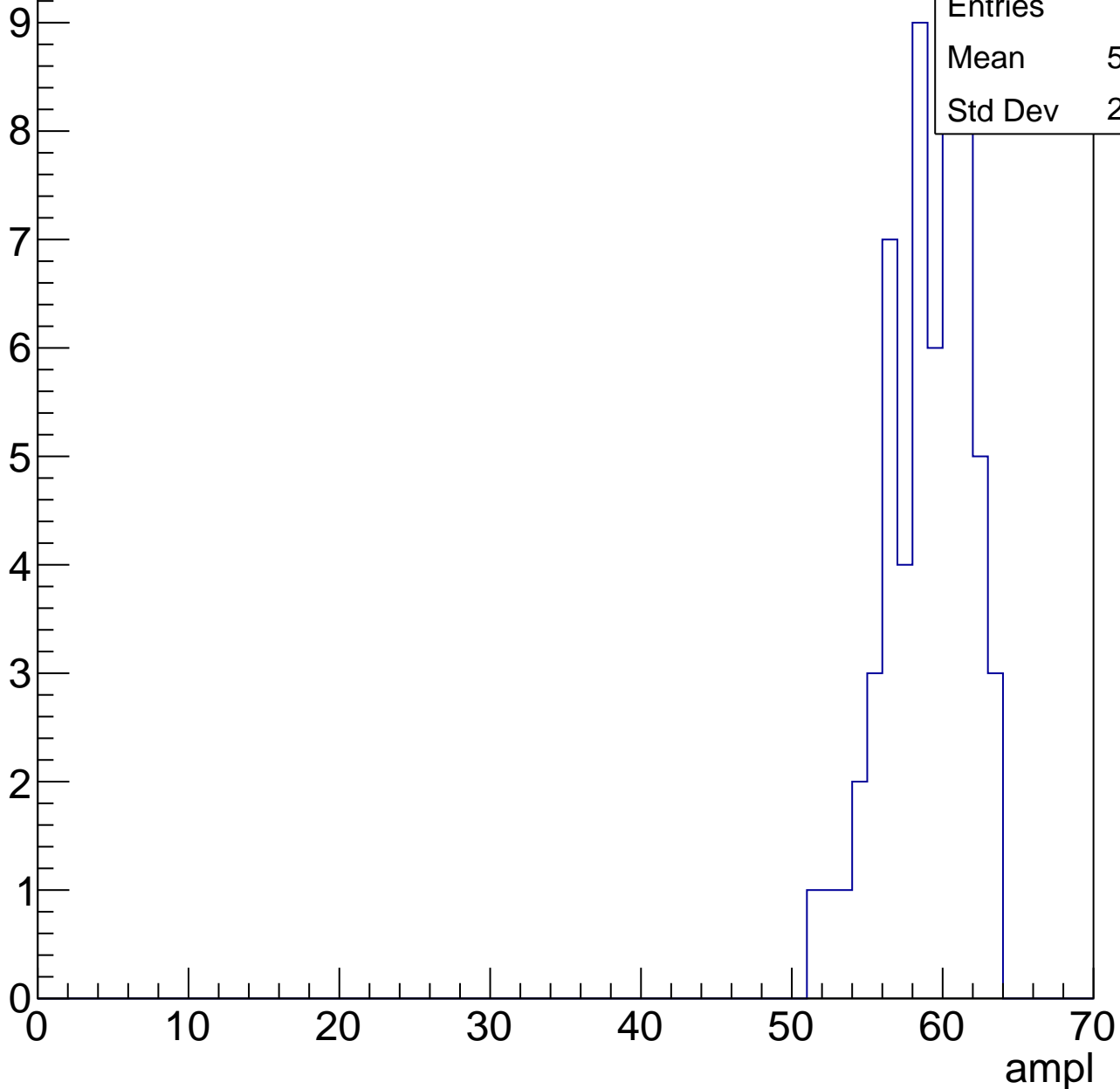
Entries	78
Mean	51.82
Std Dev	3.862



B1L003S, U13-ch103, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



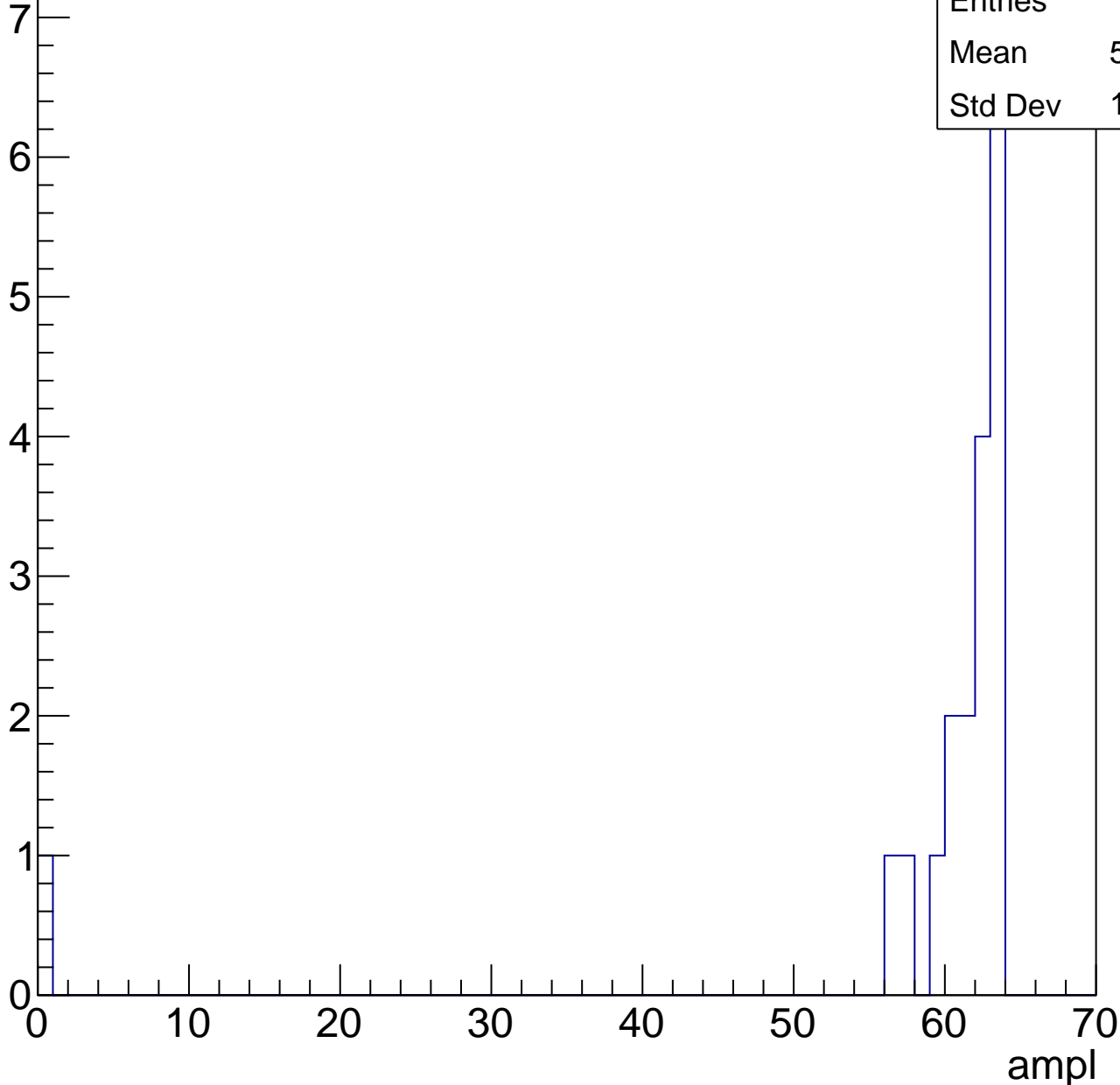
Entries	58
Mean	58.48
Std Dev	2.793

B1L003S, U13-ch103, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	58.05
Std Dev	13.83



B1L003S, U13-ch103, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch103, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U13-ch104, adc0

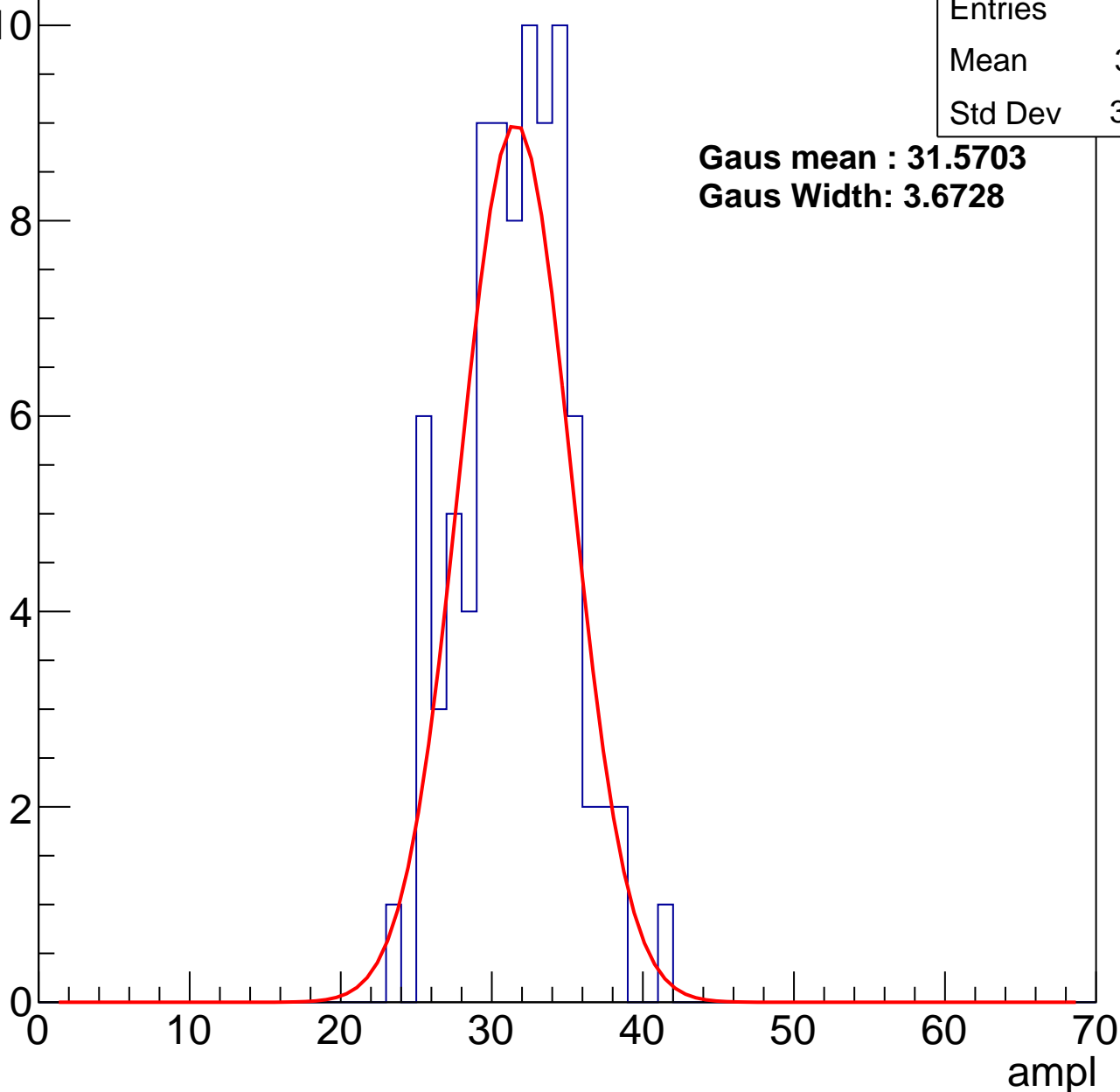
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	87
Mean	31.11
Std Dev	3.502

Gaus mean : 31.5703

Gaus Width: 3.6728



B1L003S, U13-ch104, adc1

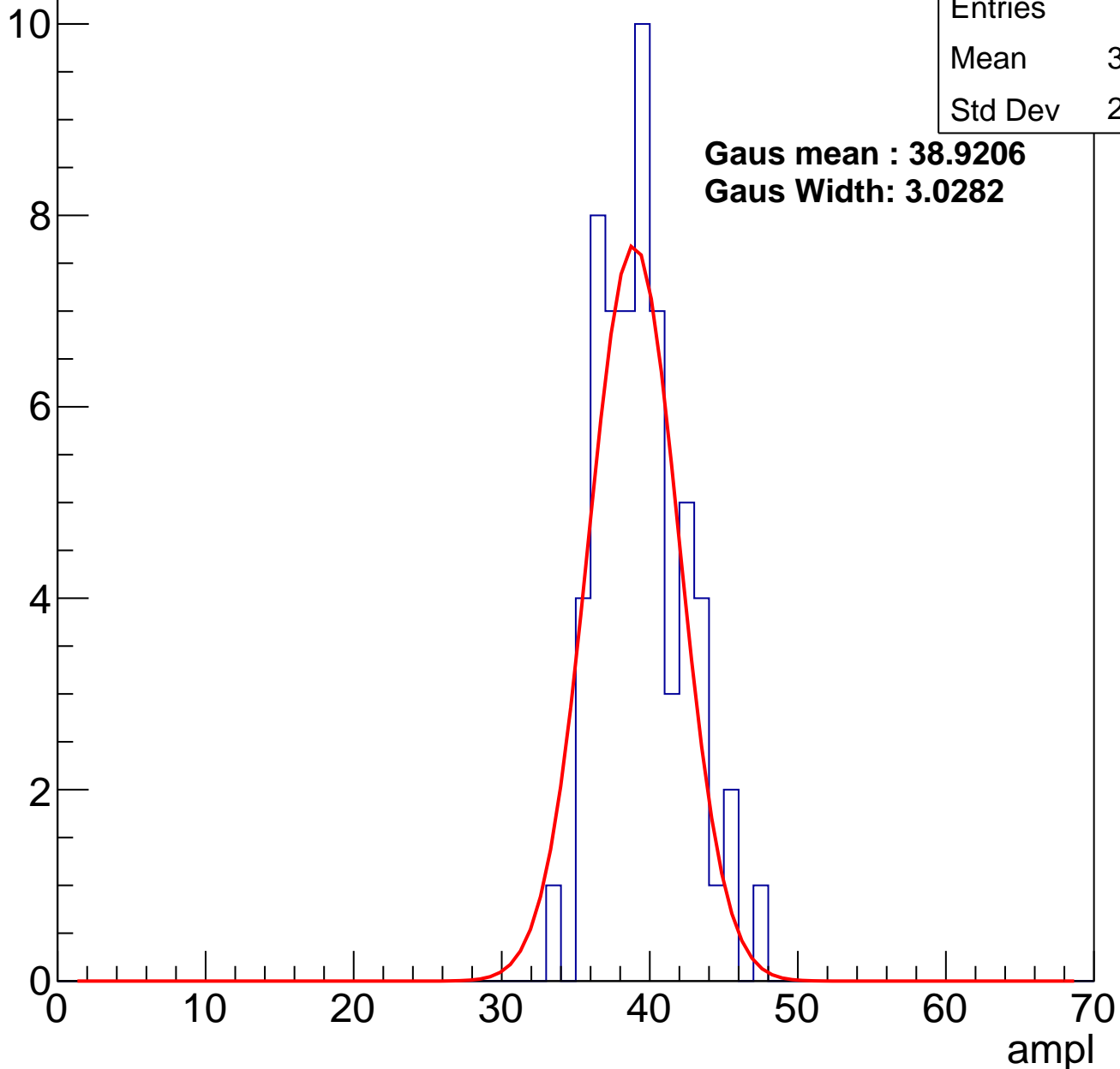
calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	39.03
Std Dev	2.875

Gaus mean : 38.9206

Gaus Width: 3.0282

Entry

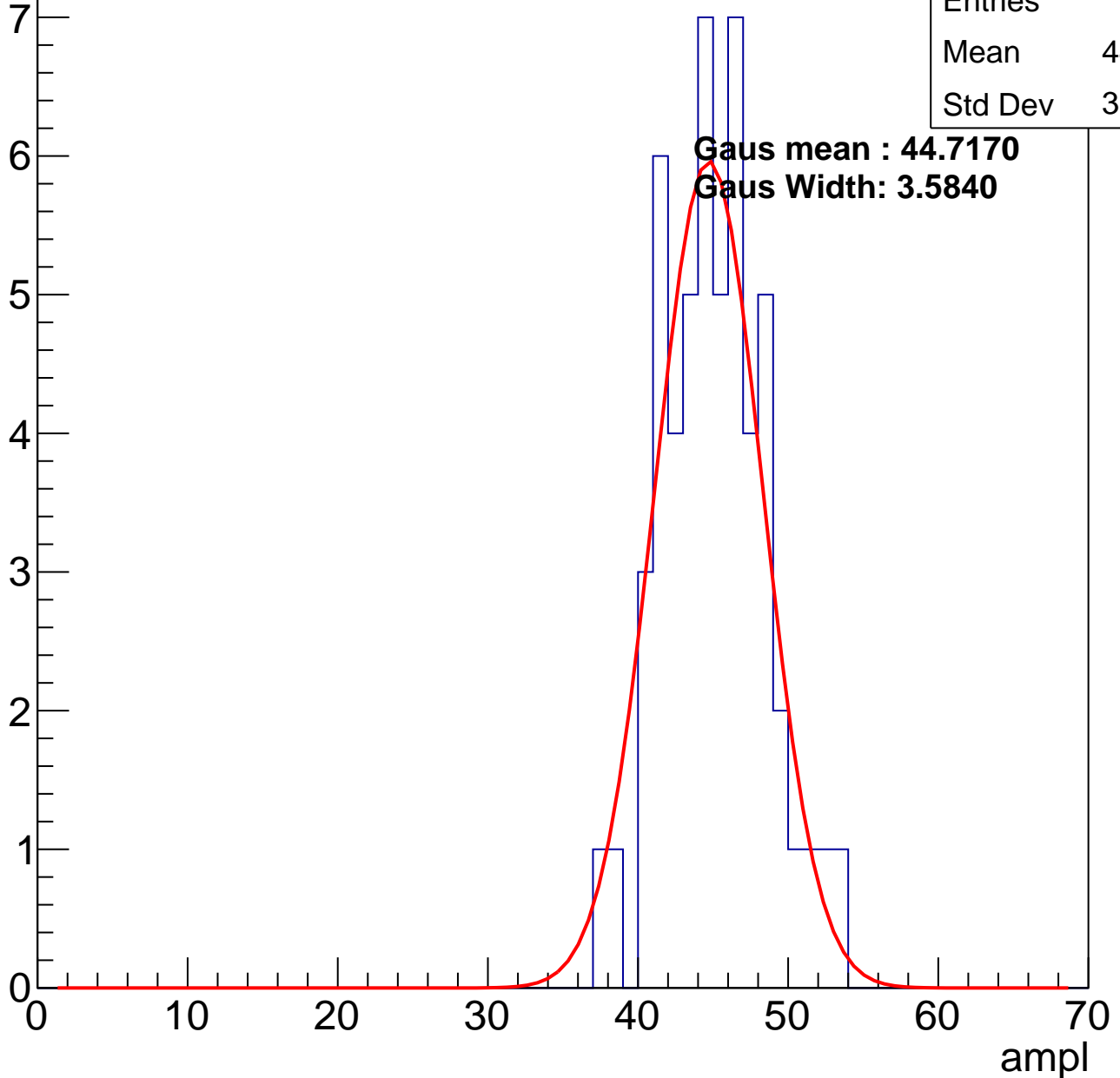


B1L003S, U13-ch104, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	44.65
Std Dev	3.373

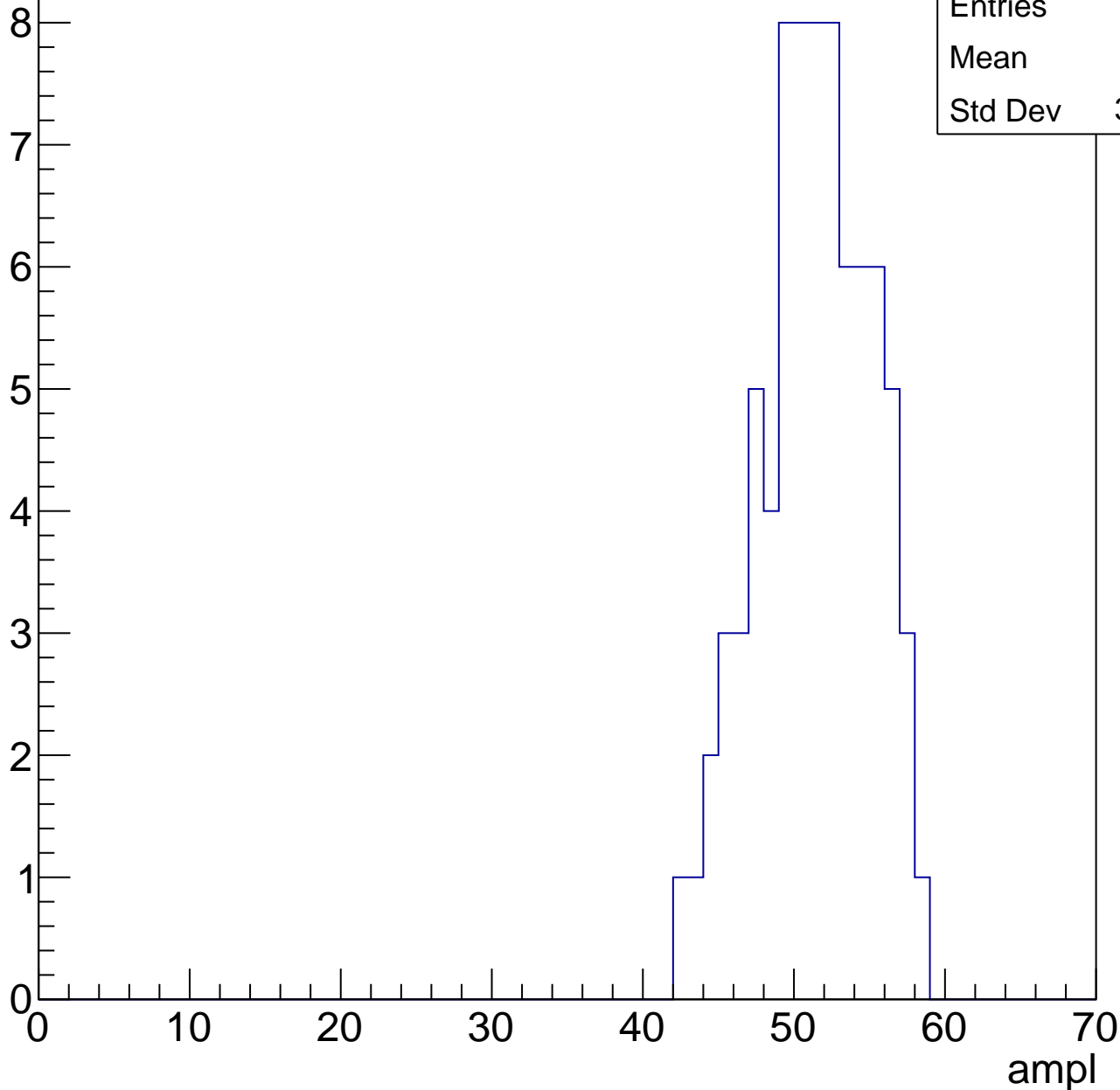


B1L003S, U13-ch104, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	50.9
Std Dev	3.661

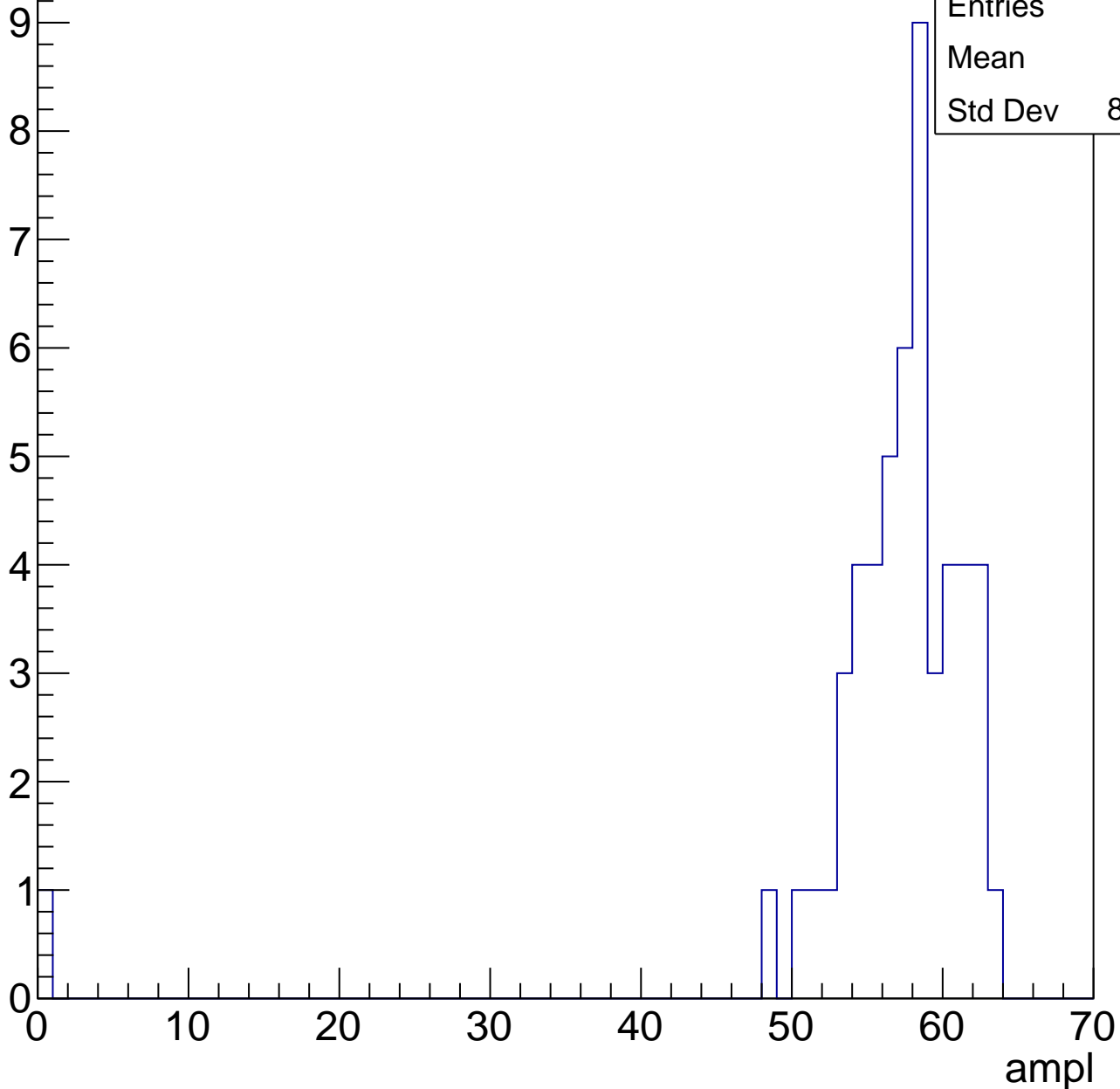


B1L003S, U13-ch104, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

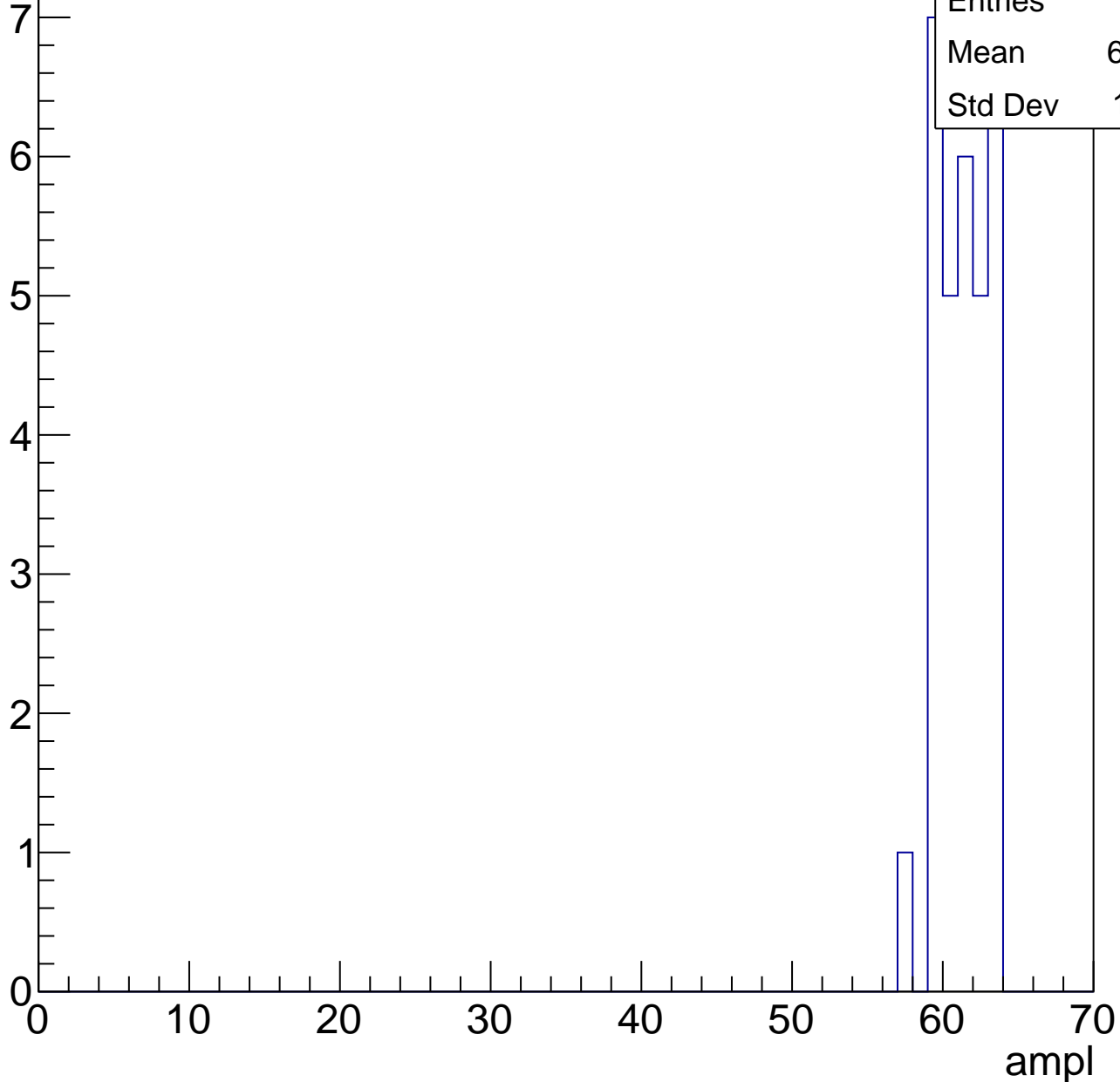
Entries	52
Mean	56
Std Dev	8.492



B1L003S, U13-ch104, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch104, adc6

calib_packv5_042523_0143.root, FC#13, port D2

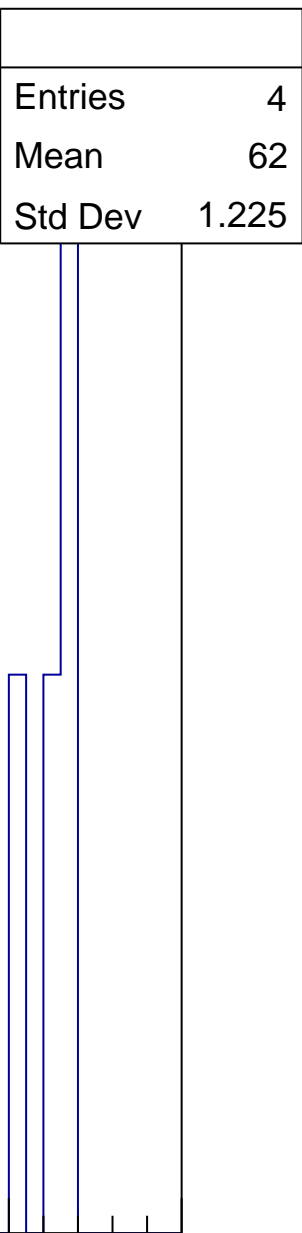
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

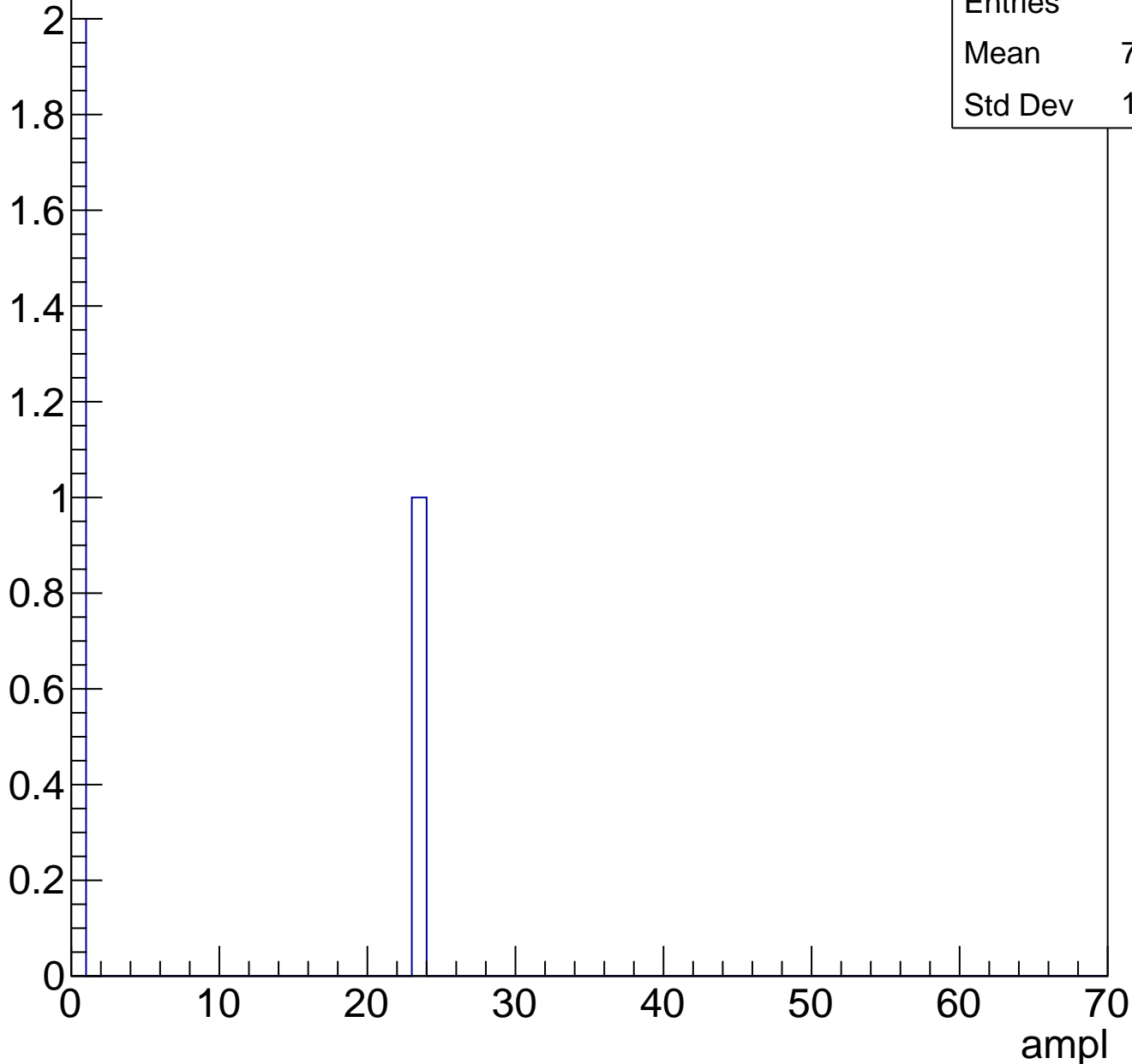
ampl



B1L003S, U13-ch104, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch105, adc0

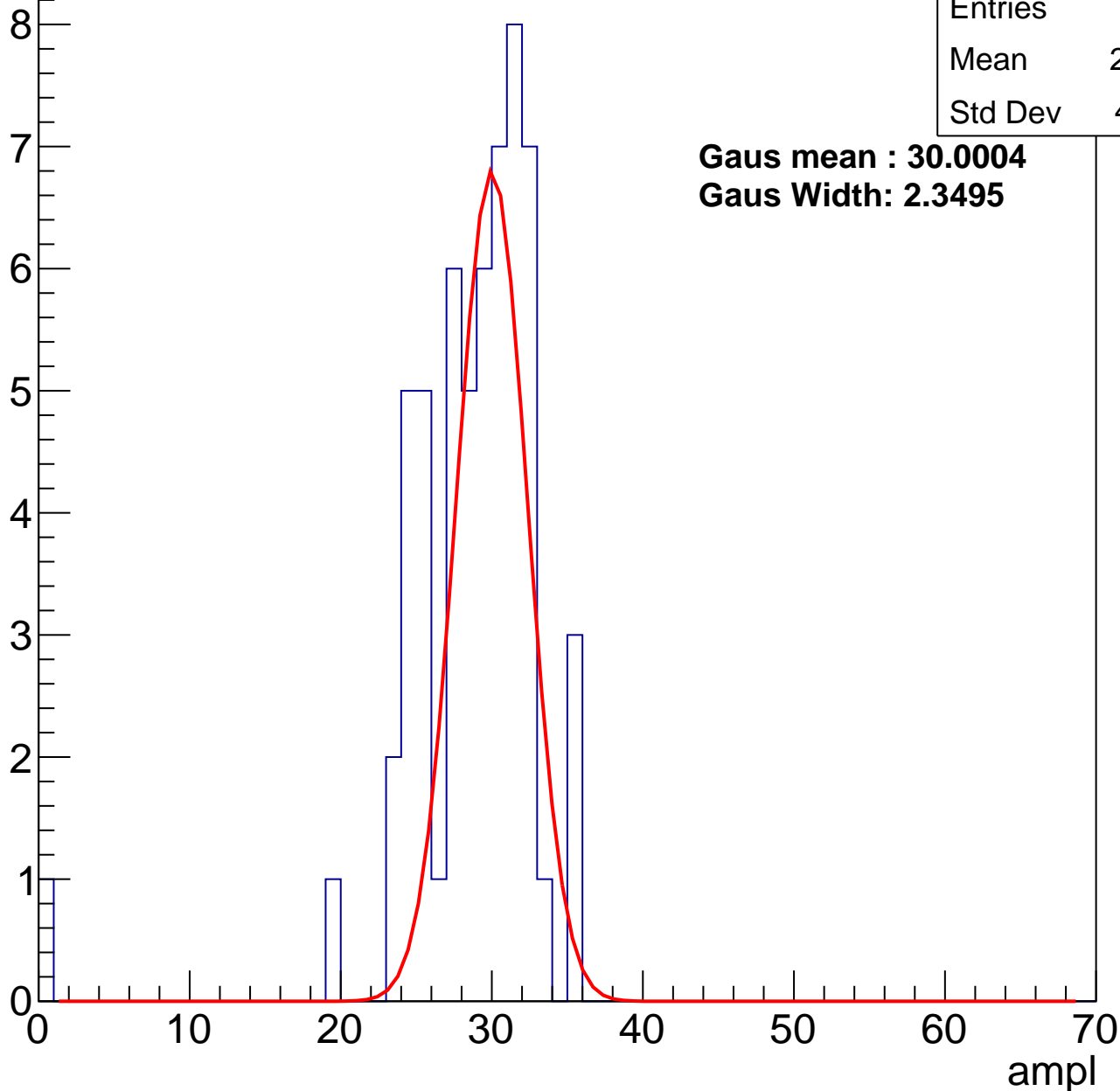
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.14
Std Dev	4.981

Gaus mean : 30.0004

Gaus Width: 2.3495



B1L003S, U13-ch105, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	36.07
Std Dev	3.793

Gaus mean : 37.0481

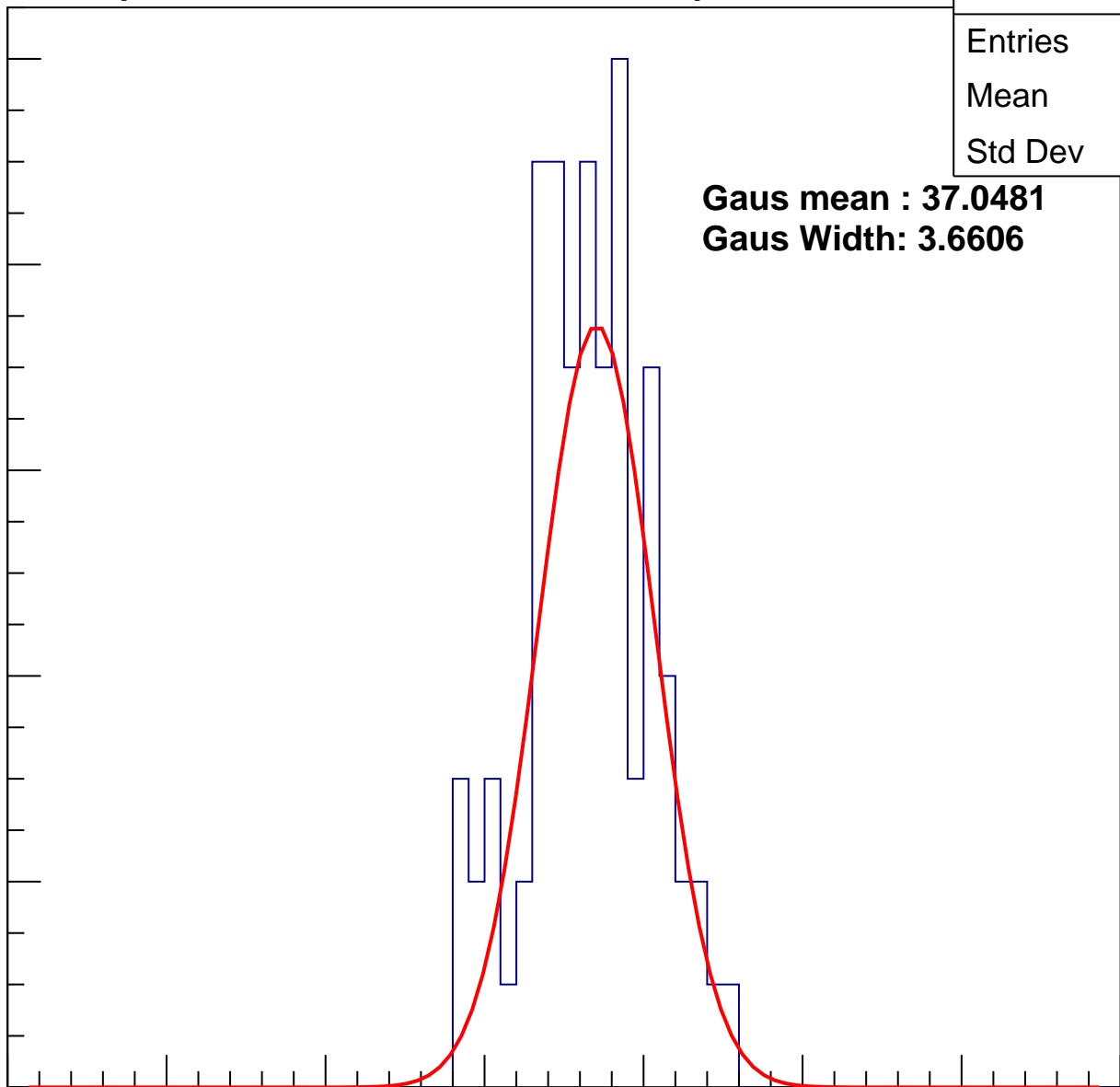
Gaus Width: 3.6606

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch105, adc2

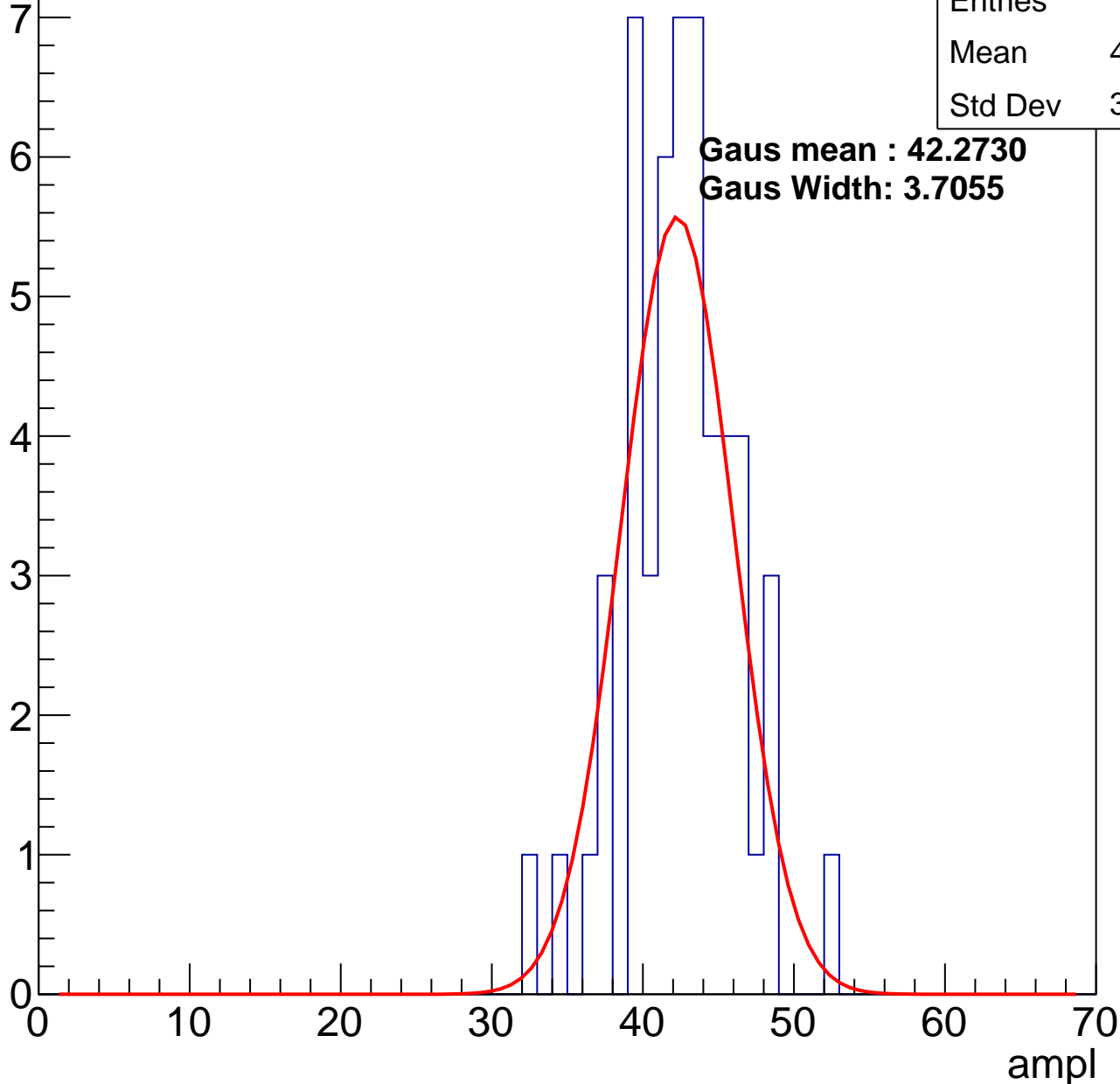
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.08
Std Dev	3.664

Gaus mean : 42.2730

Gaus Width: 3.7055

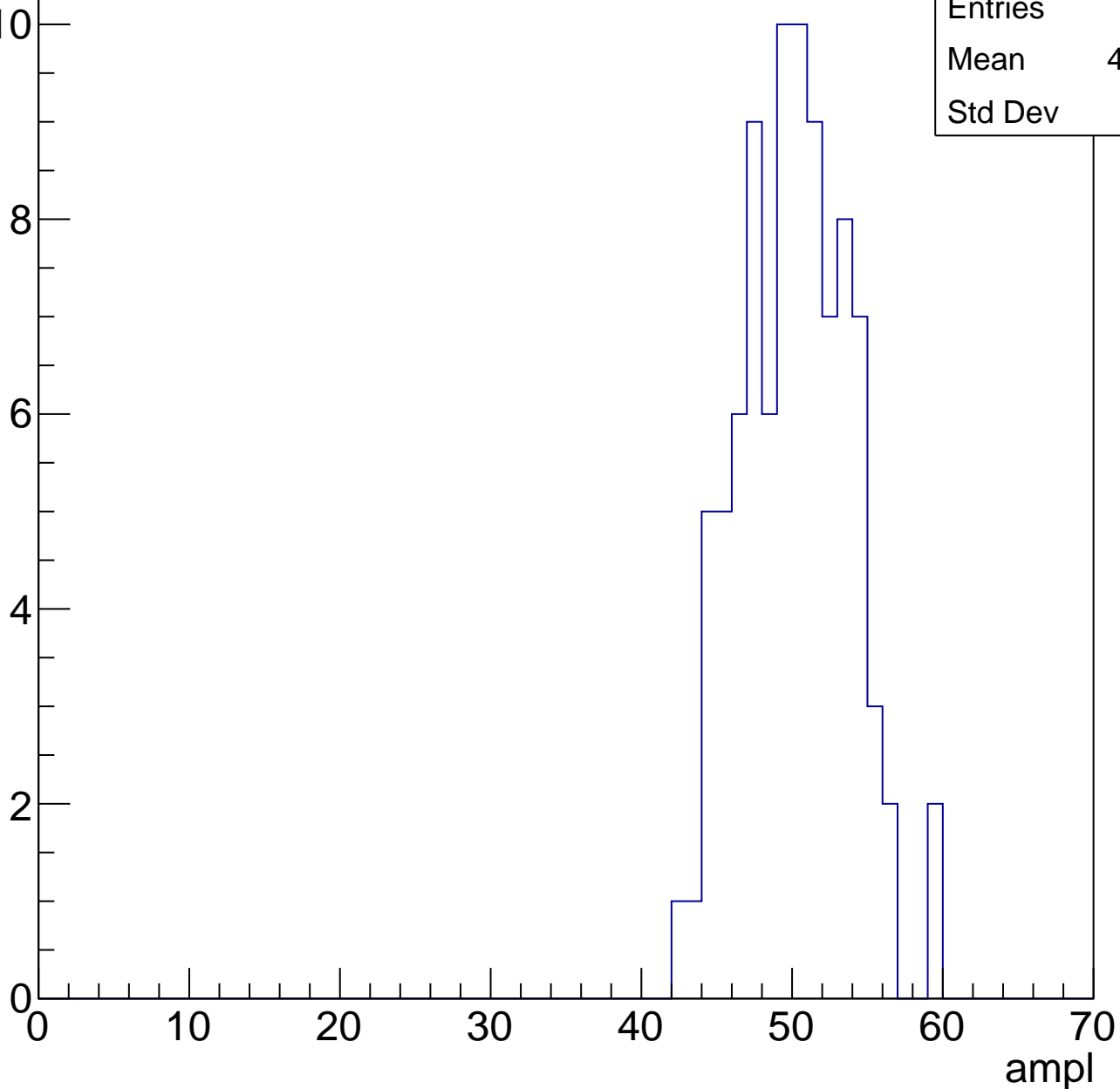


B1L003S, U13-ch105, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	91
Mean	49.75
Std Dev	3.56

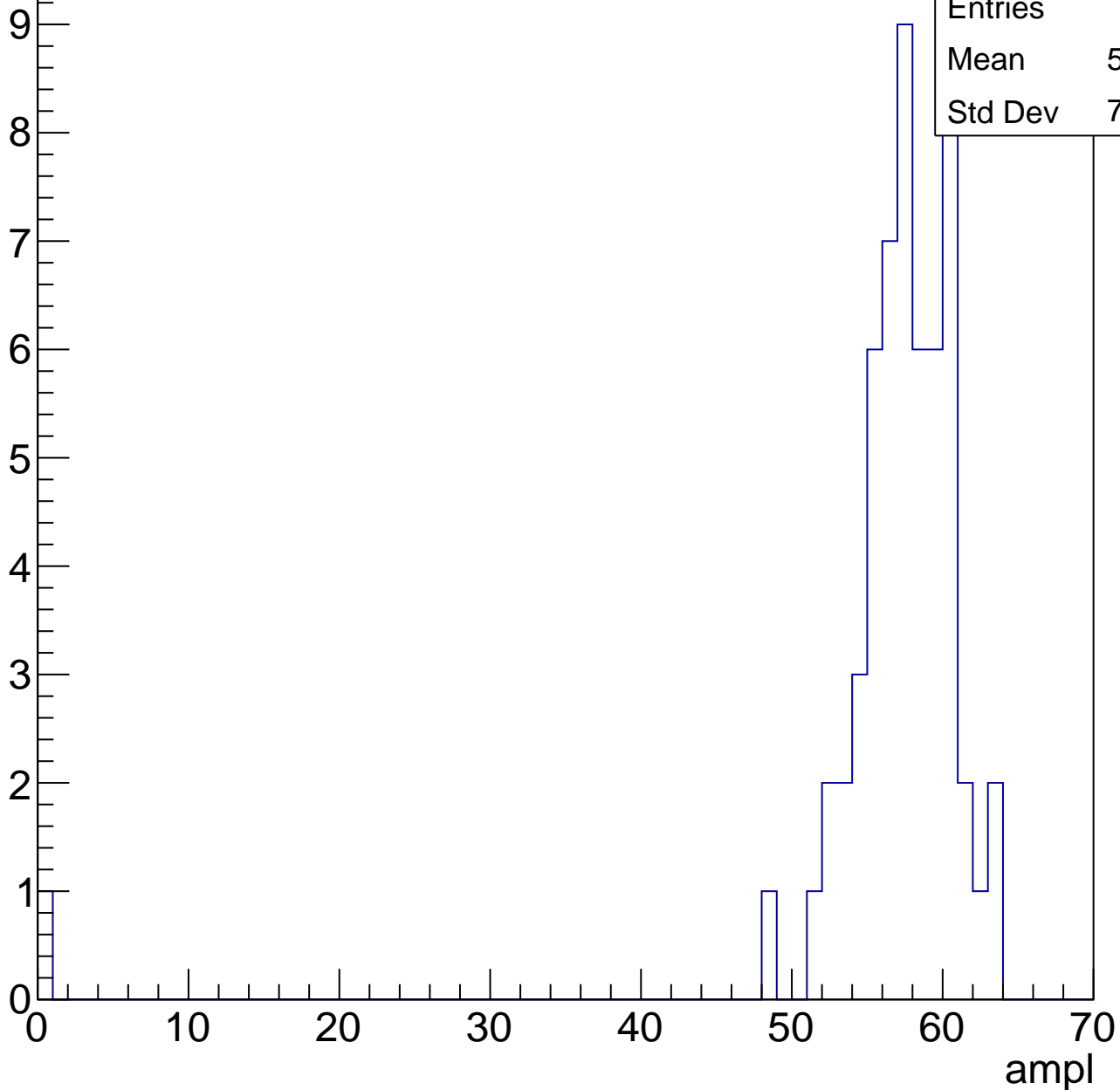


B1L003S, U13-ch105, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	56.17
Std Dev	7.994

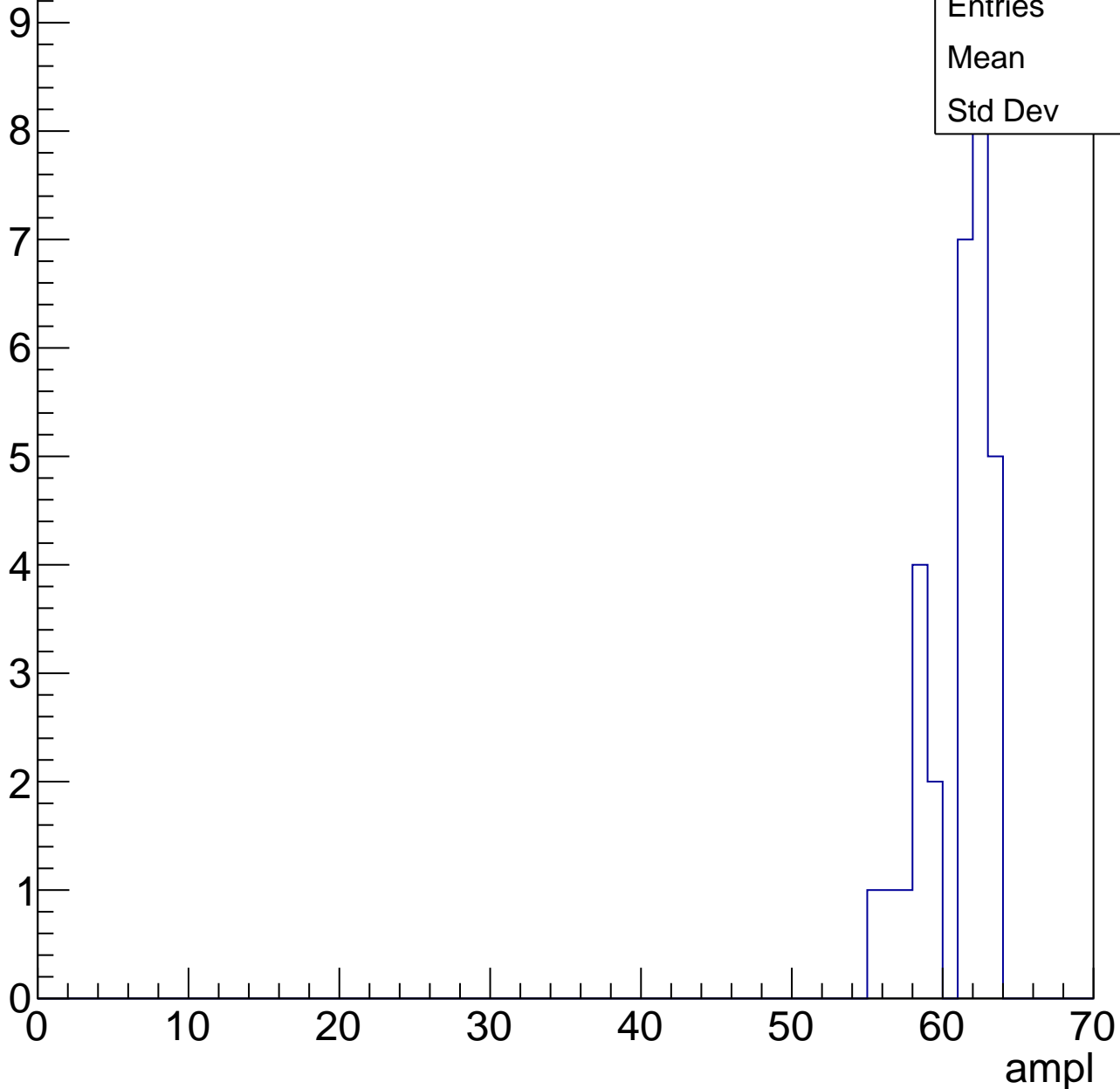


B1L003S, U13-ch105, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	30
Mean	60.6
Std Dev	2.2



B1L003S, U13-ch105, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch105, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch106, adc0

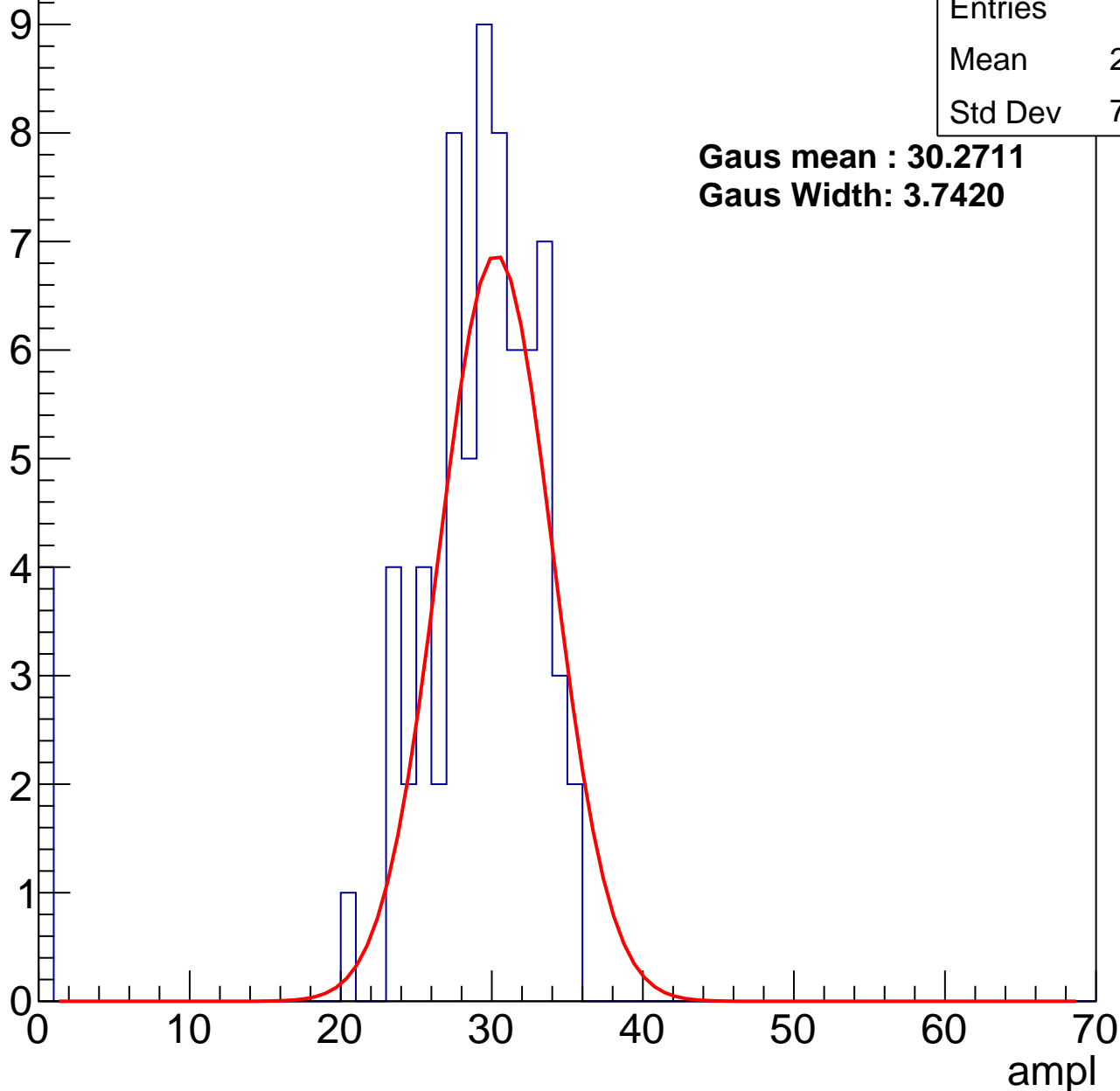
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	27.46
Std Dev	7.445

Gaus mean : 30.2711

Gaus Width: 3.7420



B1L003S, U13-ch106, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	35.97
Std Dev	3.037

Gaus mean : 36.4839

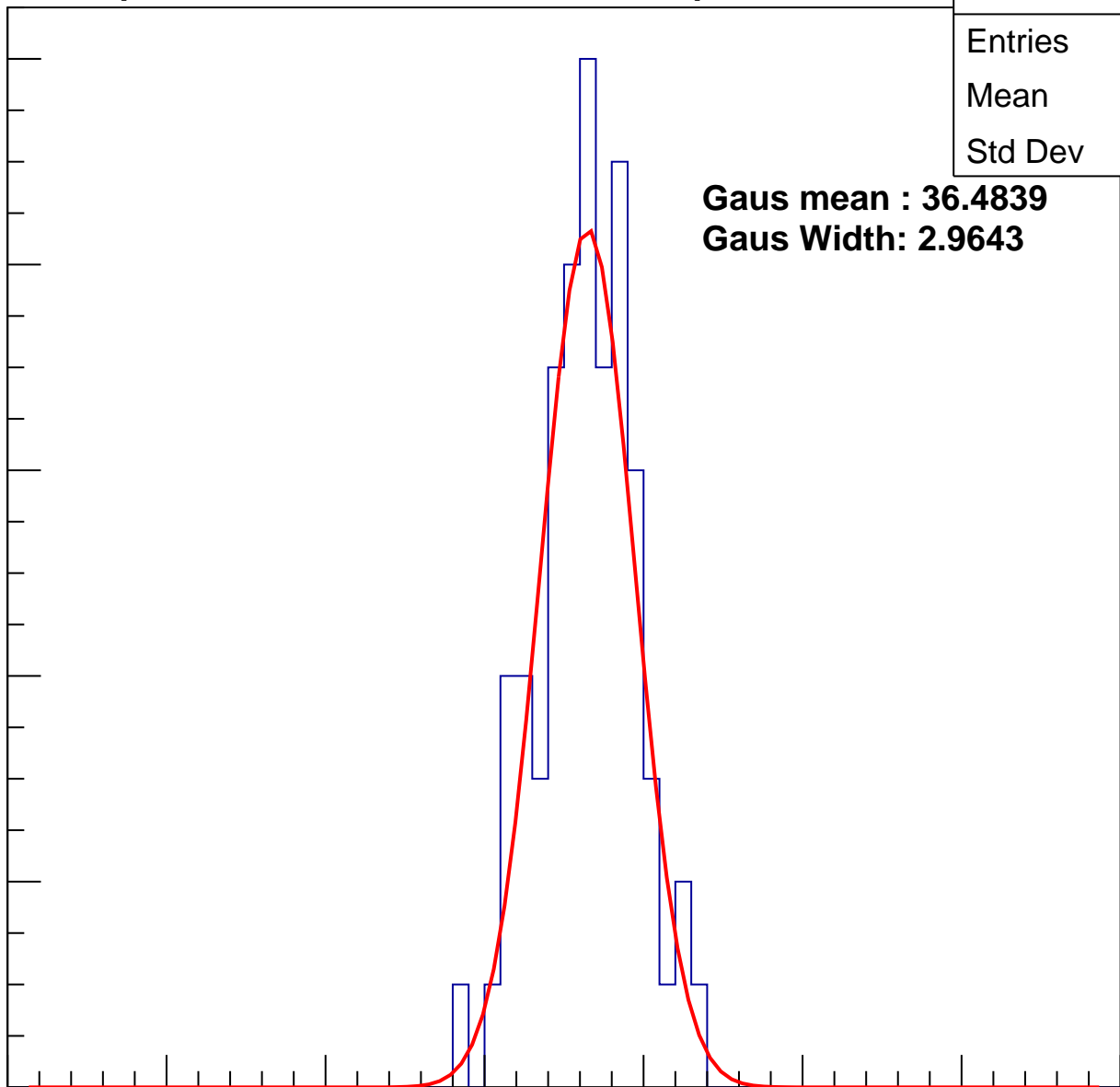
Gaus Width: 2.9643

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch106, adc2

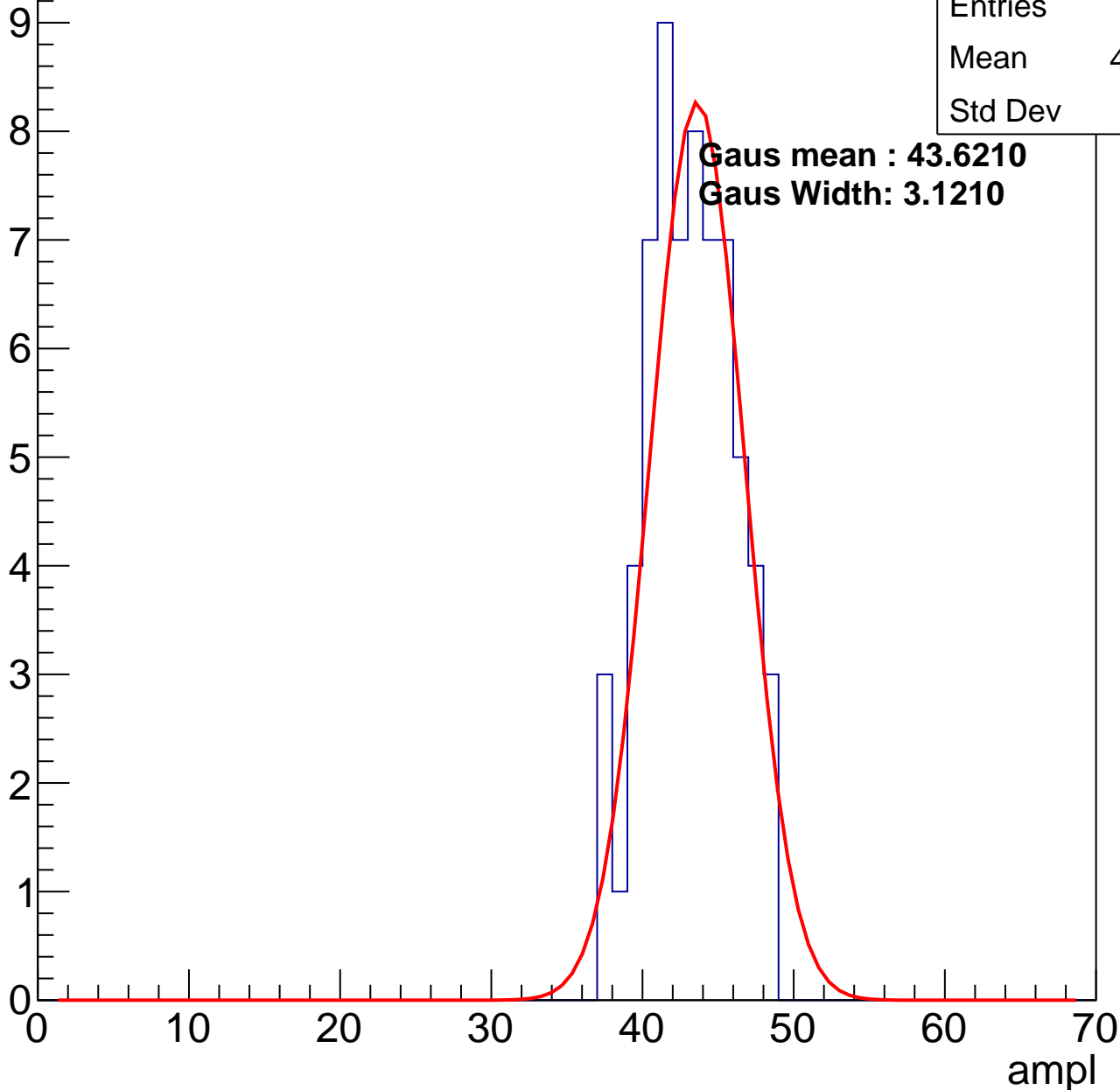
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.72
Std Dev	2.82

Gaus mean : 43.6210

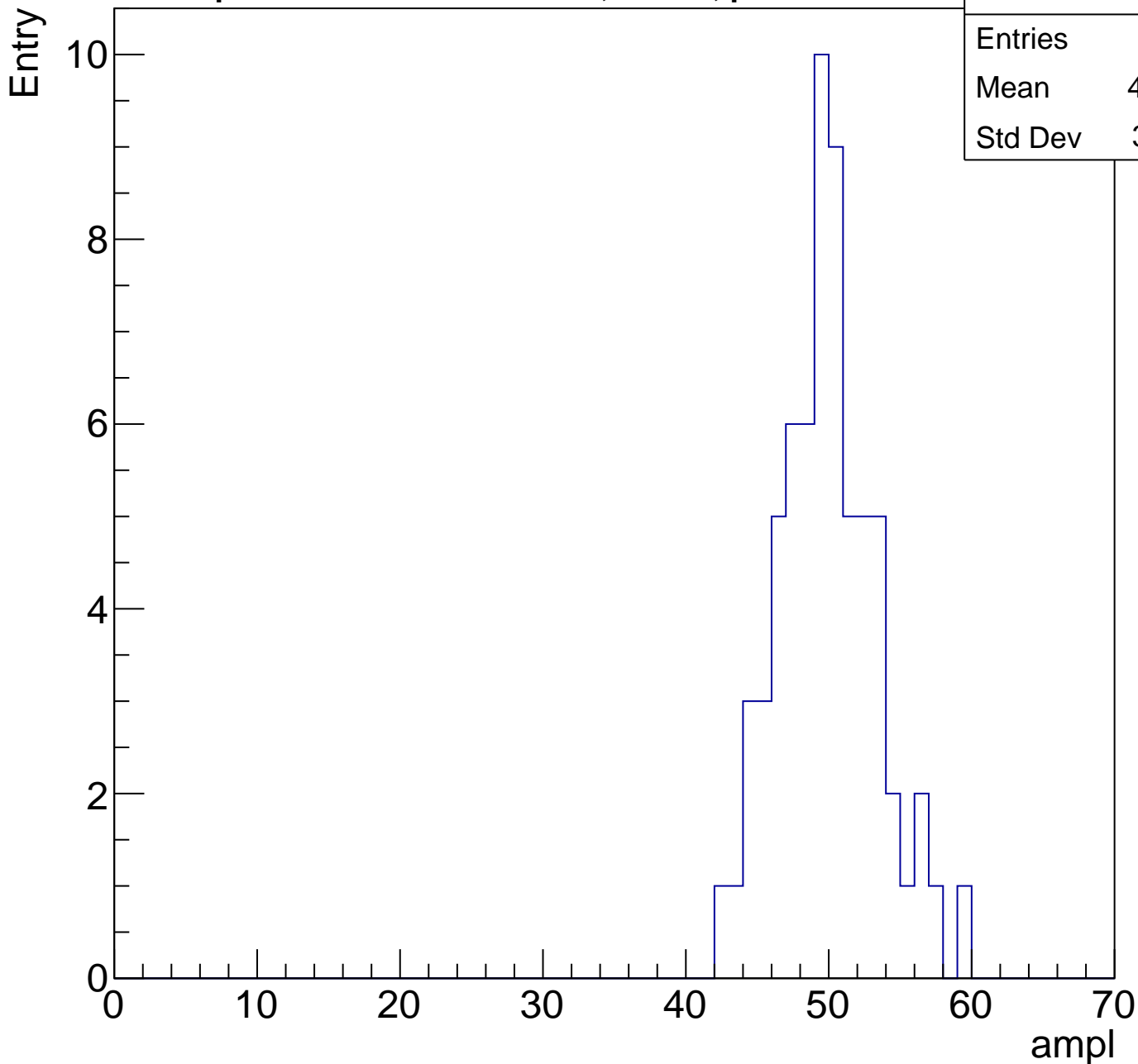
Gaus Width: 3.1210



B1L003S, U13-ch106, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	49.44
Std Dev	3.421

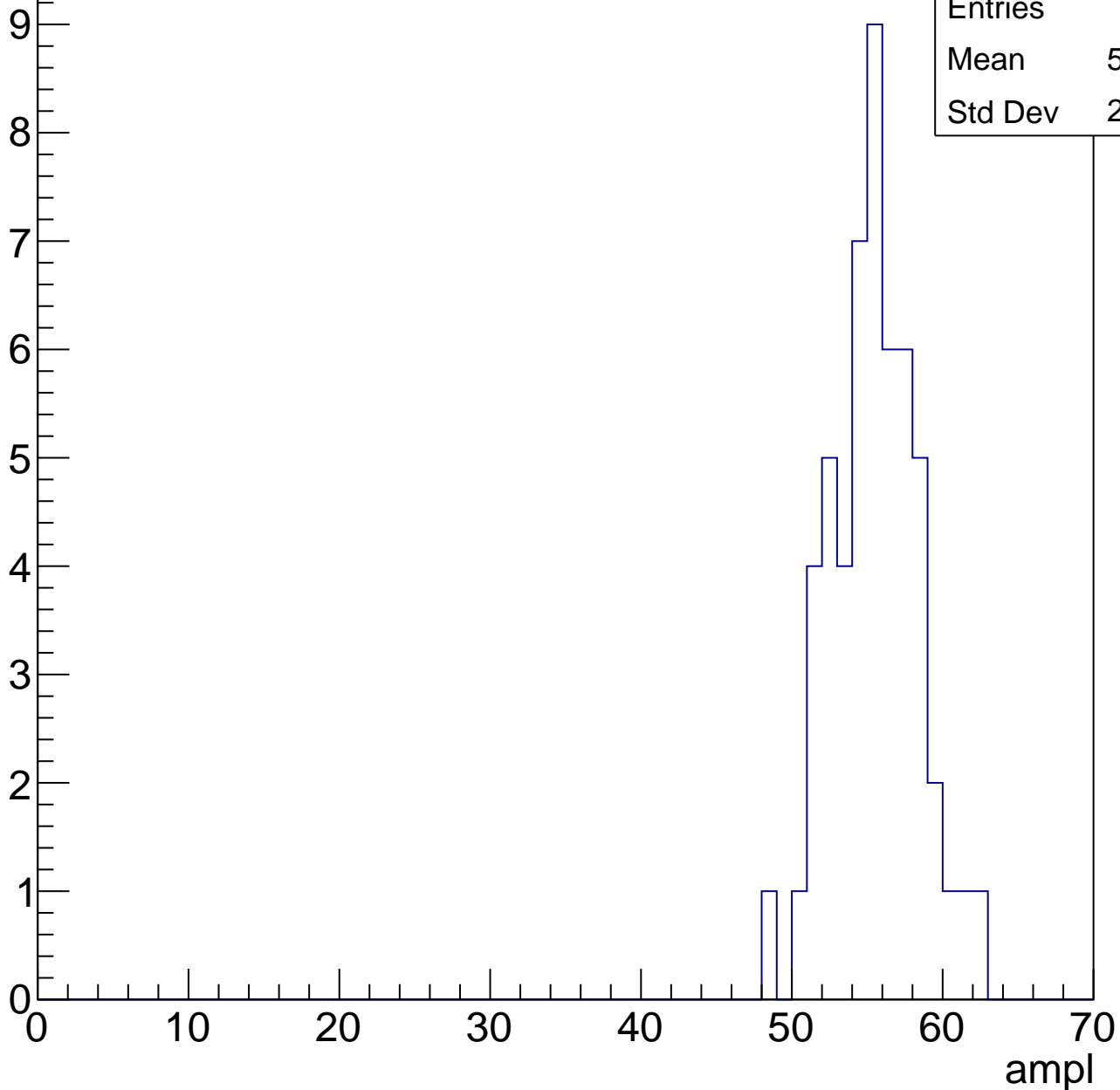


B1L003S, U13-ch106, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.02
Std Dev	2.825

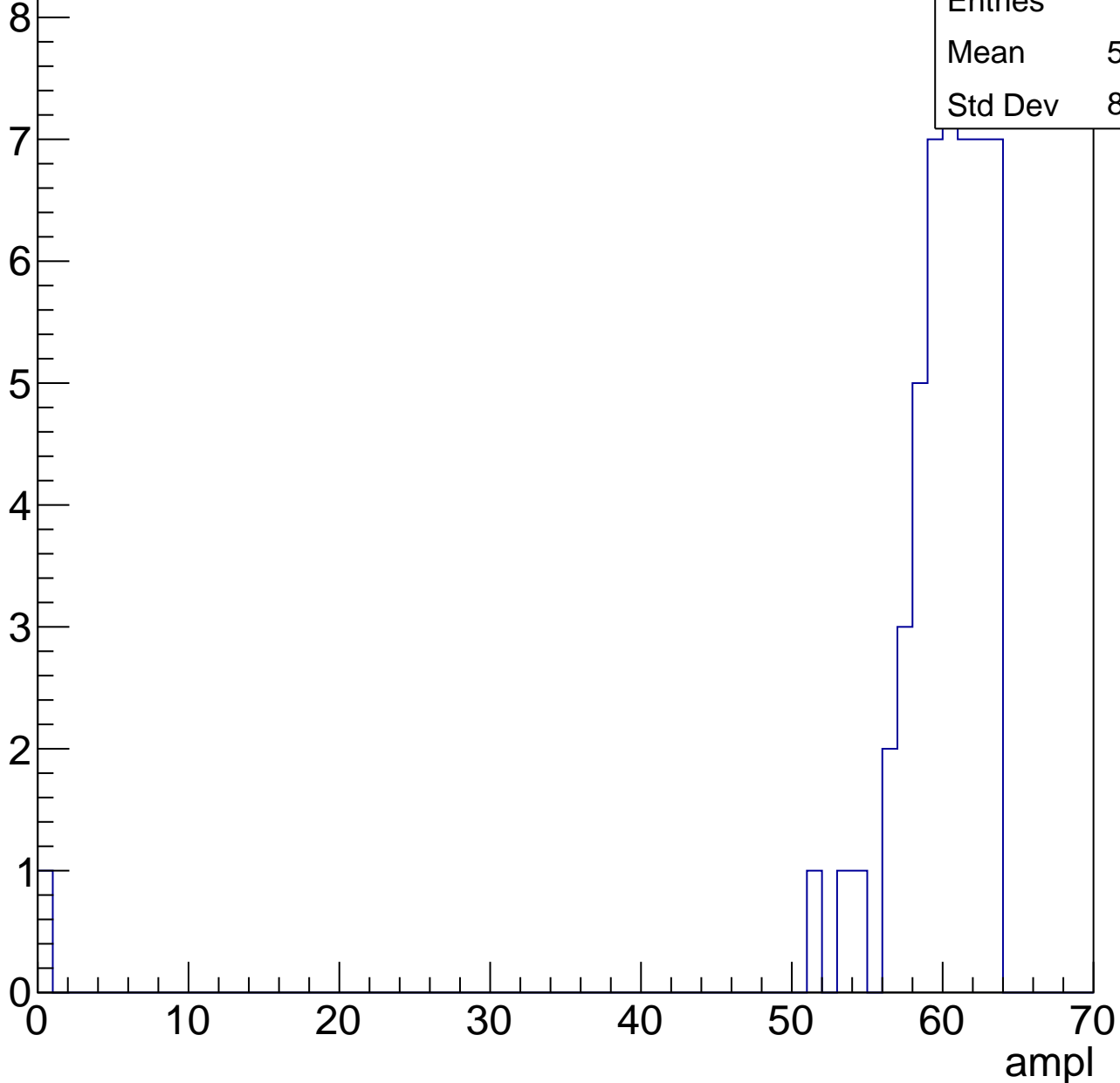


B1L003S, U13-ch106, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

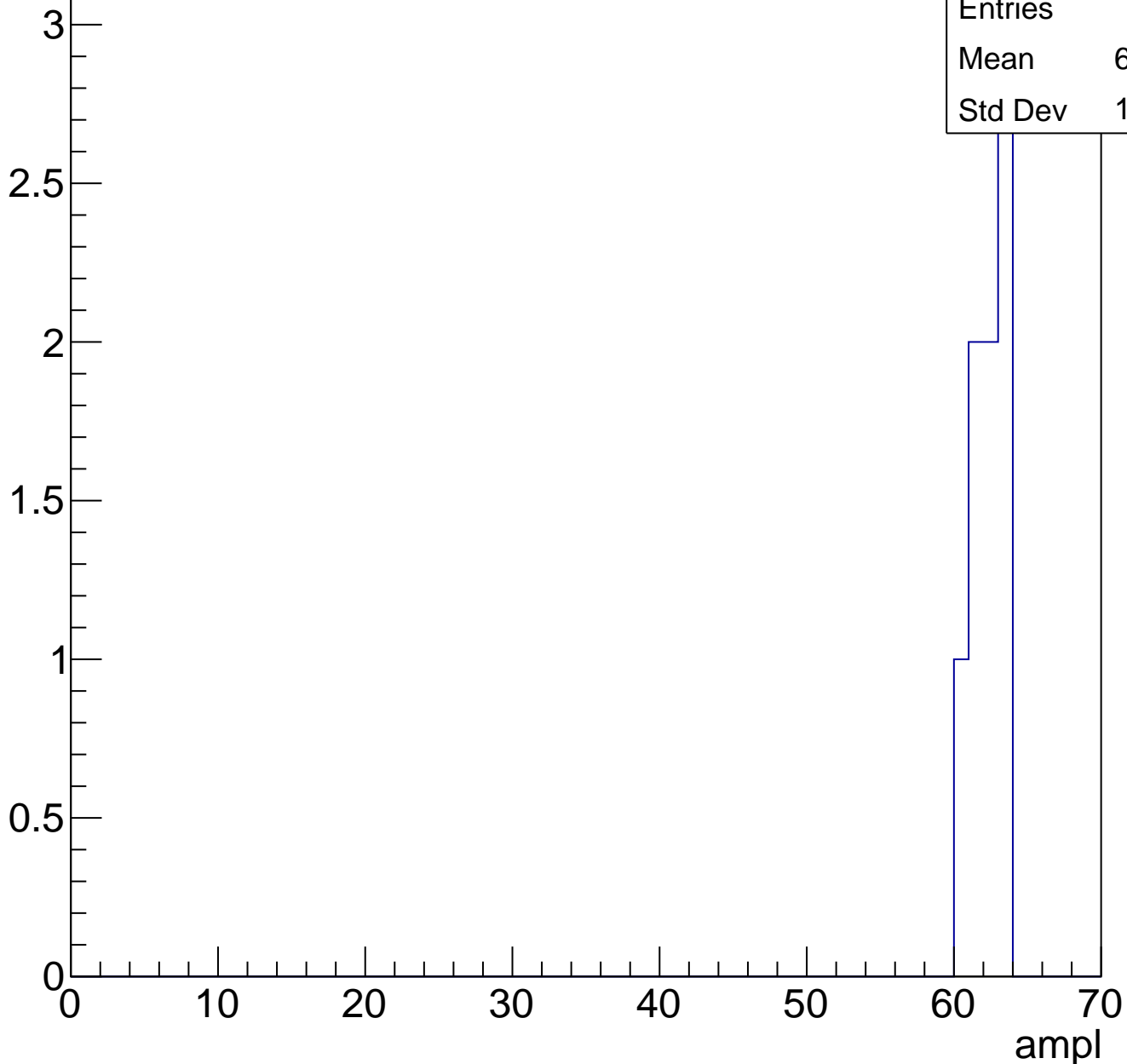
Entries	50
Mean	58.52
Std Dev	8.764



B1L003S, U13-ch106, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	8
Mean	61.88
Std Dev	1.053

B1L003S, U13-ch106, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U13-ch107, adc0

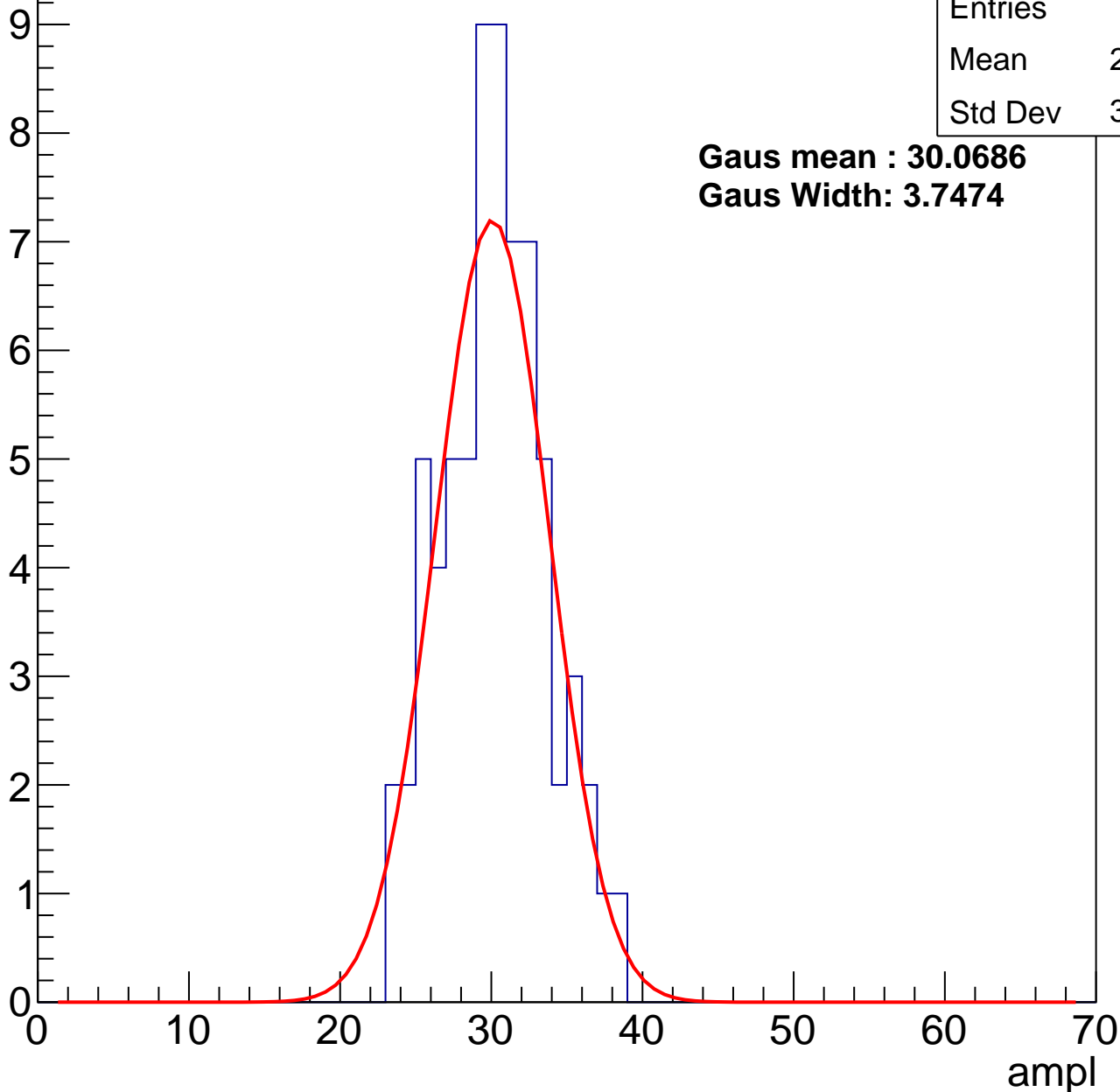
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.78
Std Dev	3.409

Gaus mean : 30.0686

Gaus Width: 3.7474



B1L003S, U13-ch107, adc1

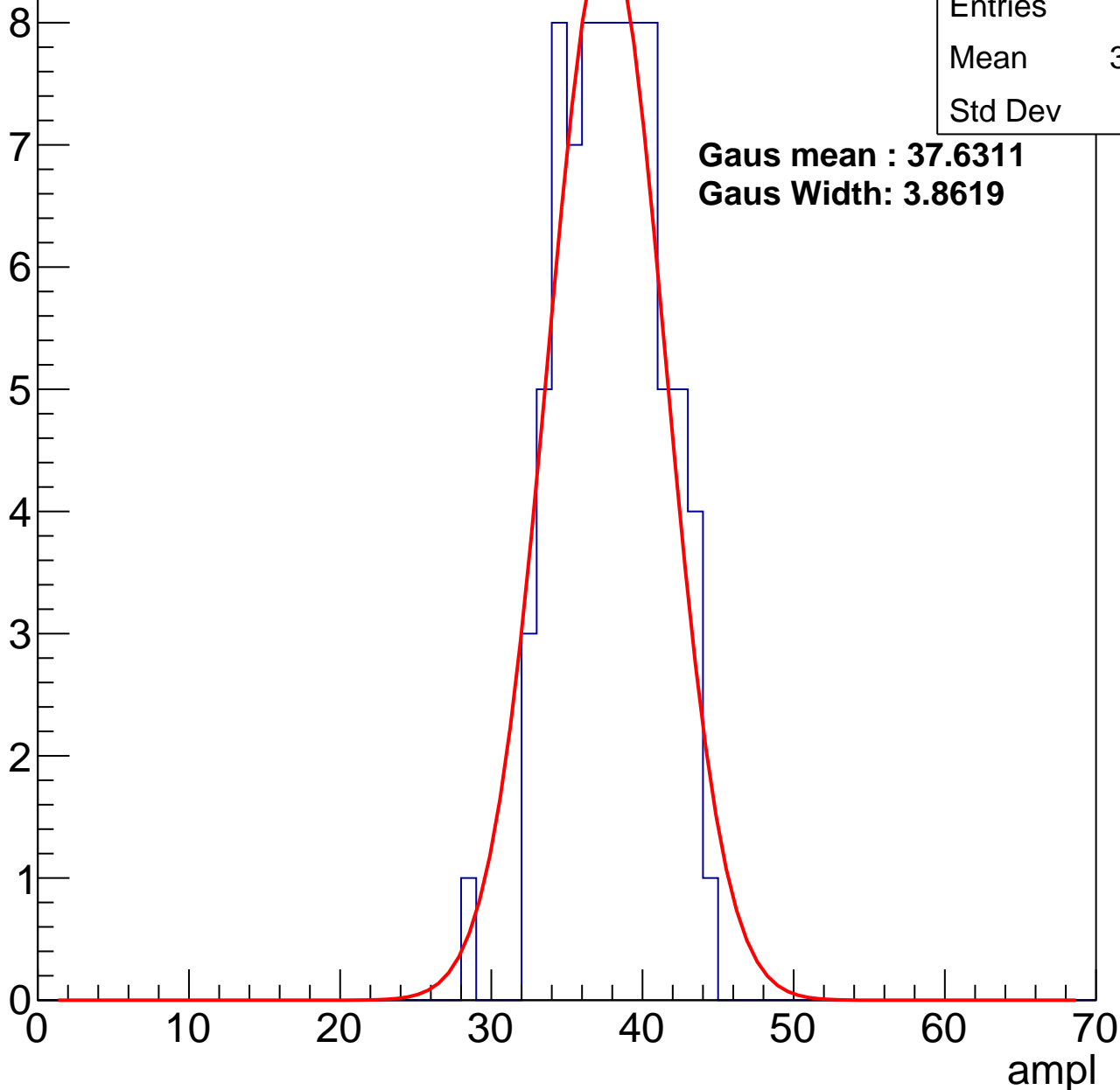
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	37.43
Std Dev	3.26

Gaus mean : 37.6311

Gaus Width: 3.8619



B1L003S, U13-ch107, adc2

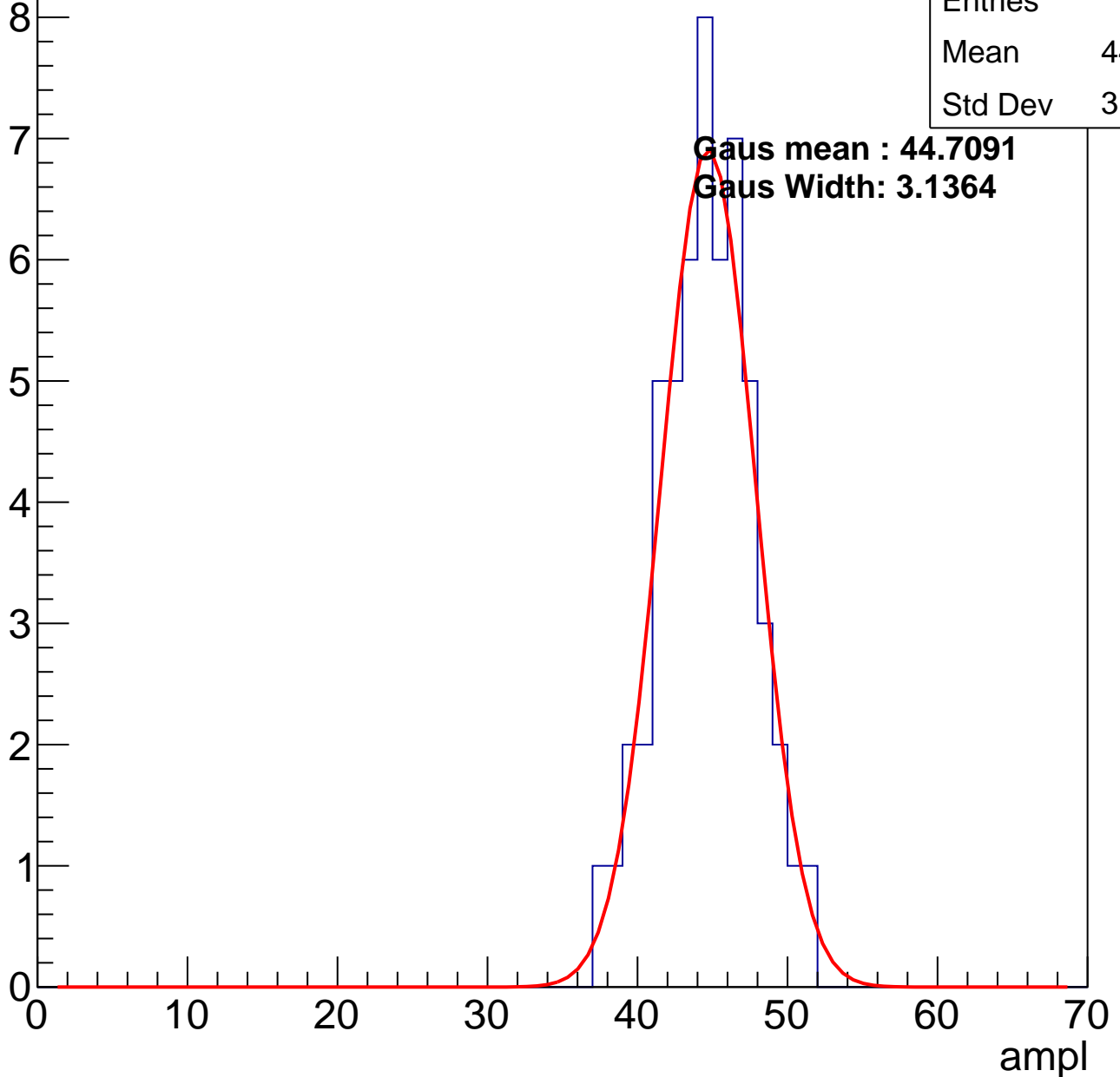
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	44.15
Std Dev	3.012

Gaus mean : 44.7091

Gaus Width: 3.1364

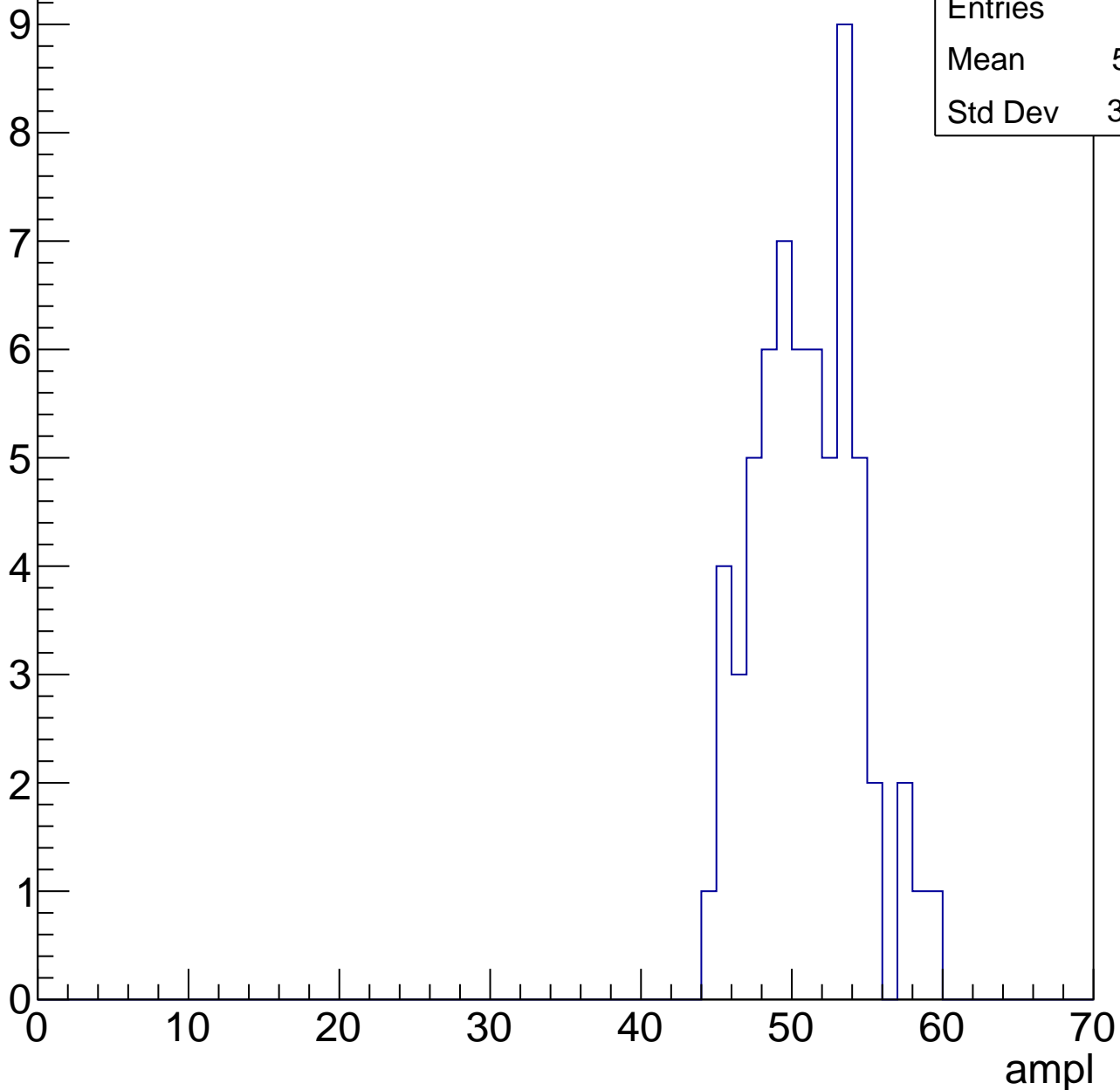


B1L003S, U13-ch107, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

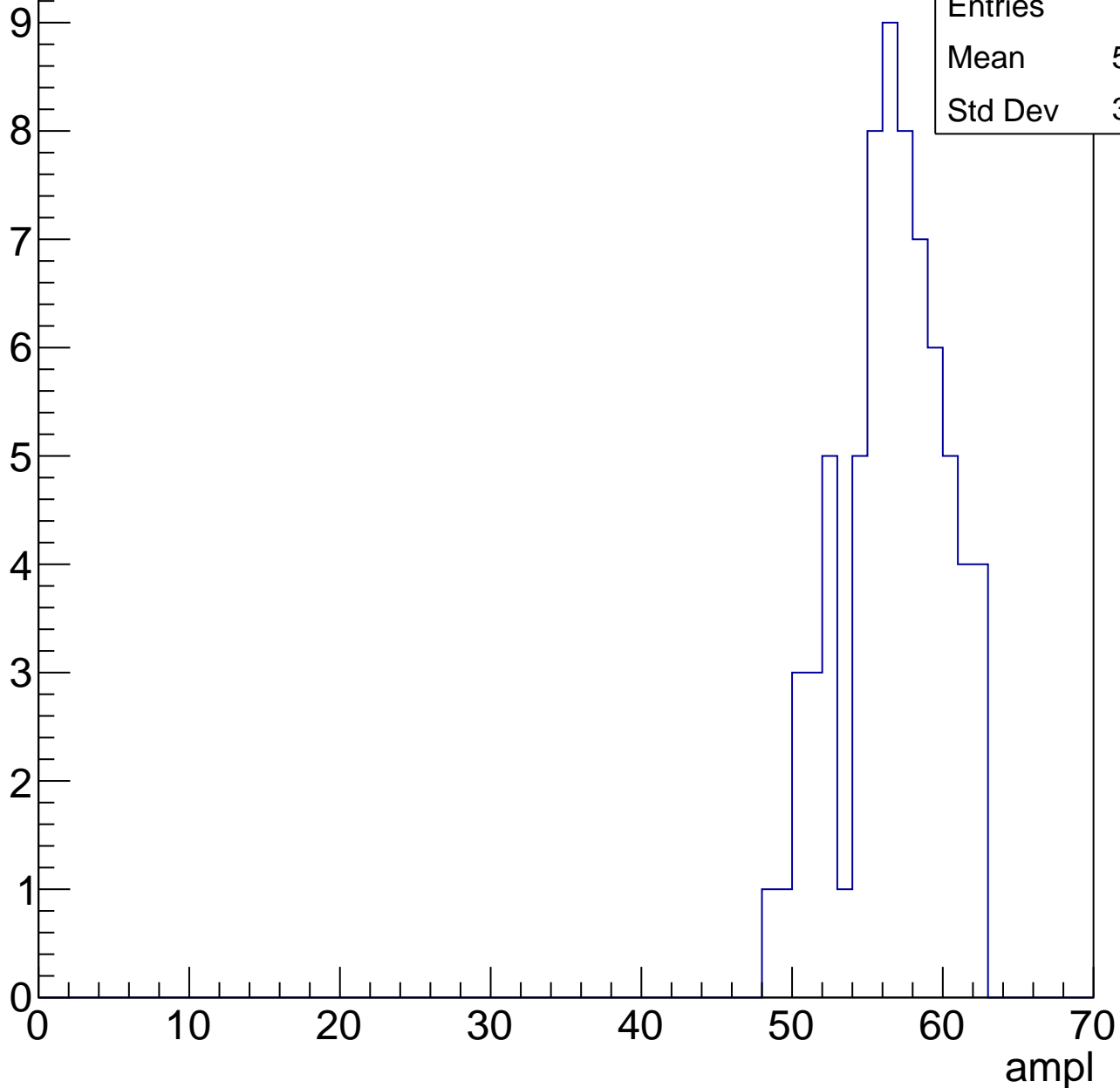
Entries	63
Mean	50.51
Std Dev	3.389



B1L003S, U13-ch107, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

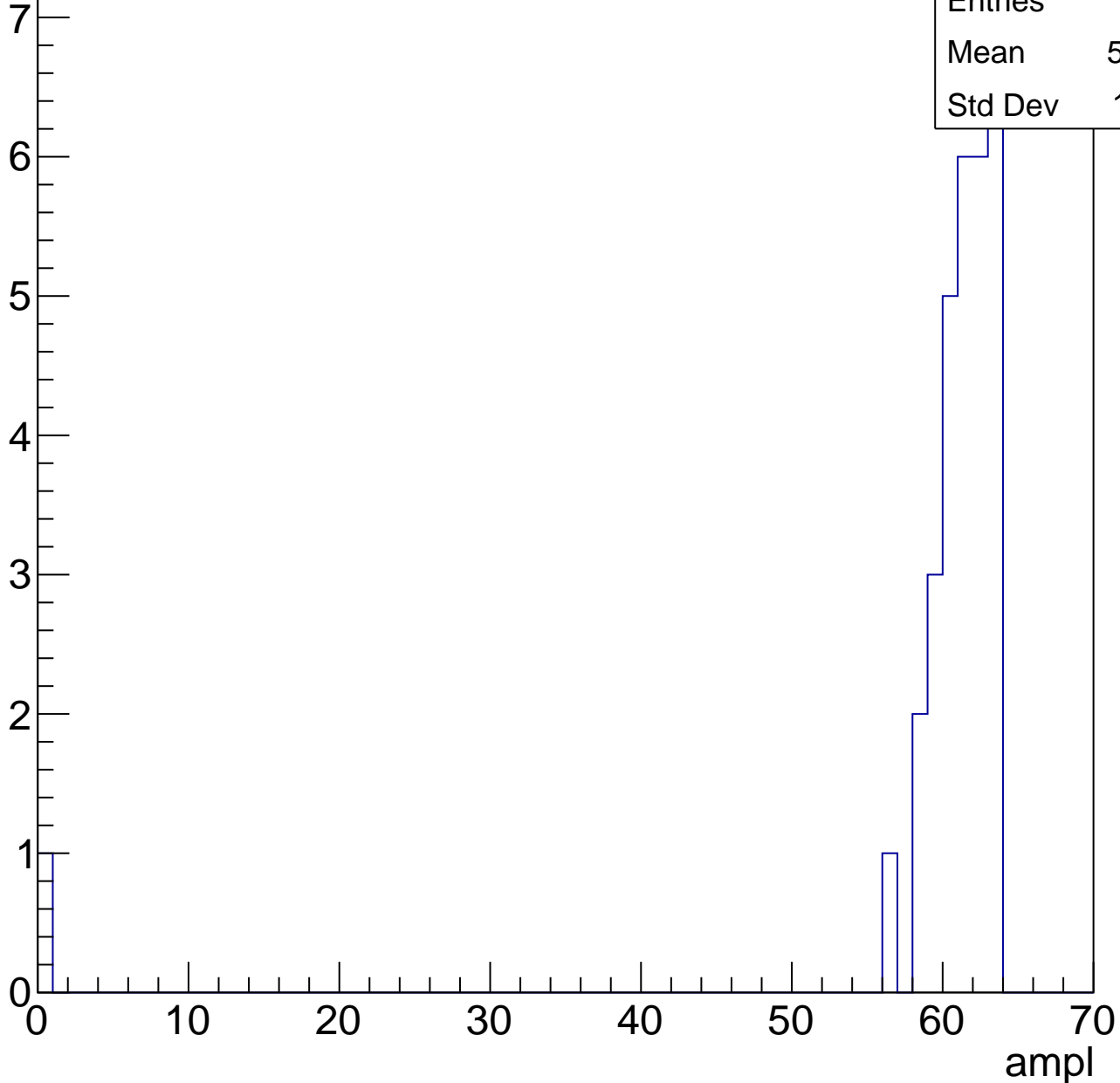


B1L003S, U13-ch107, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	31
Mean	58.97
Std Dev	10.91



B1L003S, U13-ch107, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch107, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch108, adc0

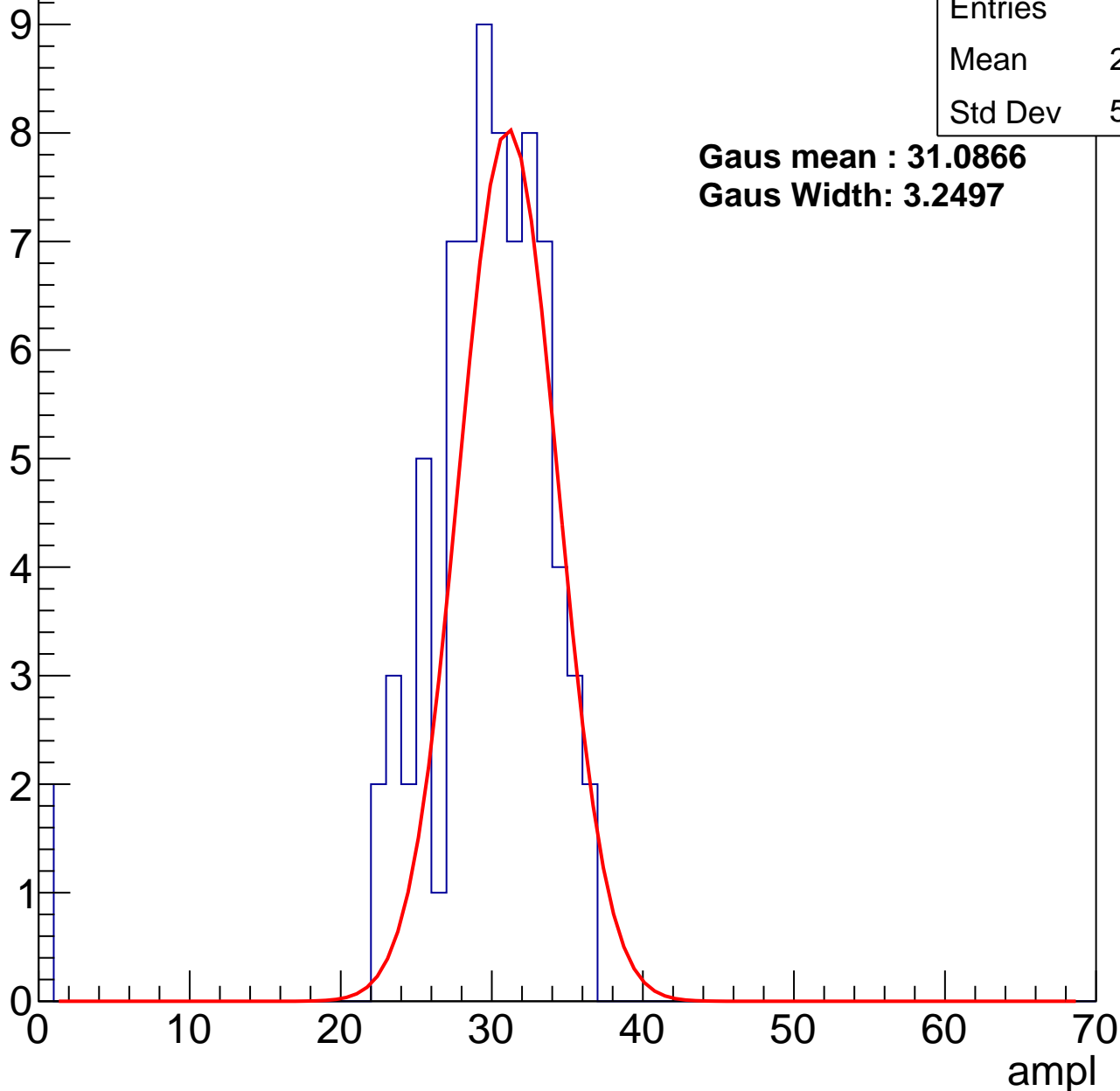
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	28.77
Std Dev	5.793

Gaus mean : 31.0866

Gaus Width: 3.2497



B1L003S, U13-ch108, adc1

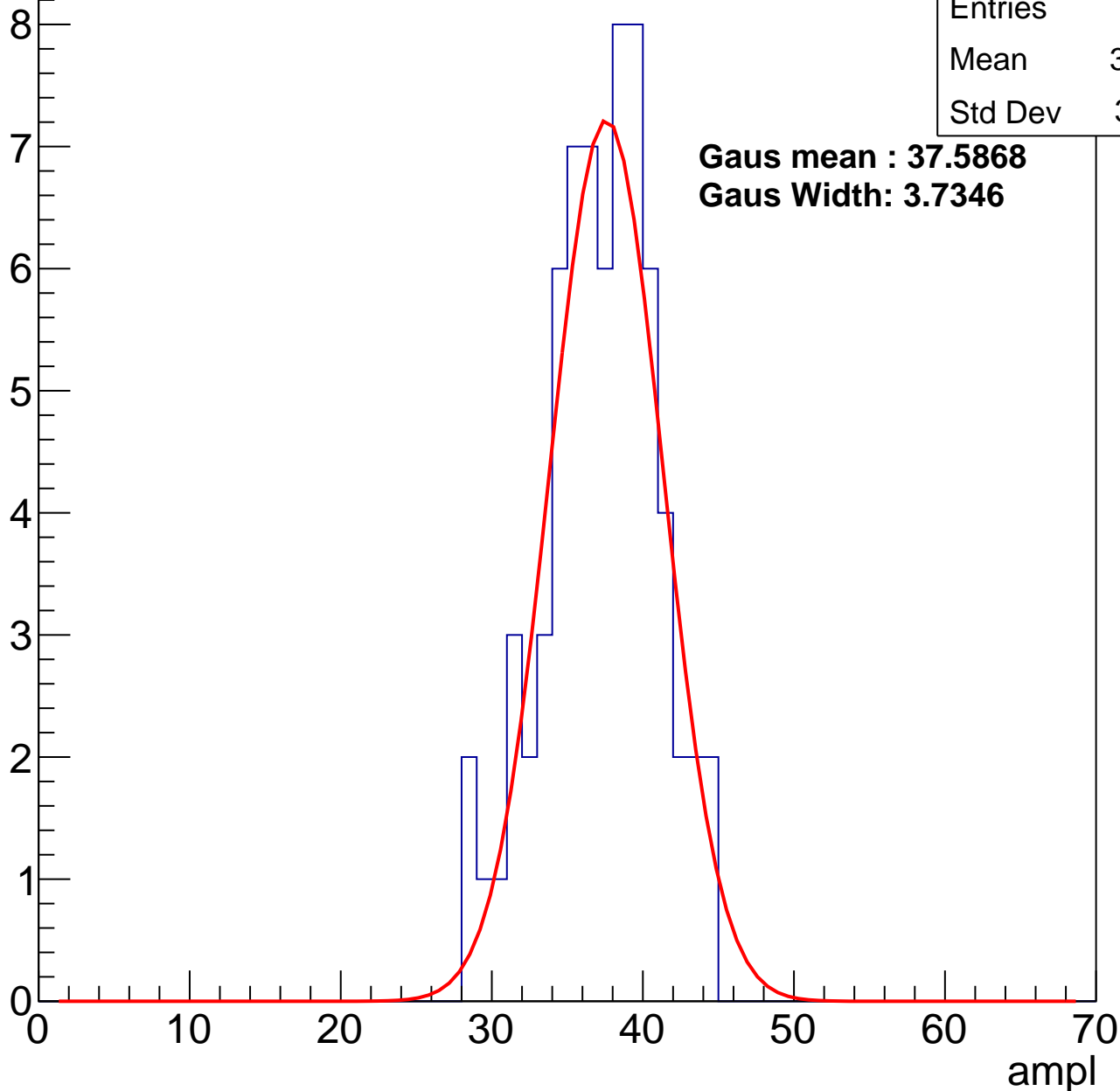
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.74
Std Dev	3.671

Gaus mean : 37.5868

Gaus Width: 3.7346



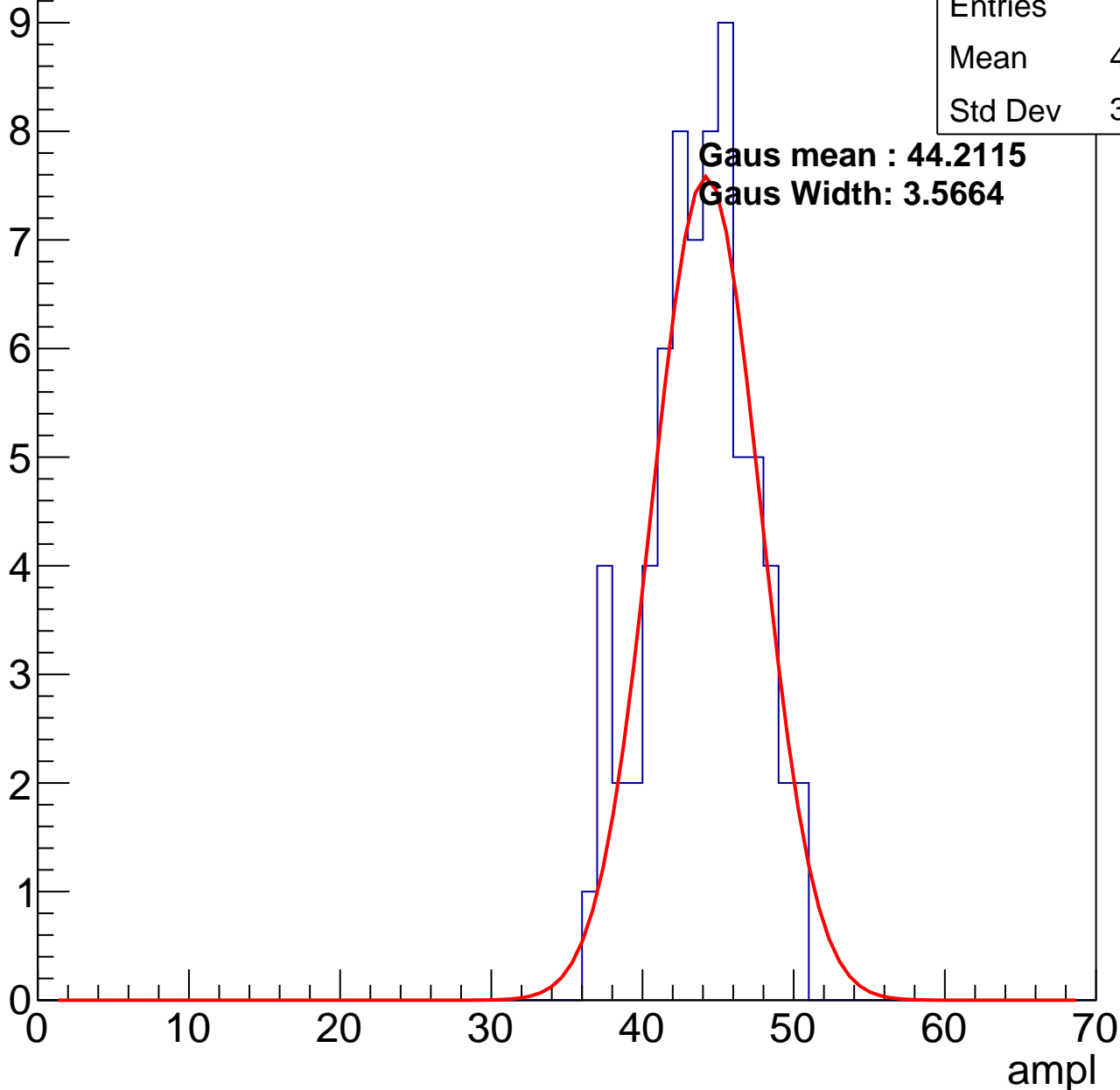
B1L003S, U13-ch108, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.38
Std Dev	3.345

Gaus mean : 44.2115
Gaus Width: 3.5664

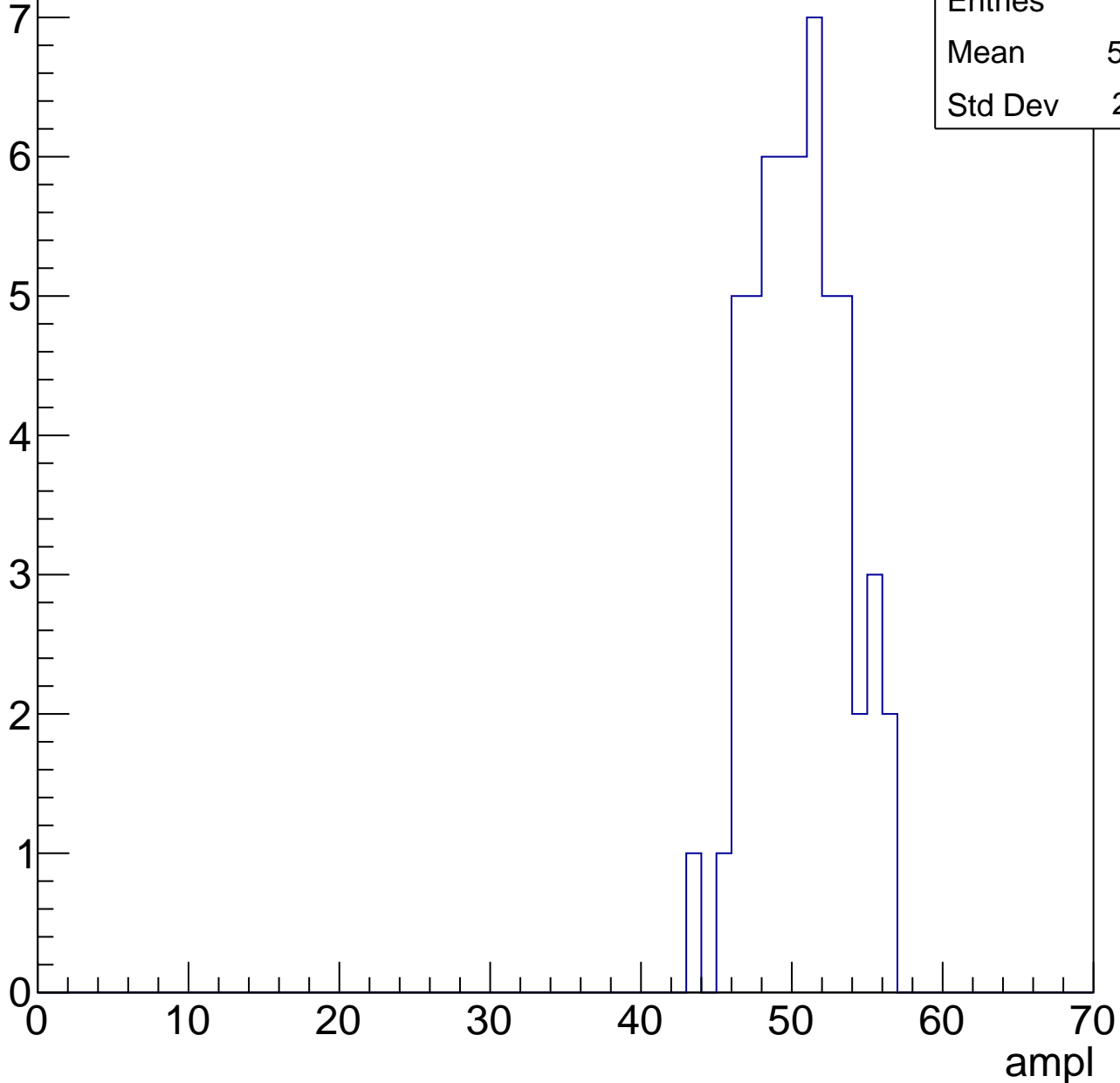


B1L003S, U13-ch108, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	50.04
Std Dev	2.981

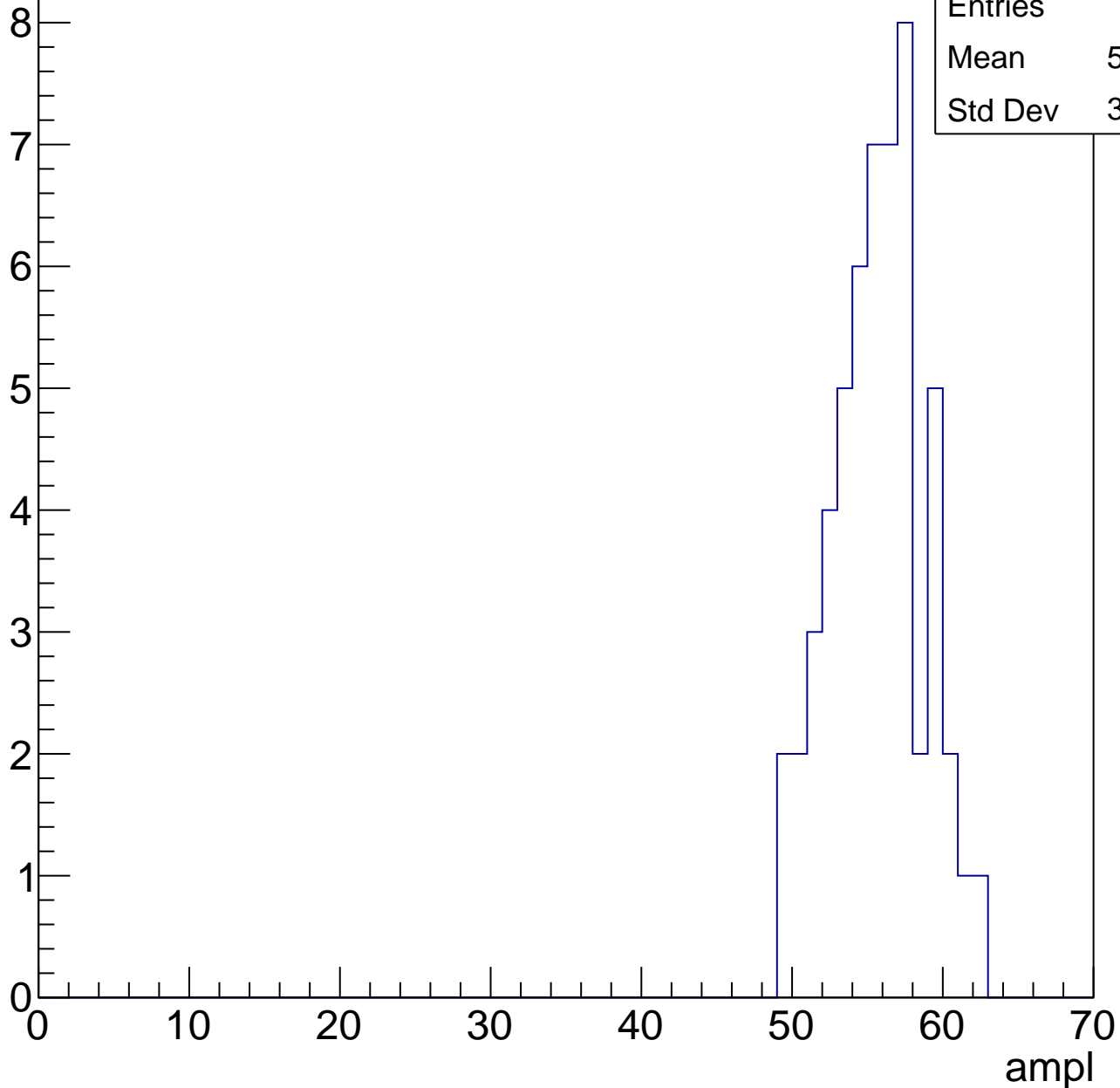


B1L003S, U13-ch108, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.18
Std Dev	3.022

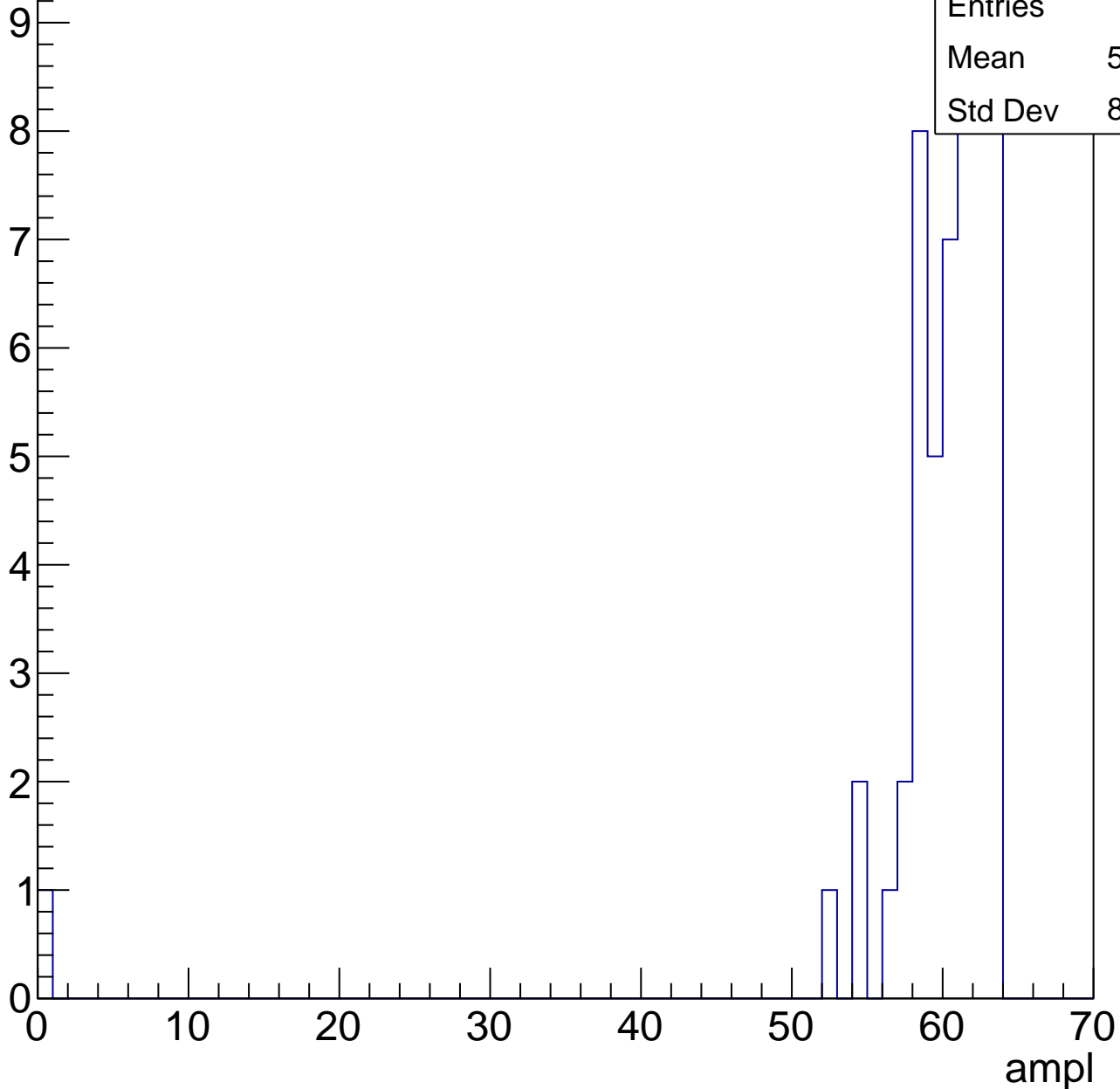


B1L003S, U13-ch108, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	58.85
Std Dev	8.614



B1L003S, U13-ch108, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch108, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U13-ch109, adc0

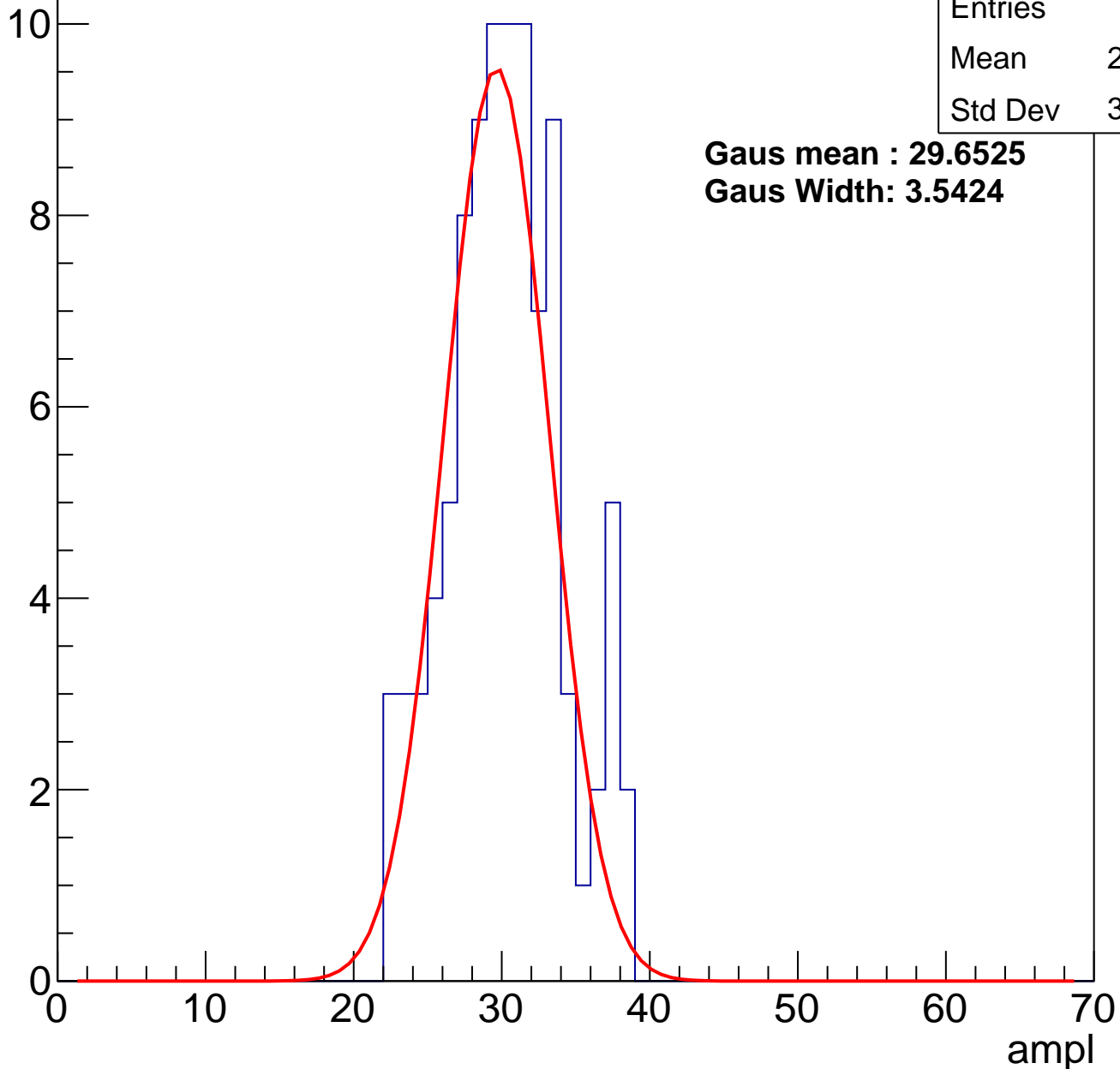
calib_packv5_042523_0143.root, FC#13, port D2

Entries	94
Mean	29.74
Std Dev	3.826

Gaus mean : 29.6525

Gaus Width: 3.5424

Entry



B1L003S, U13-ch109, adc1

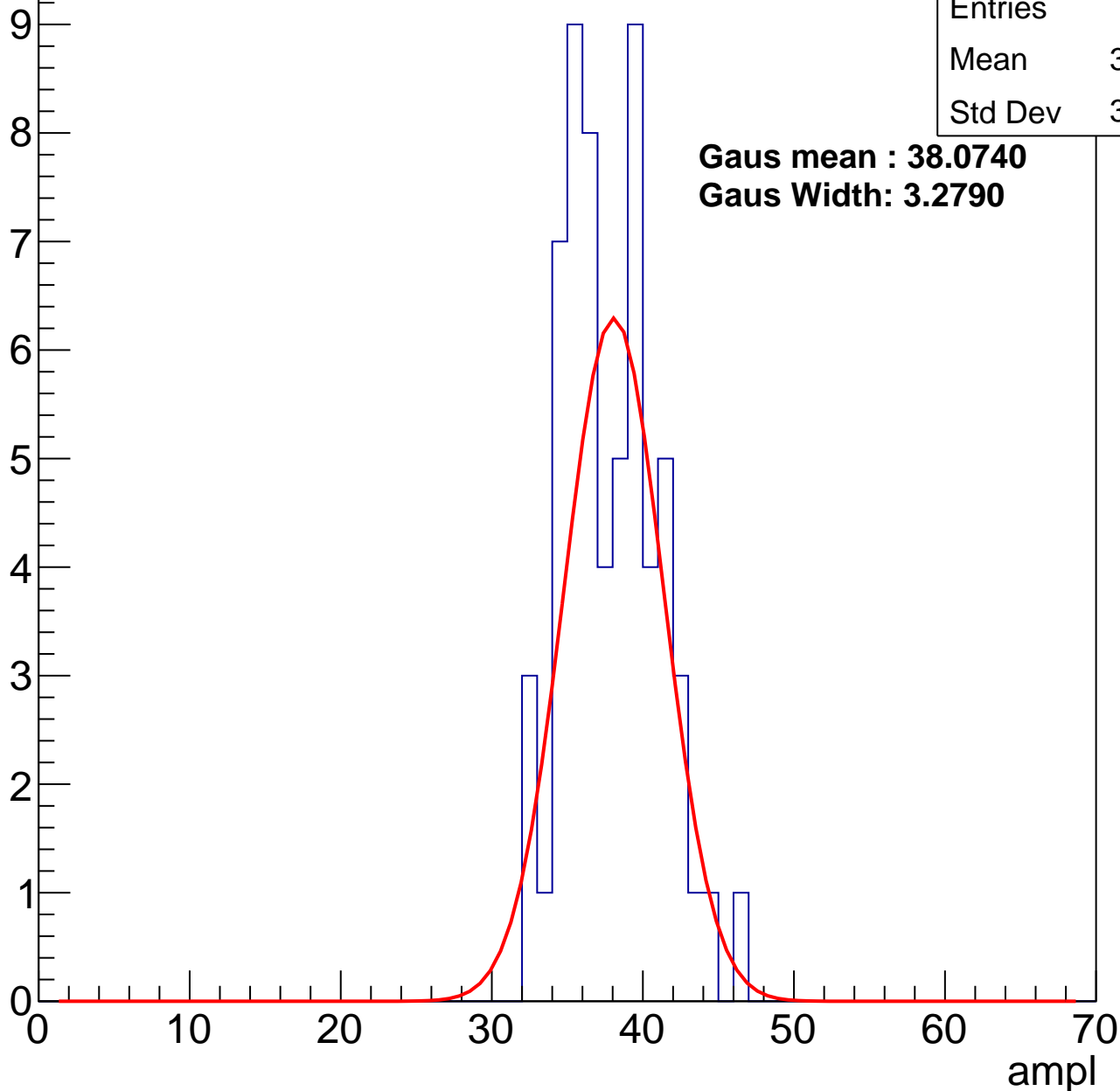
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	37.43
Std Dev	3.097

Gaus mean : 38.0740

Gaus Width: 3.2790



B1L003S, U13-ch109, adc2

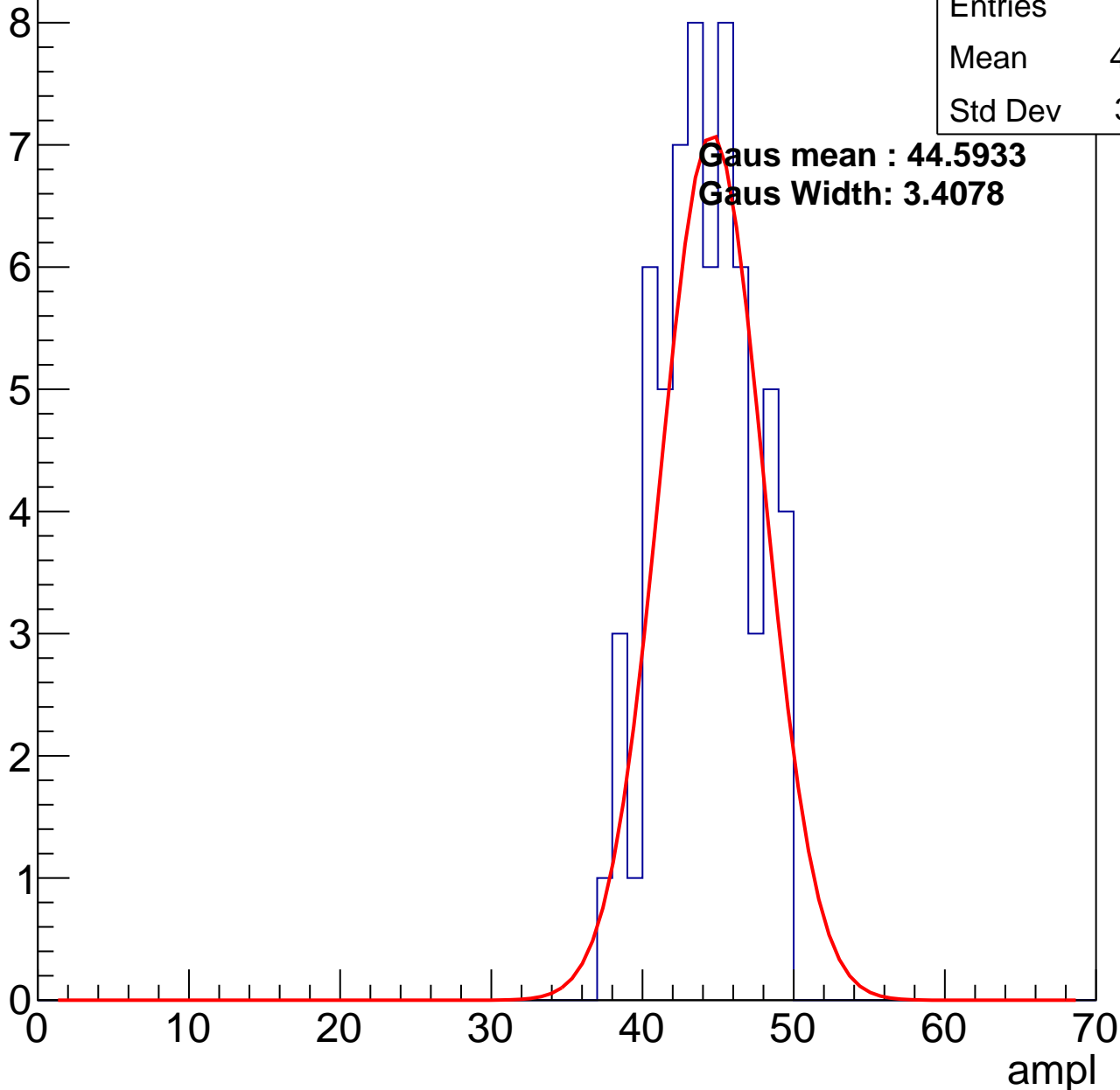
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.65
Std Dev	3.061

Gaus mean : 44.5933

Gaus Width: 3.4078



B1L003S, U13-ch109, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	50.58
Std Dev	3.361

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

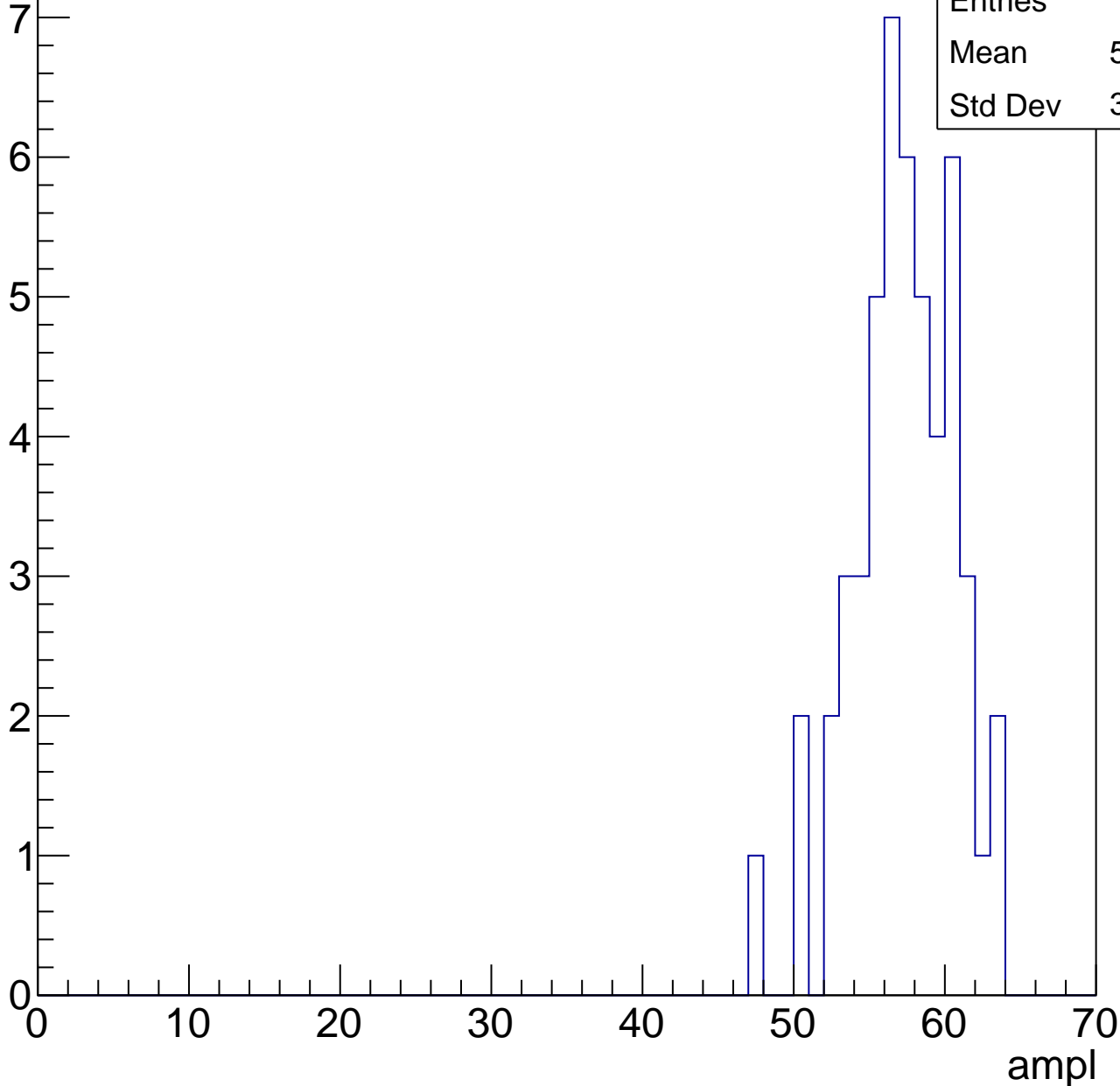


B1L003S, U13-ch109, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	56.76
Std Dev	3.368

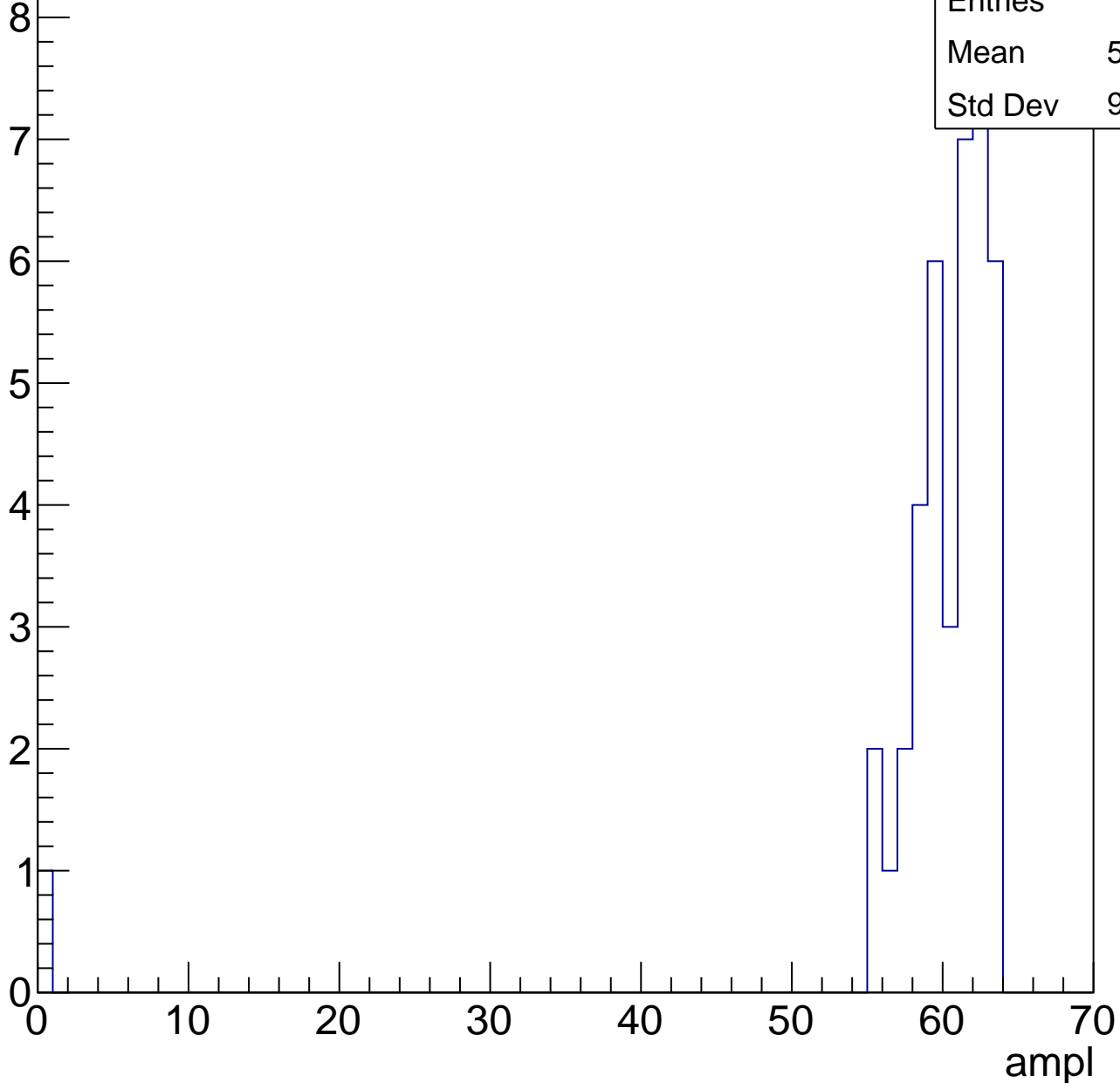


B1L003S, U13-ch109, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	58.67
Std Dev	9.655



B1L003S, U13-ch109, adc6

calib_packv5_042523_0143.root, FC#13, port D2

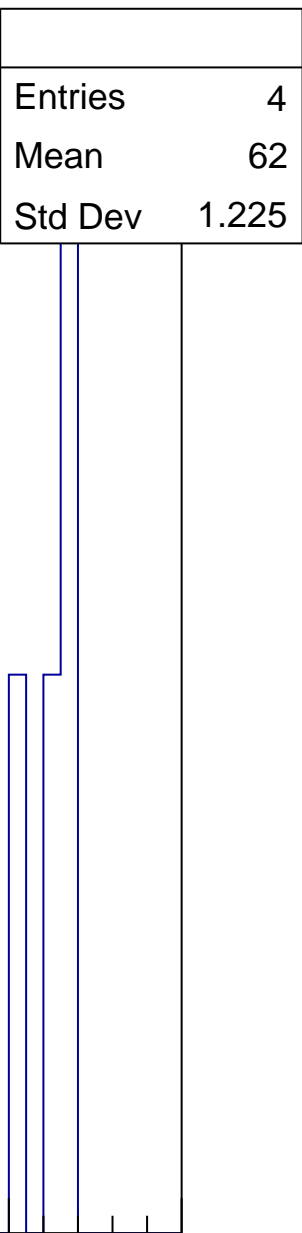
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

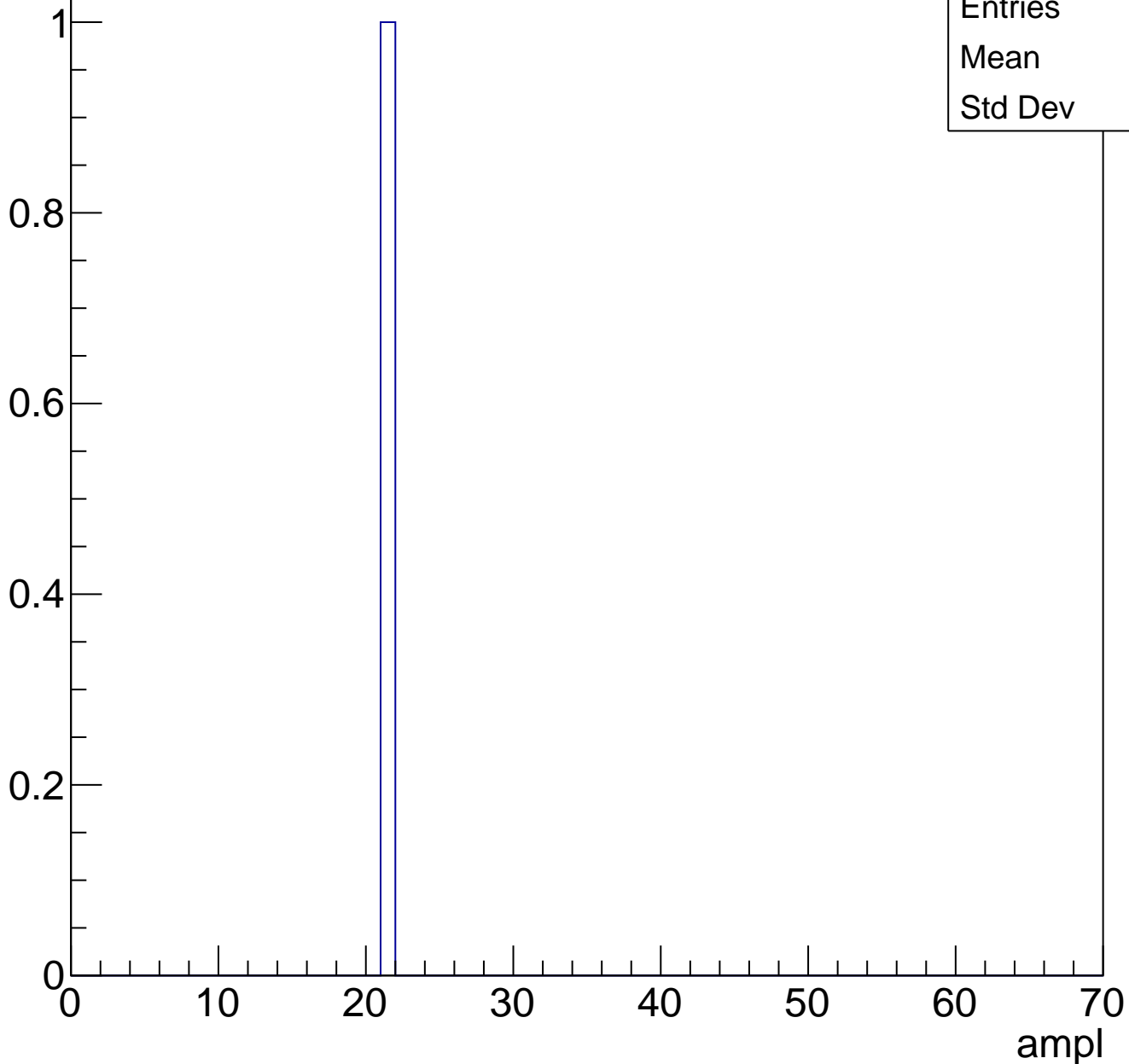
ampl



B1L003S, U13-ch109, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch110, adc0

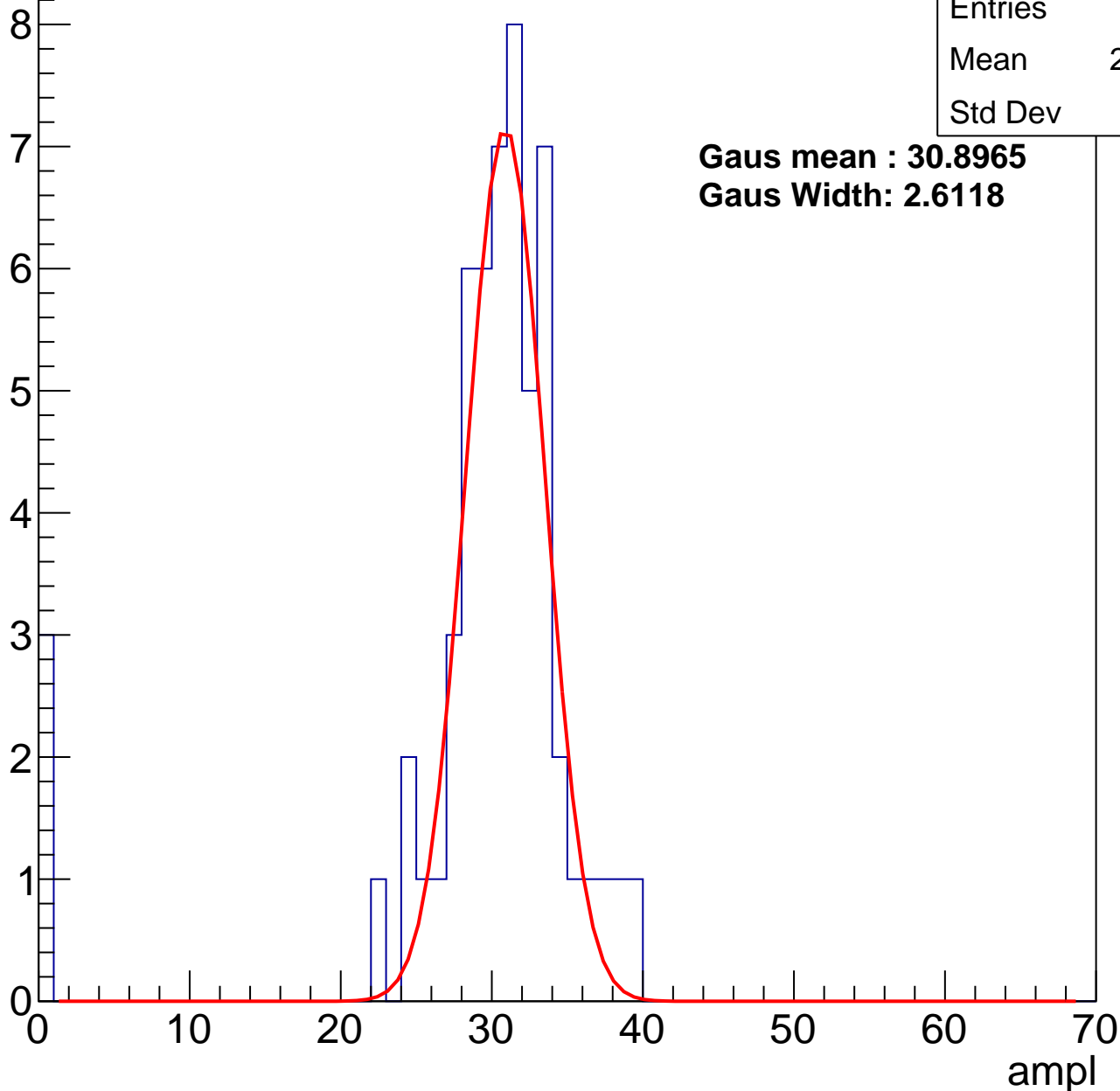
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	28.88
Std Dev	7.54

Gaus mean : 30.8965

Gaus Width: 2.6118



B1L003S, U13-ch110, adc1

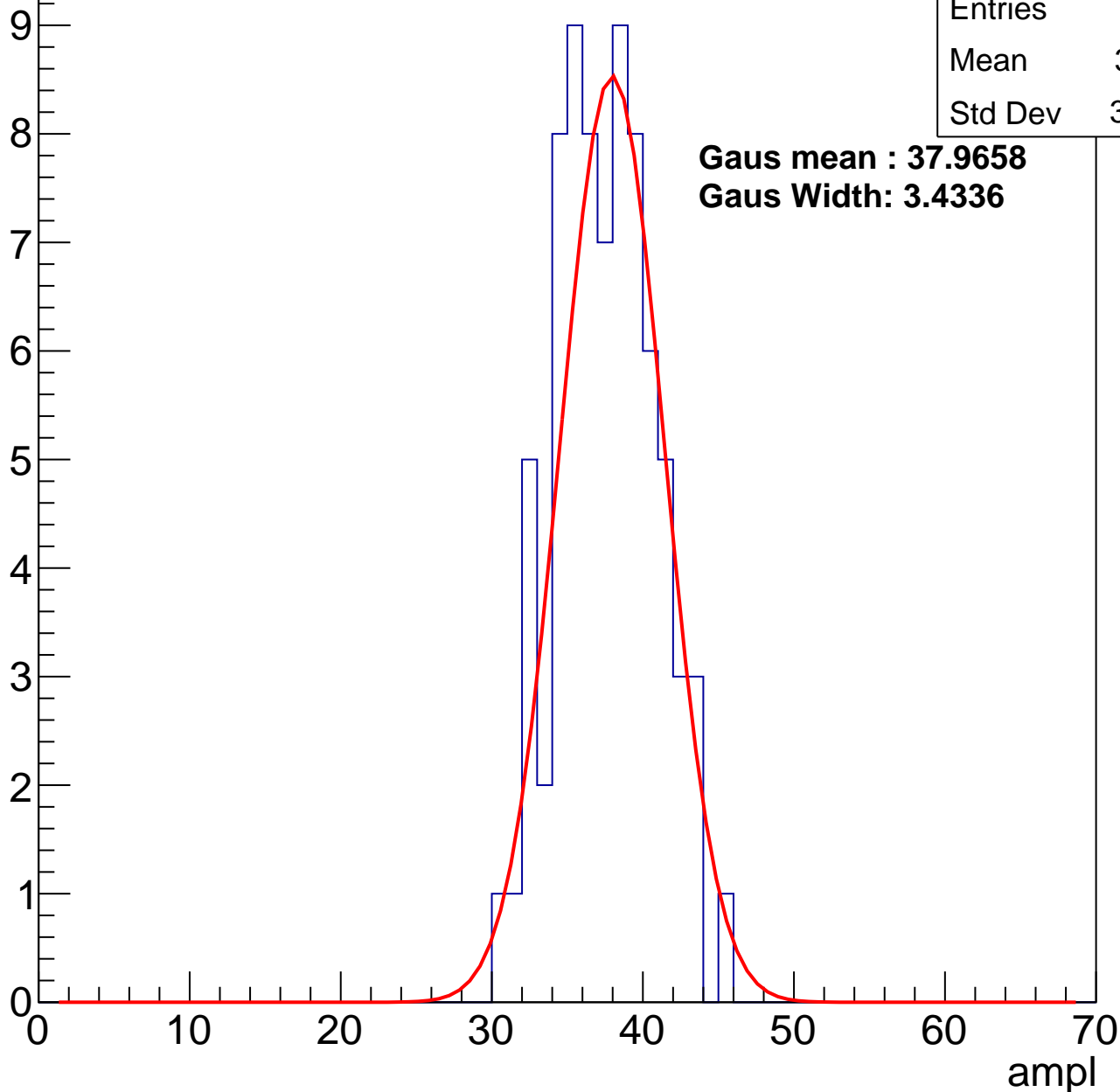
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	37.11
Std Dev	3.202

Gaus mean : 37.9658

Gaus Width: 3.4336



B1L003S, U13-ch110, adc2

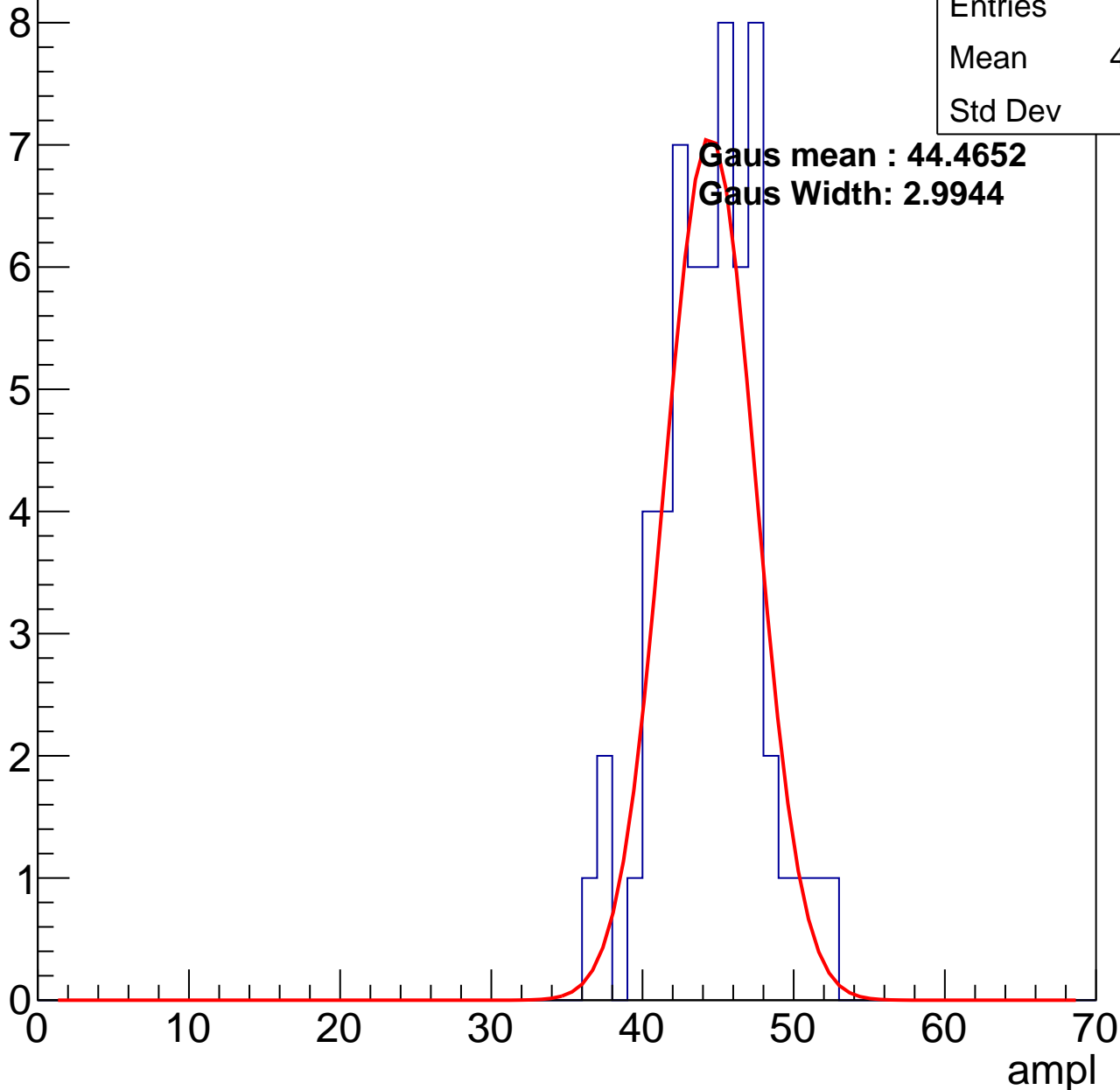
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.05
Std Dev	3.27

Gaus mean : 44.4652

Gaus Width: 2.9944

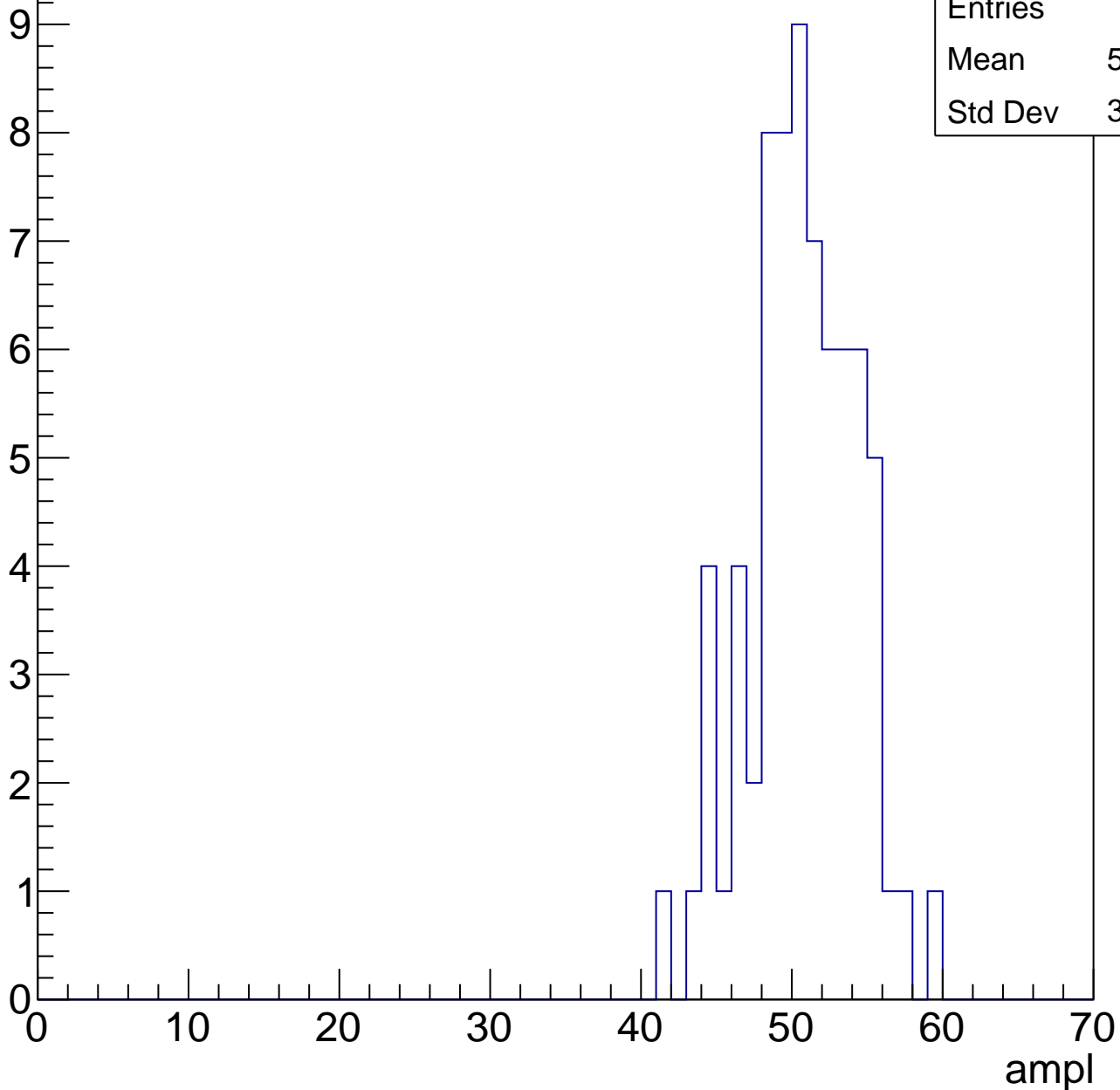


B1L003S, U13-ch110, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	50.24
Std Dev	3.538

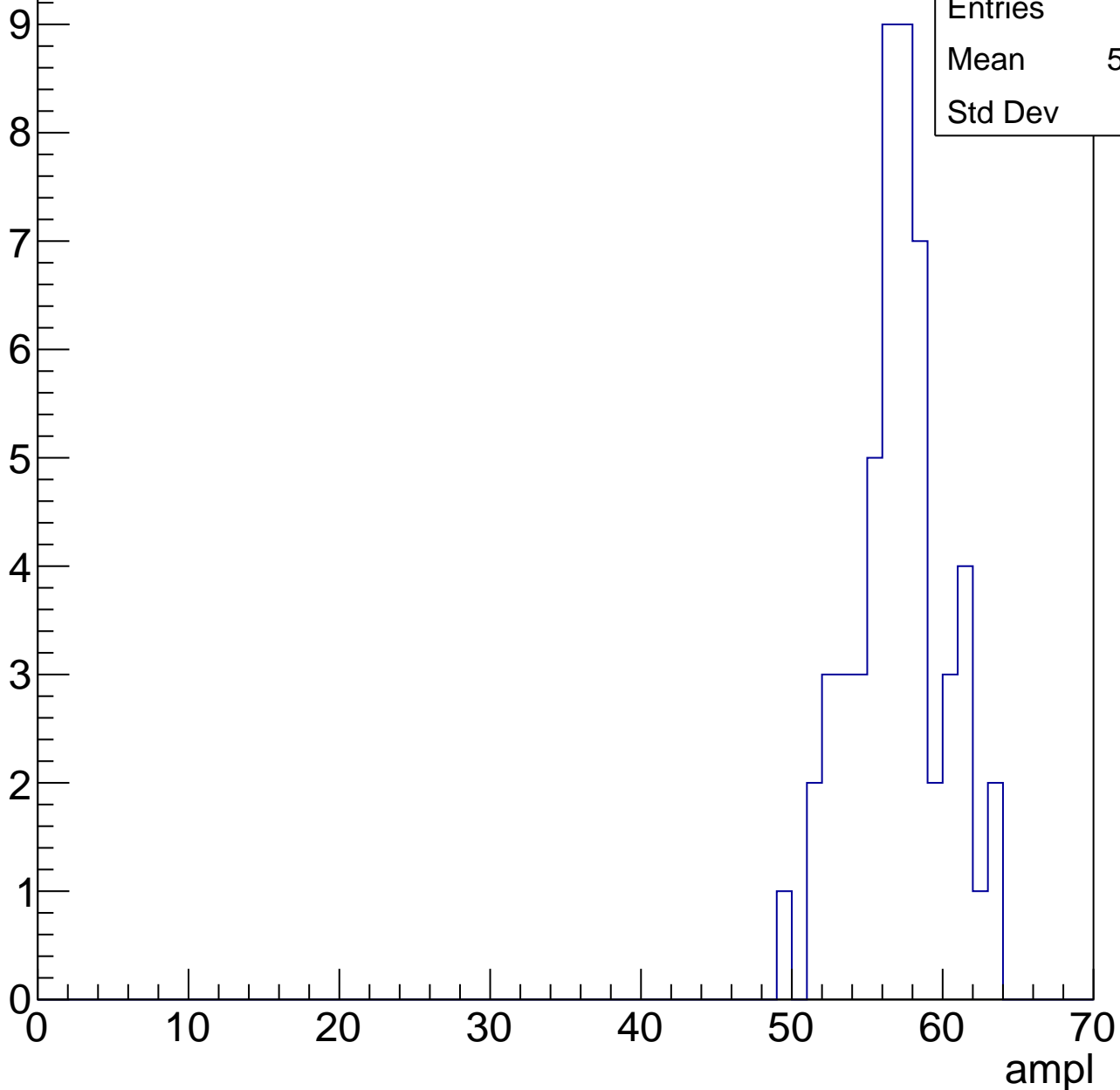


B1L003S, U13-ch110, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.59
Std Dev	3.07

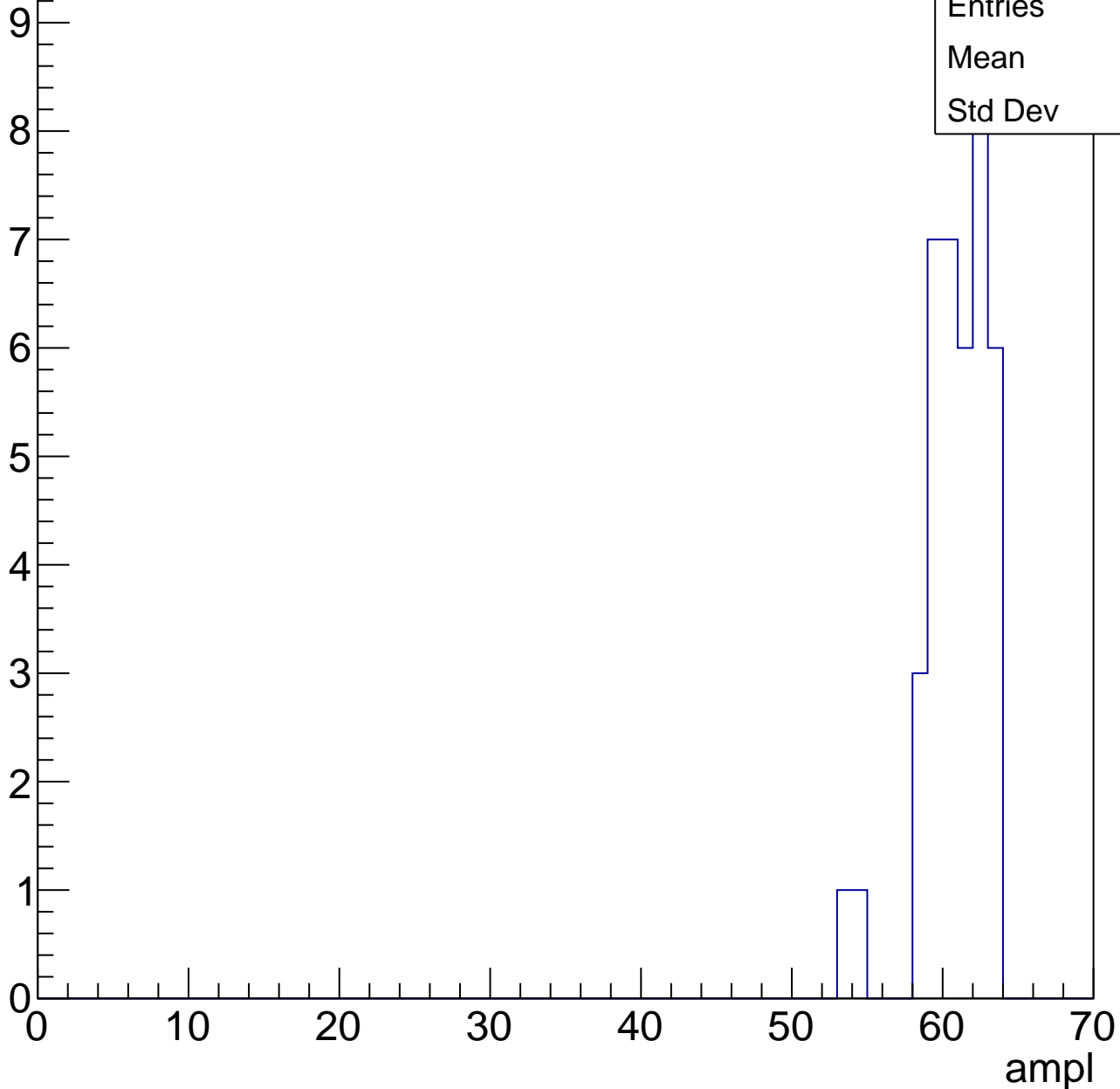


B1L003S, U13-ch110, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	60.4
Std Dev	2.2



B1L003S, U13-ch110, adc6

calib_packv5_042523_0143.root, FC#13, port D2

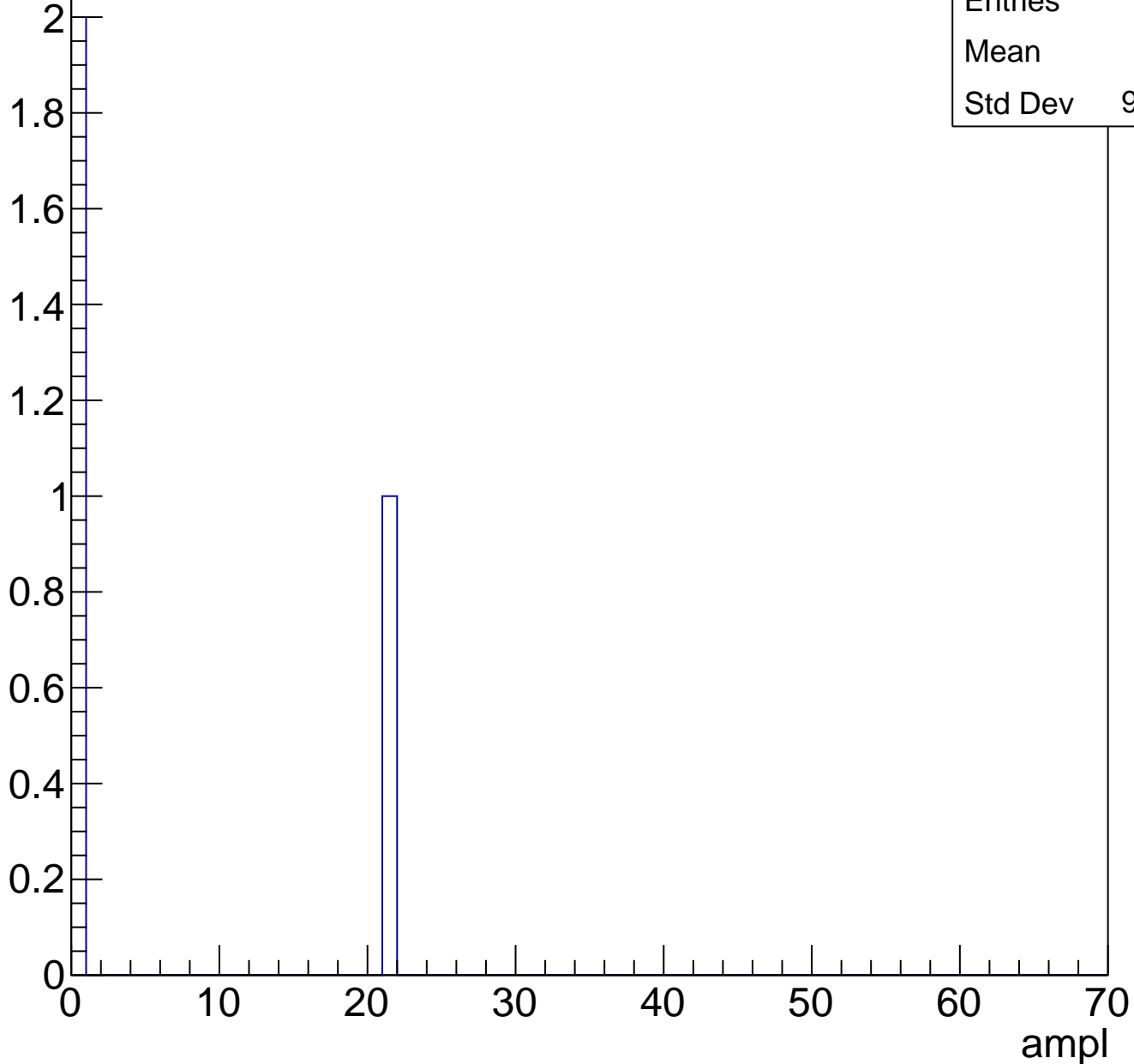
Entry



B1L003S, U13-ch110, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L003S, U13-ch111, adc0

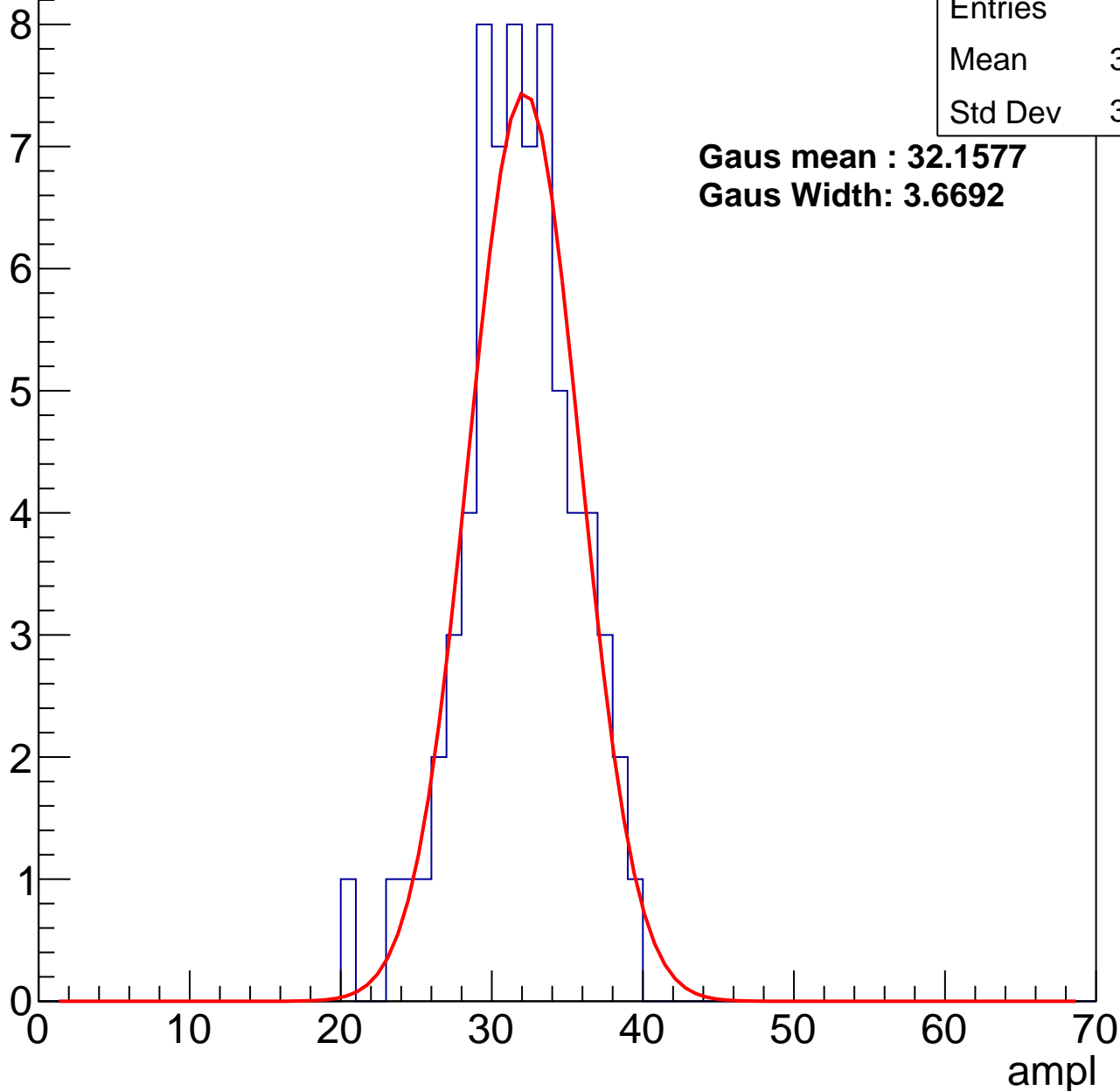
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	31.36
Std Dev	3.688

Gaus mean : 32.1577

Gaus Width: 3.6692



B1L003S, U13-ch111, adc1

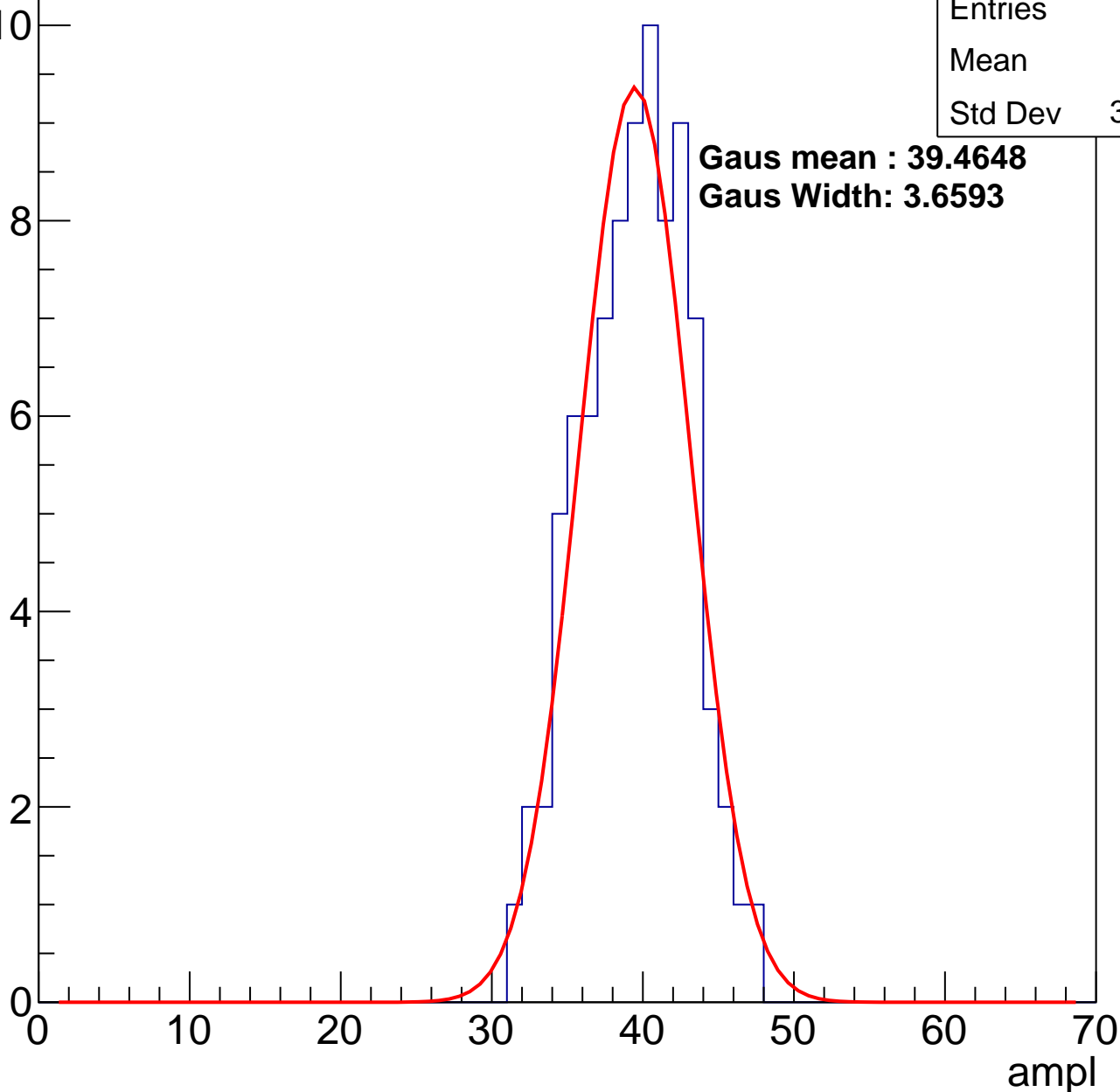
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	87
Mean	39
Std Dev	3.457

Gaus mean : 39.4648

Gaus Width: 3.6593



B1L003S, U13-ch111, adc2

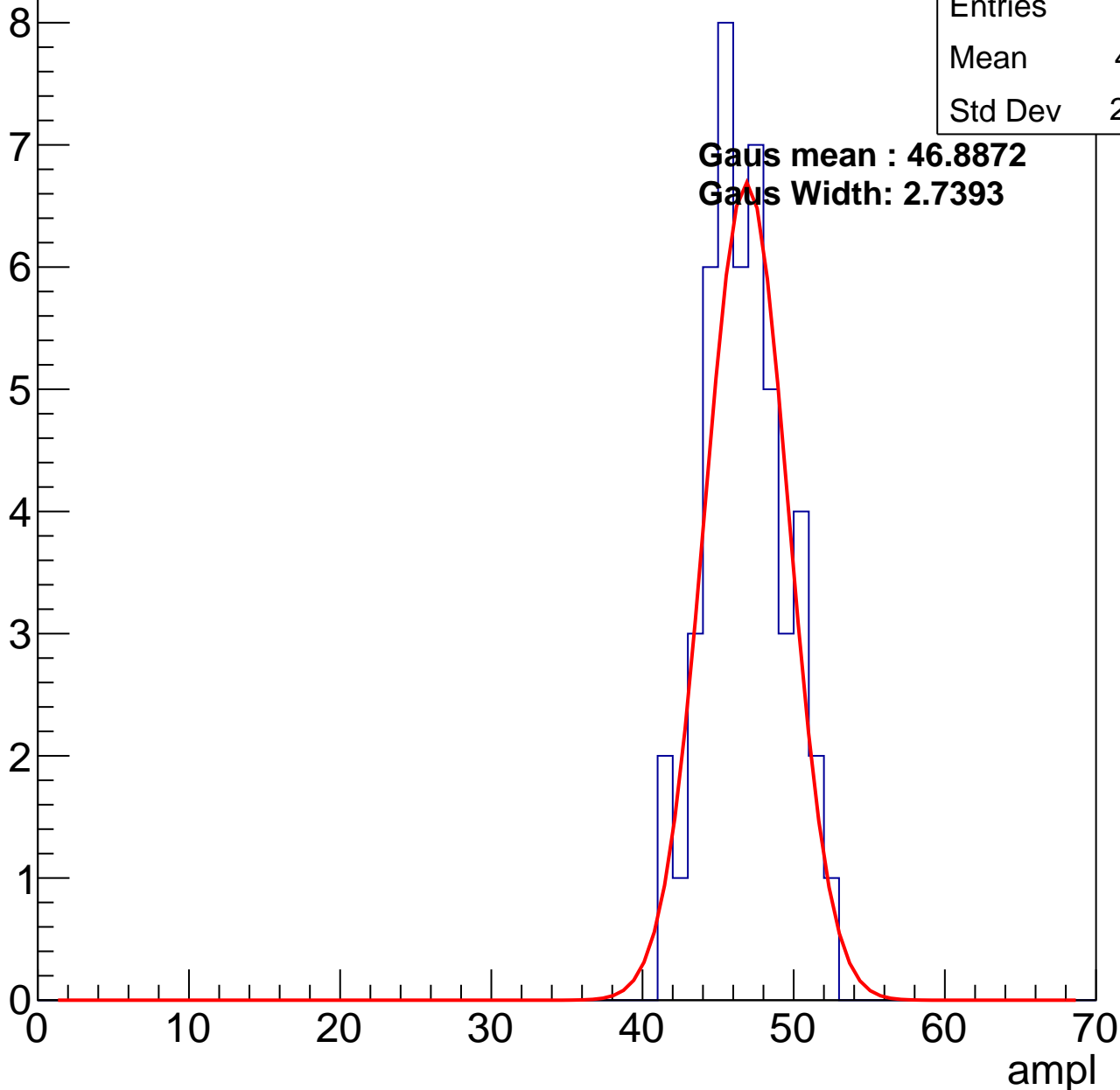
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	46.31
Std Dev	2.599

Gaus mean : 46.8872

Gaus Width: 2.7393

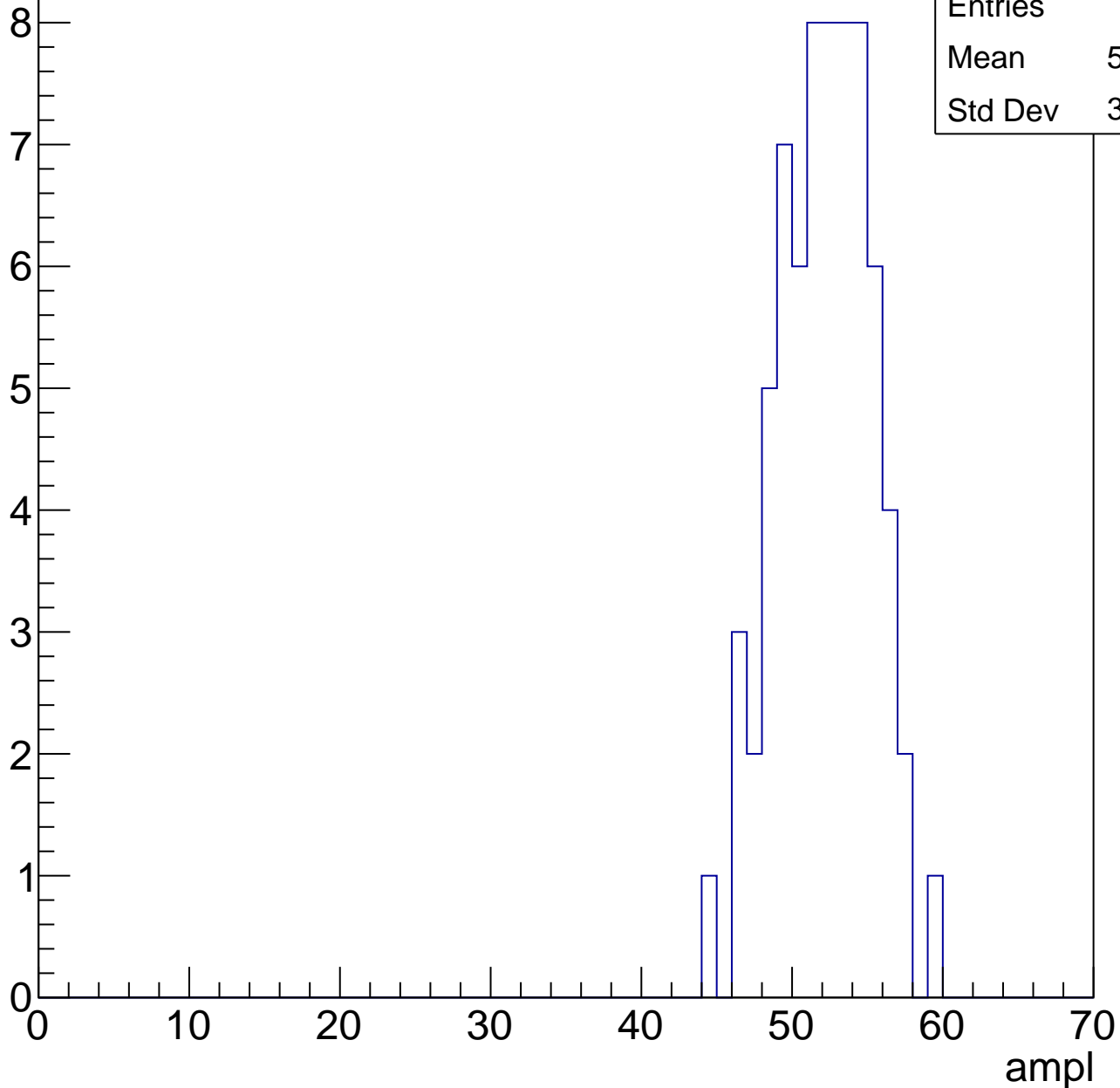


B1L003S, U13-ch111, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	51.68
Std Dev	3.062

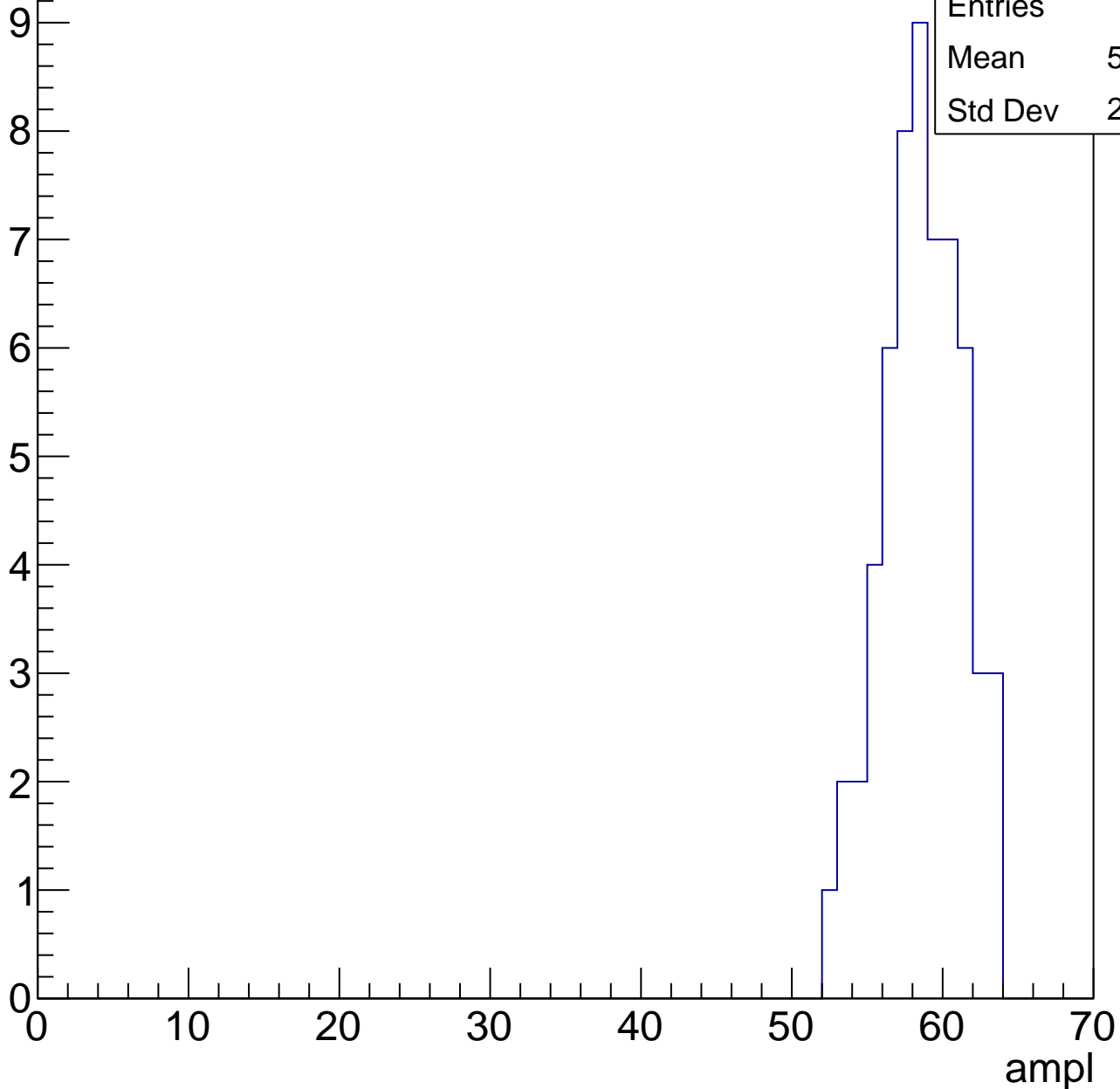


B1L003S, U13-ch111, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.17
Std Dev	2.614

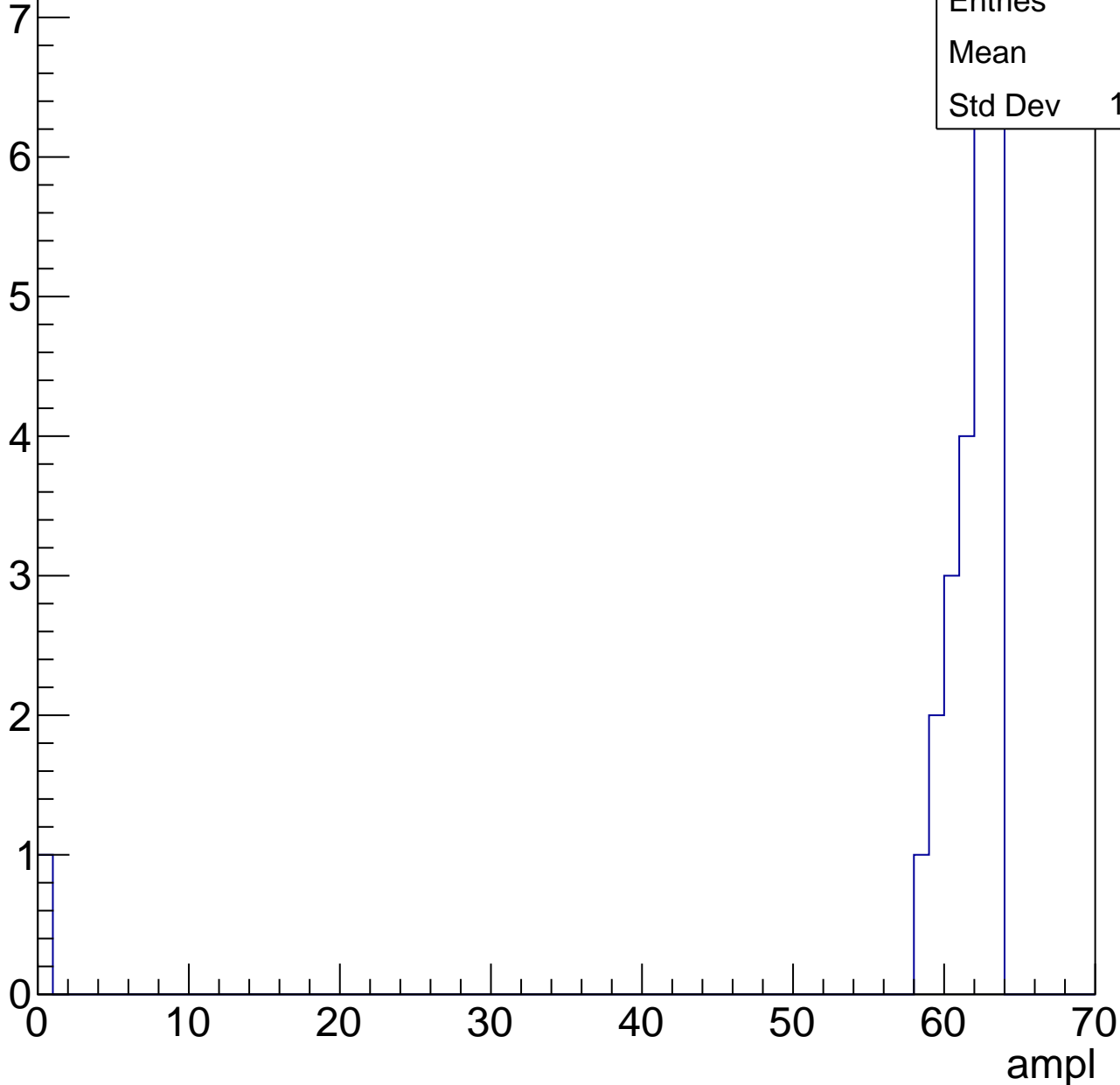


B1L003S, U13-ch111, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	25
Mean	59
Std Dev	12.13



B1L003S, U13-ch111, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch111, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch112, adc0

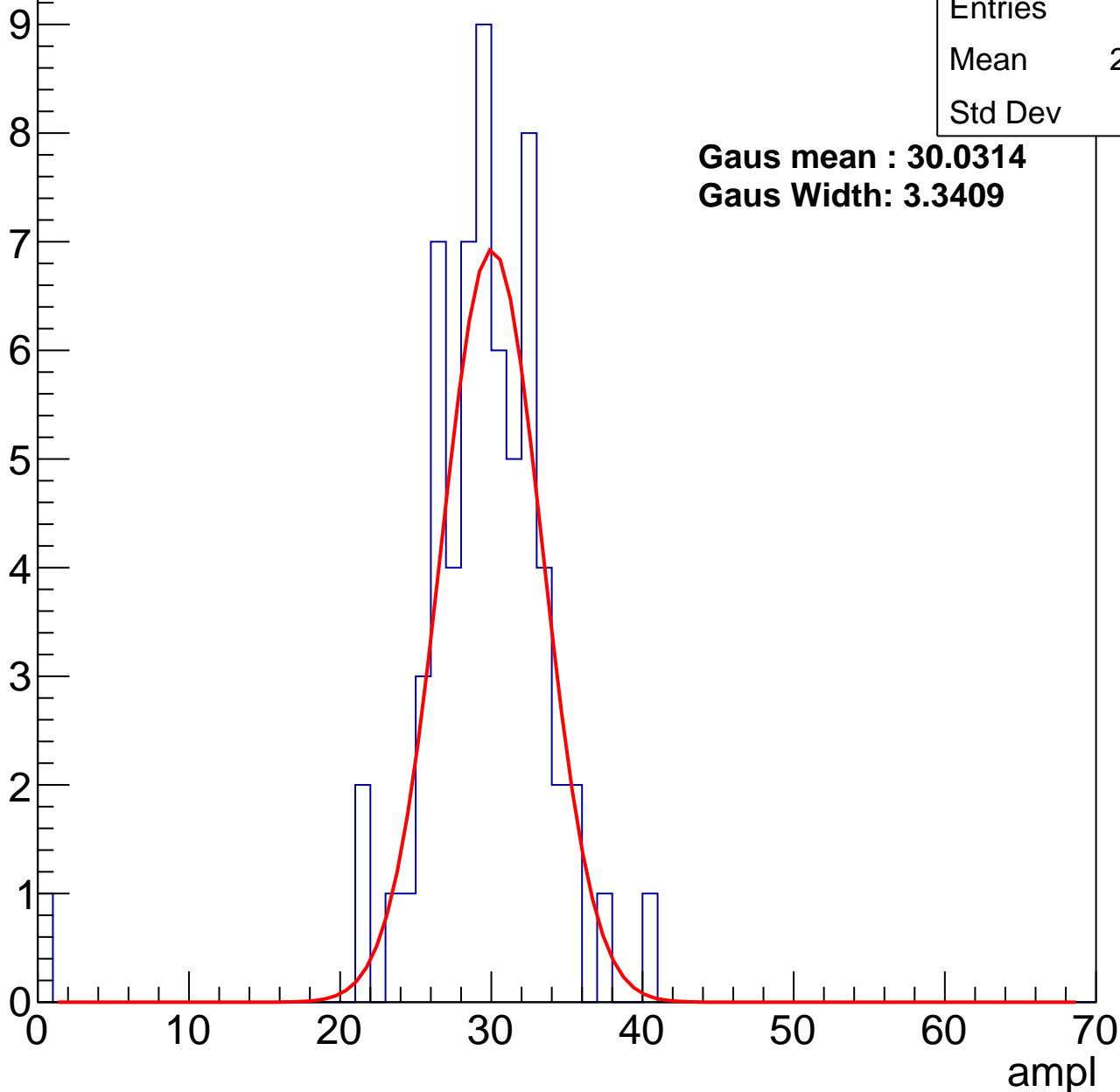
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.89
Std Dev	5.05

Gaus mean : 30.0314

Gaus Width: 3.3409



B1L003S, U13-ch112, adc1

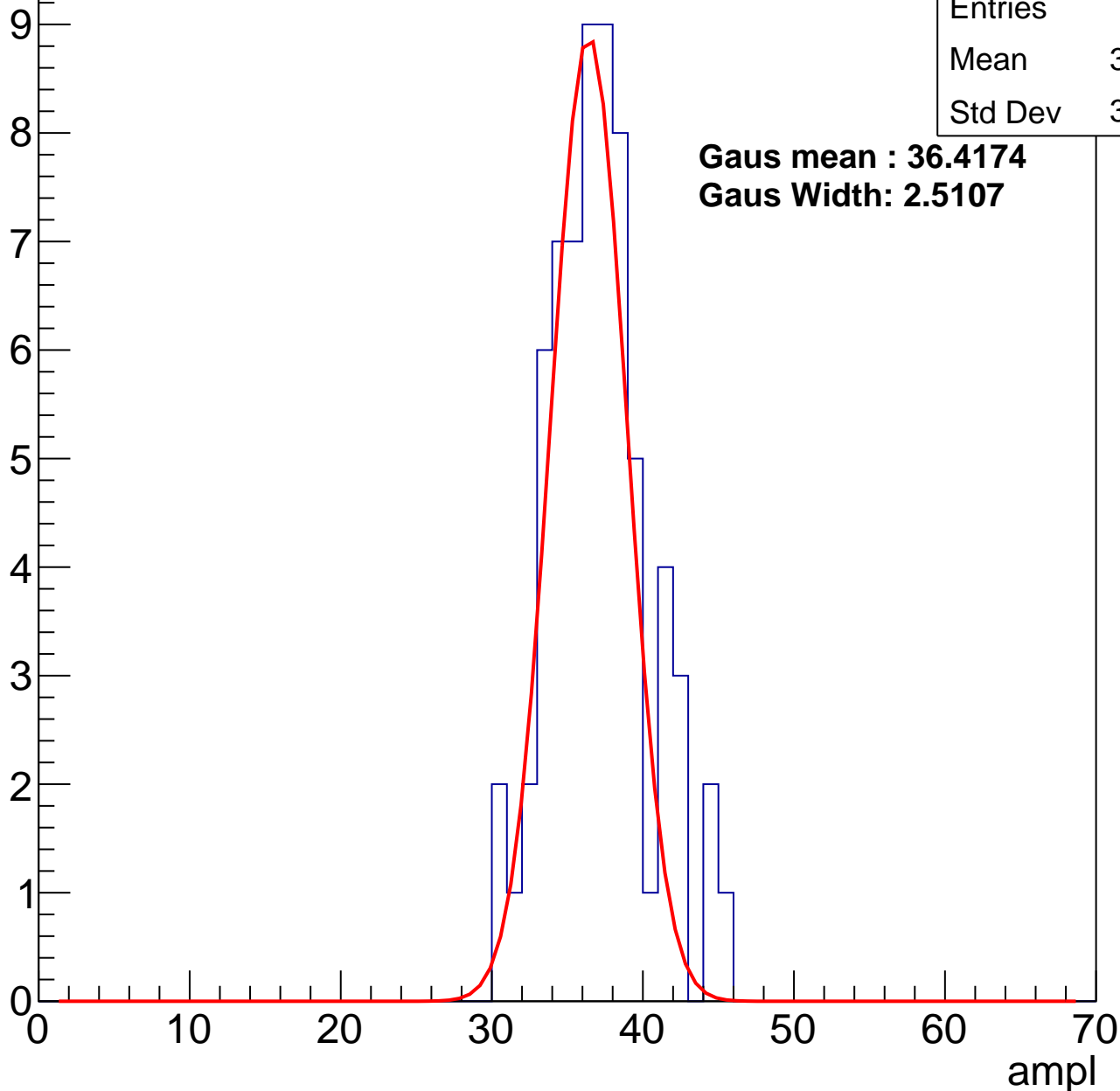
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.64
Std Dev	3.254

Gaus mean : 36.4174

Gaus Width: 2.5107



B1L003S, U13-ch112, adc2

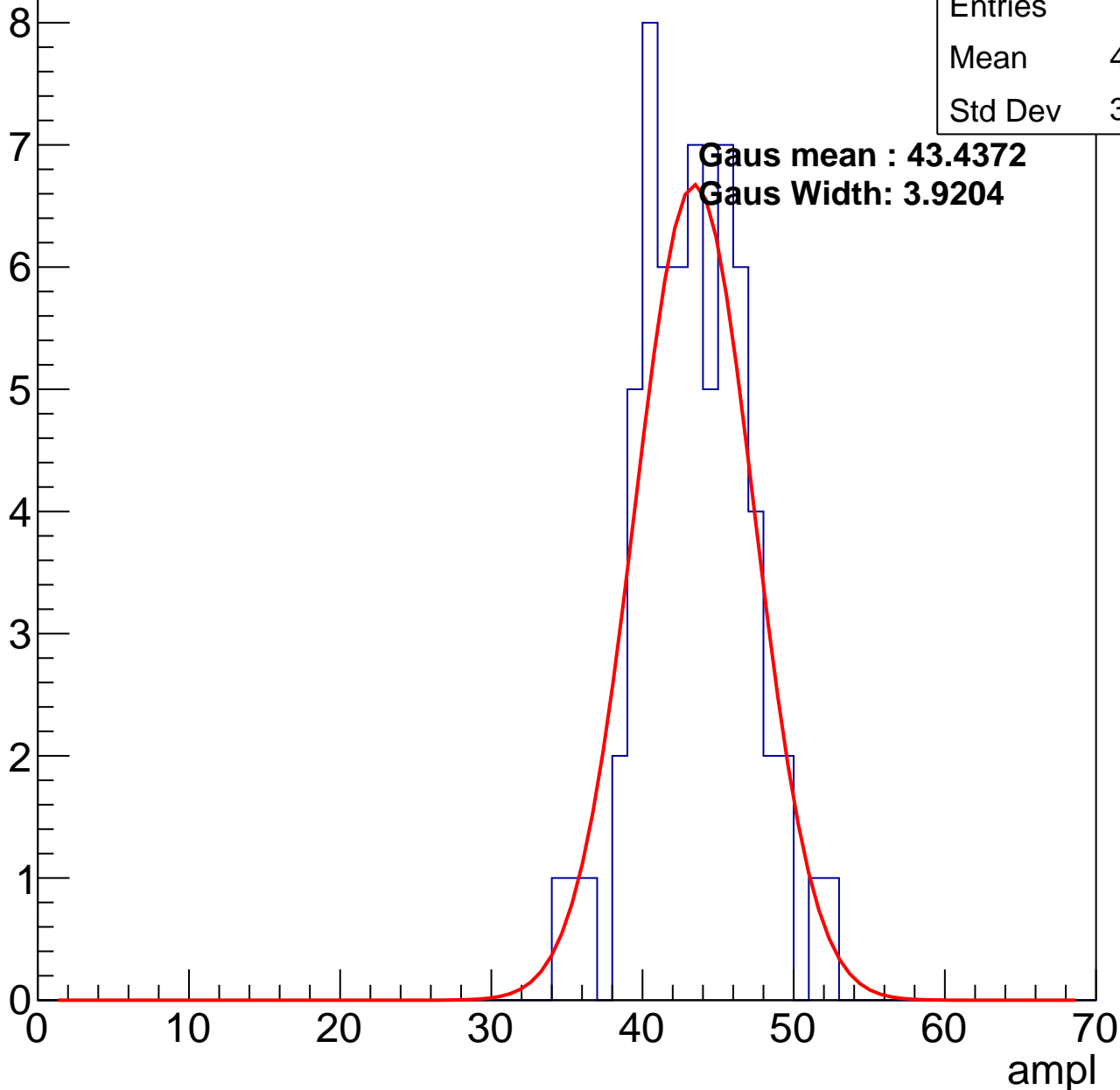
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.94
Std Dev	3.603

Gaus mean : 43.4372

Gaus Width: 3.9204

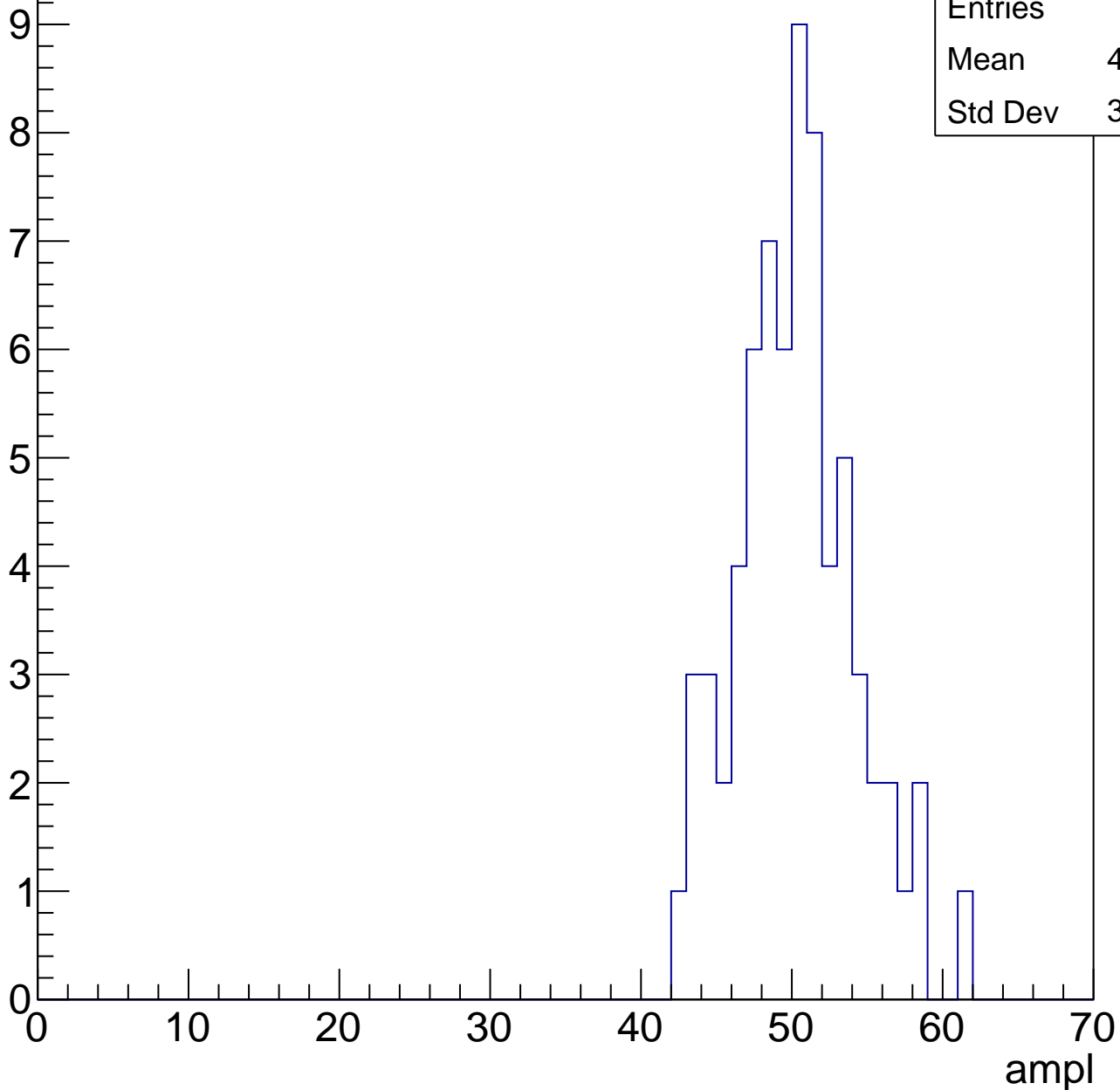


B1L003S, U13-ch112, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.83
Std Dev	3.912

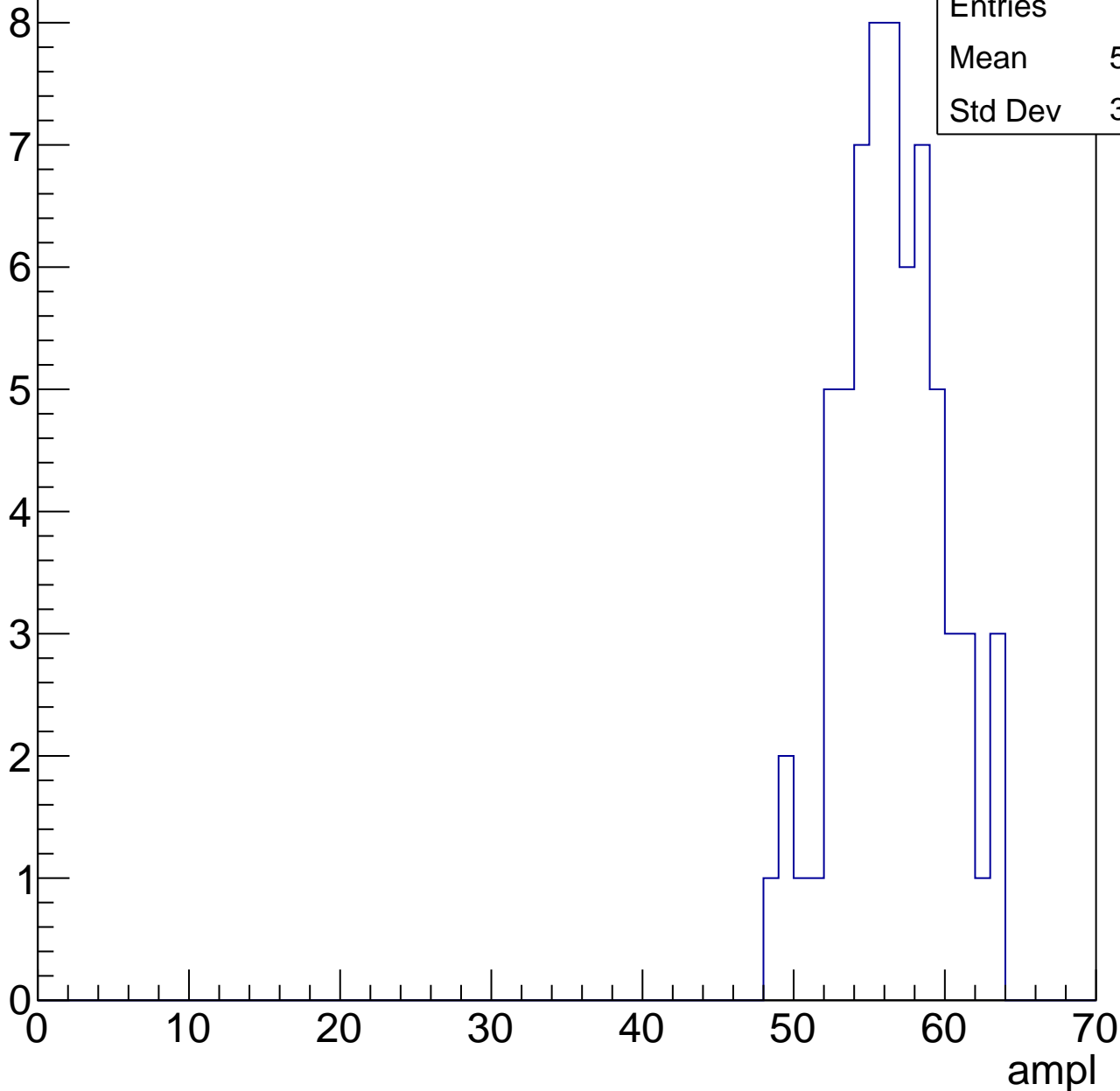


B1L003S, U13-ch112, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	55.98
Std Dev	3.413

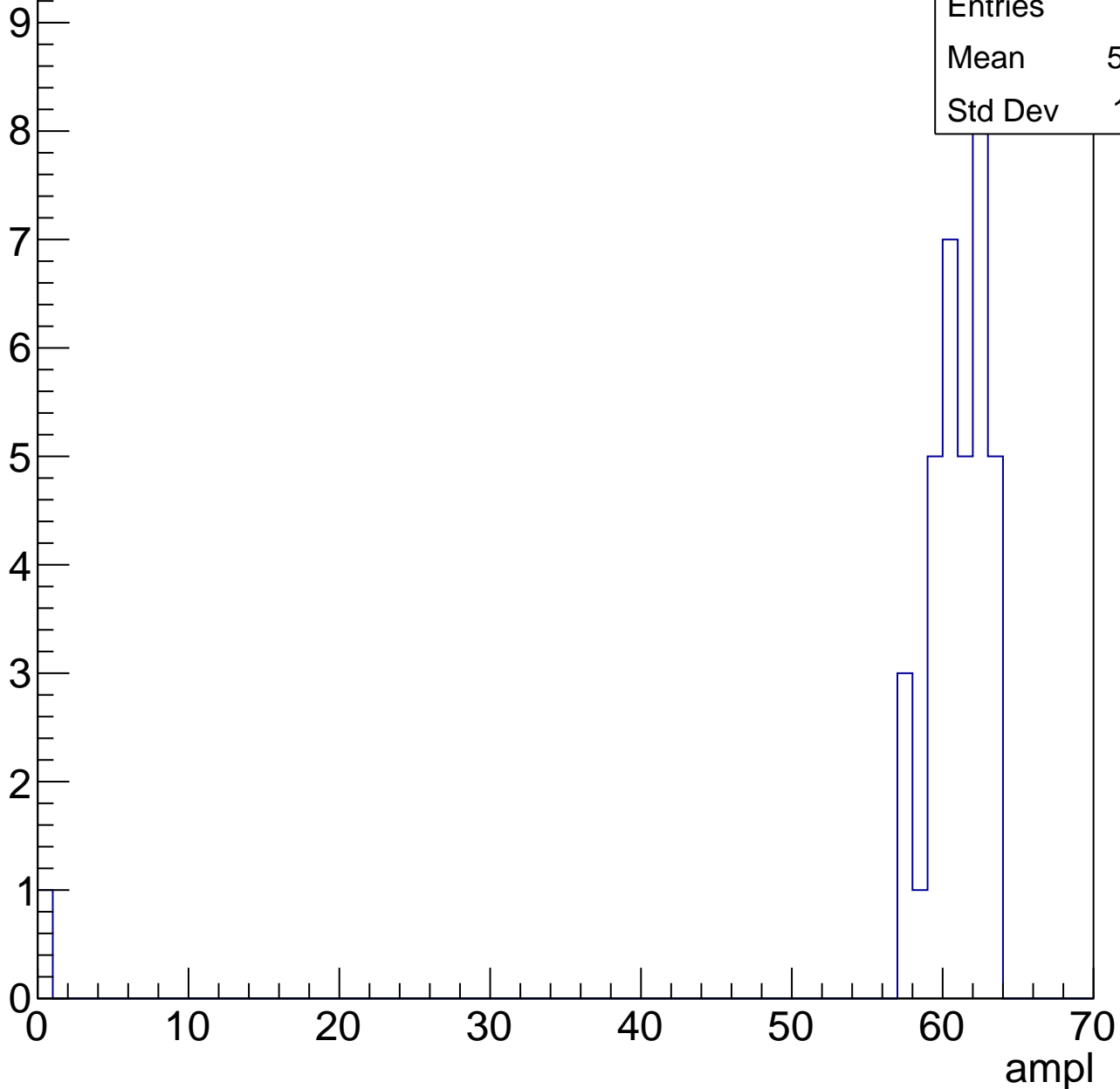


B1L003S, U13-ch112, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	58.94
Std Dev	10.11



B1L003S, U13-ch112, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch112, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch113, adc0

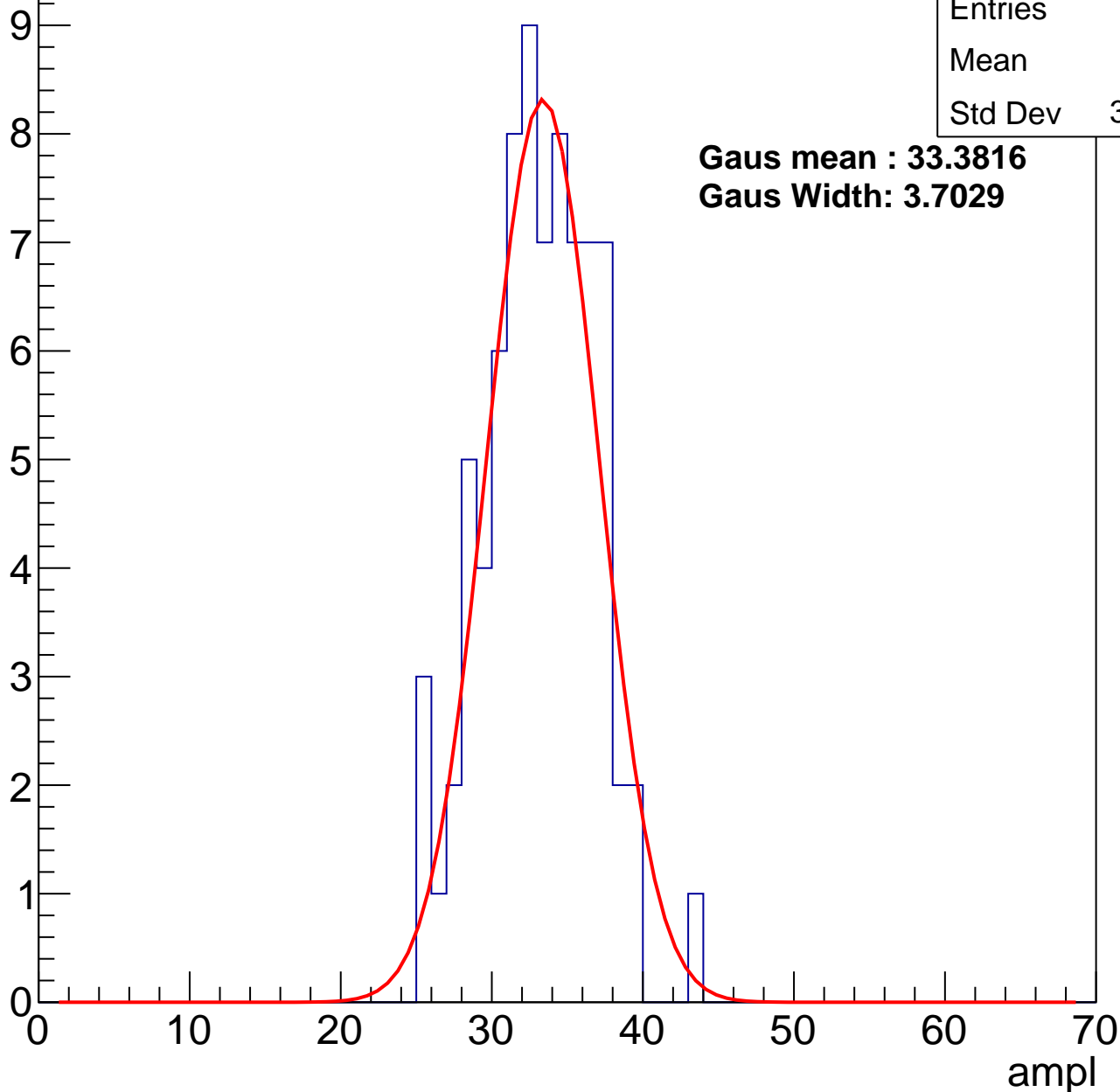
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	32.7
Std Dev	3.598

Gaus mean : 33.3816

Gaus Width: 3.7029



B1L003S, U13-ch113, adc1

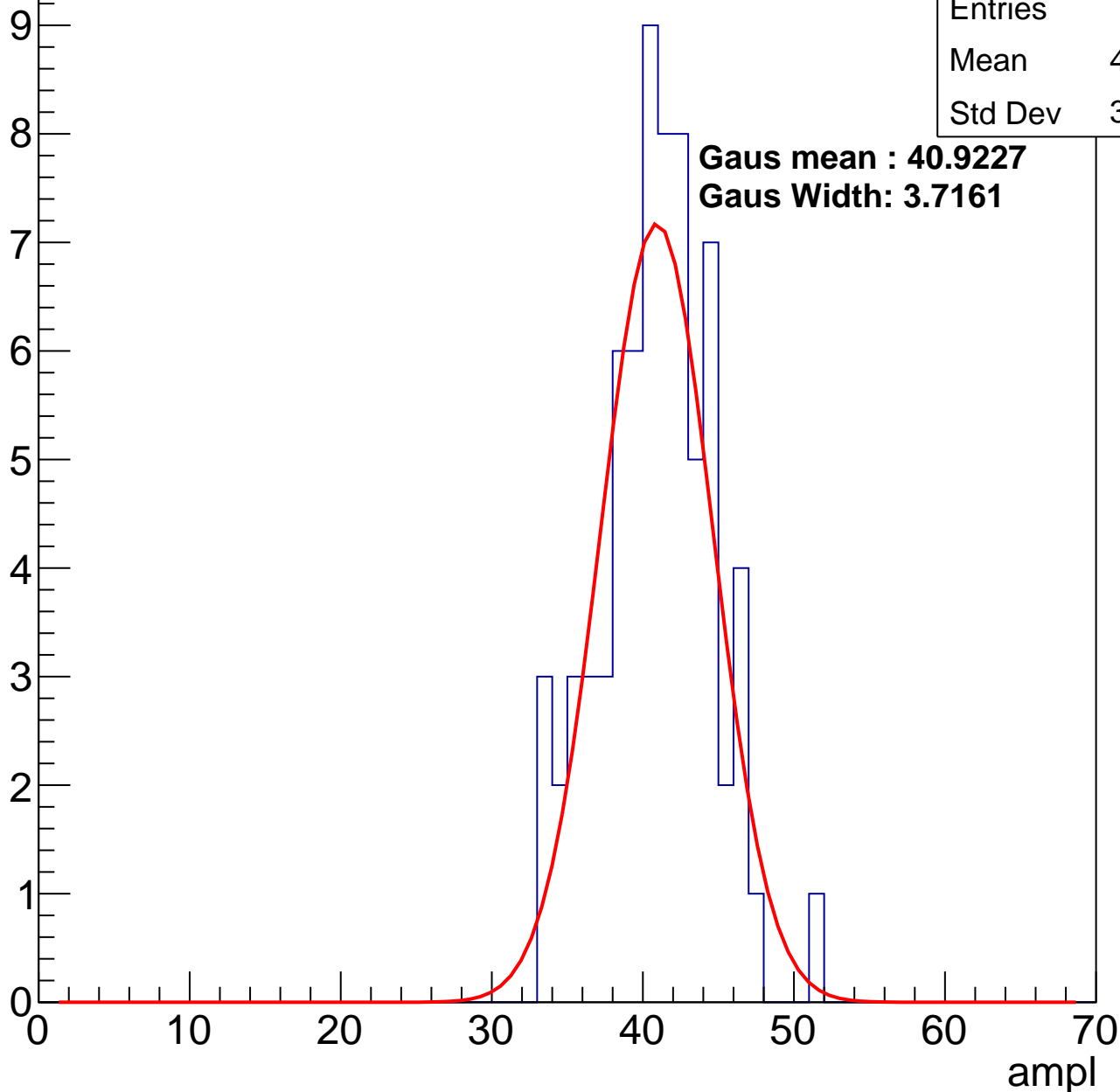
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	40.45
Std Dev	3.649

Gaus mean : 40.9227

Gaus Width: 3.7161



B1L003S, U13-ch113, adc2

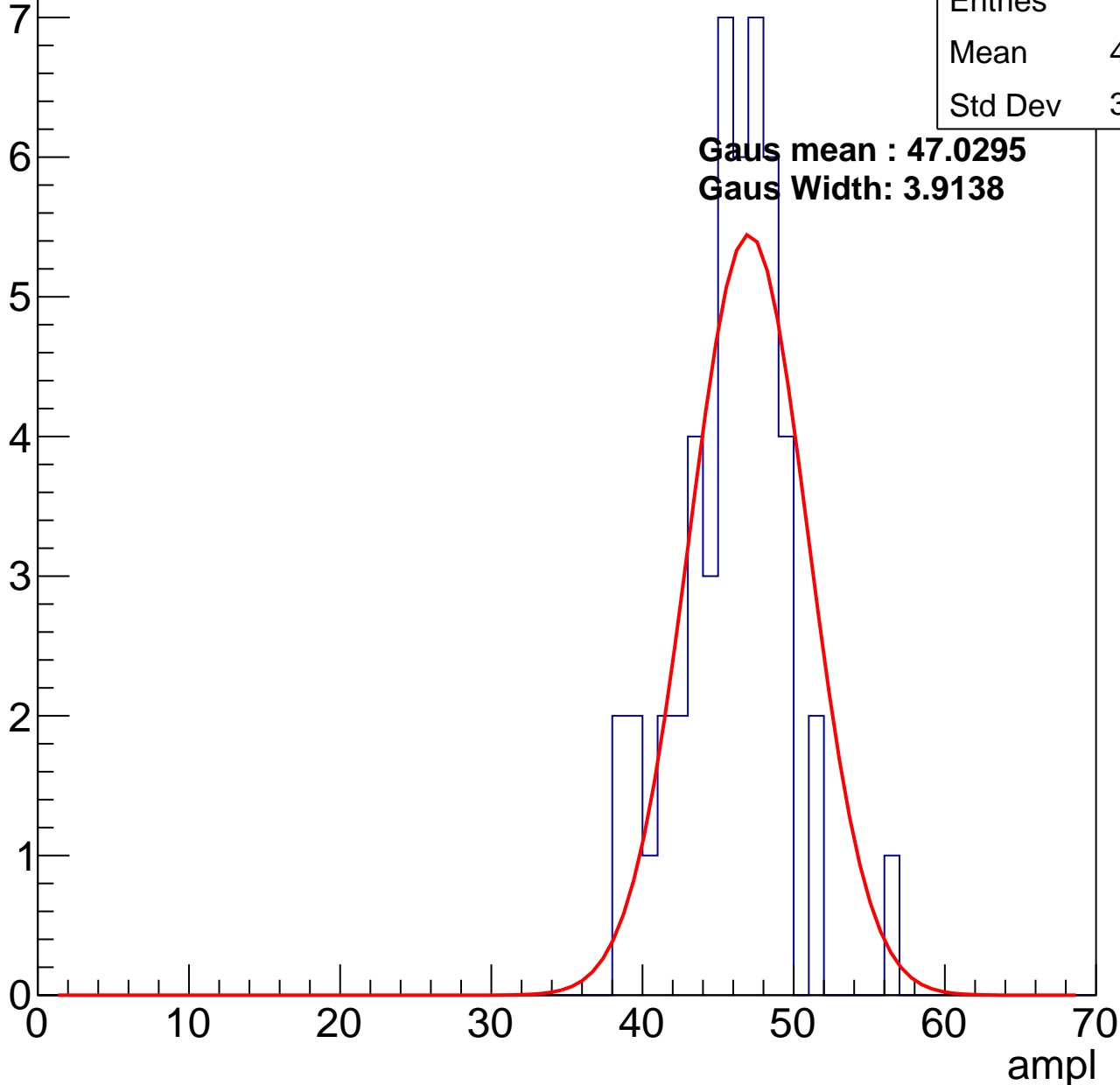
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	45.43
Std Dev	3.499

Gaus mean : 47.0295

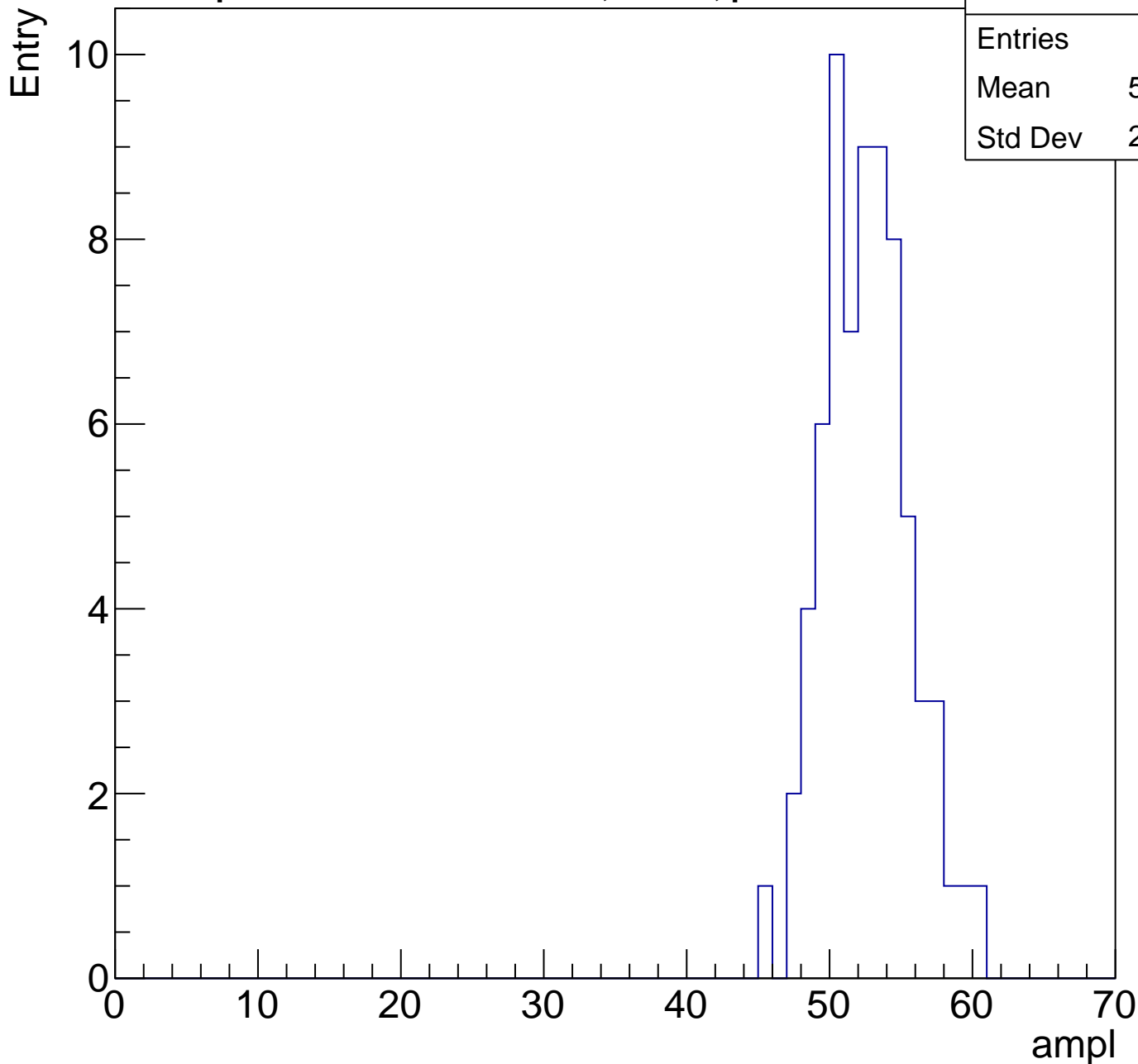
Gaus Width: 3.9138



B1L003S, U13-ch113, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	52.14
Std Dev	2.977

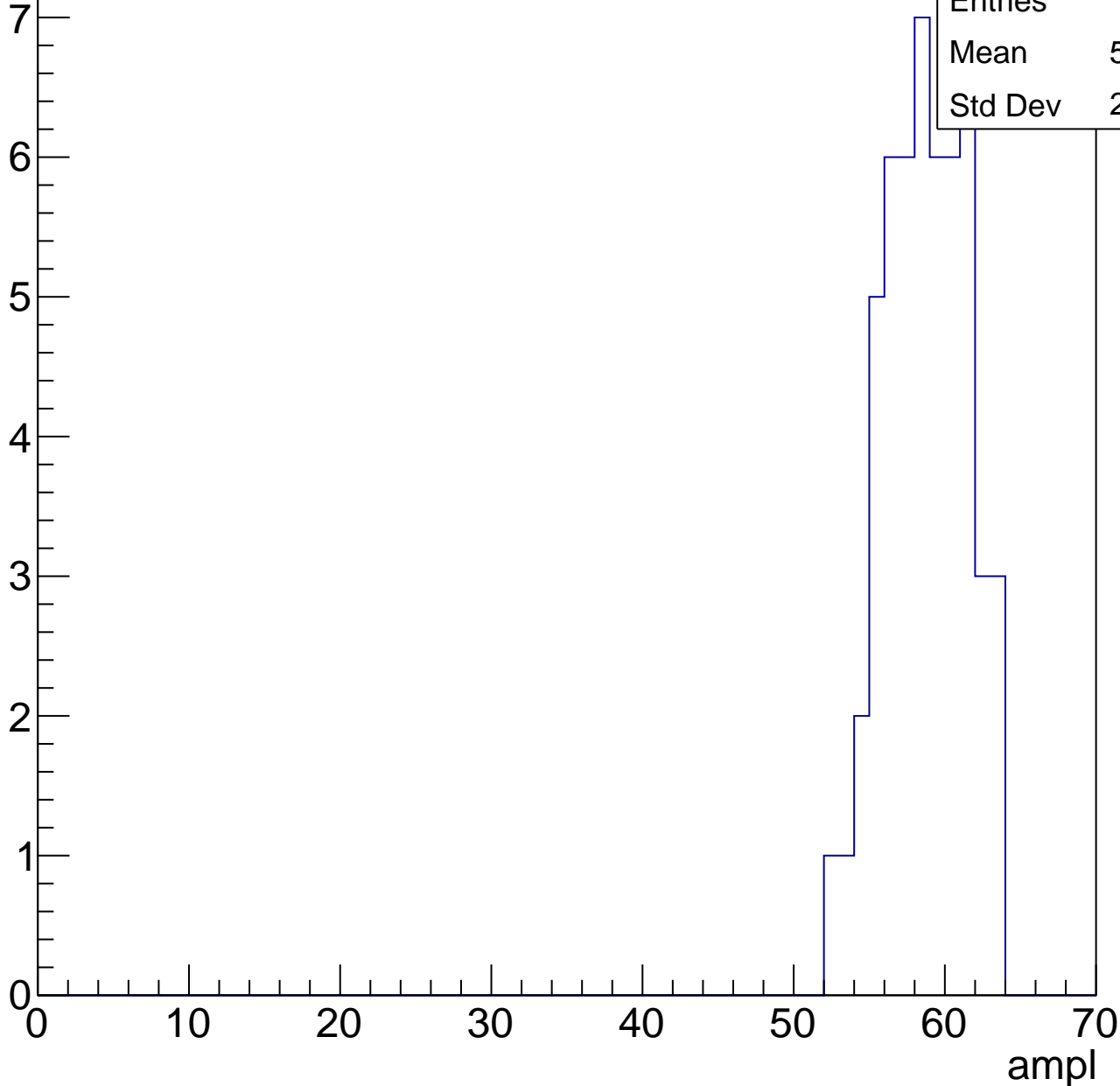


B1L003S, U13-ch113, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.26
Std Dev	2.679

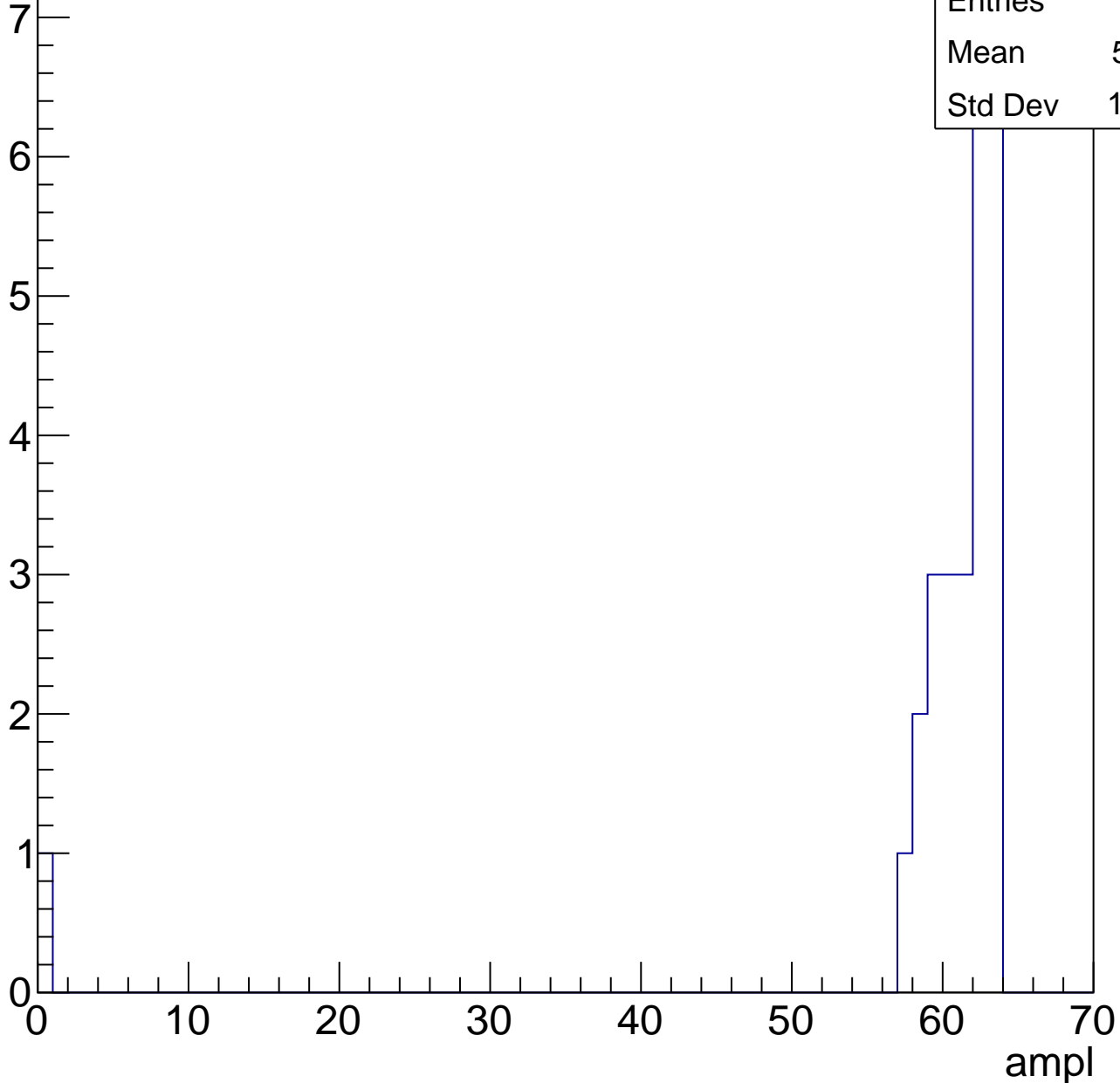


B1L003S, U13-ch113, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	27
Mean	58.81
Std Dev	11.67



B1L003S, U13-ch113, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch113, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L003S, U13-ch114, adc0

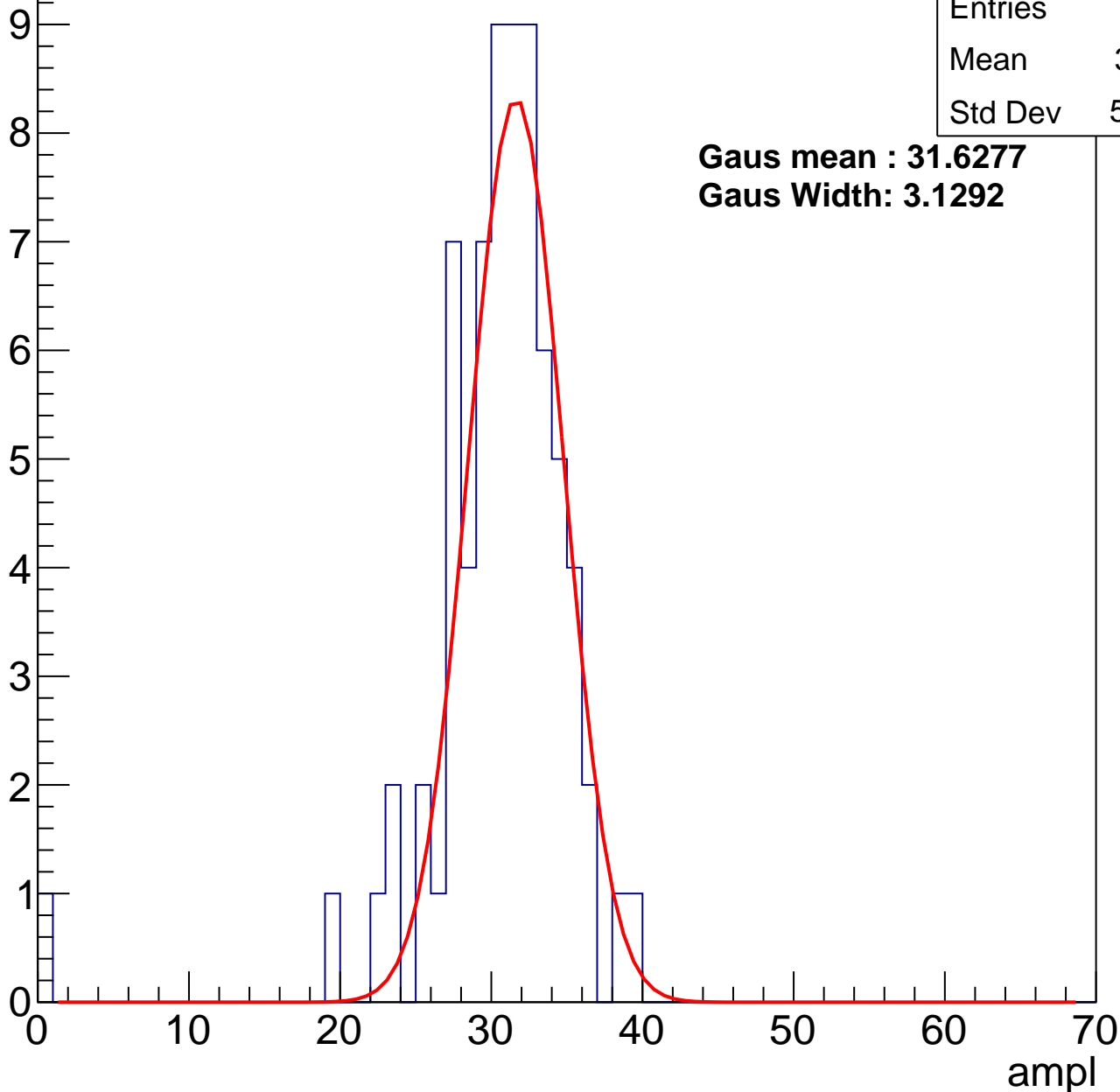
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.01
Std Dev	5.043

Gaus mean : 31.6277

Gaus Width: 3.1292



B1L003S, U13-ch114, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	38.05
Std Dev	3.006

Gaus mean : 38.4514

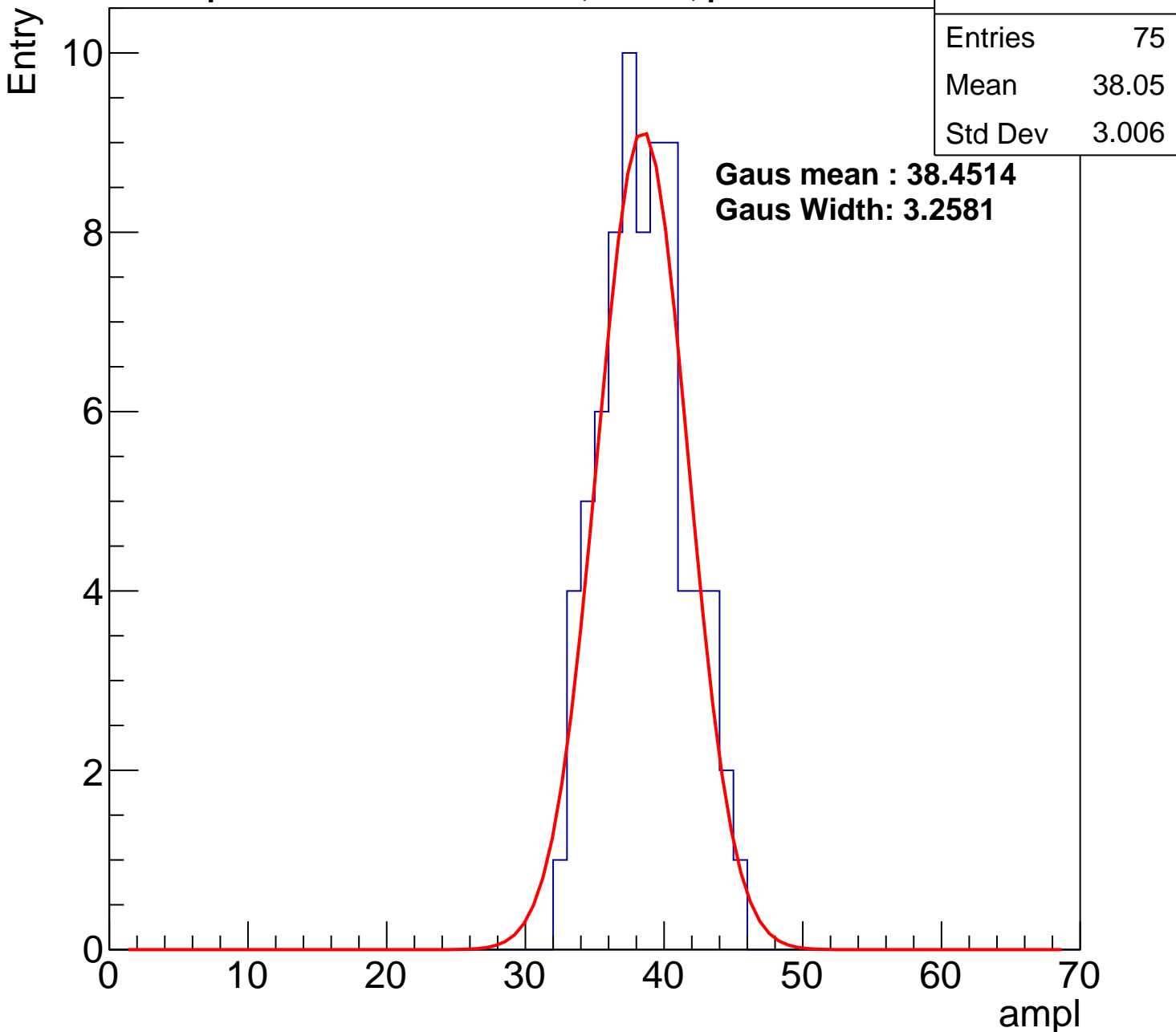
Gaus Width: 3.2581

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch114, adc2

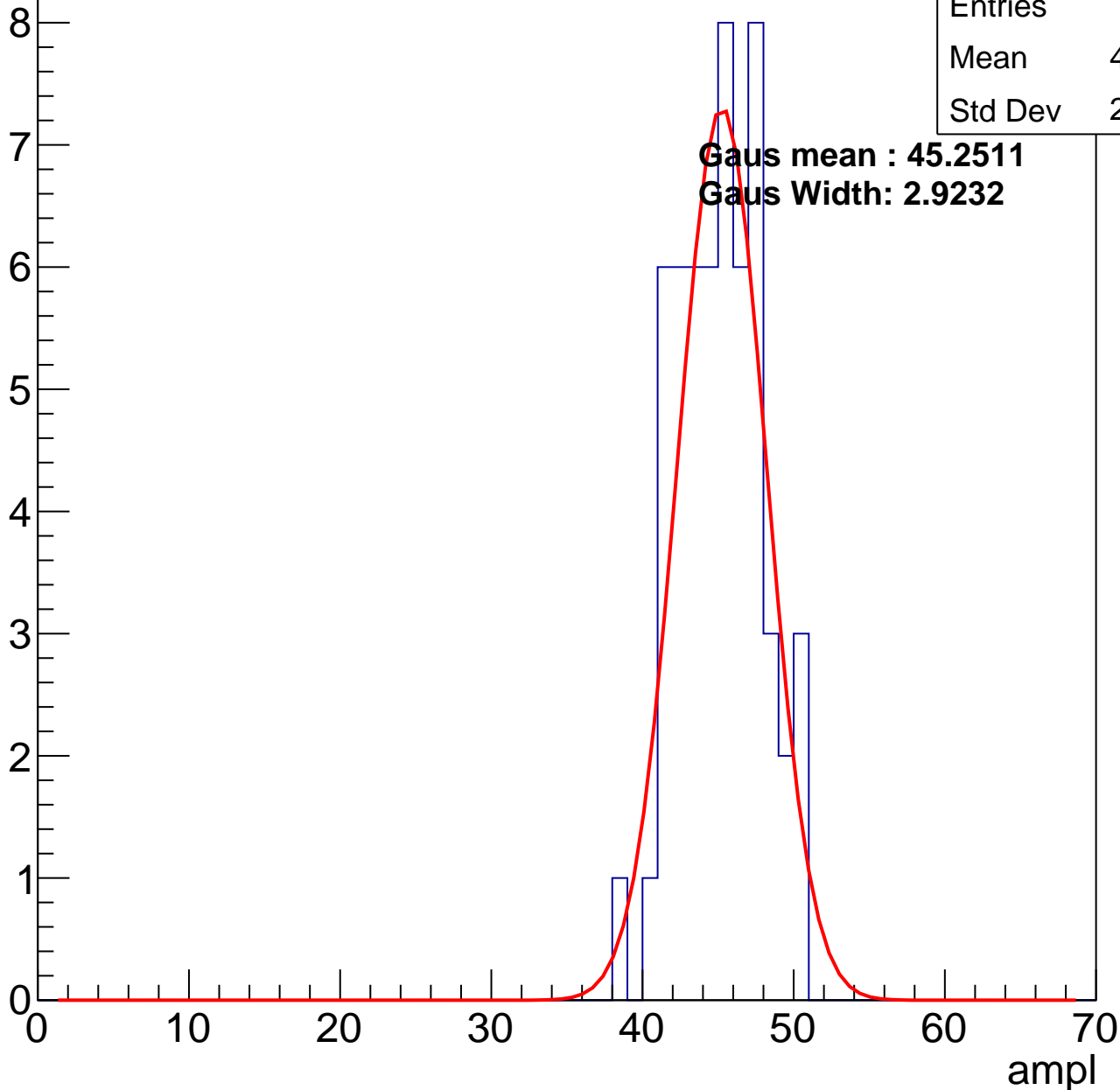
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	44.68
Std Dev	2.739

Gaus mean : 45.2511

Gaus Width: 2.9232

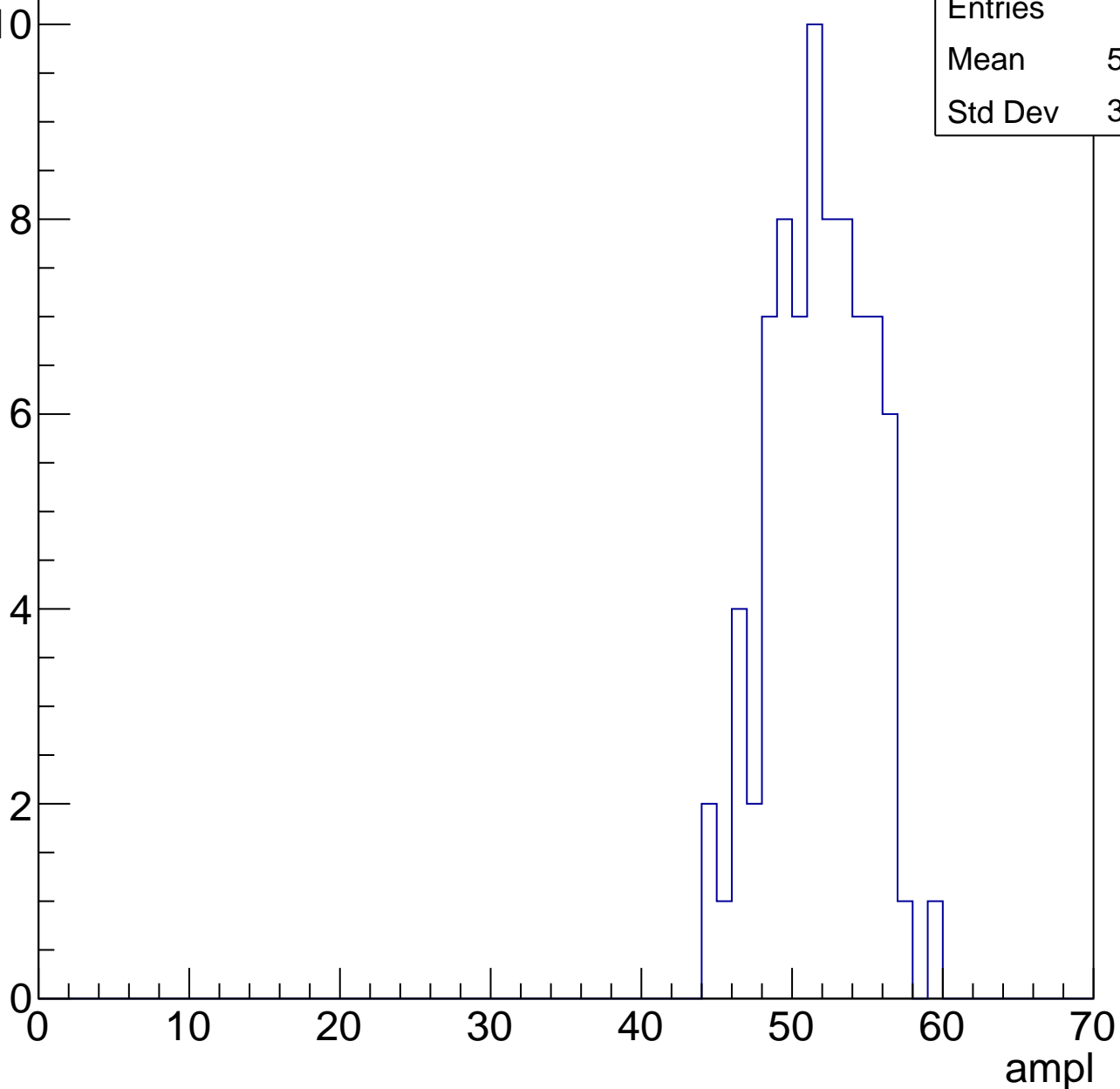


B1L003S, U13-ch114, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

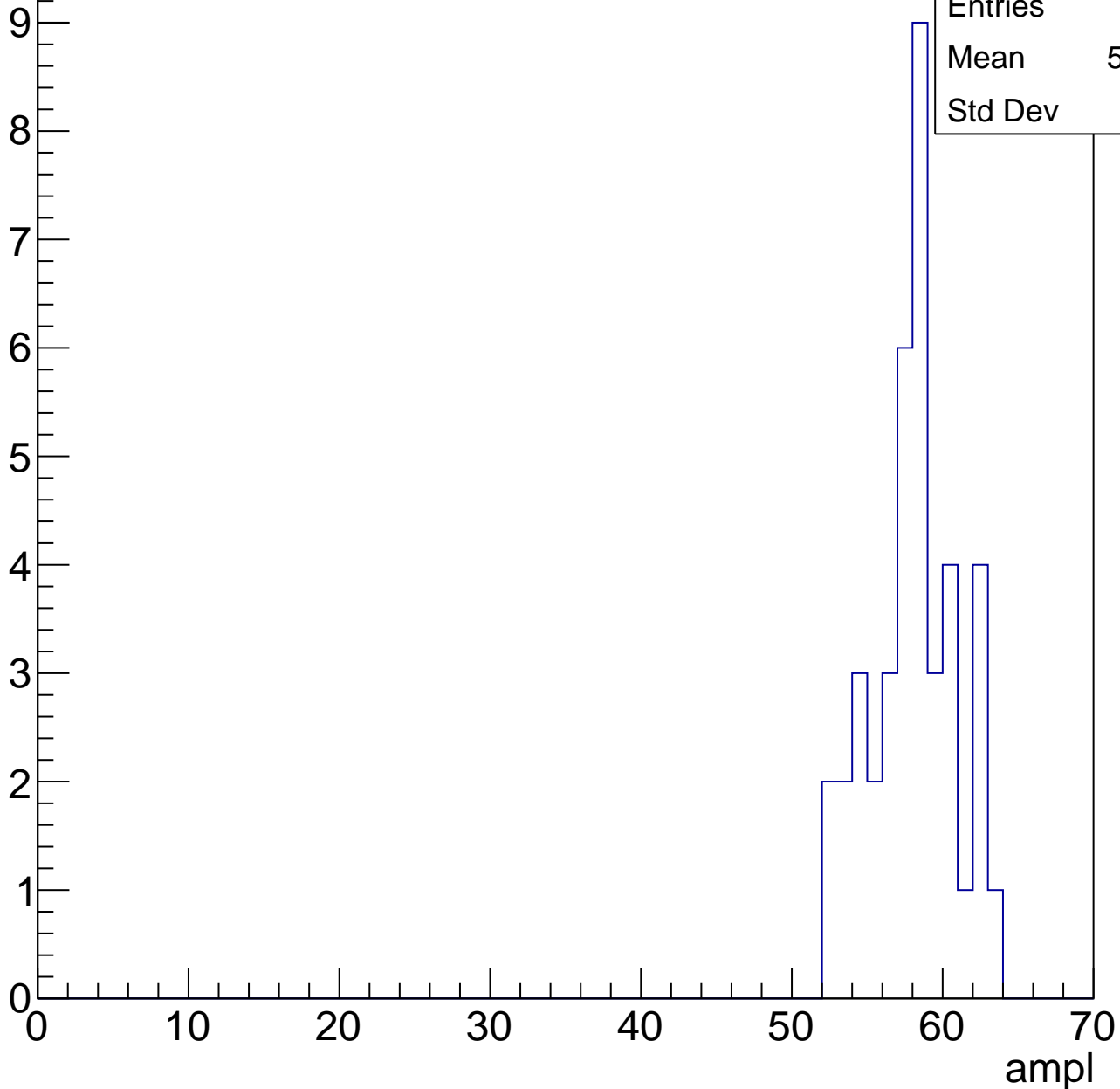
Entries	79
Mean	51.32
Std Dev	3.232



B1L003S, U13-ch114, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



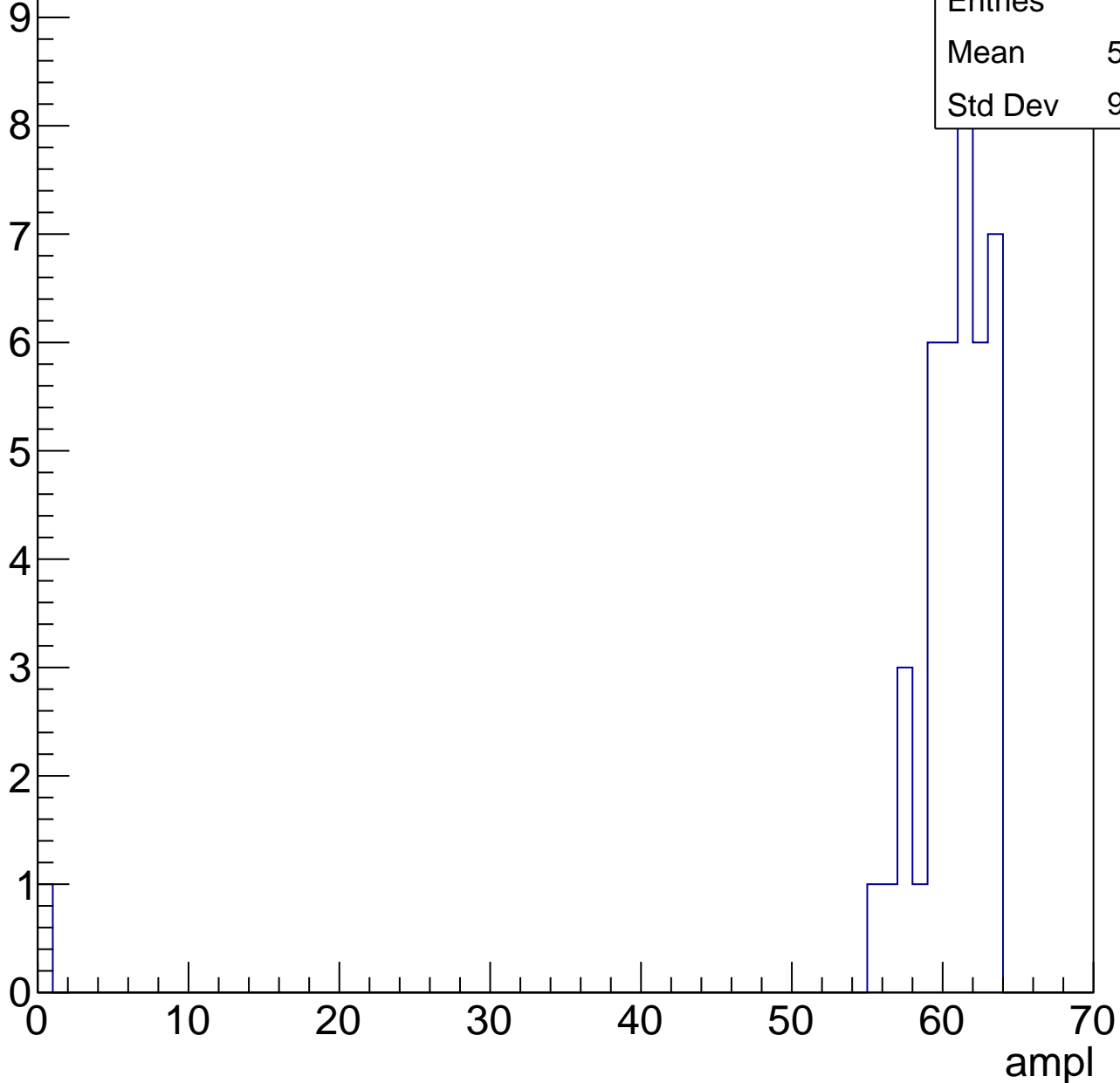
Entries	40
Mean	57.58
Std Dev	2.81

B1L003S, U13-ch114, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.93
Std Dev	9.534



B1L003S, U13-ch114, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L003S, U13-ch114, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch115, adc0

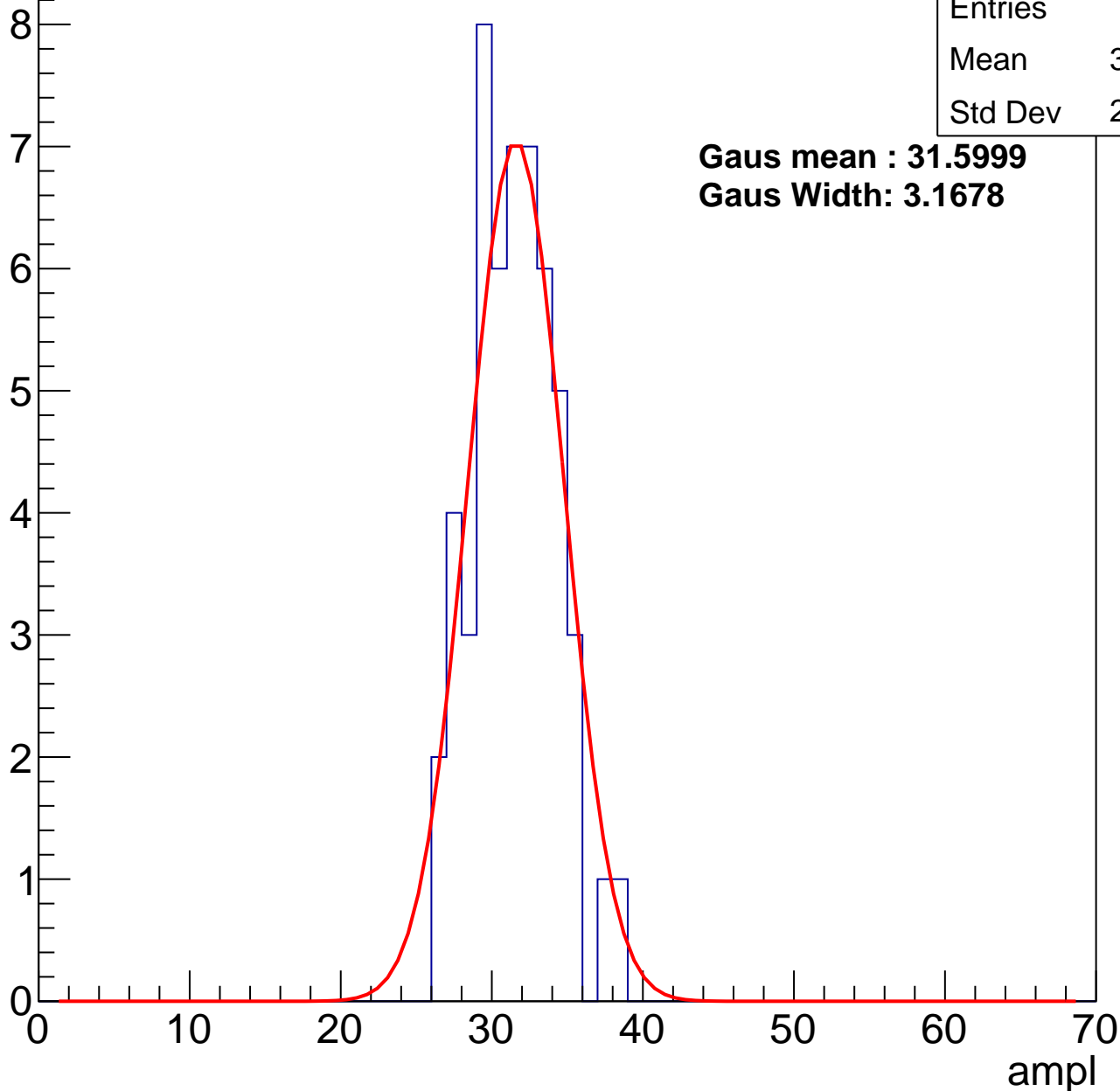
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	31.04
Std Dev	2.705

Gaus mean : 31.5999

Gaus Width: 3.1678



B1L003S, U13-ch115, adc1

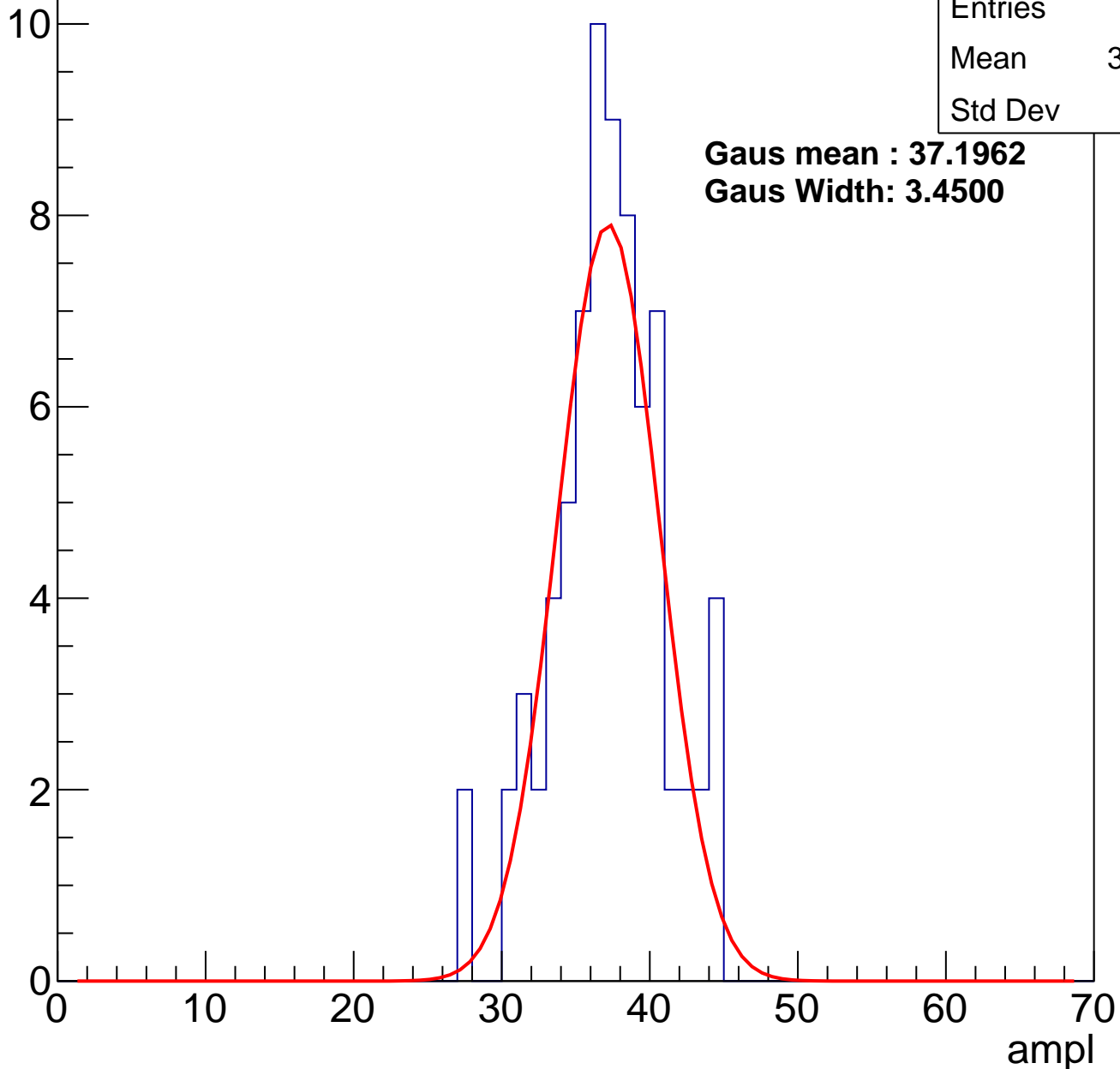
calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	36.76
Std Dev	3.73

Gaus mean : 37.1962

Gaus Width: 3.4500

Entry



B1L003S, U13-ch115, adc2

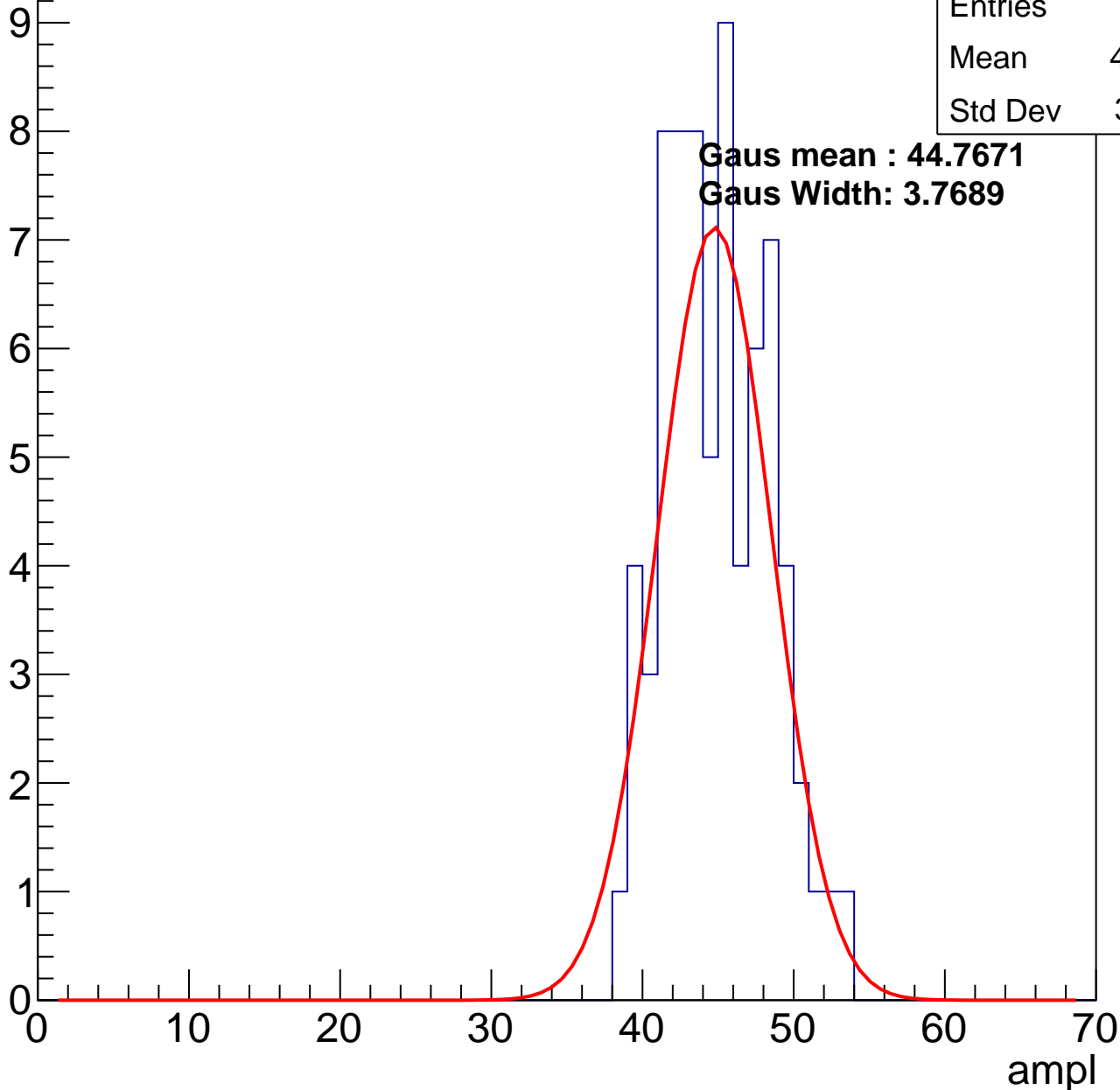
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.46
Std Dev	3.411

Gaus mean : 44.7671

Gaus Width: 3.7689

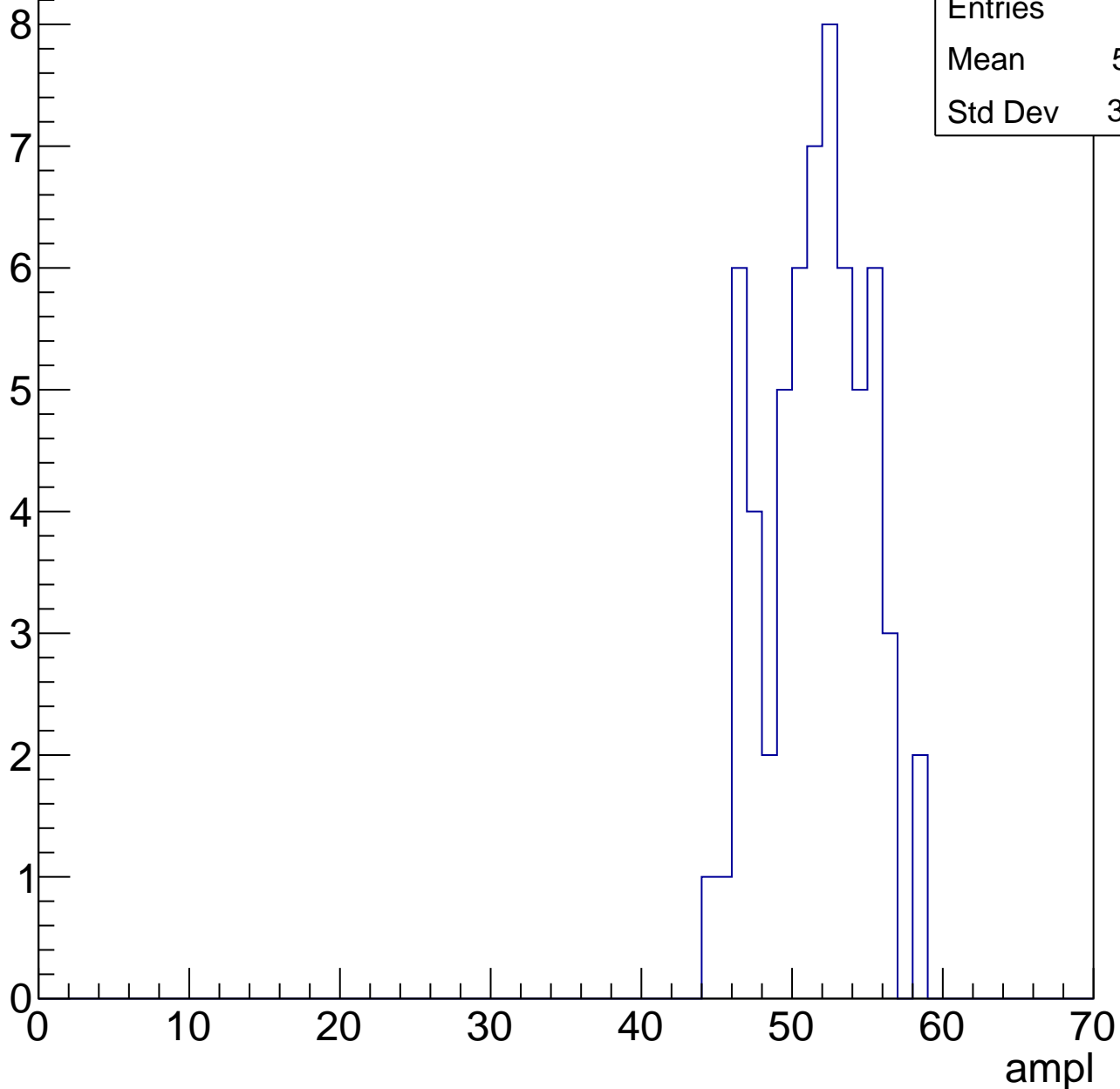


B1L003S, U13-ch115, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

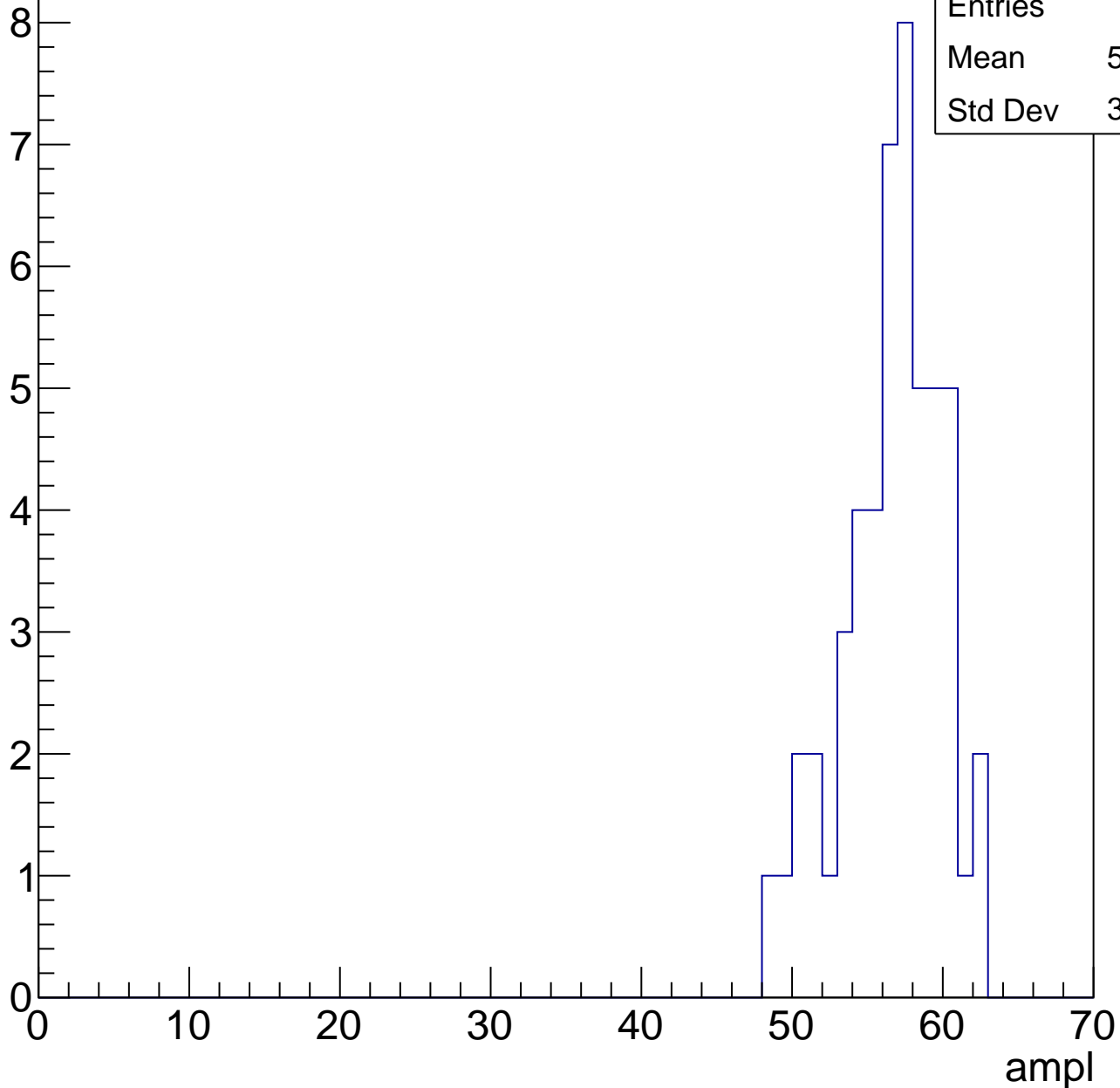
Entries	62
Mean	51.11
Std Dev	3.332



B1L003S, U13-ch115, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch115, adc5

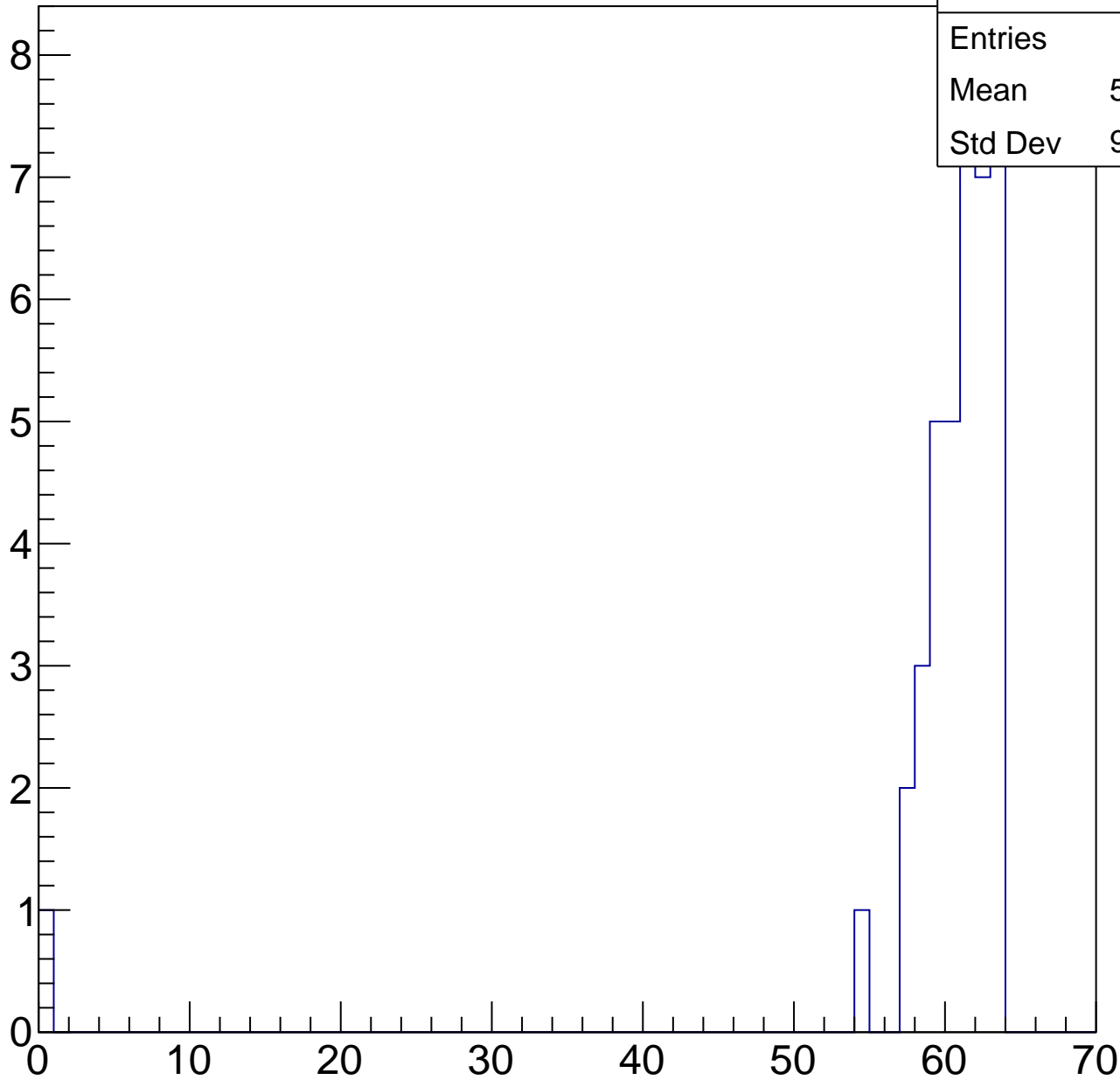
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	59.08
Std Dev	9.676

8
7
6
5
4
3
2
1
0

ampl



B1L003S, U13-ch115, adc6

calib_packv5_042523_0143.root, FC#13, port D2

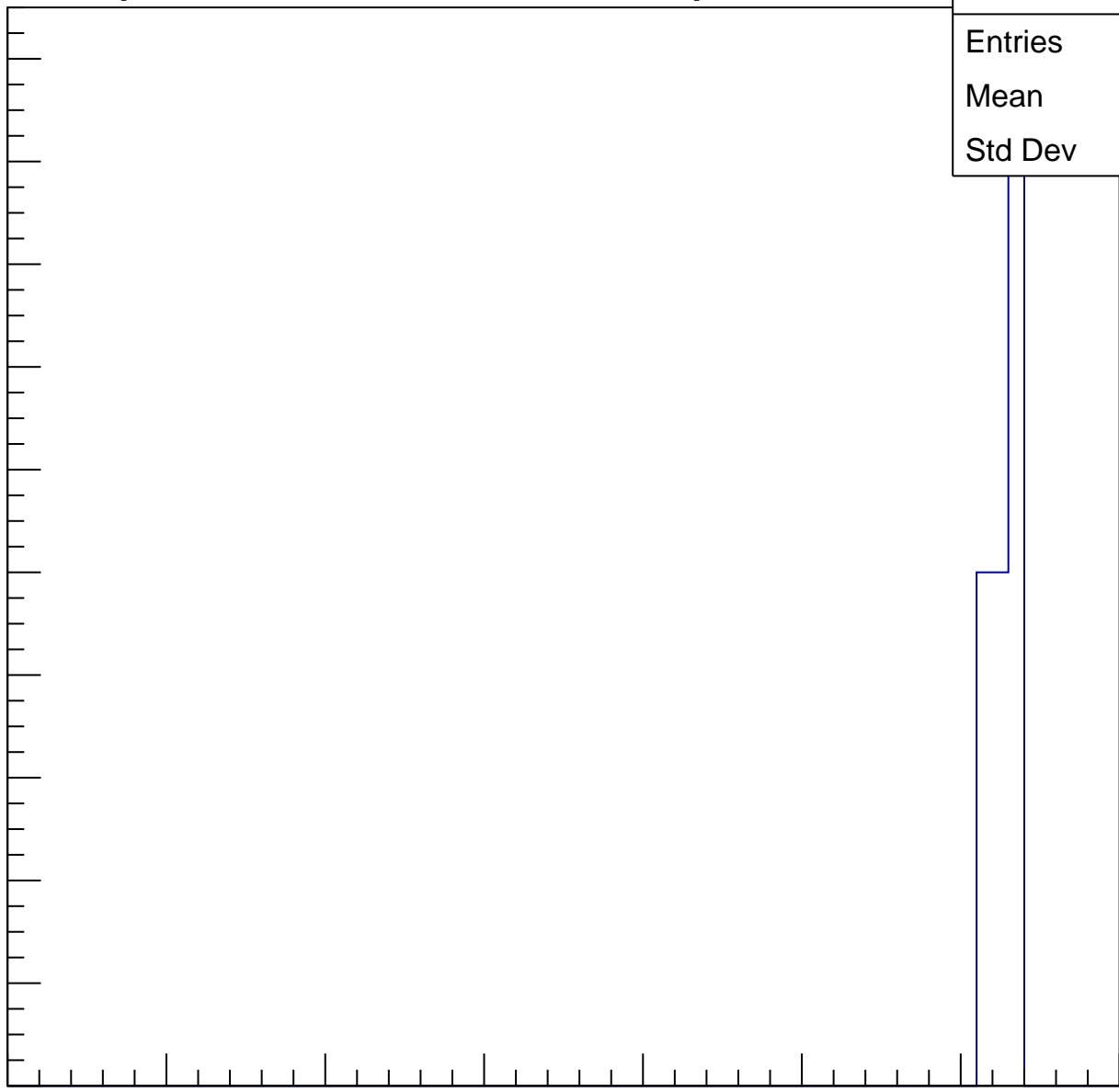
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

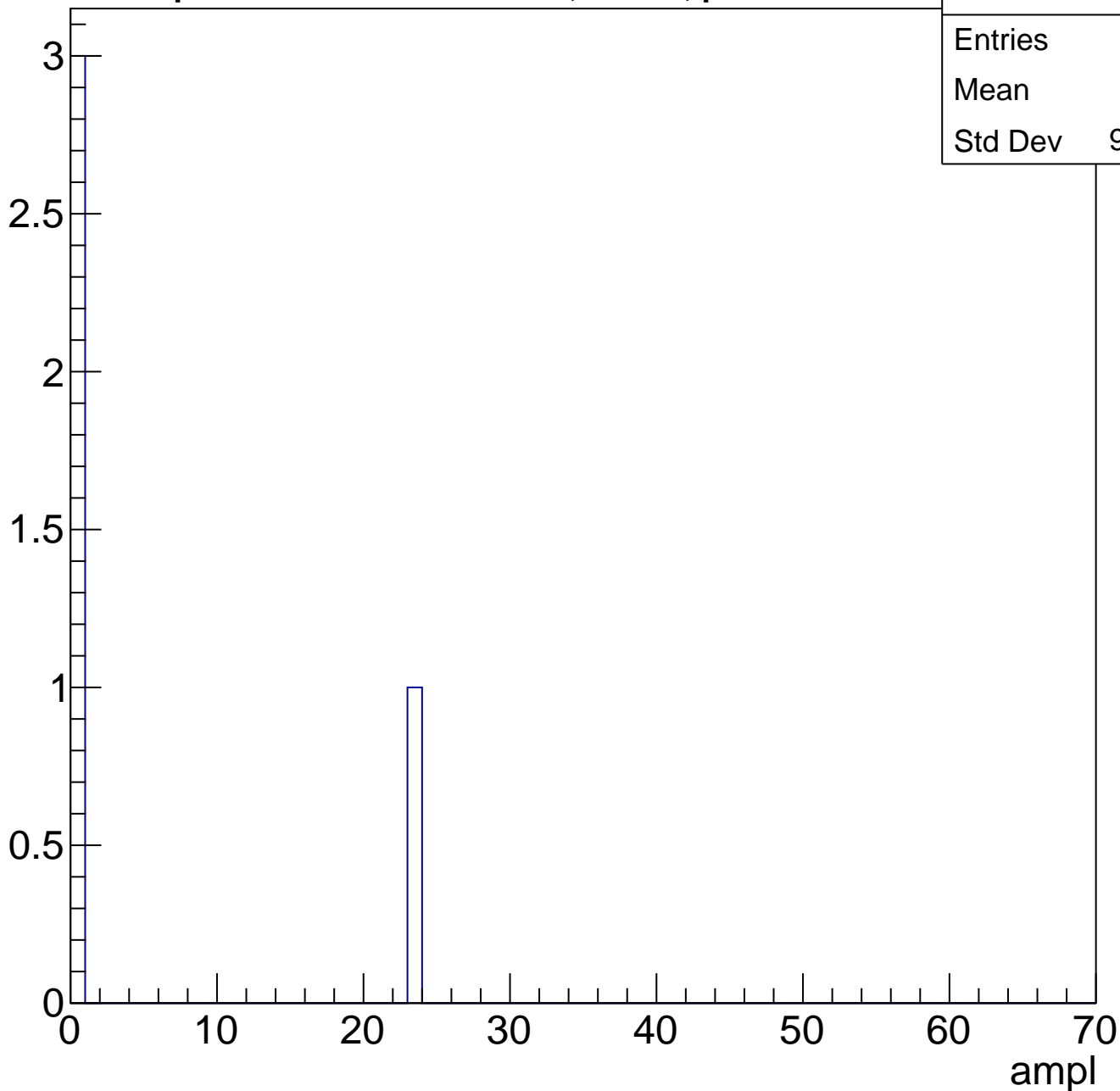
ampl



B1L003S, U13-ch115, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch116, adc0

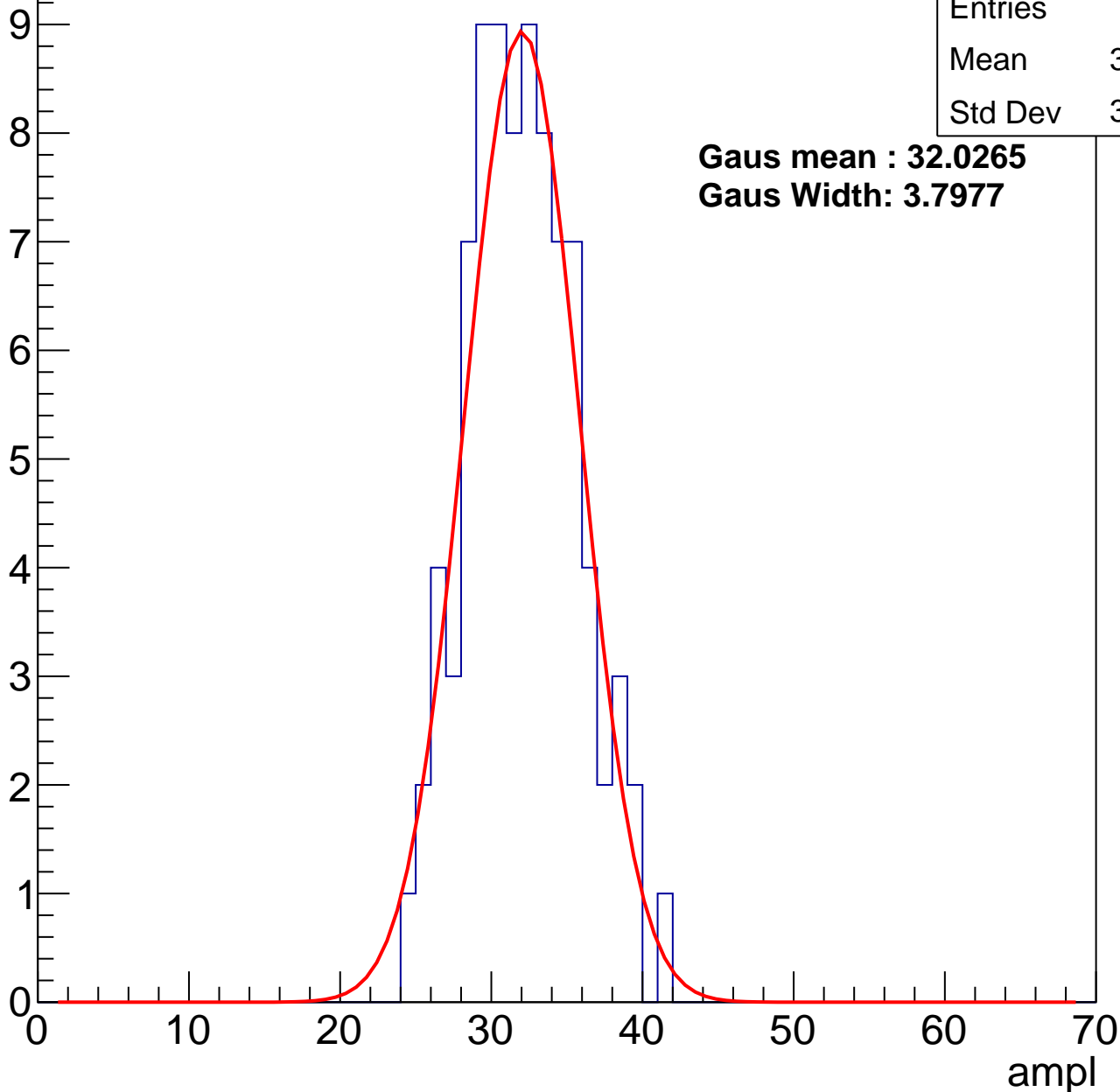
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	31.63
Std Dev	3.586

Gaus mean : 32.0265

Gaus Width: 3.7977



B1L003S, U13-ch116, adc1

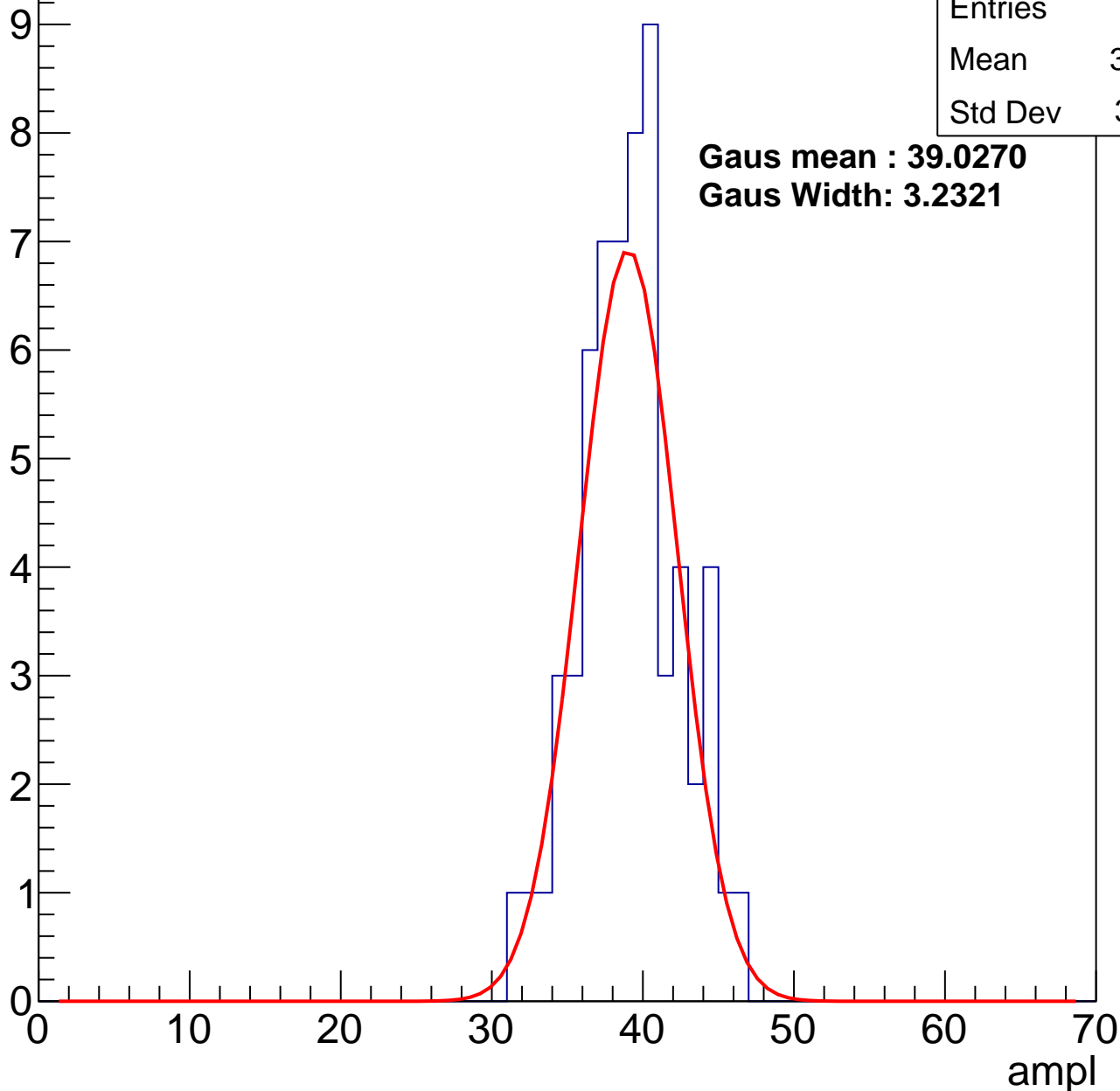
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	38.69
Std Dev	3.211

Gaus mean : 39.0270

Gaus Width: 3.2321



B1L003S, U13-ch116, adc2

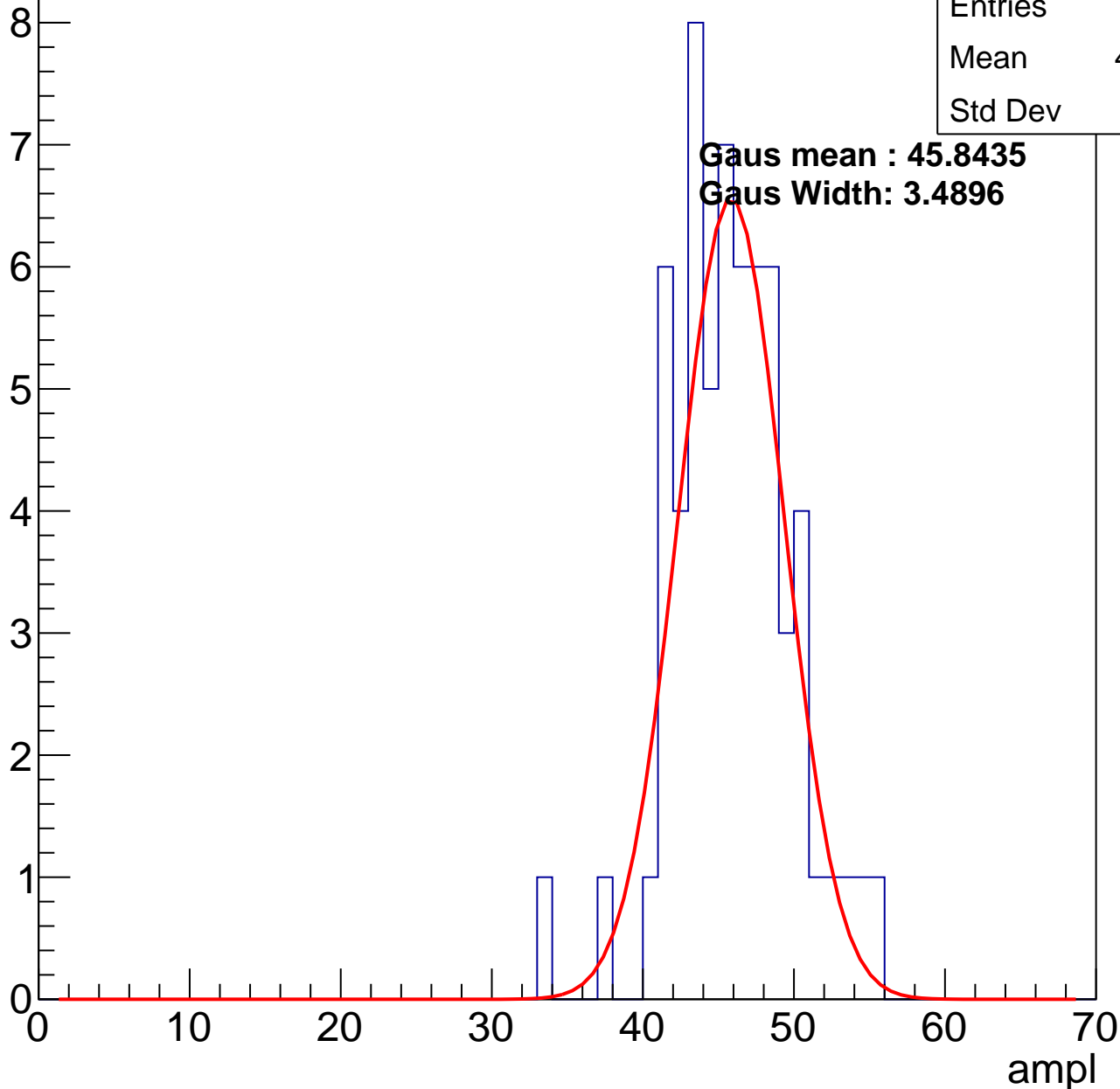
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	45.41
Std Dev	3.89

Gaus mean : 45.8435

Gaus Width: 3.4896

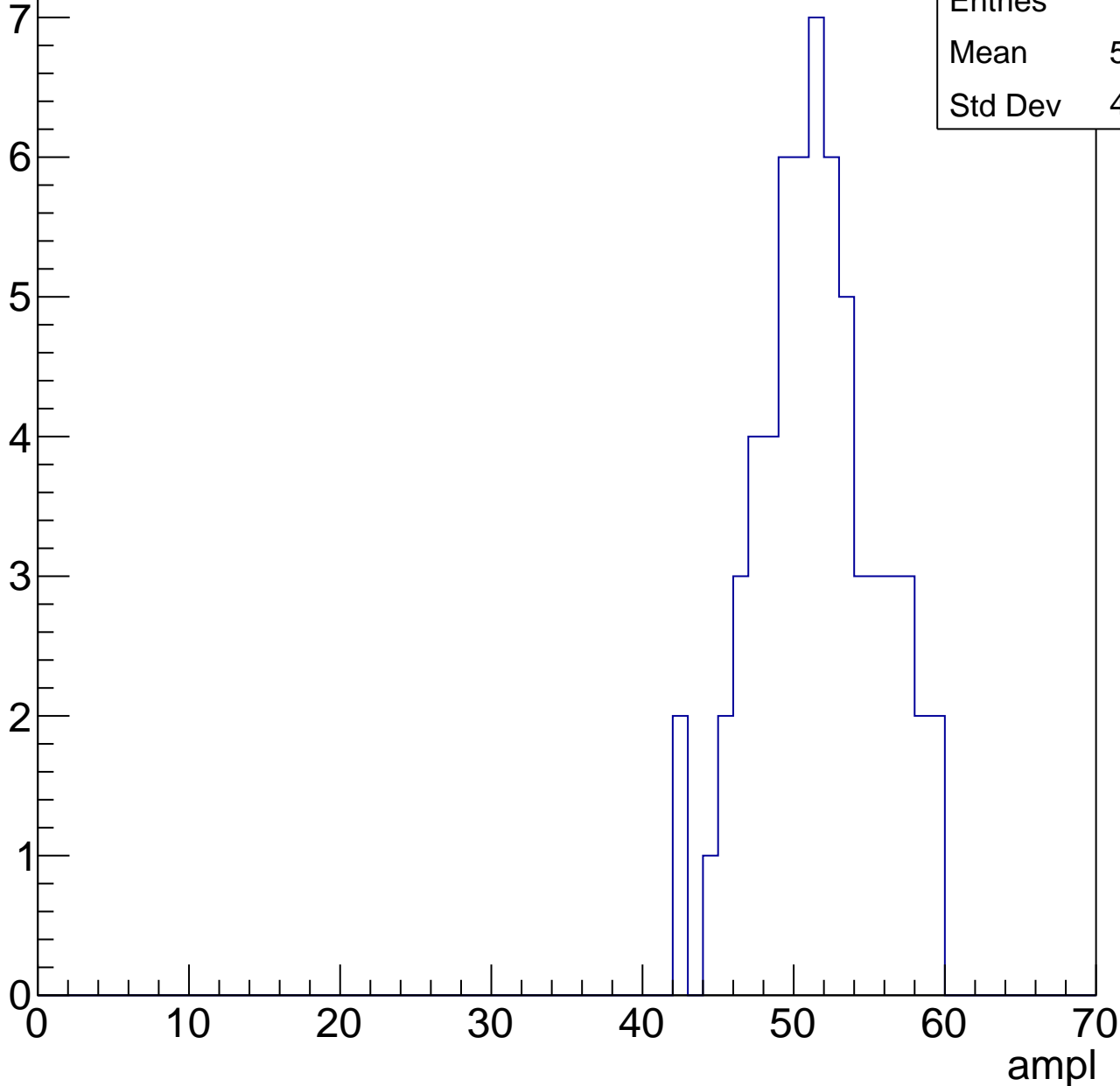


B1L003S, U13-ch116, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	51.03
Std Dev	4.012

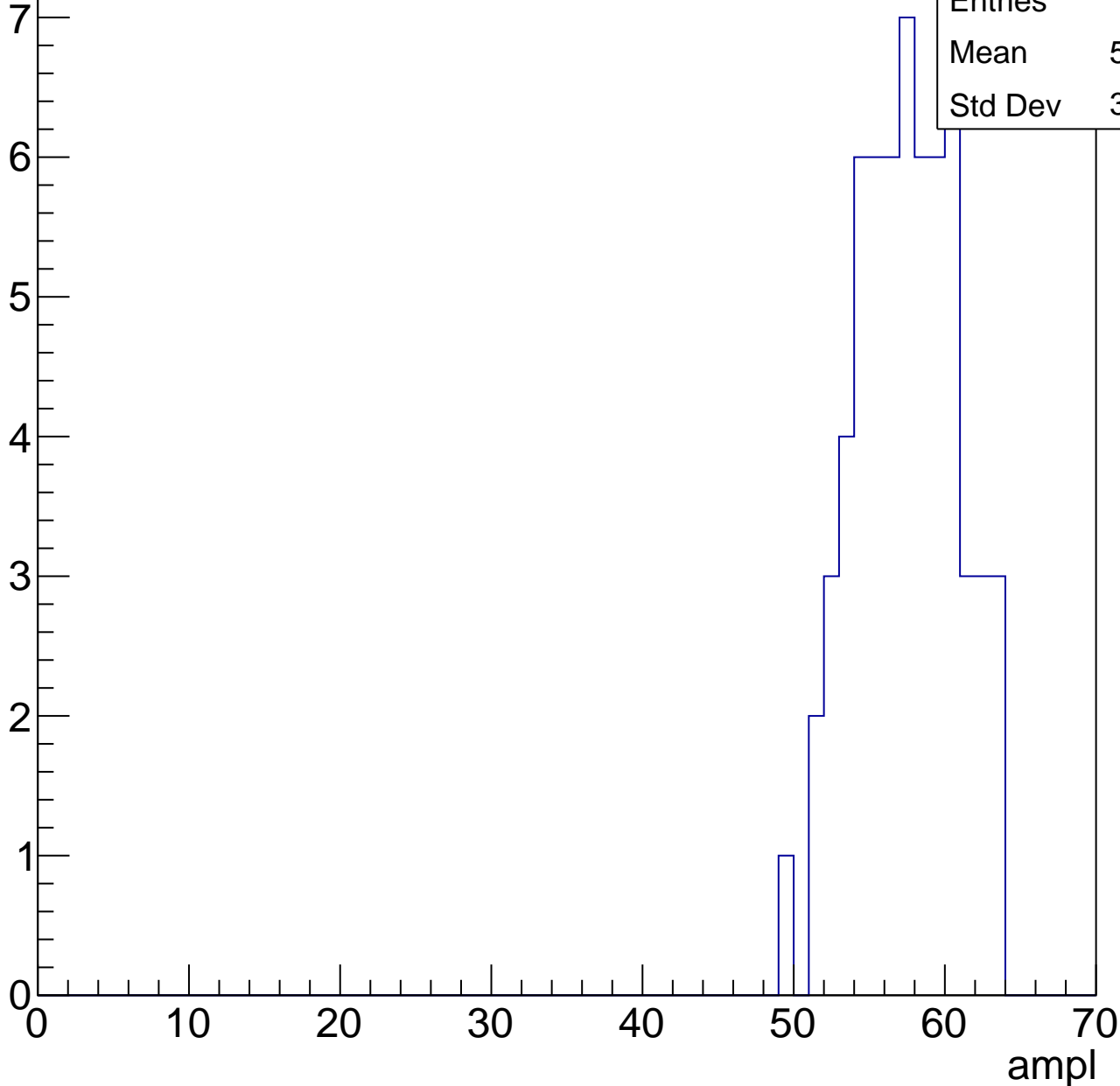


B1L003S, U13-ch116, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.95
Std Dev	3.292

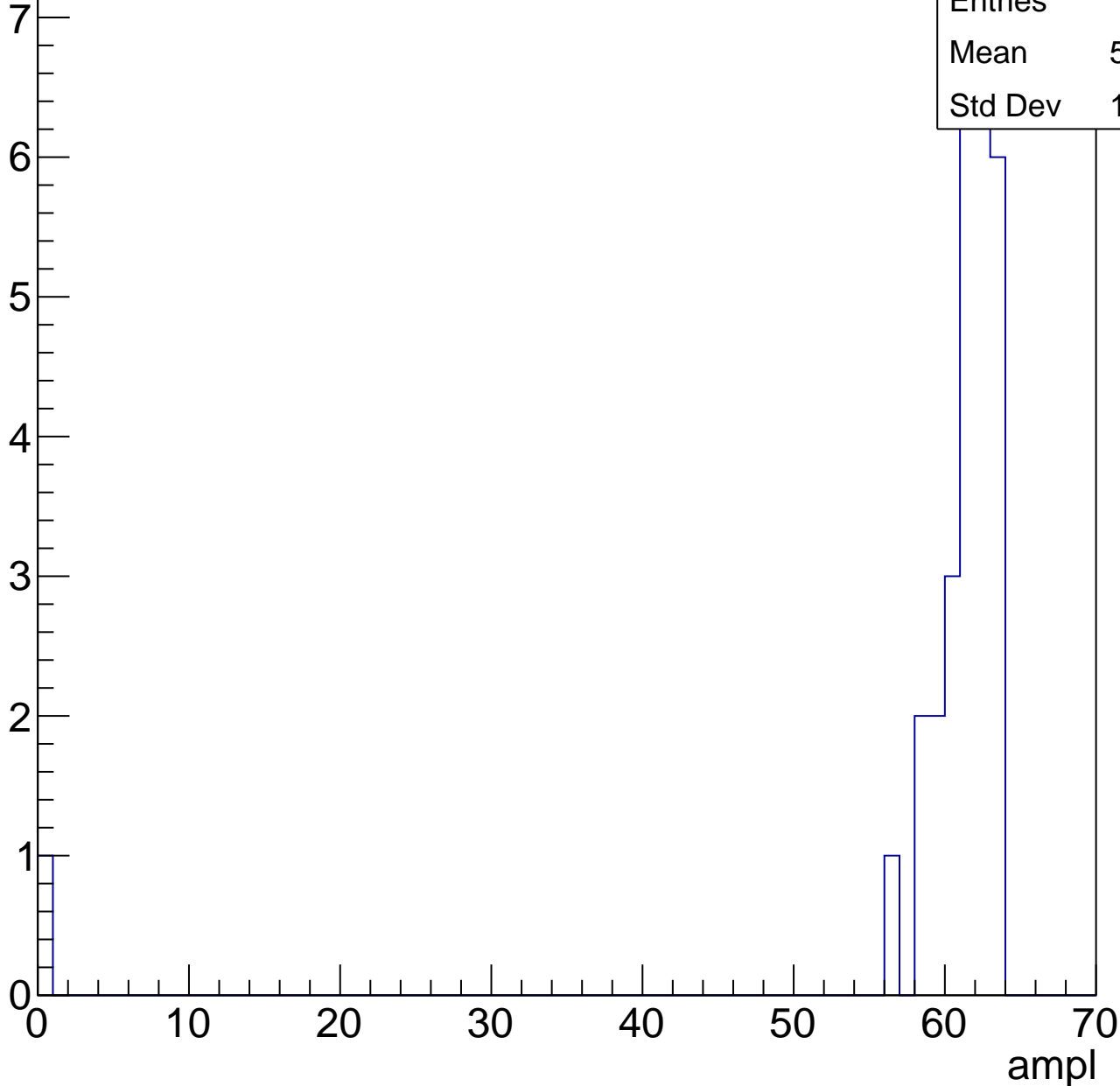


B1L003S, U13-ch116, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	29
Mean	58.93
Std Dev	11.27



B1L003S, U13-ch116, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U13-ch116, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch117, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	29.93
Std Dev	6.242

Gaus mean : 32.2324

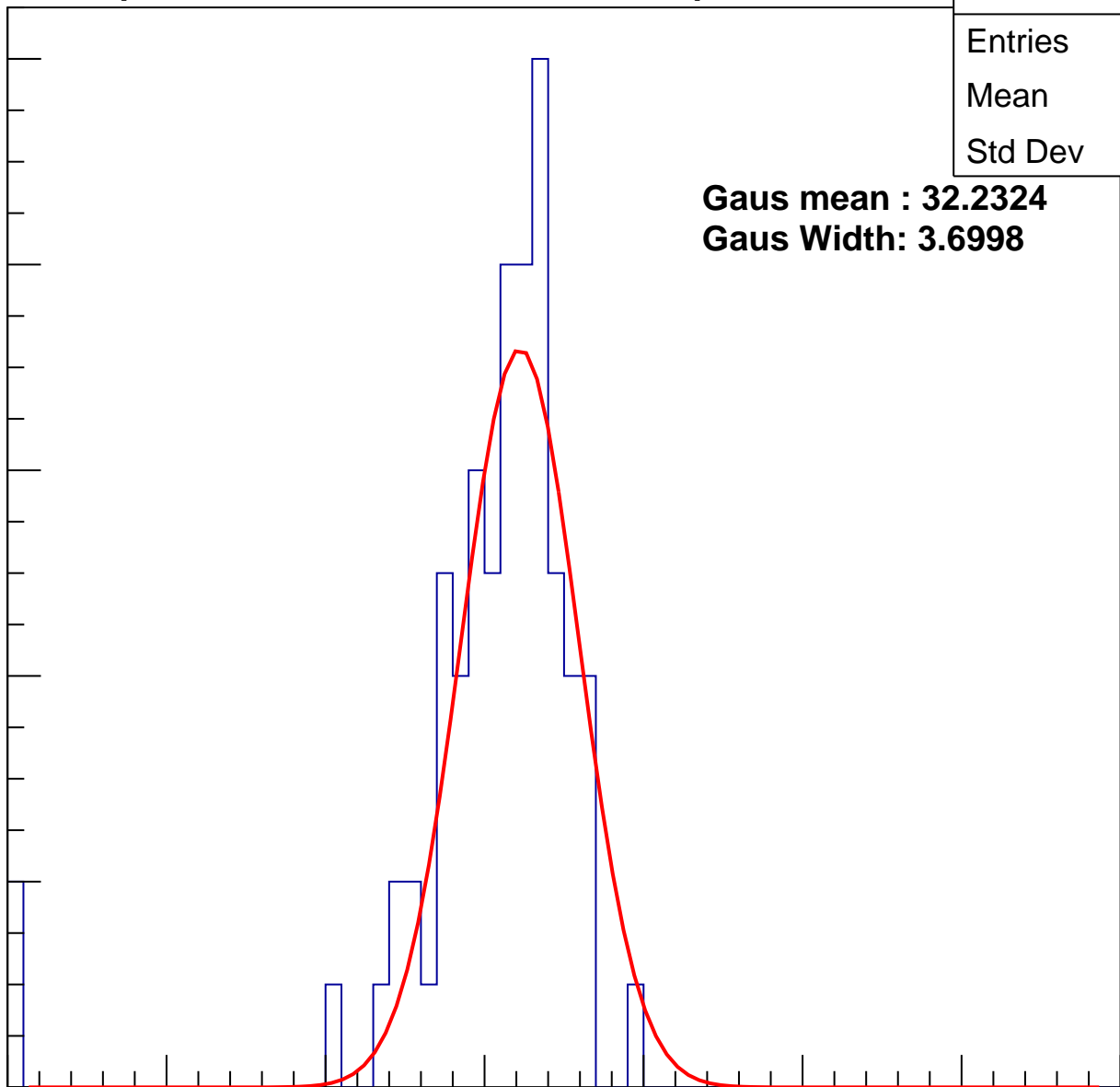
Gaus Width: 3.6998

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch117, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	38.01
Std Dev	3.478

Gaus mean : 38.2893

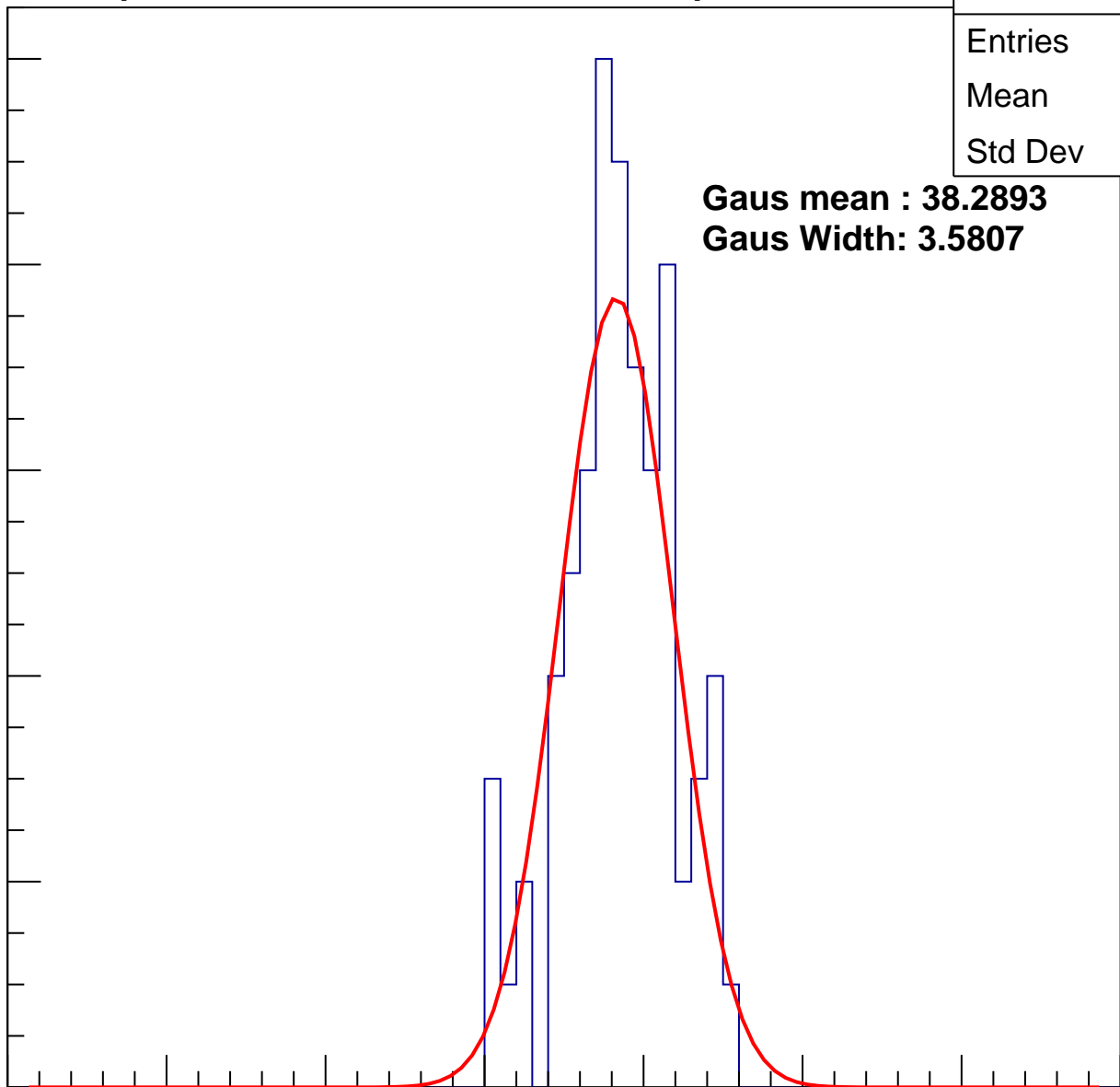
Gaus Width: 3.5807

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch117, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	45.47
Std Dev	3.526

Gaus mean : 45.7901

Gaus Width: 3.4916

10

8

6

4

2

0

0

10

20

30

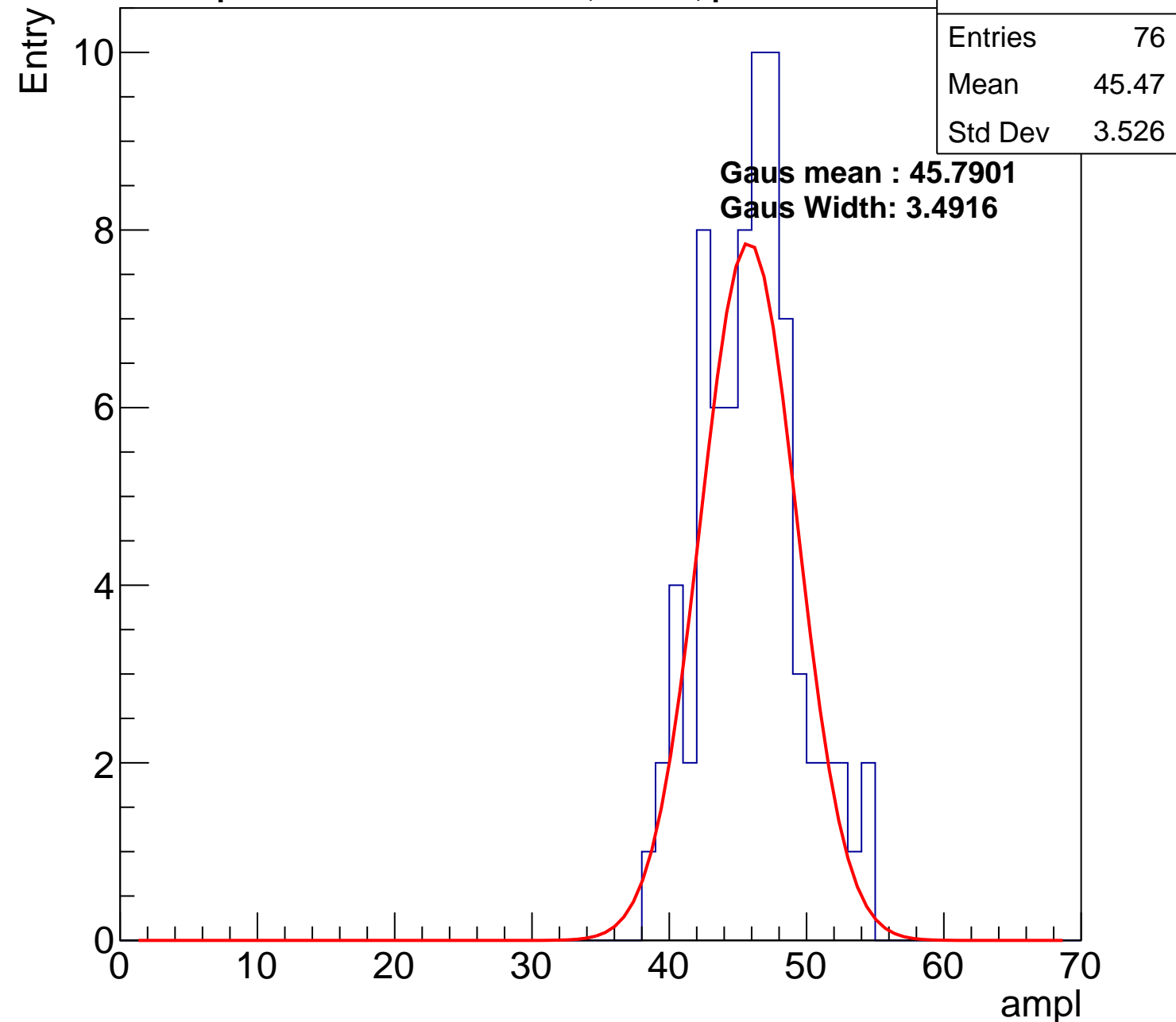
40

50

60

70

ampl

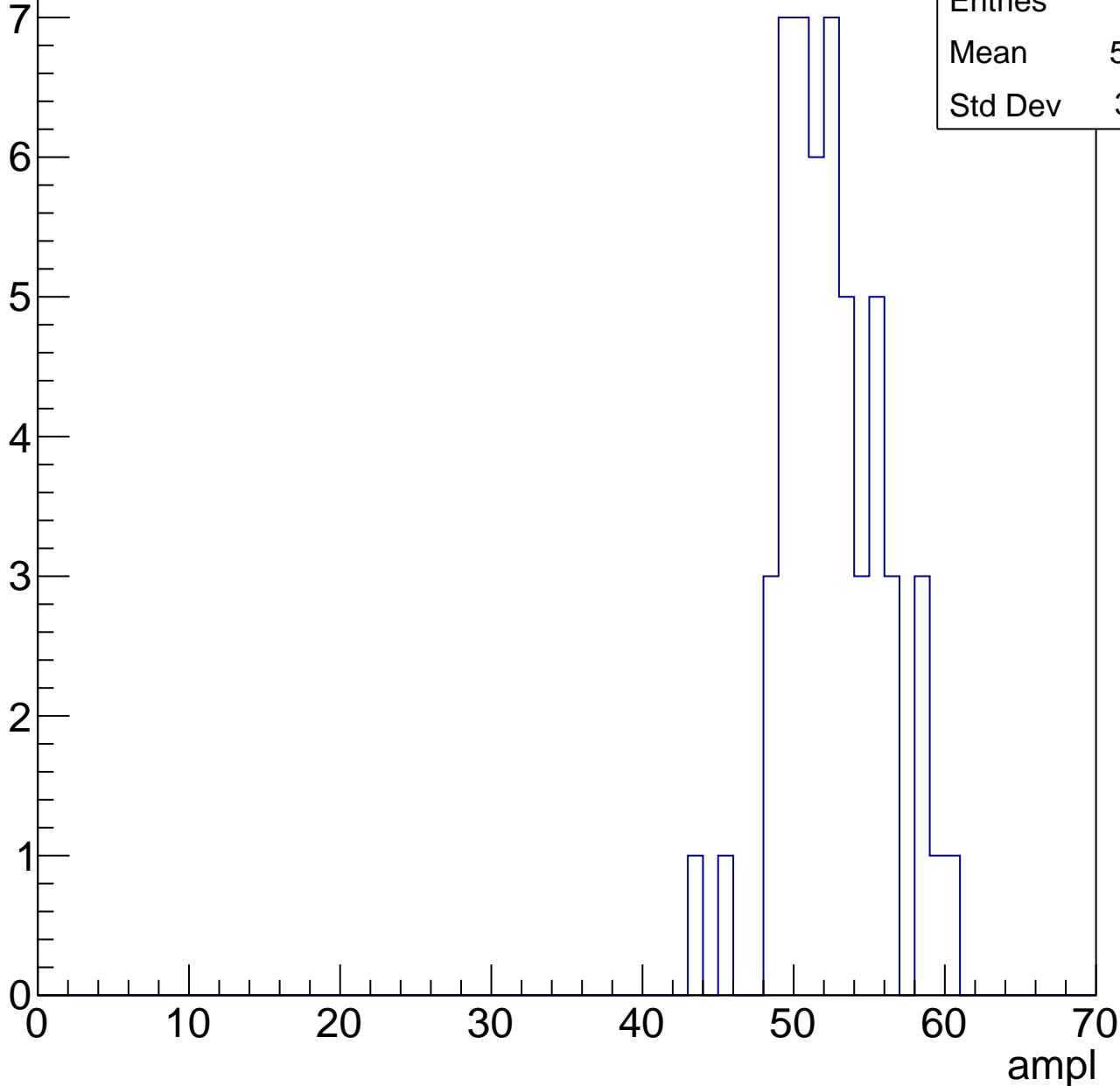


B1L003S, U13-ch117, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	52.04
Std Dev	3.381

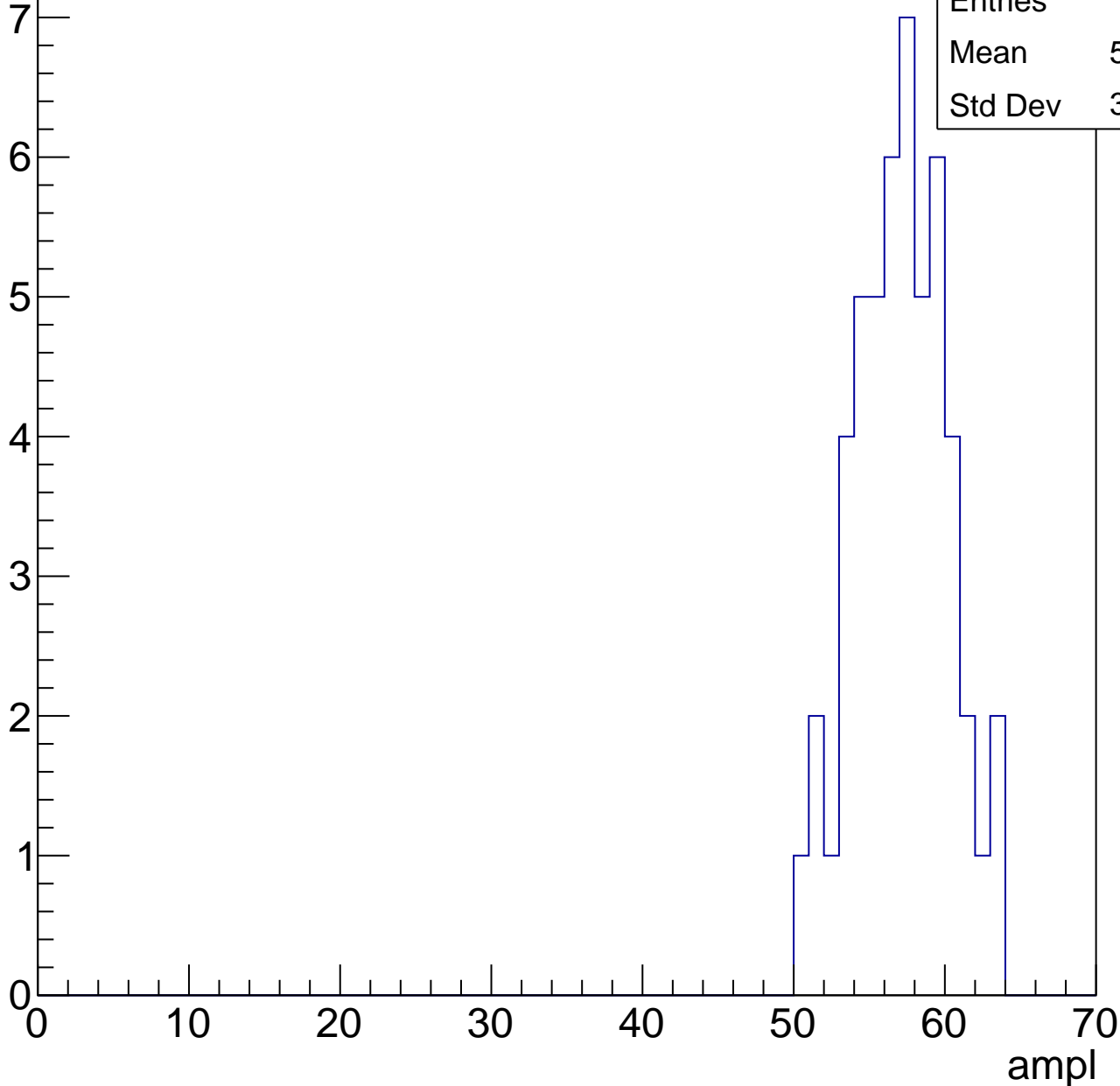


B1L003S, U13-ch117, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.67
Std Dev	3.034

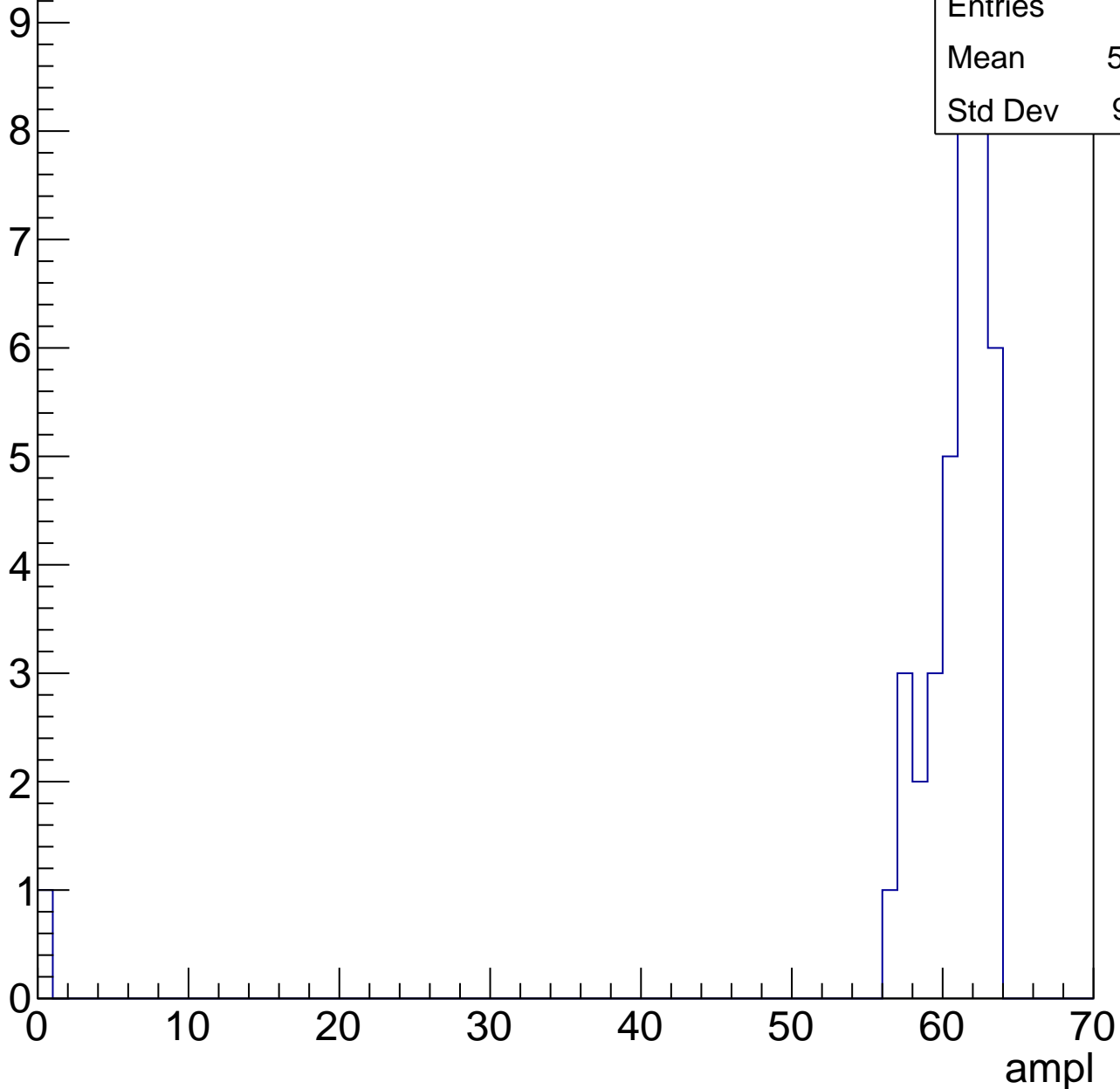


B1L003S, U13-ch117, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59.05
Std Dev	9.891



B1L003S, U13-ch117, adc6

calib_packv5_042523_0143.root, FC#13, port D2

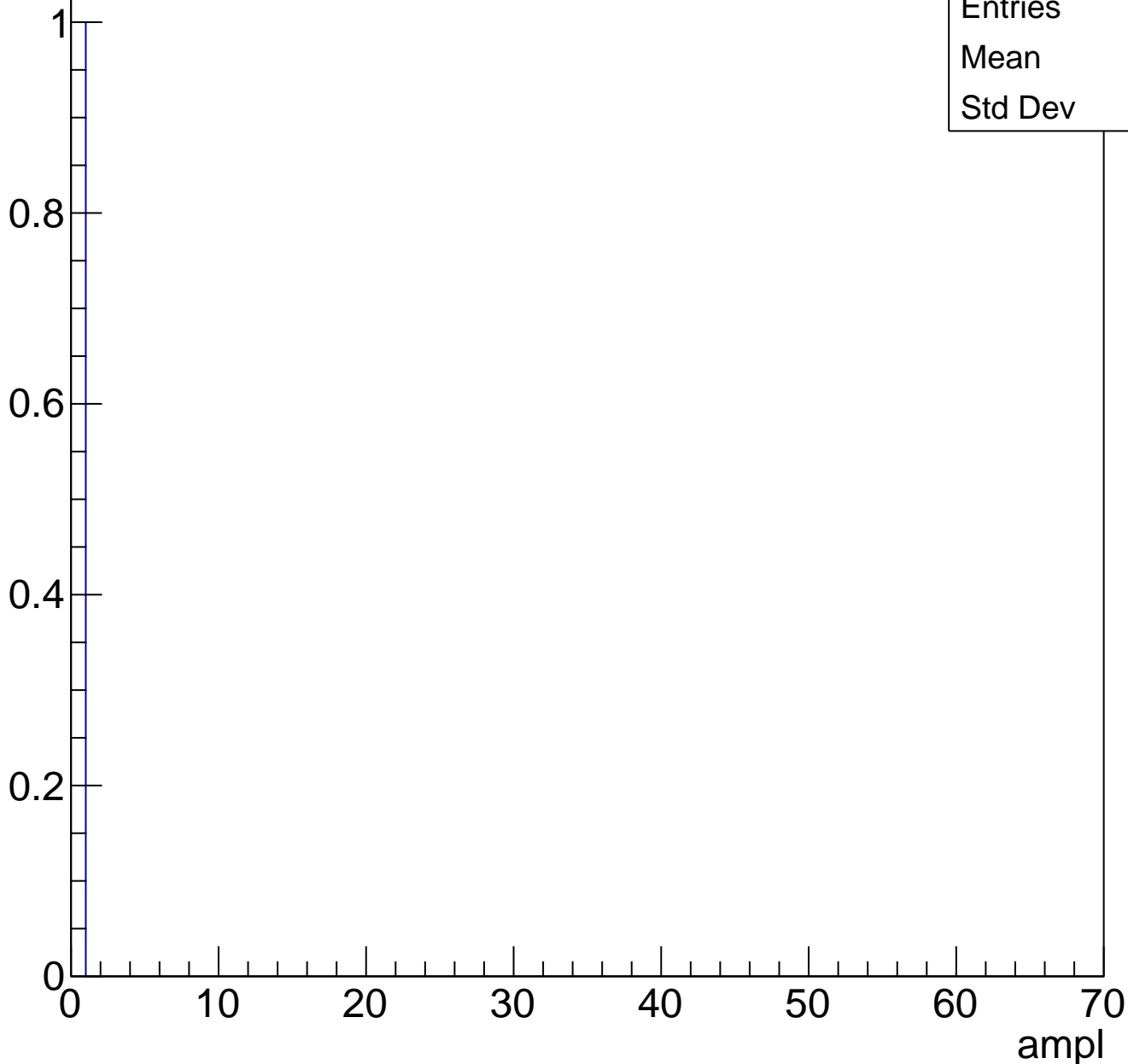
Entry



B1L003S, U13-ch117, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch118, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	30.94
Std Dev	4.968

Gaus mean : 31.8451

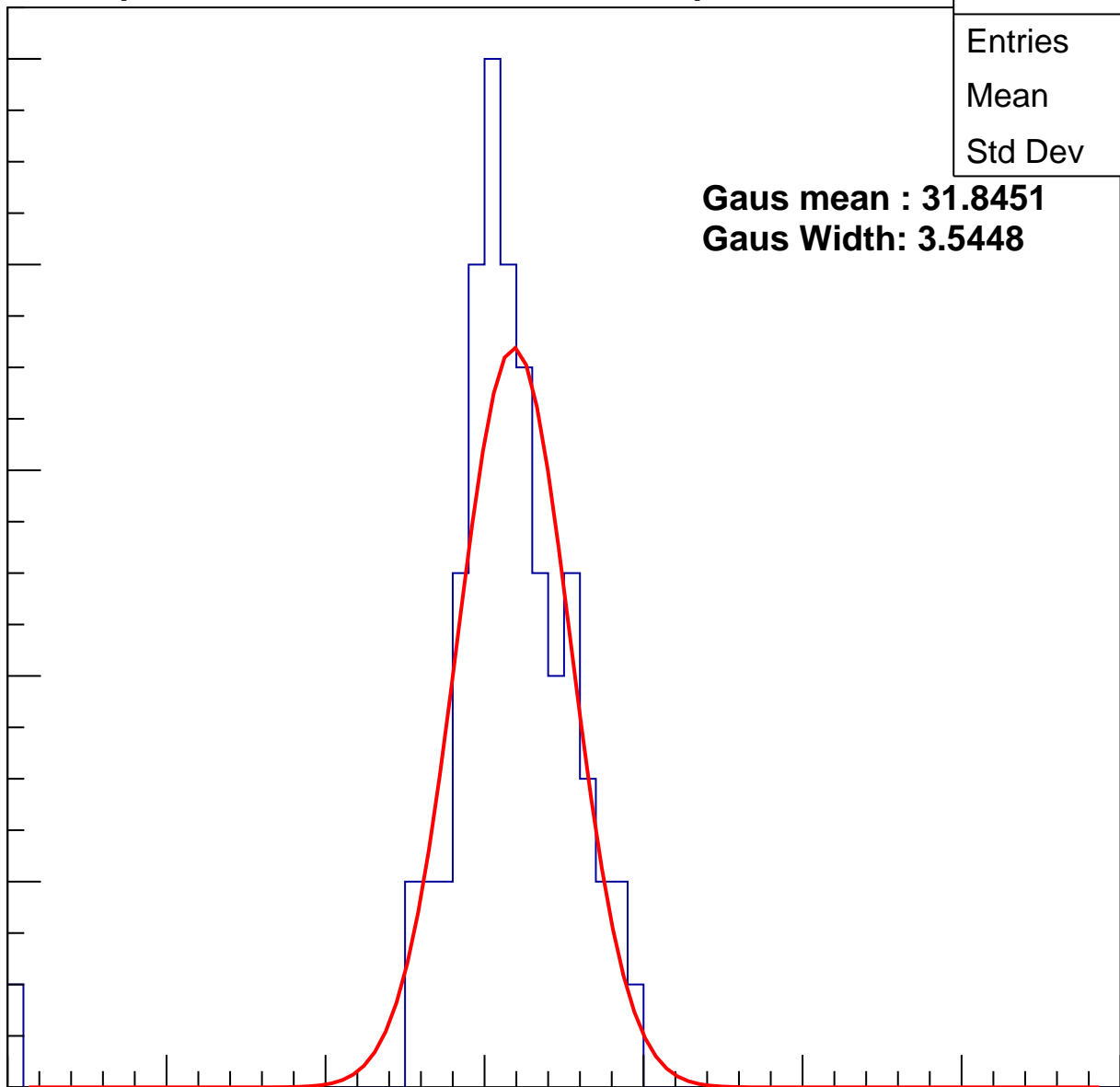
Gaus Width: 3.5448

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U13-ch118, adc1

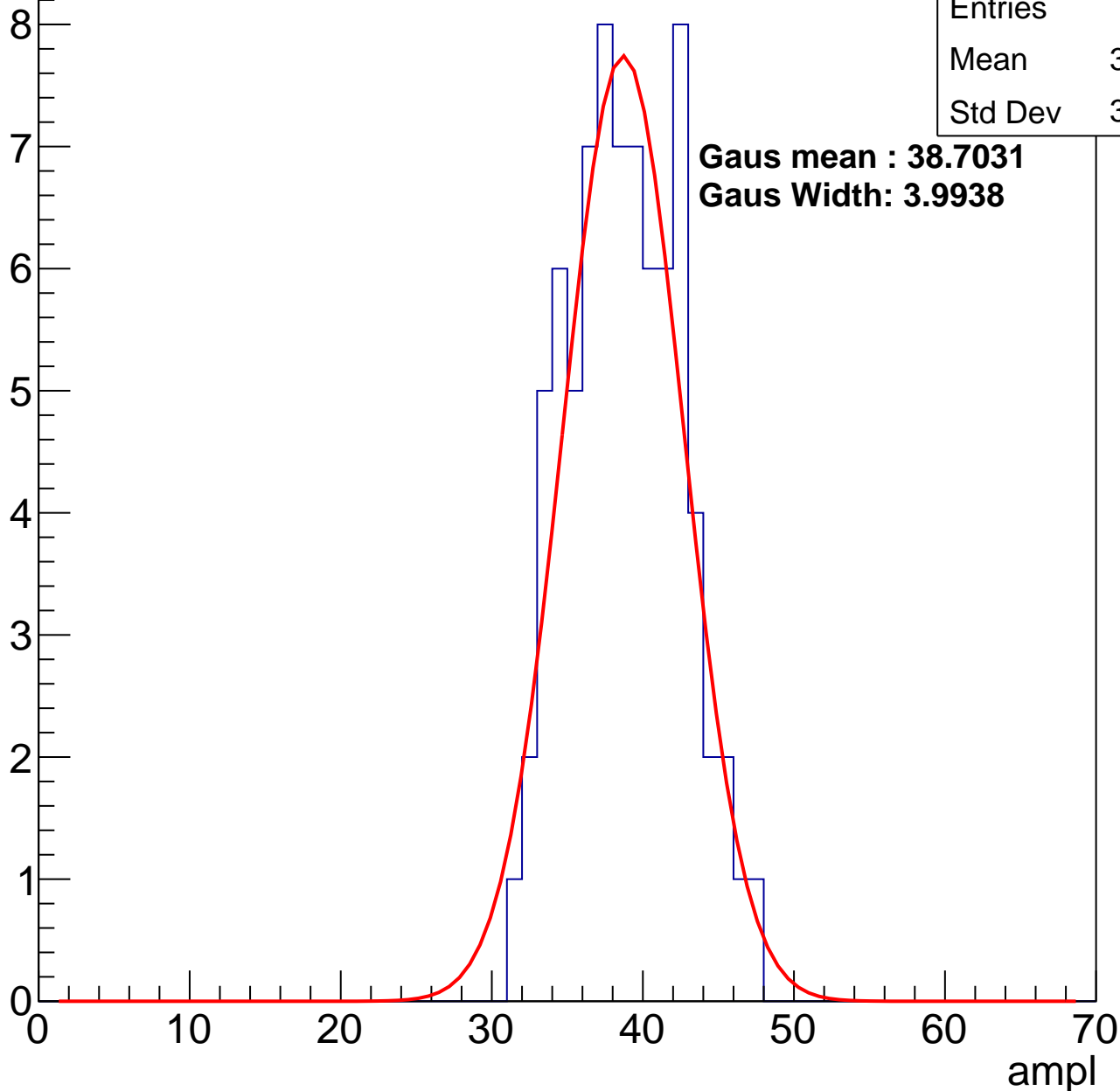
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	38.35
Std Dev	3.654

Gaus mean : 38.7031

Gaus Width: 3.9938



B1L003S, U13-ch118, adc2

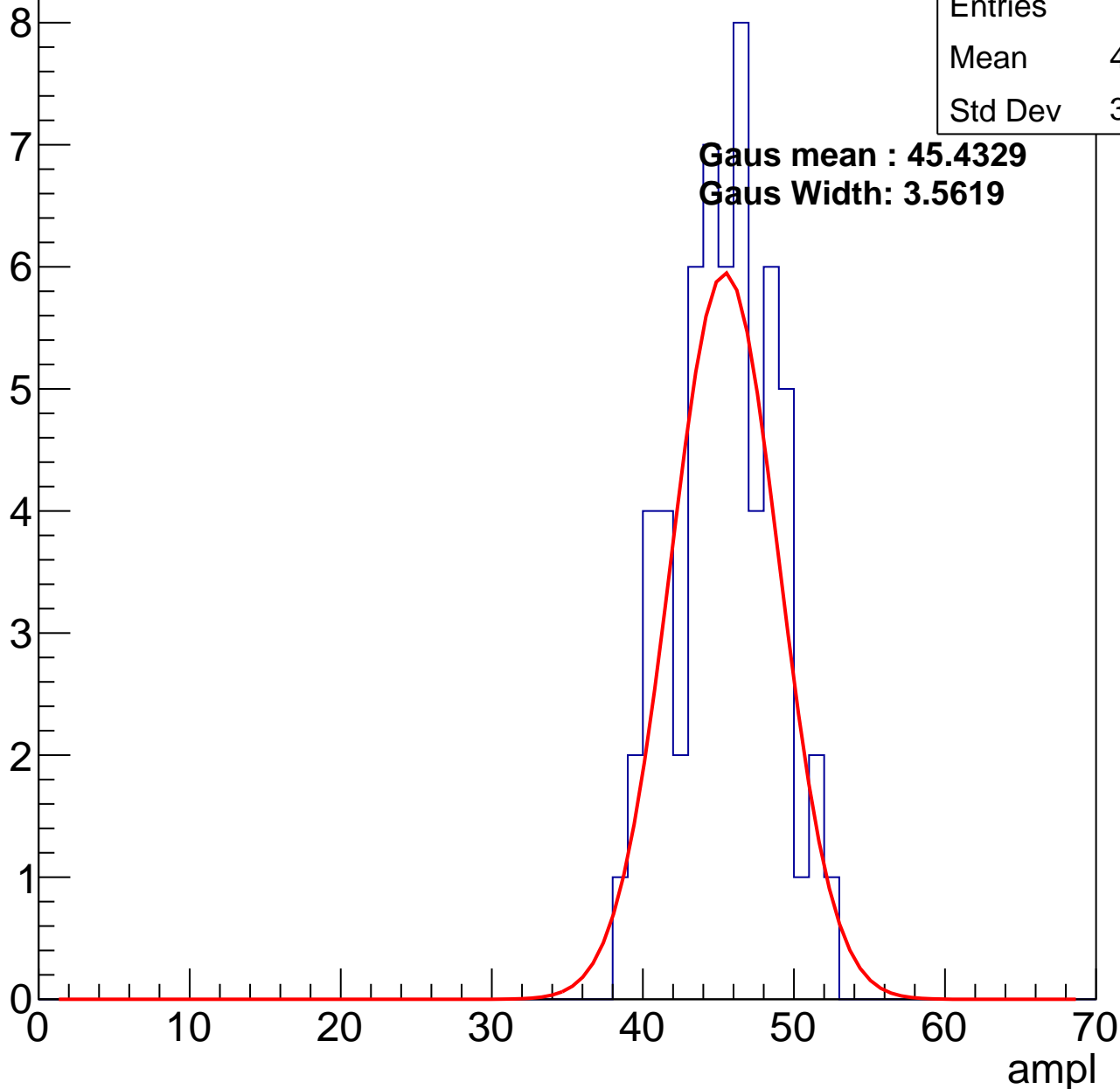
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.97
Std Dev	3.288

Gaus mean : 45.4329

Gaus Width: 3.5619

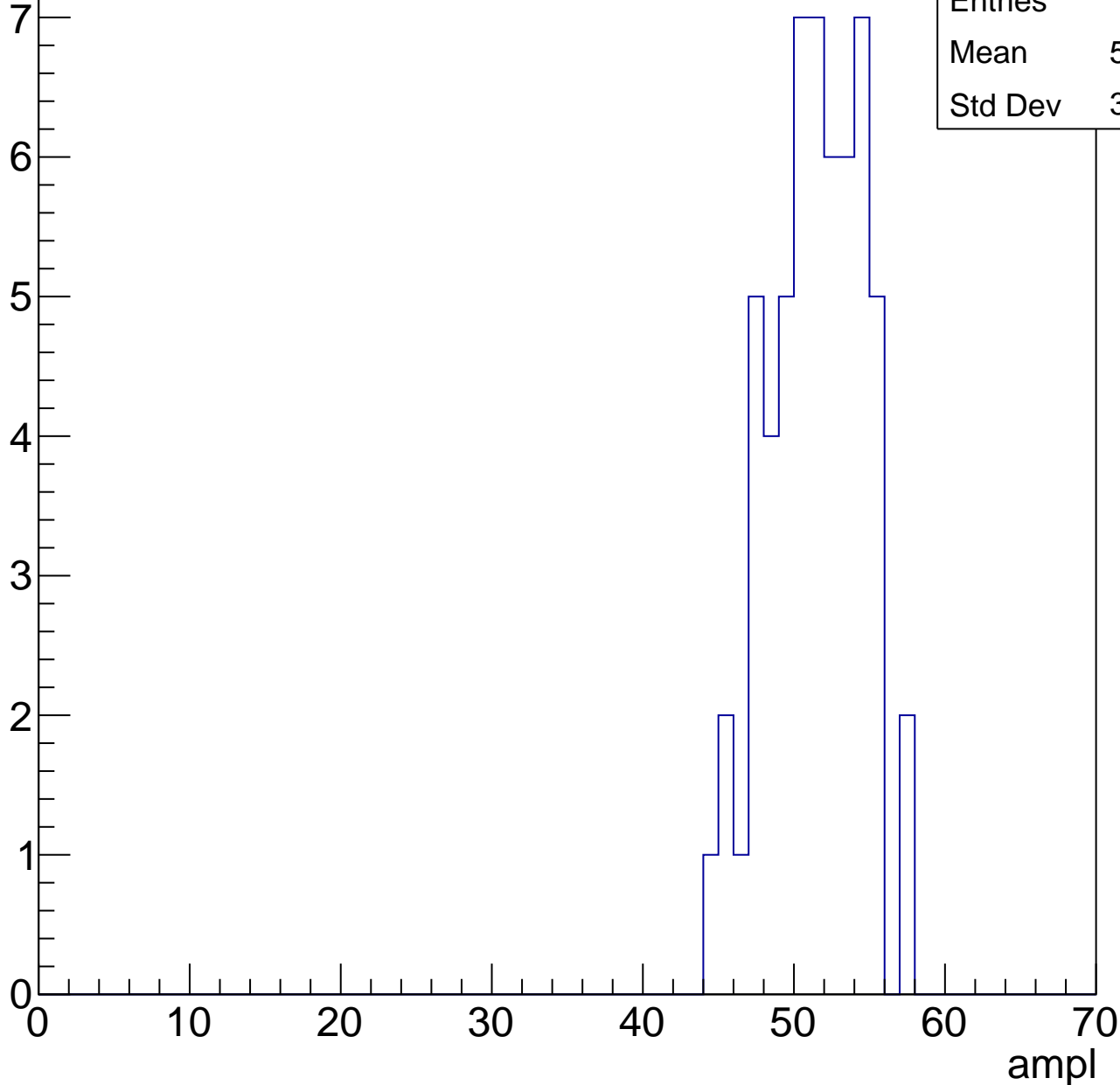


B1L003S, U13-ch118, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	50.97
Std Dev	3.034

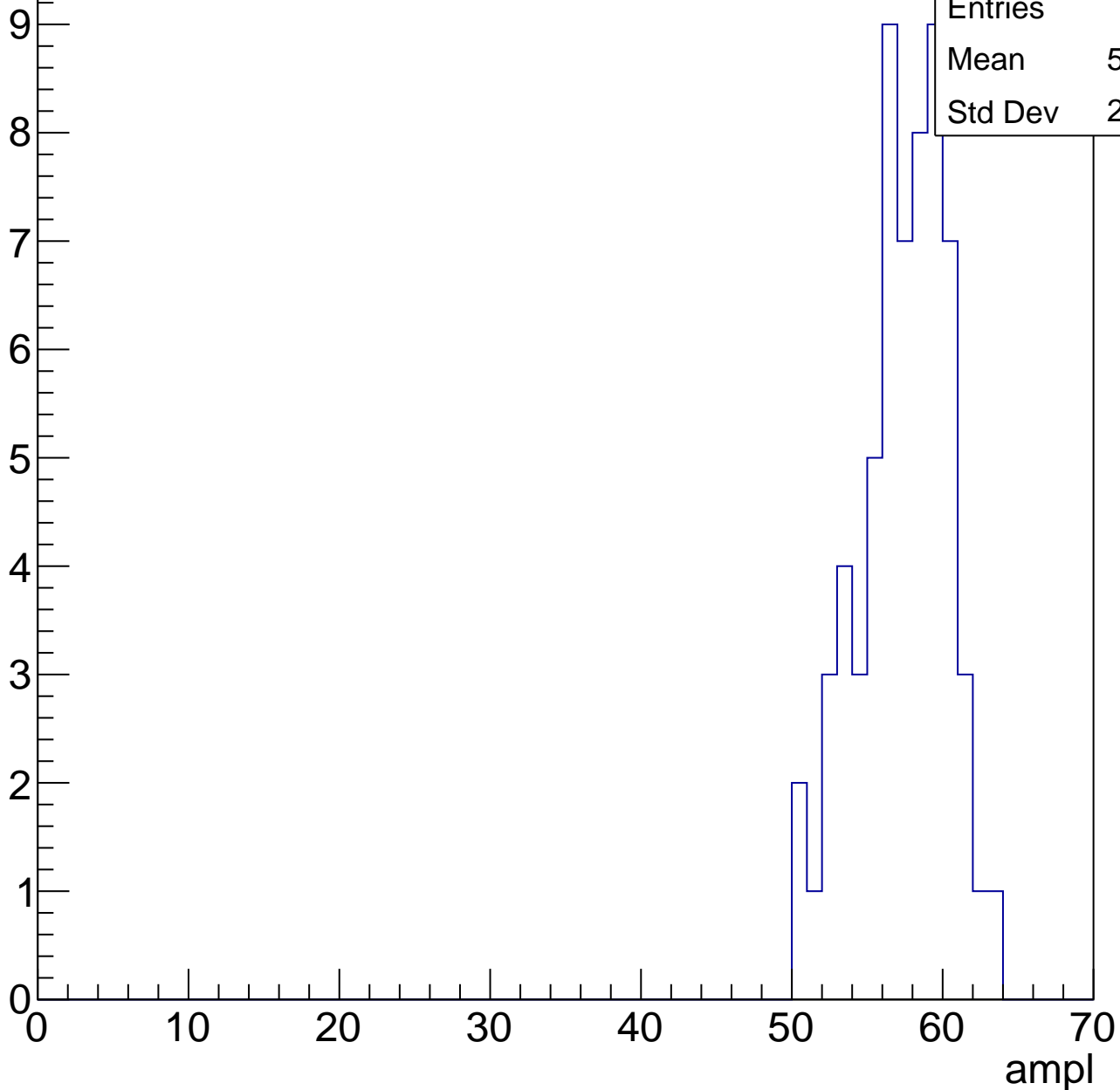


B1L003S, U13-ch118, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.86
Std Dev	2.938

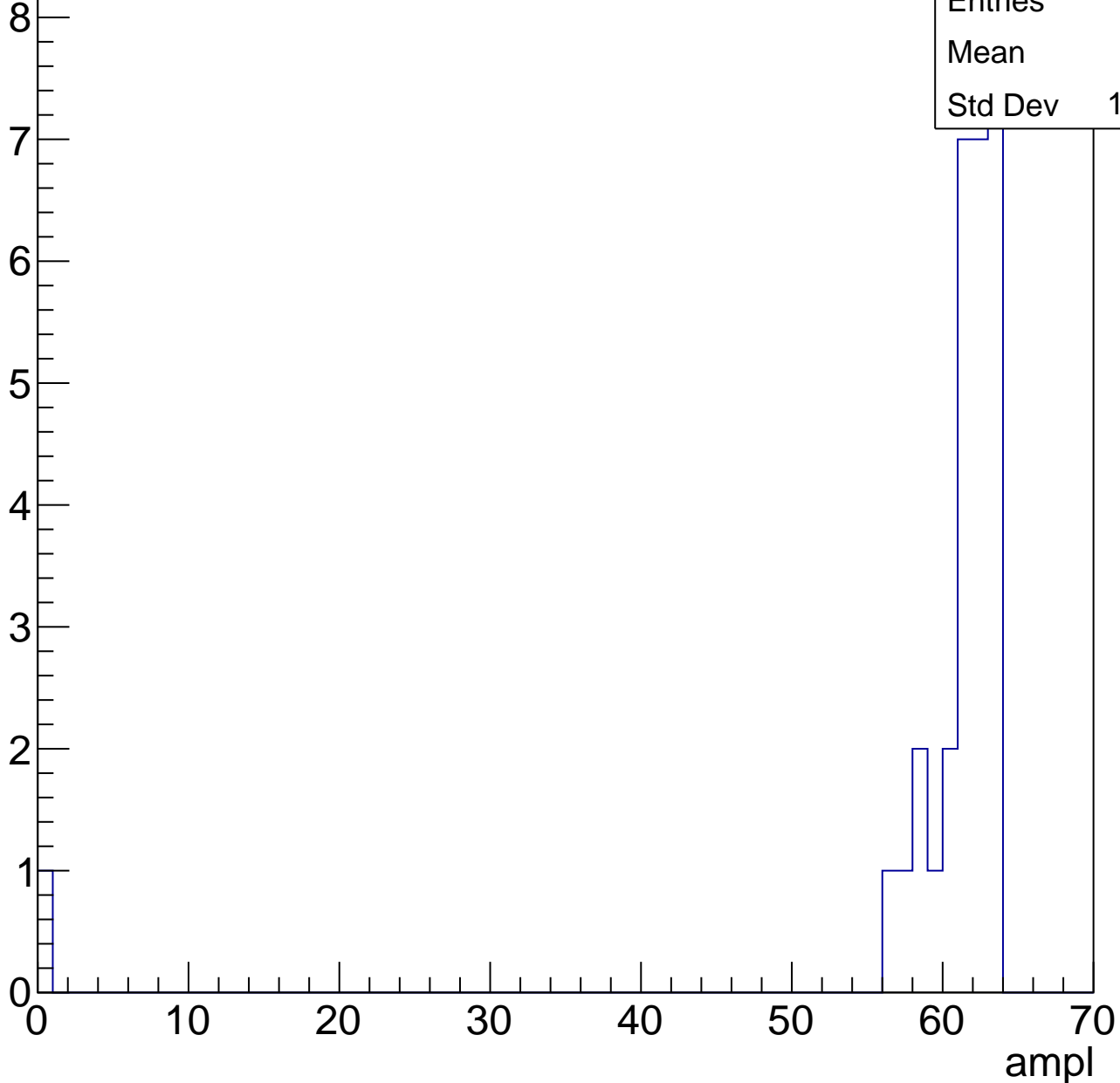


B1L003S, U13-ch118, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	30
Mean	59.1
Std Dev	11.13



B1L003S, U13-ch118, adc6

calib_packv5_042523_0143.root, FC#13, port D2

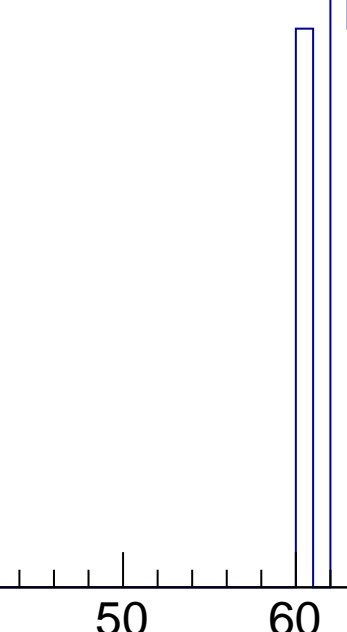
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch118, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch119, adc0

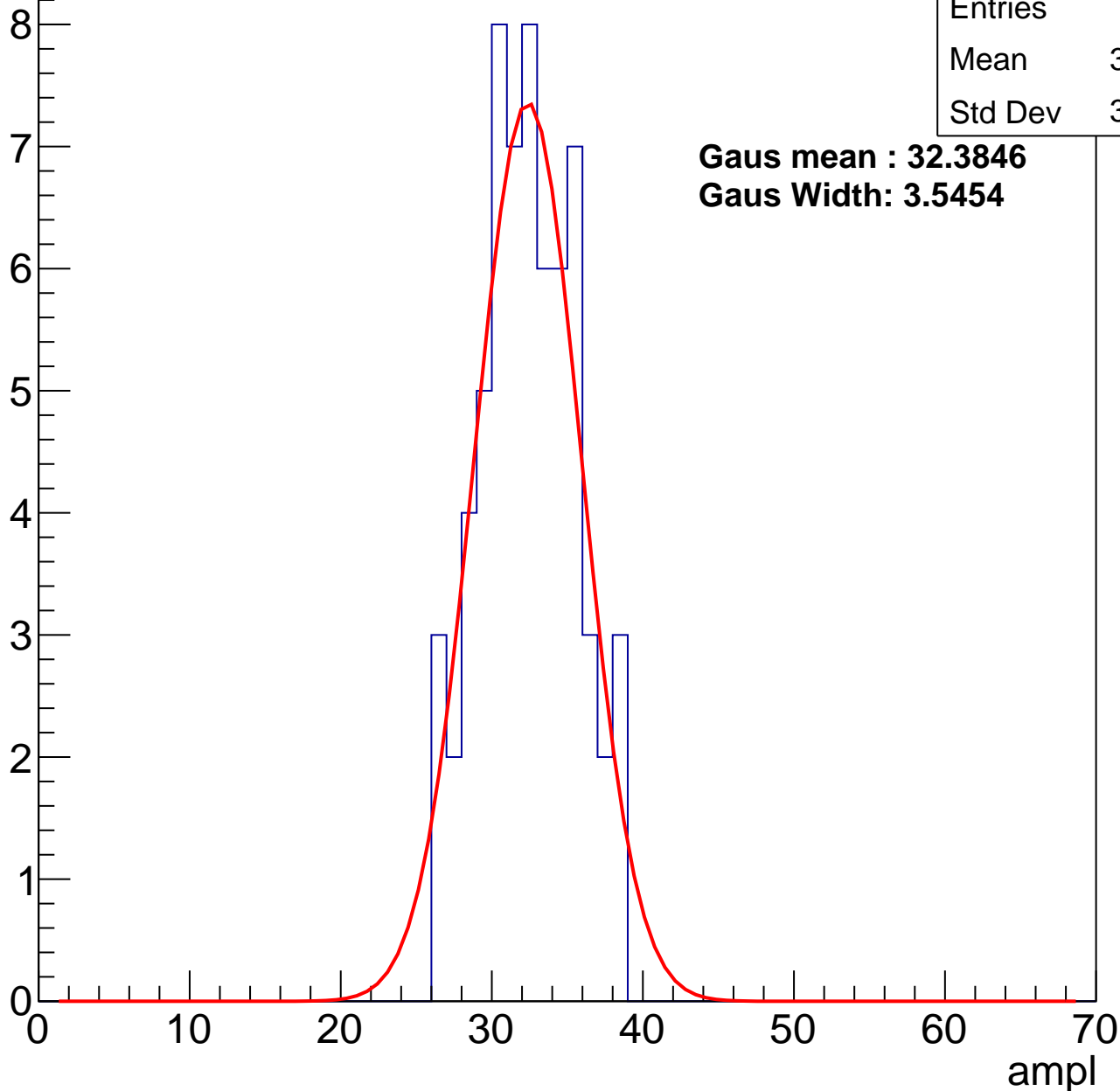
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	31.95
Std Dev	3.074

Gaus mean : 32.3846

Gaus Width: 3.5454



B1L003S, U13-ch119, adc1

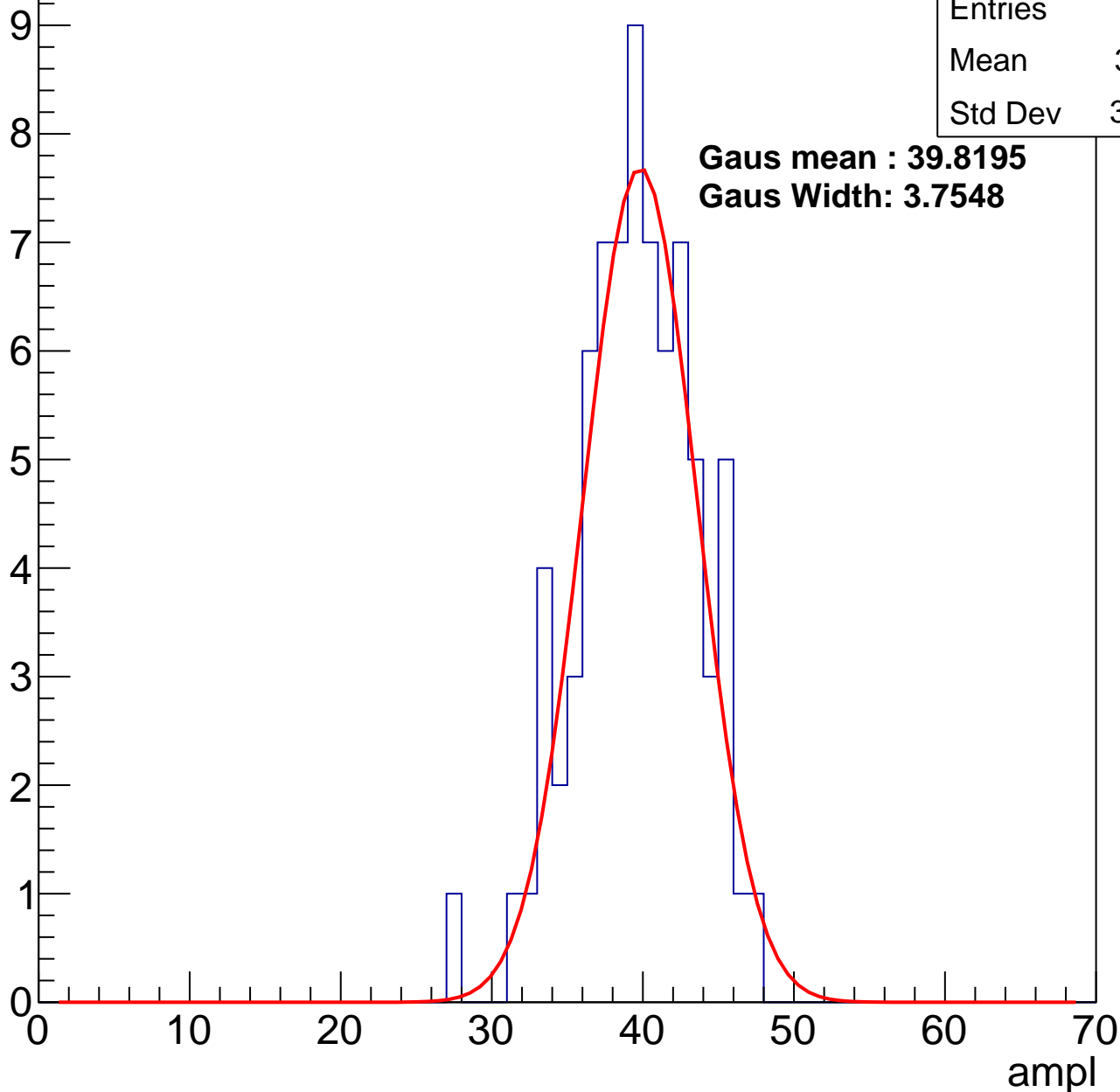
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	39.11
Std Dev	3.855

Gaus mean : 39.8195

Gaus Width: 3.7548



B1L003S, U13-ch119, adc2

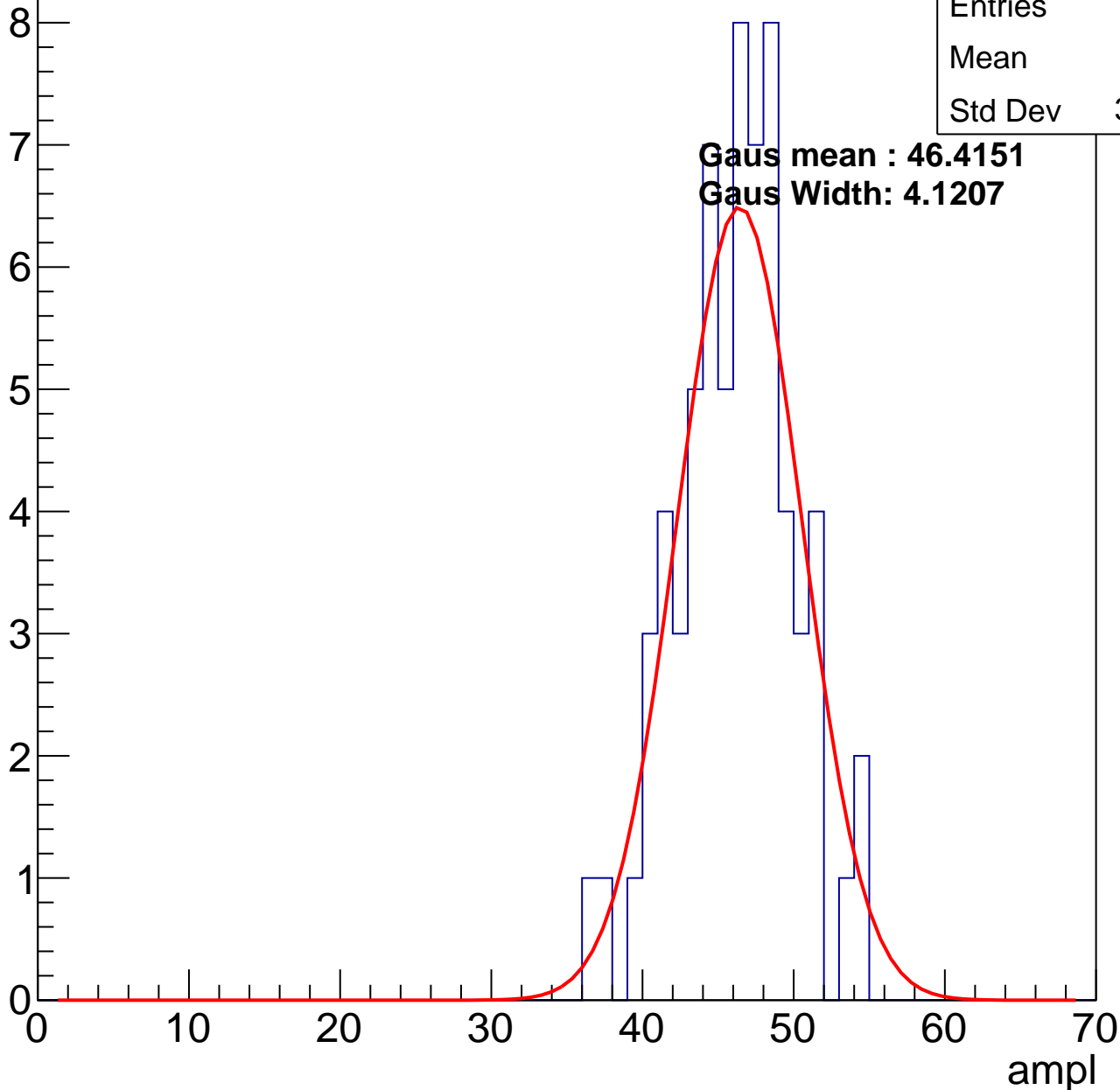
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	45.7
Std Dev	3.781

Gaus mean : 46.4151

Gaus Width: 4.1207

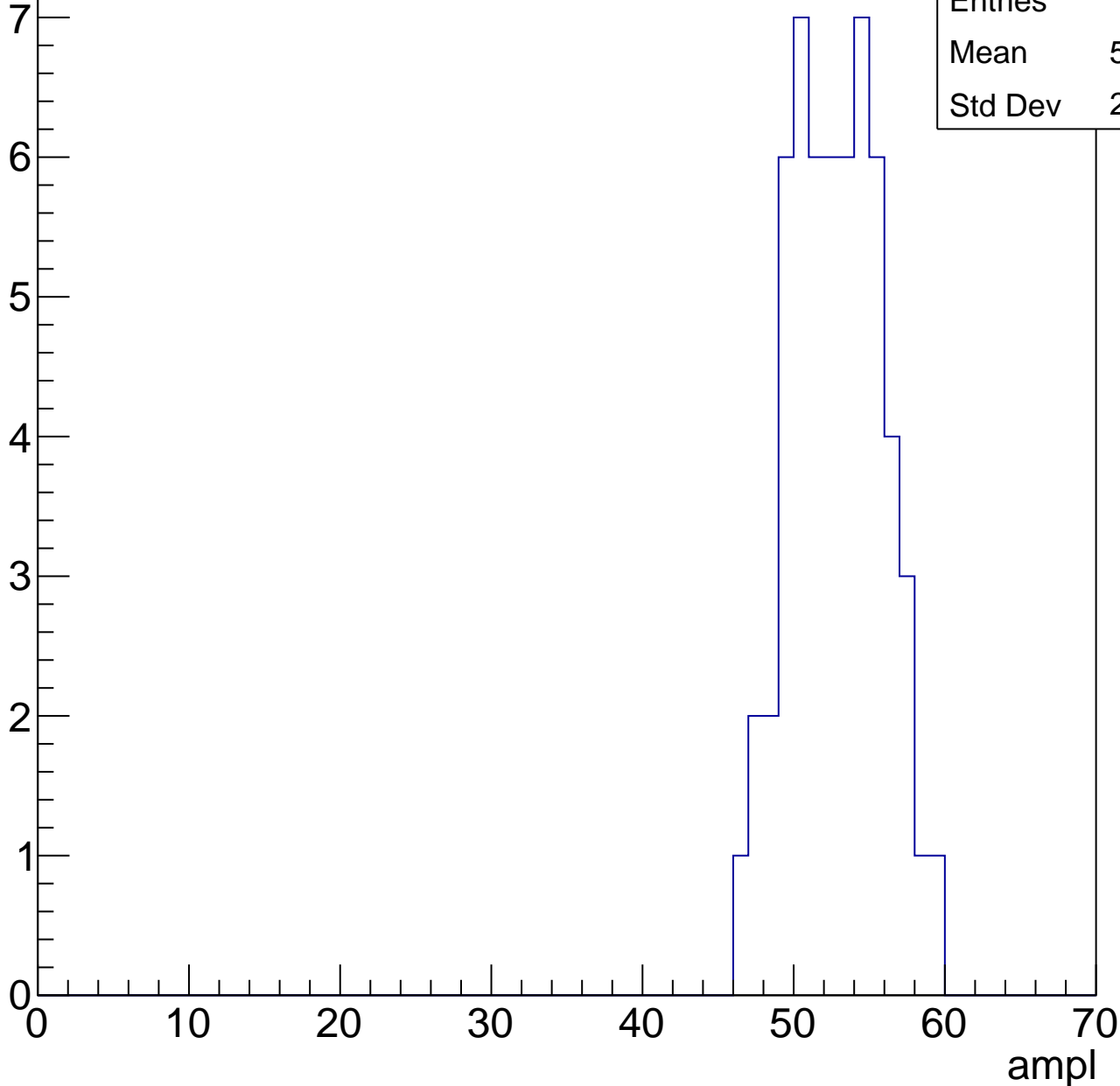


B1L003S, U13-ch119, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	52.34
Std Dev	2.969

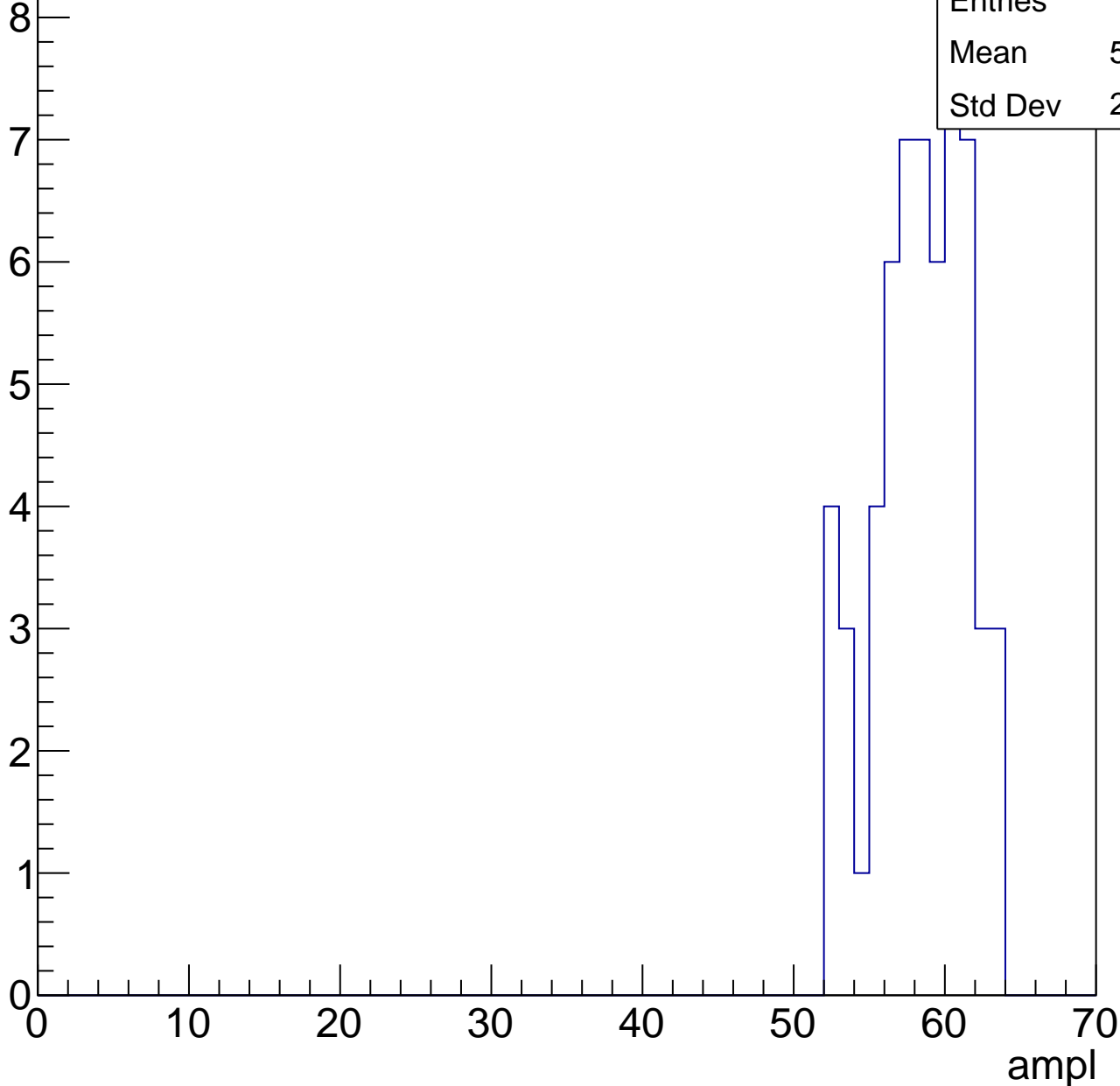


B1L003S, U13-ch119, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.93
Std Dev	2.985

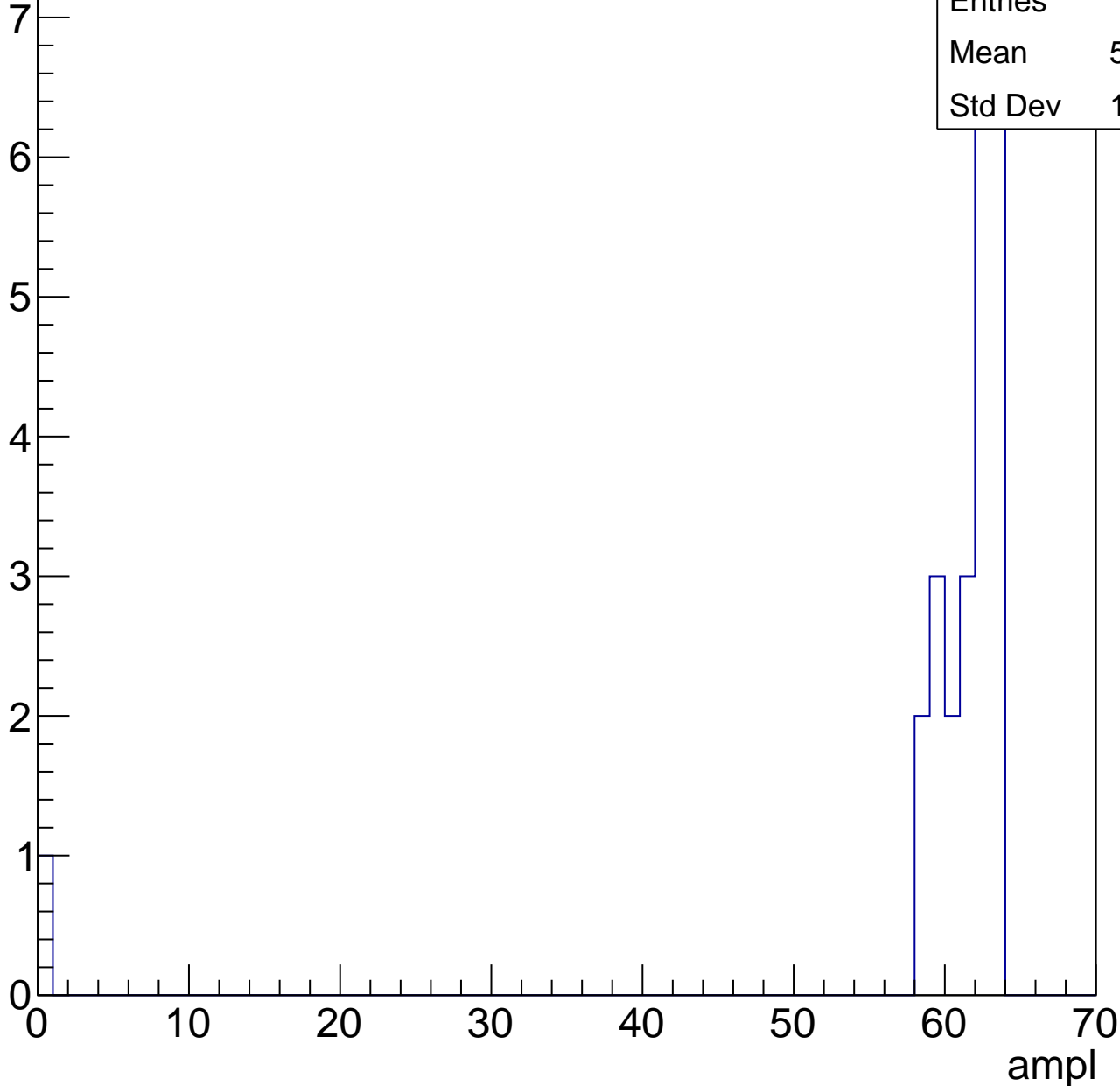


B1L003S, U13-ch119, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	25
Mean	58.84
Std Dev	12.12



B1L003S, U13-ch119, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

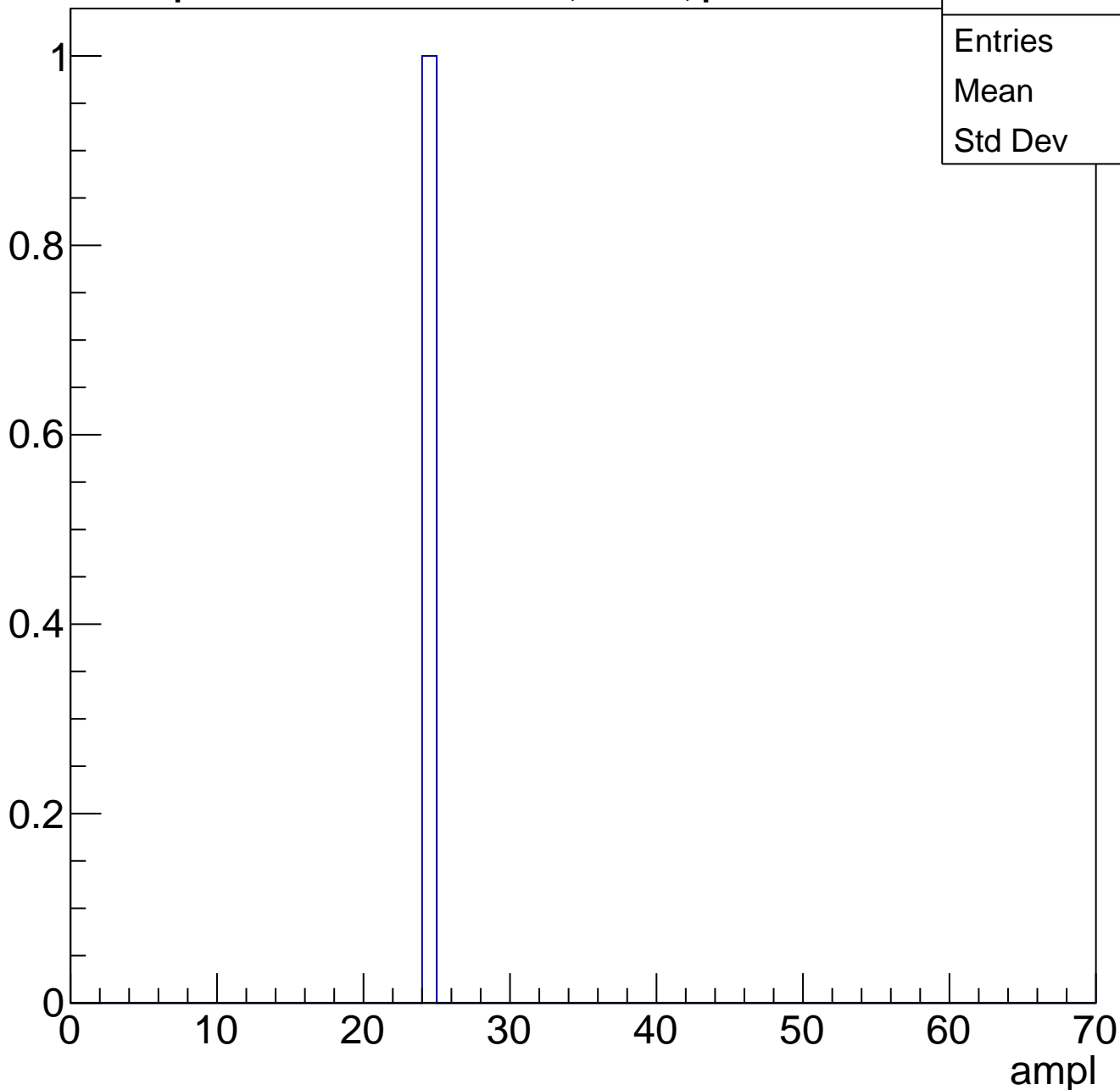


Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch119, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	24
Std Dev	0

B1L003S, U13-ch120, adc0

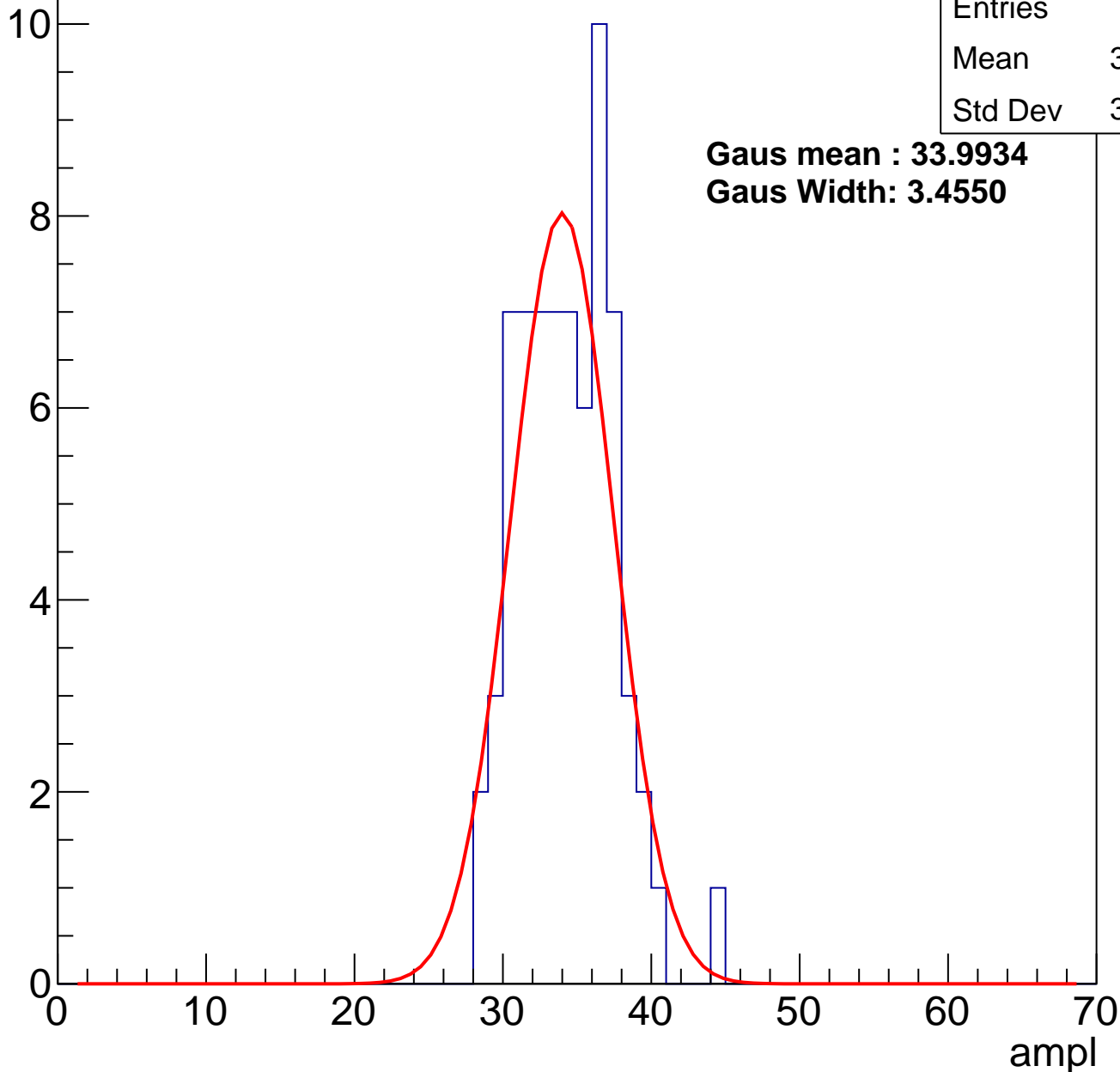
calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	33.83
Std Dev	3.158

Gaus mean : 33.9934

Gaus Width: 3.4550

Entry



B1L003S, U13-ch120, adc1

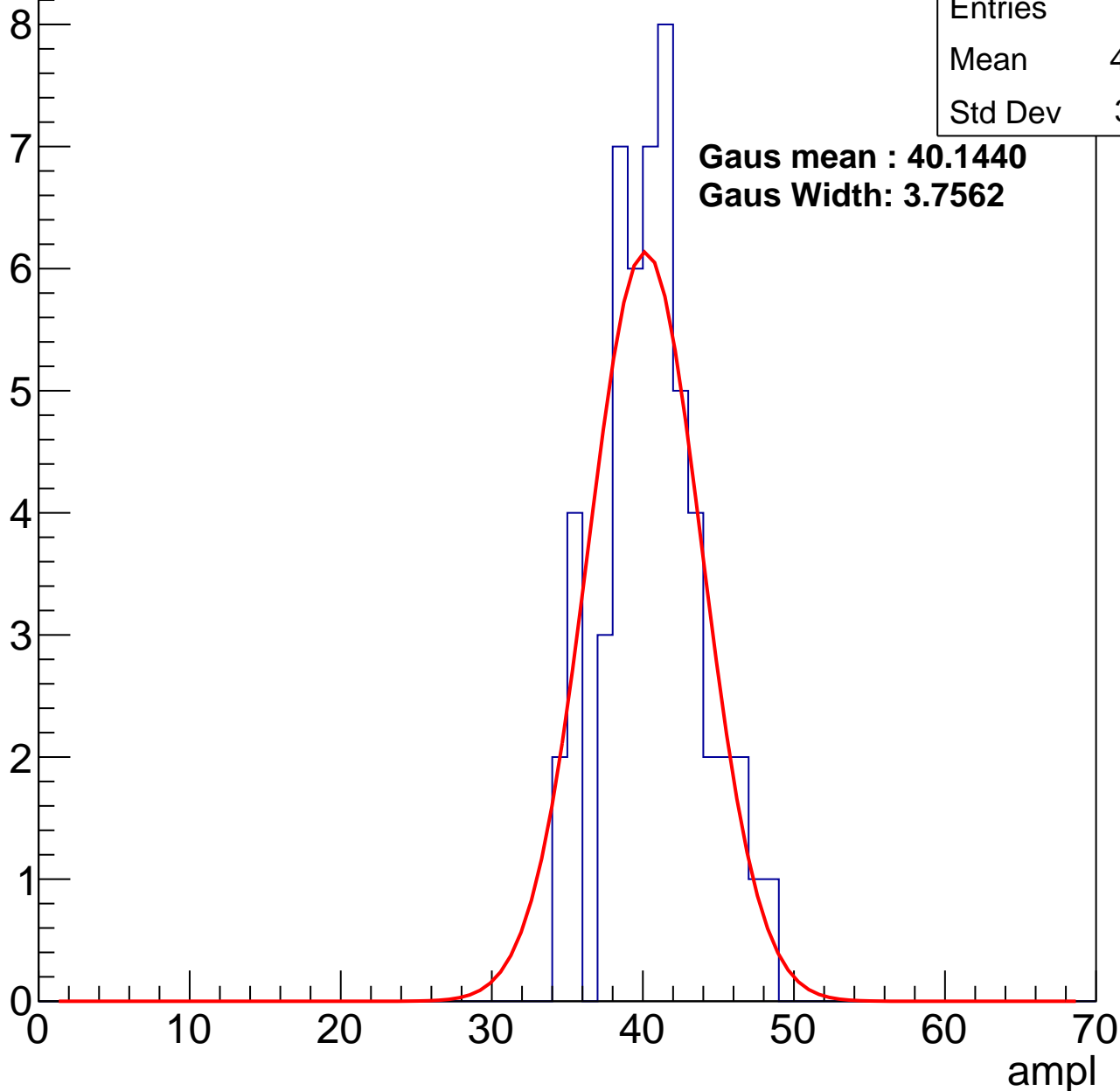
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	40.26
Std Dev	3.221

Gaus mean : 40.1440

Gaus Width: 3.7562



B1L003S, U13-ch120, adc2

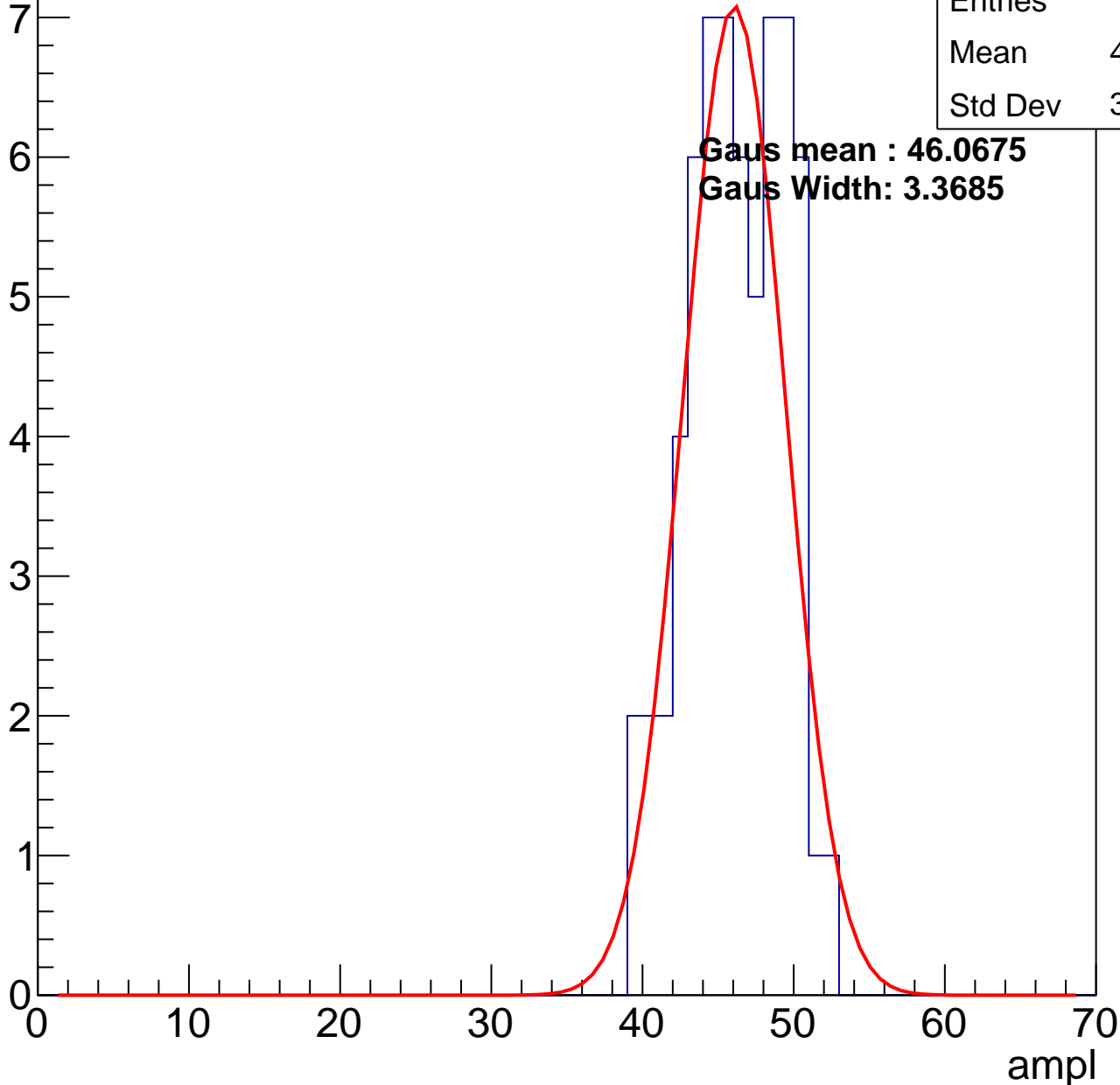
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	45.75
Std Dev	3.142

Gaus mean : 46.0675

Gaus Width: 3.3685

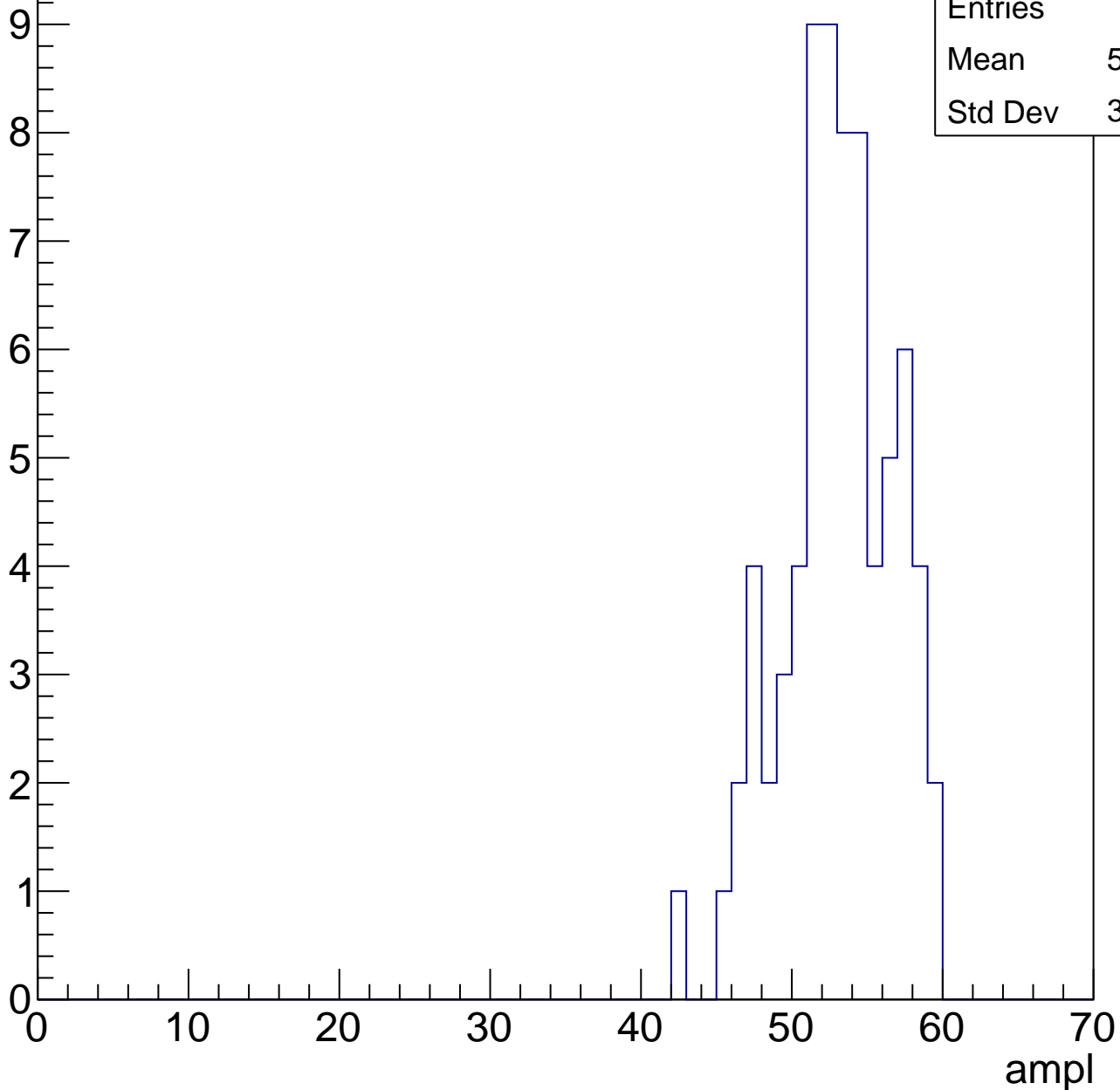


B1L003S, U13-ch120, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	52.57
Std Dev	3.593

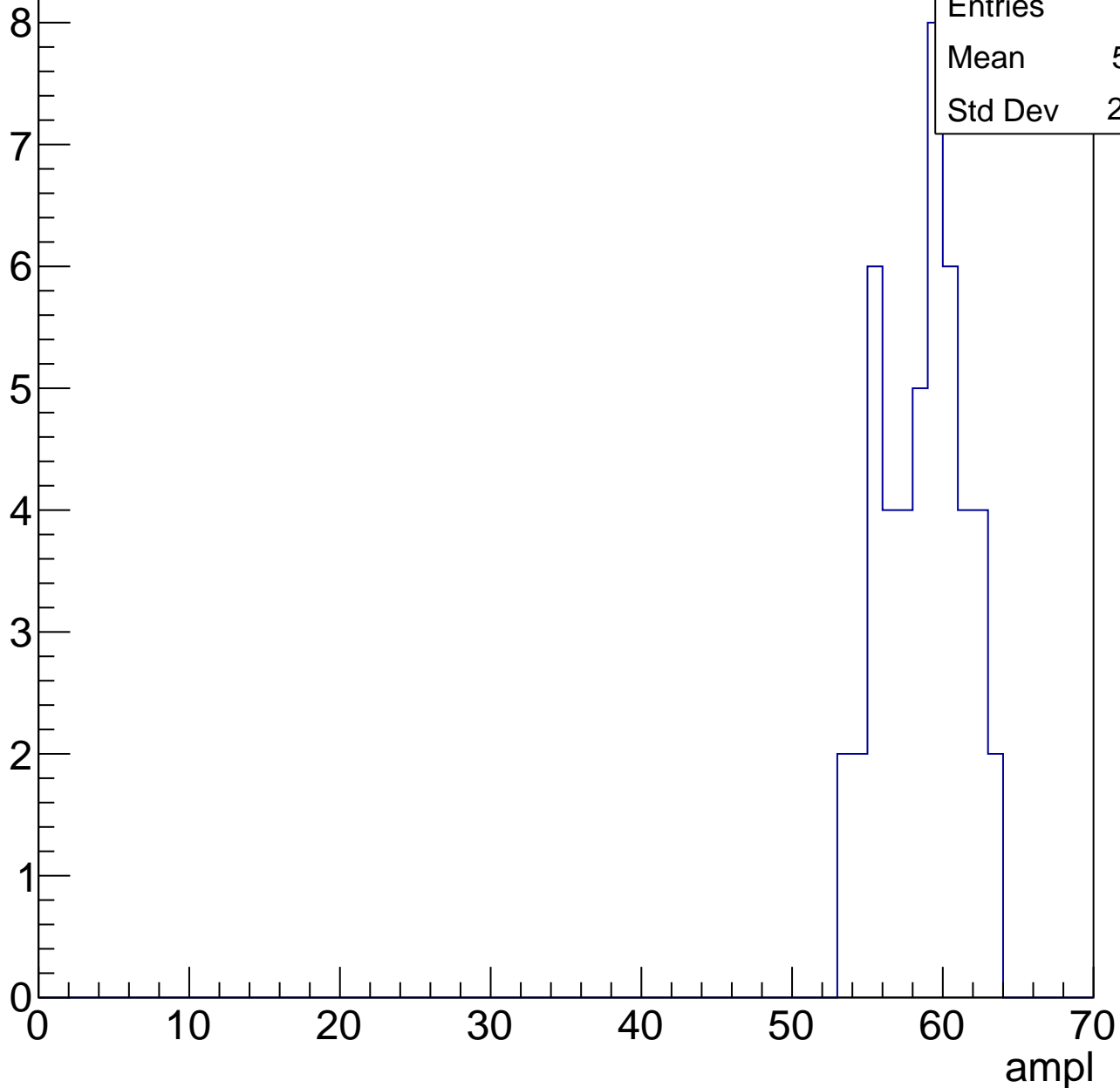


B1L003S, U13-ch120, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	58.21
Std Dev	2.673

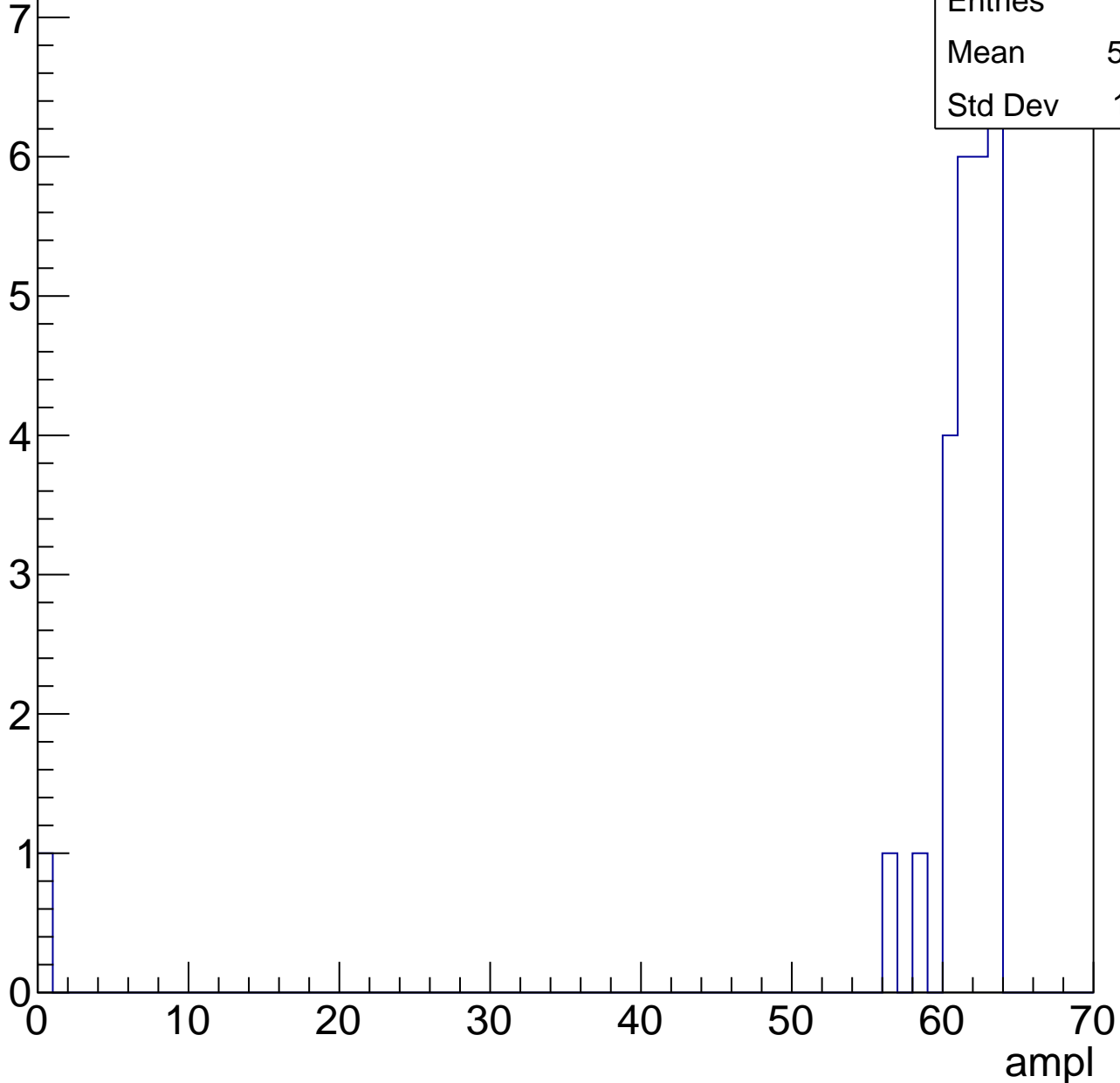


B1L003S, U13-ch120, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	26
Mean	58.96
Std Dev	11.91



B1L003S, U13-ch120, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch120, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch121, adc0

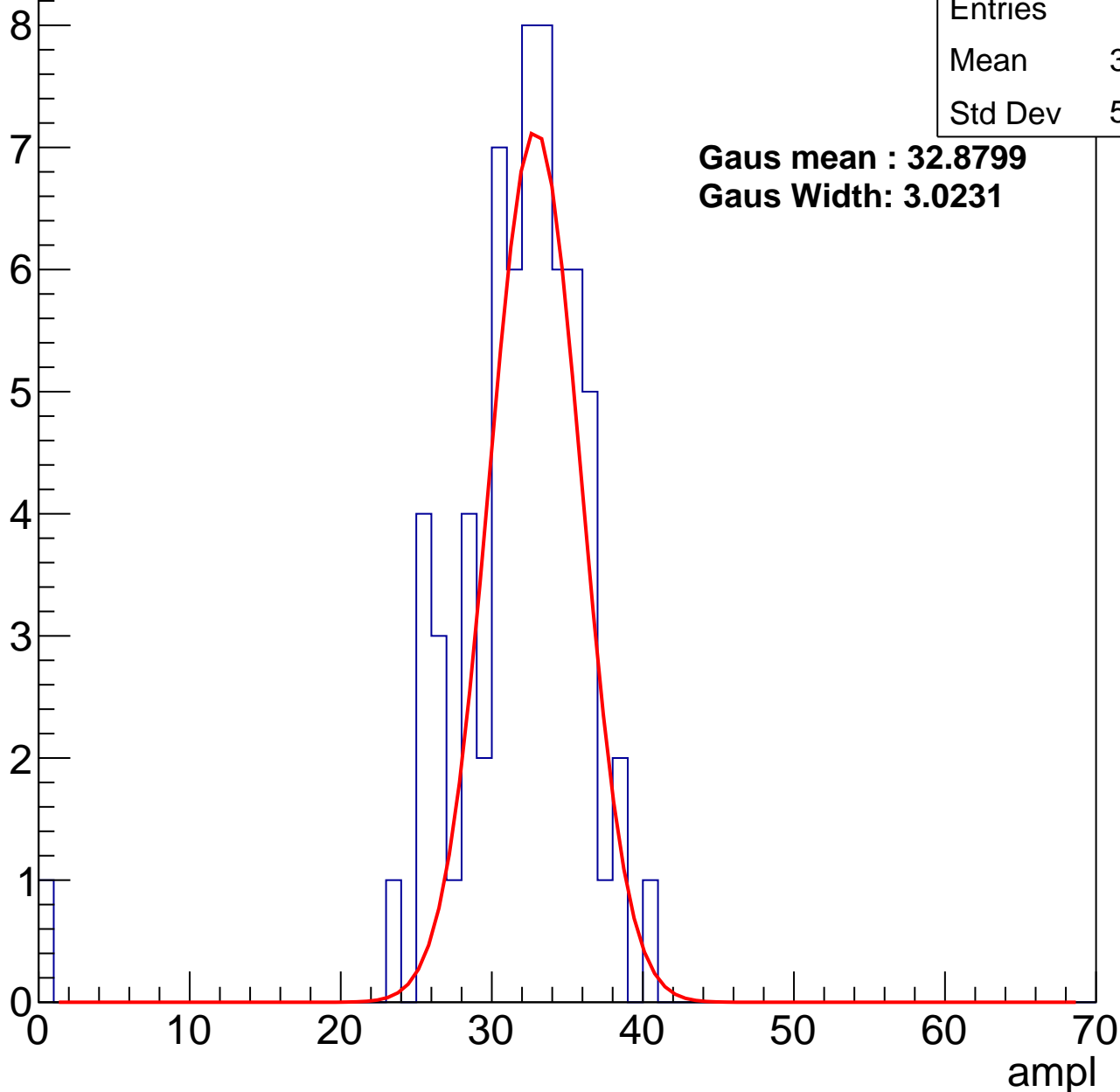
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	31.23
Std Dev	5.277

Gaus mean : 32.8799

Gaus Width: 3.0231



B1L003S, U13-ch121, adc1

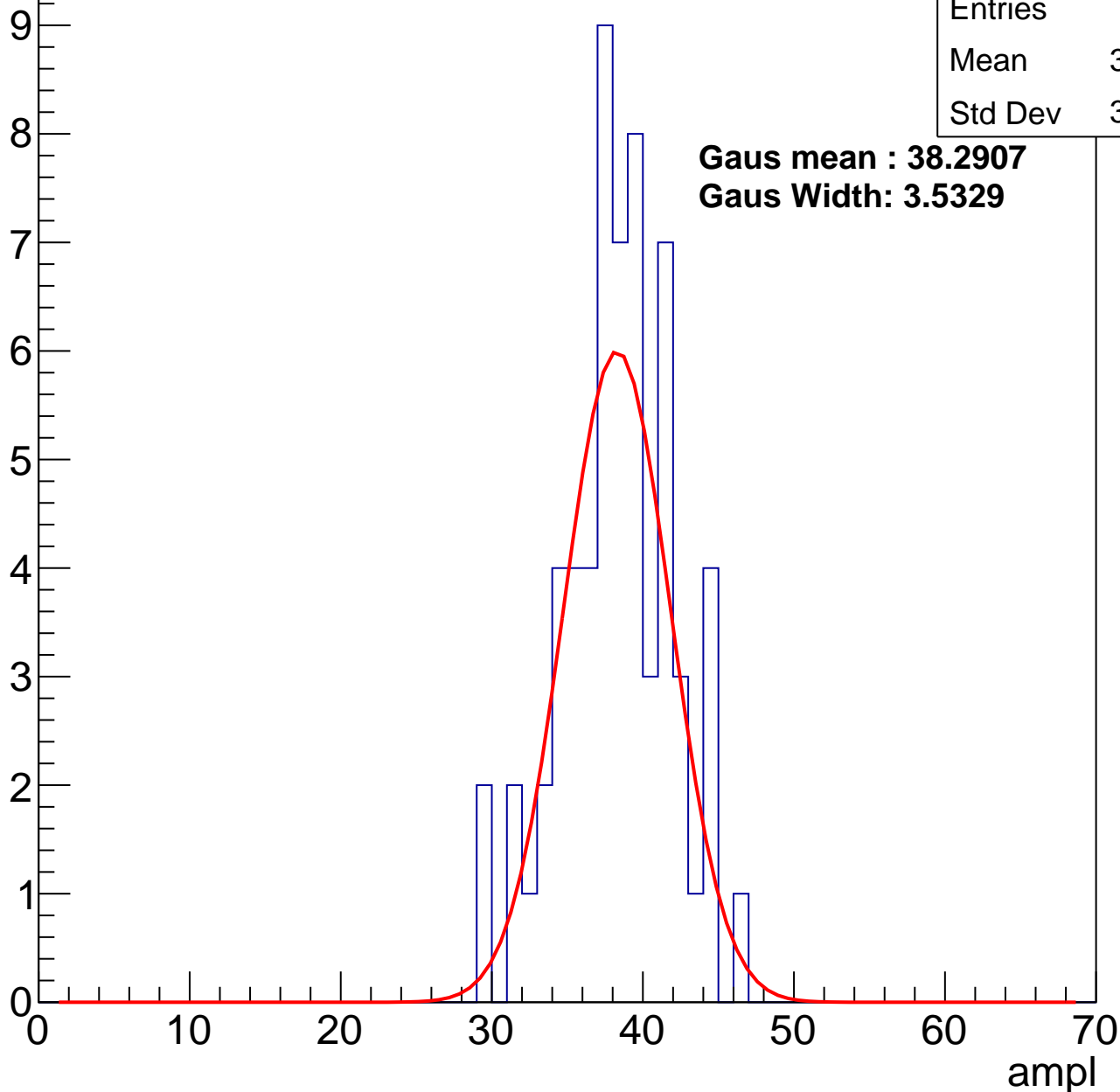
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.85
Std Dev	3.667

Gaus mean : 38.2907

Gaus Width: 3.5329



B1L003S, U13-ch121, adc2

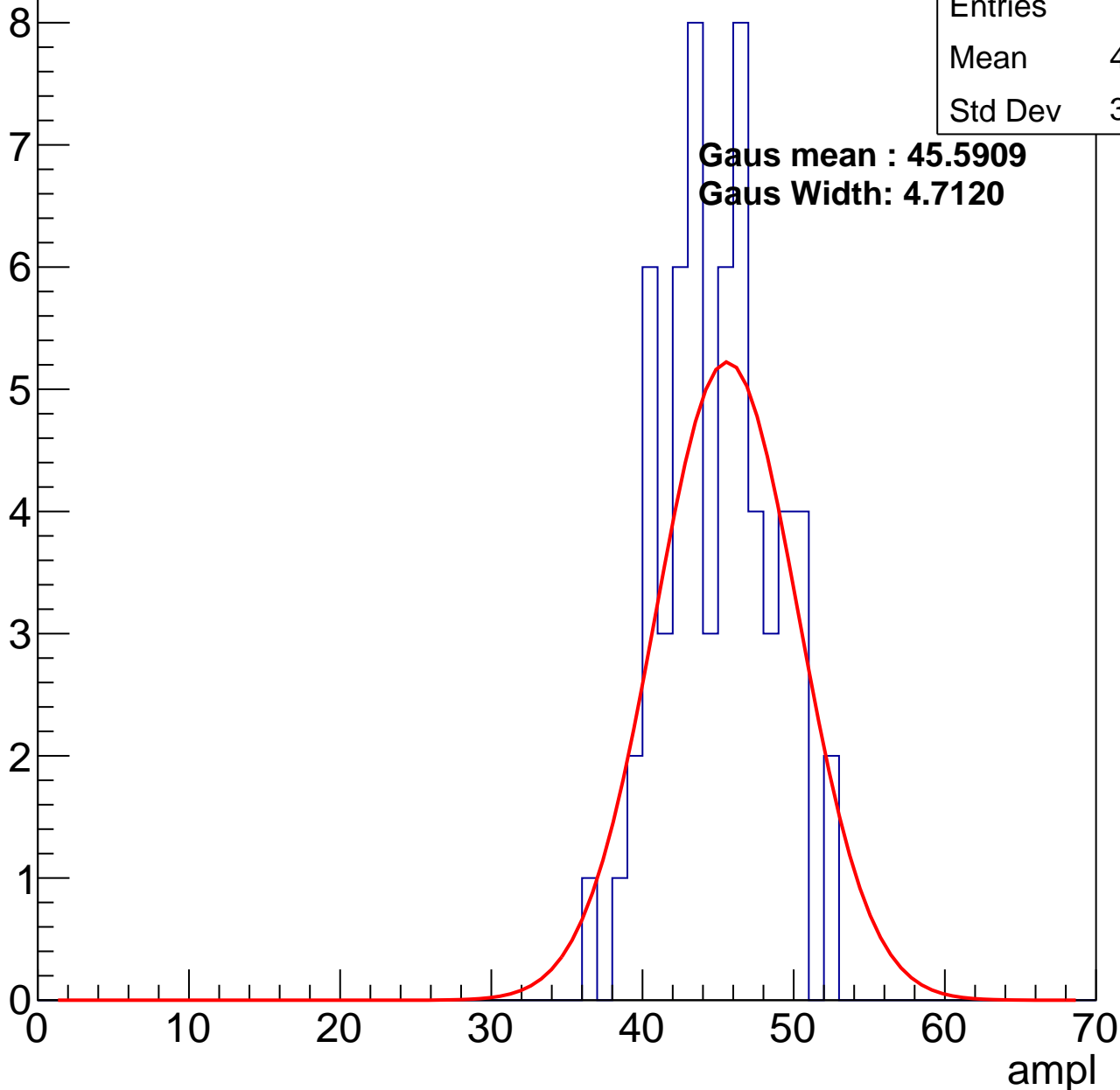
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	44.48
Std Dev	3.588

Gaus mean : 45.5909

Gaus Width: 4.7120

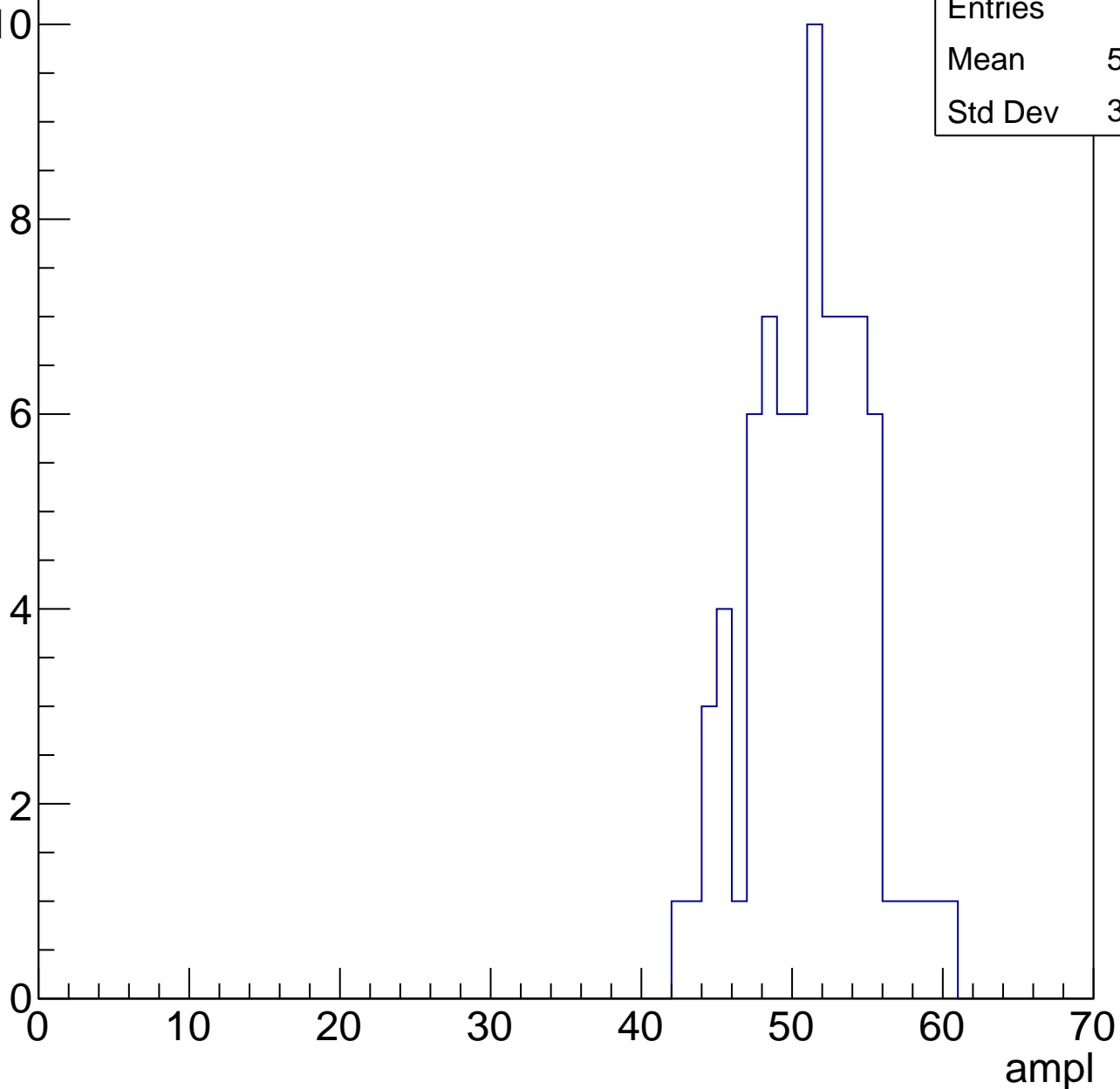


B1L003S, U13-ch121, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	50.62
Std Dev	3.759

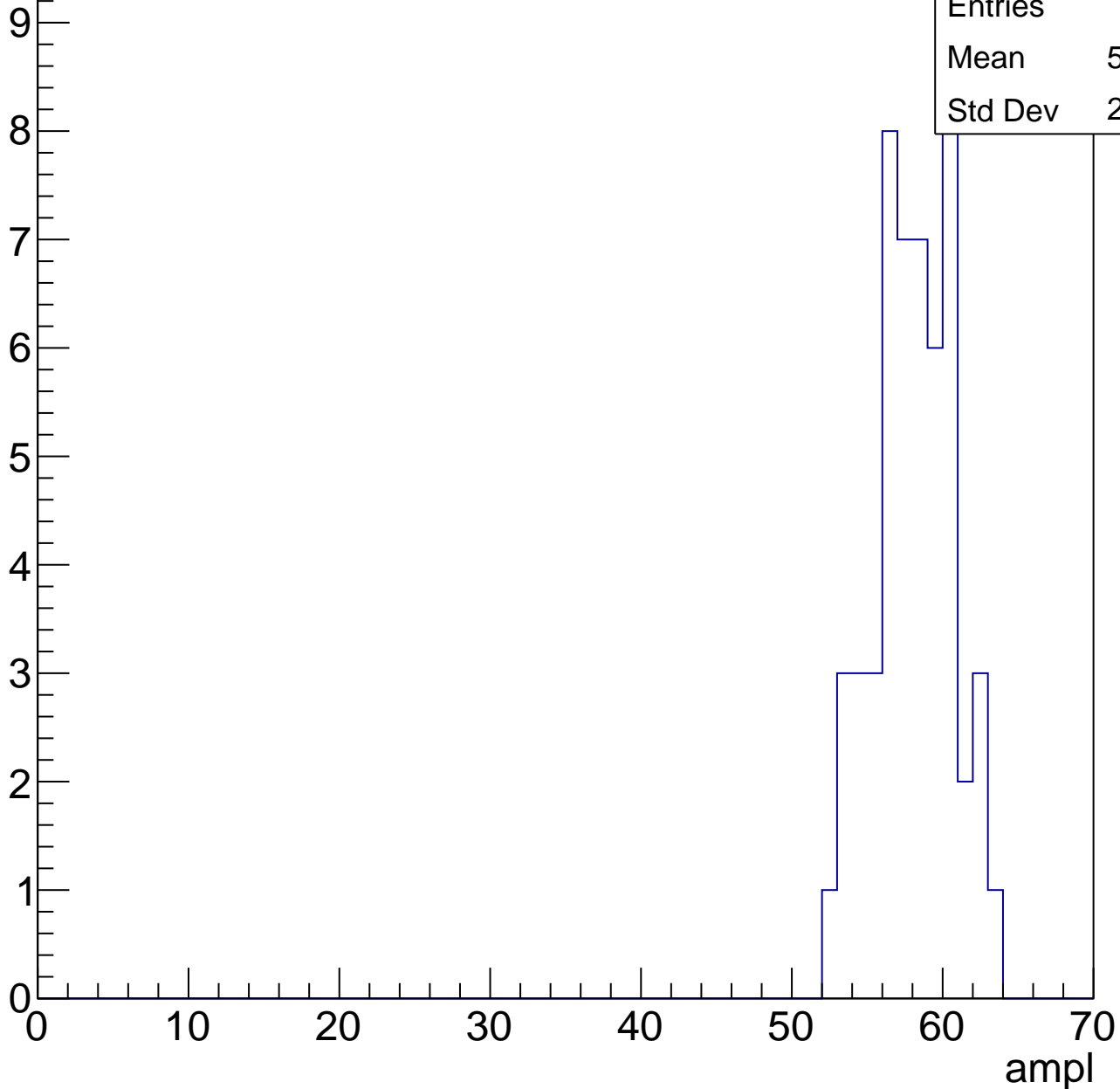


B1L003S, U13-ch121, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	57.66
Std Dev	2.577

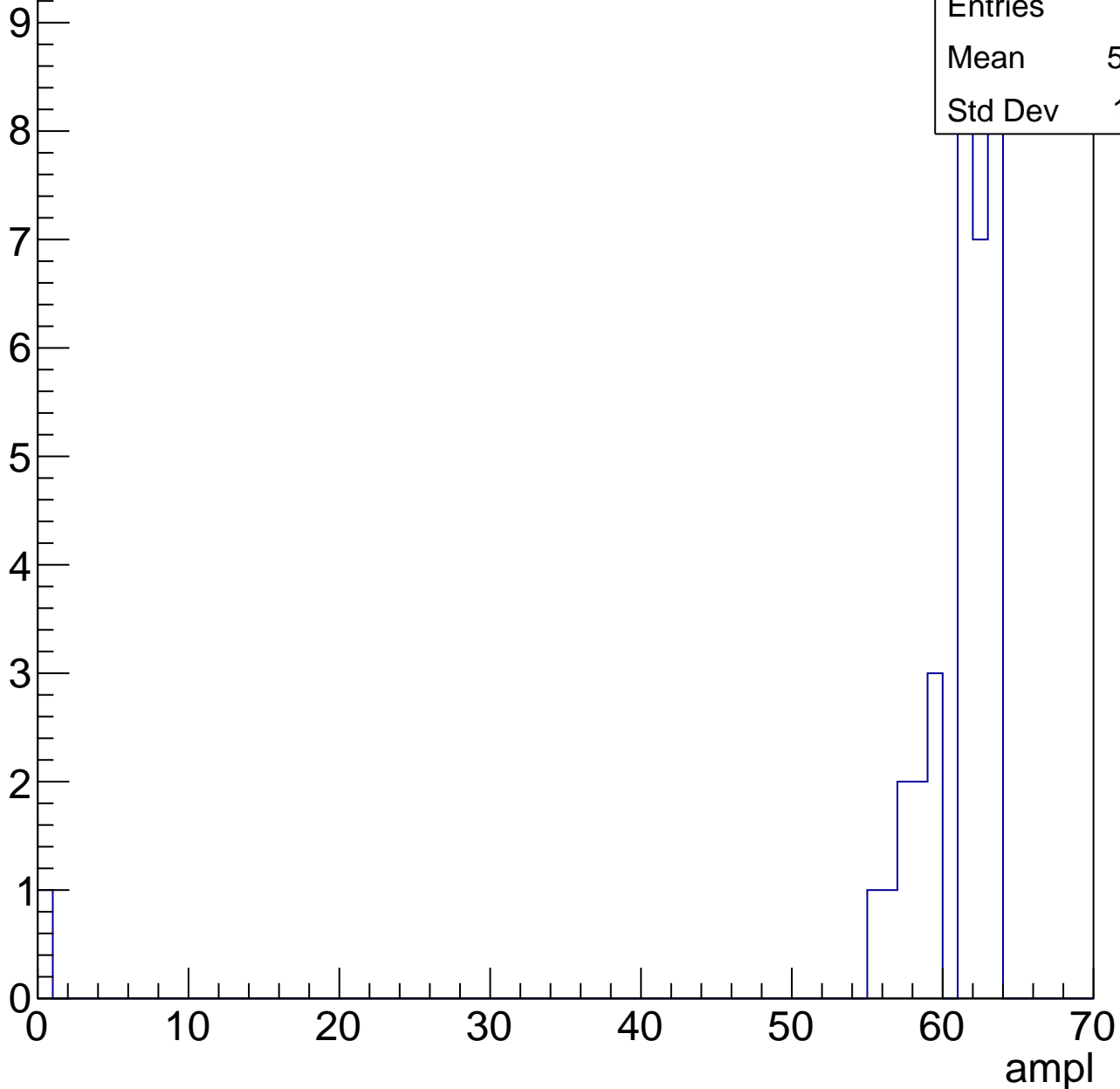


B1L003S, U13-ch121, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	59.03
Std Dev	10.51



B1L003S, U13-ch121, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch121, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch122, adc0

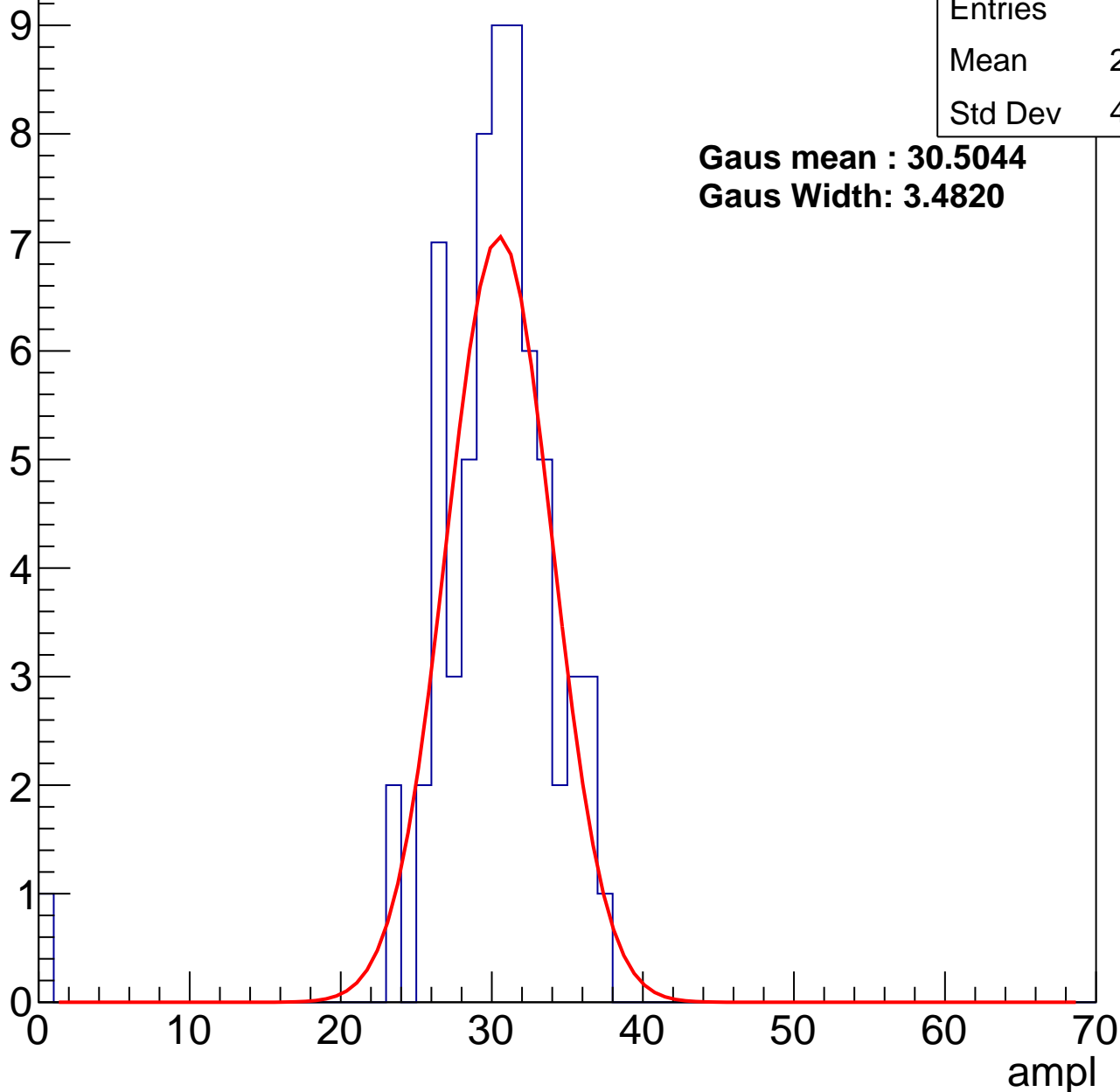
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.62
Std Dev	4.842

Gaus mean : 30.5044

Gaus Width: 3.4820



B1L003S, U13-ch122, adc1

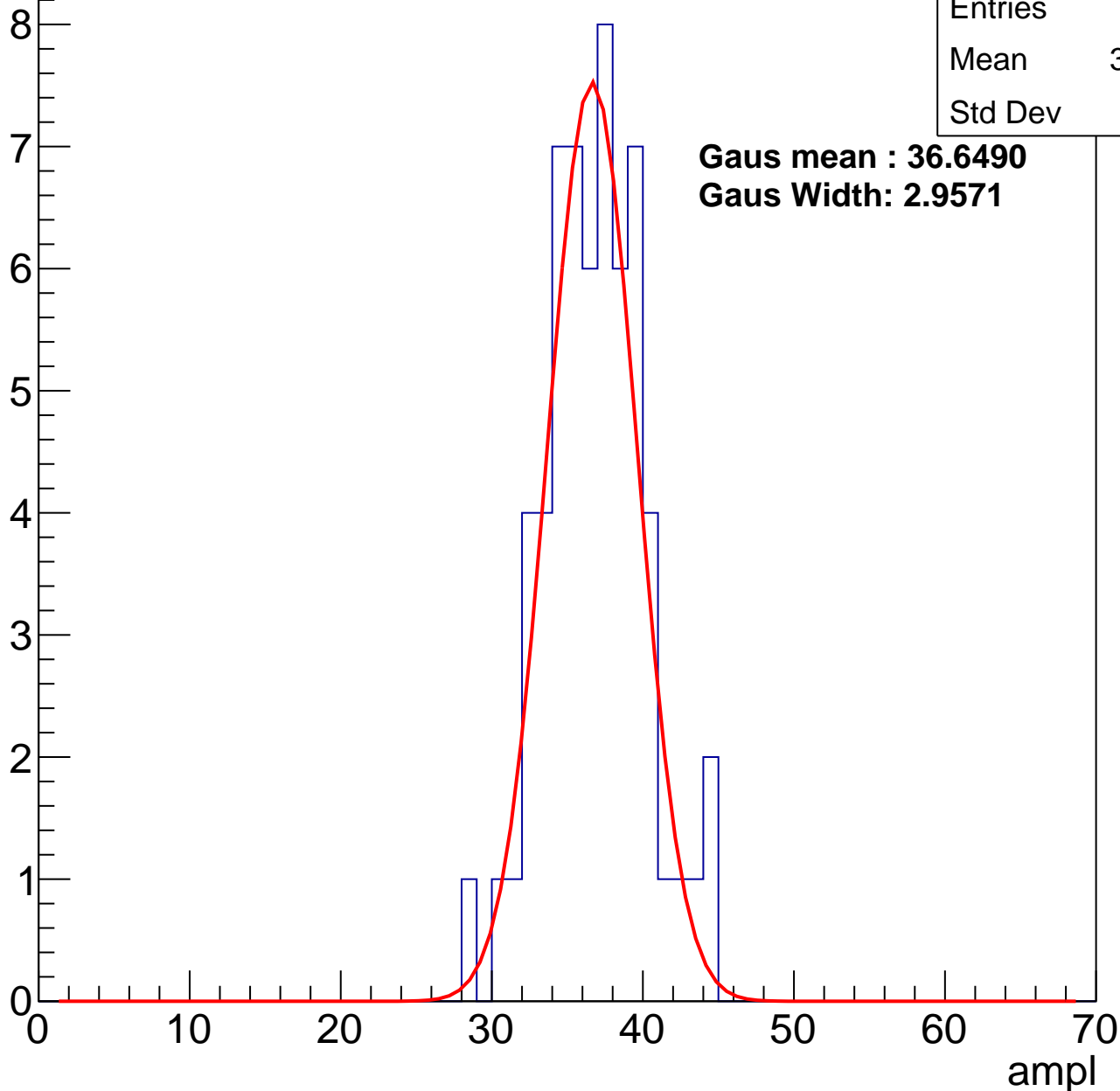
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.38
Std Dev	3.26

Gaus mean : 36.6490

Gaus Width: 2.9571



B1L003S, U13-ch122, adc2

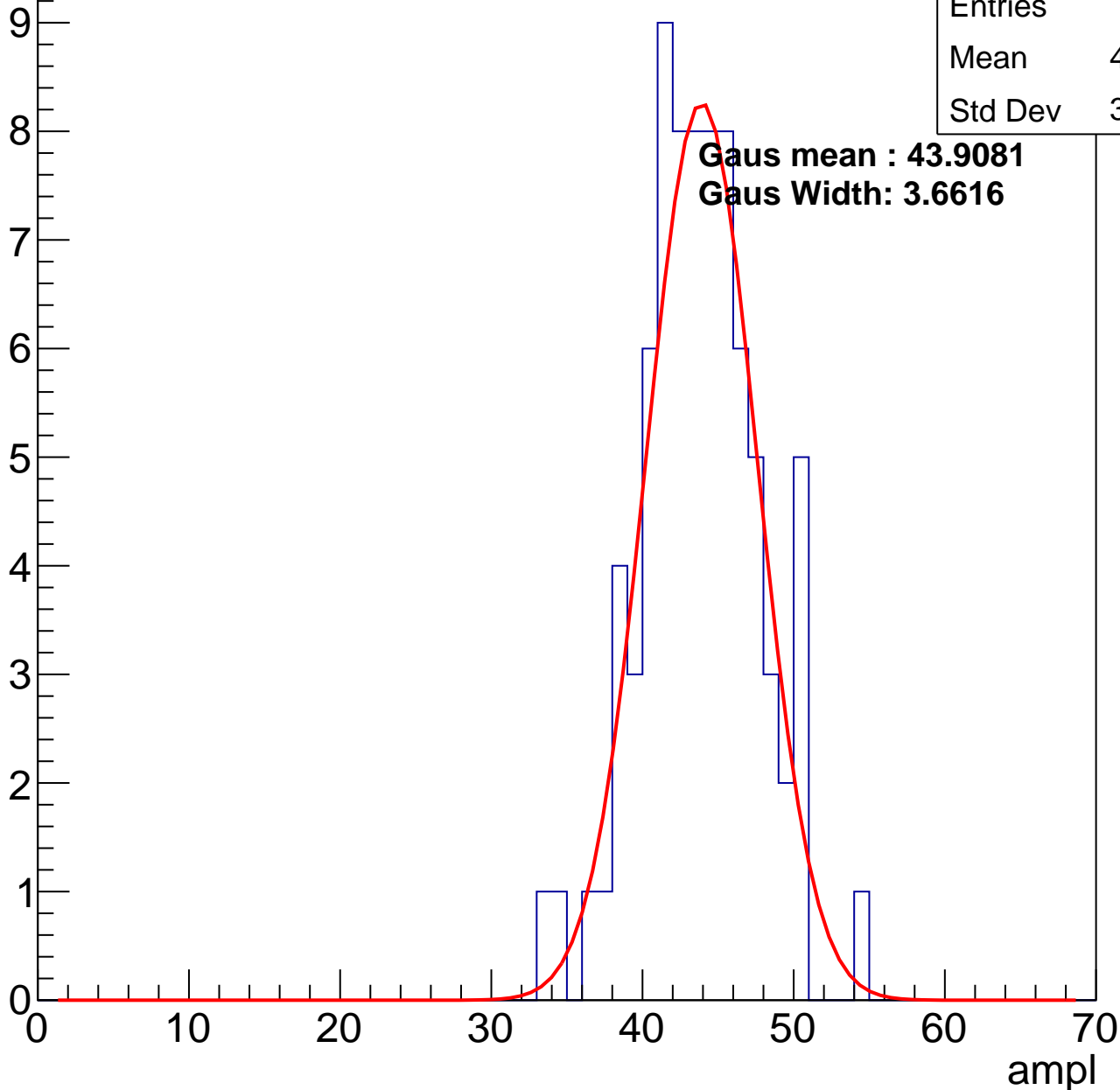
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	43.34
Std Dev	3.857

Gaus mean : 43.9081

Gaus Width: 3.6616

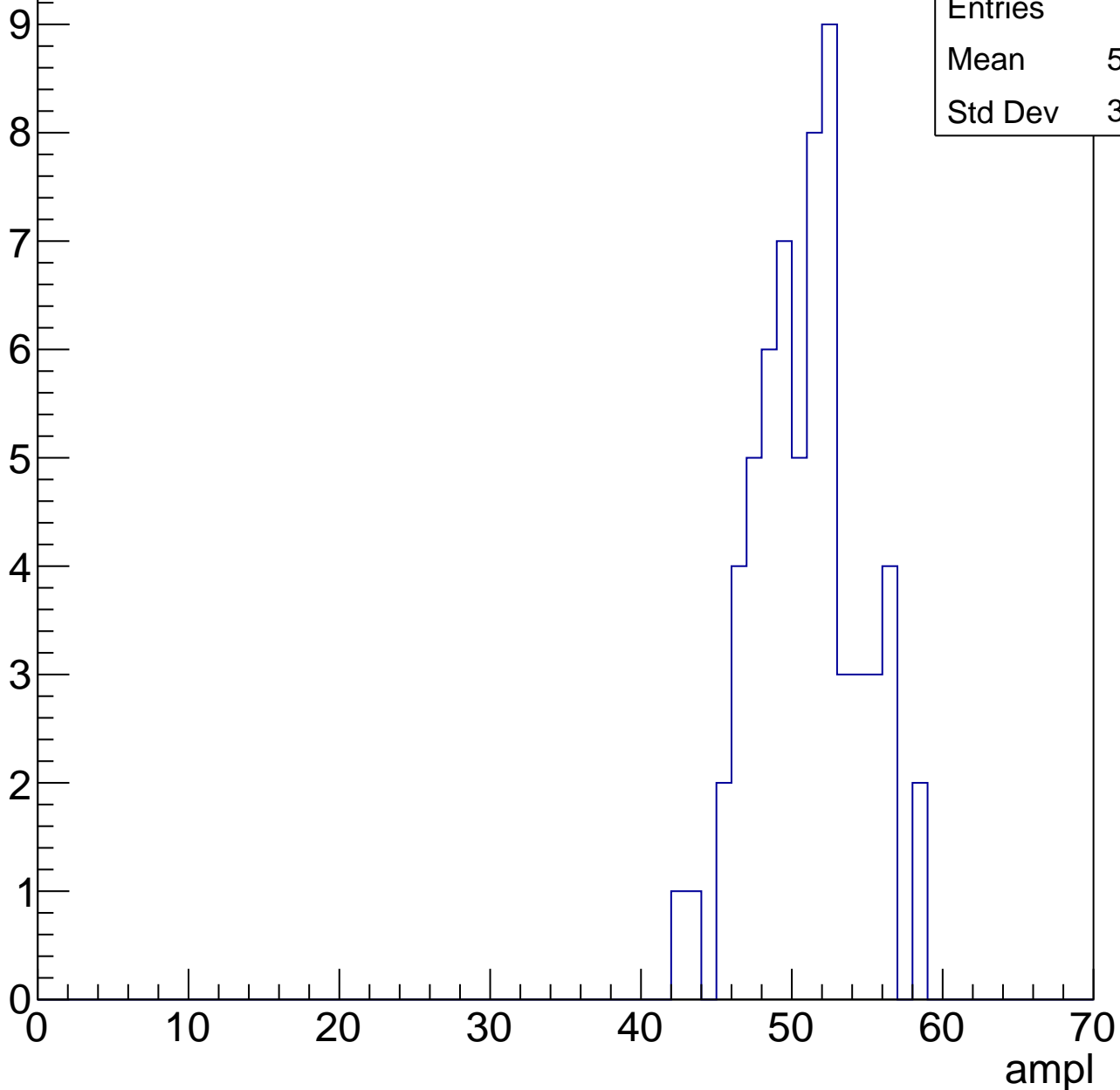


B1L003S, U13-ch122, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	50.43
Std Dev	3.467

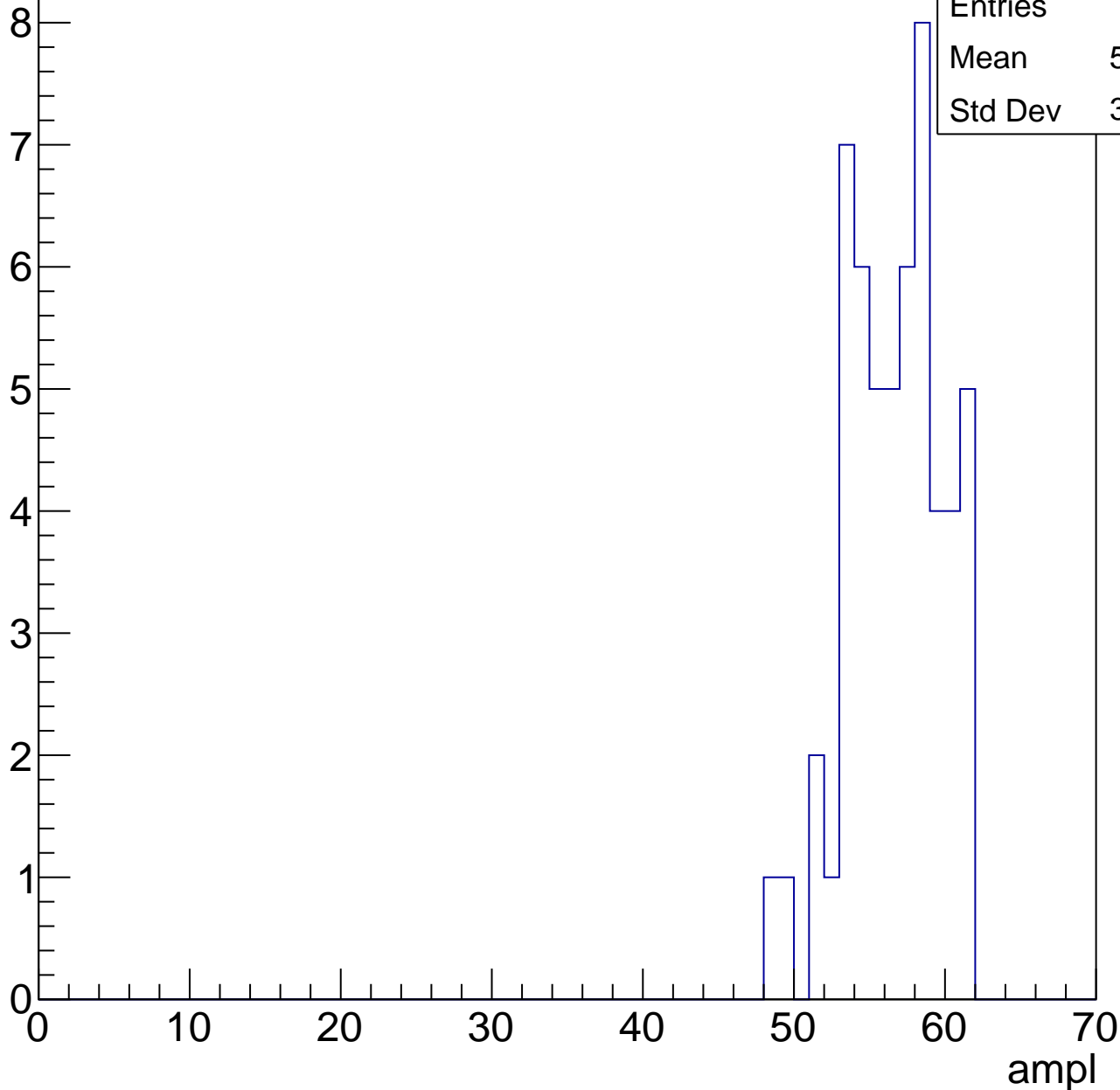


B1L003S, U13-ch122, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	56.15
Std Dev	3.113

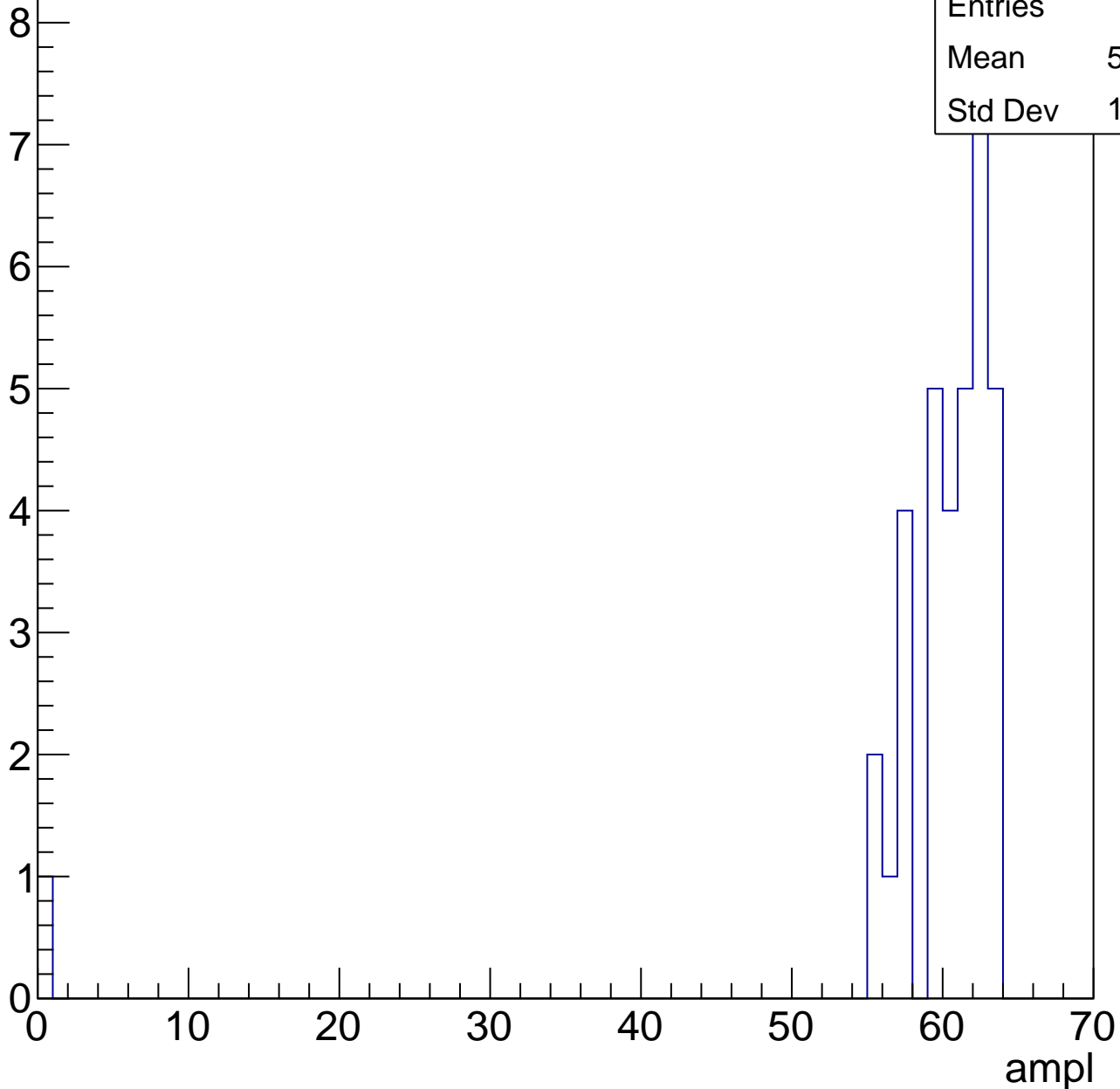


B1L003S, U13-ch122, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

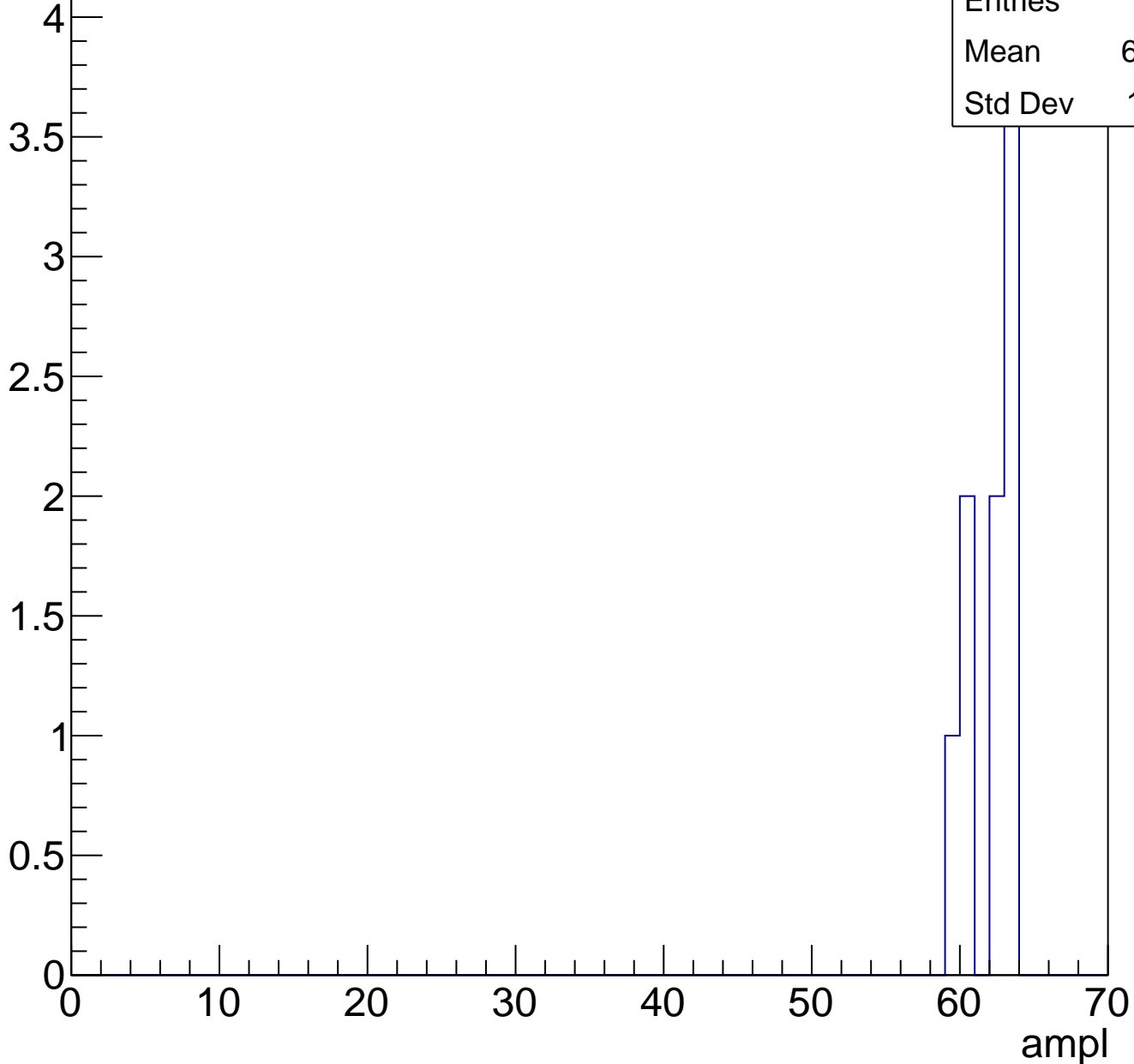
Entries	35
Mean	58.43
Std Dev	10.29



B1L003S, U13-ch122, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch122, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U13-ch123, adc0

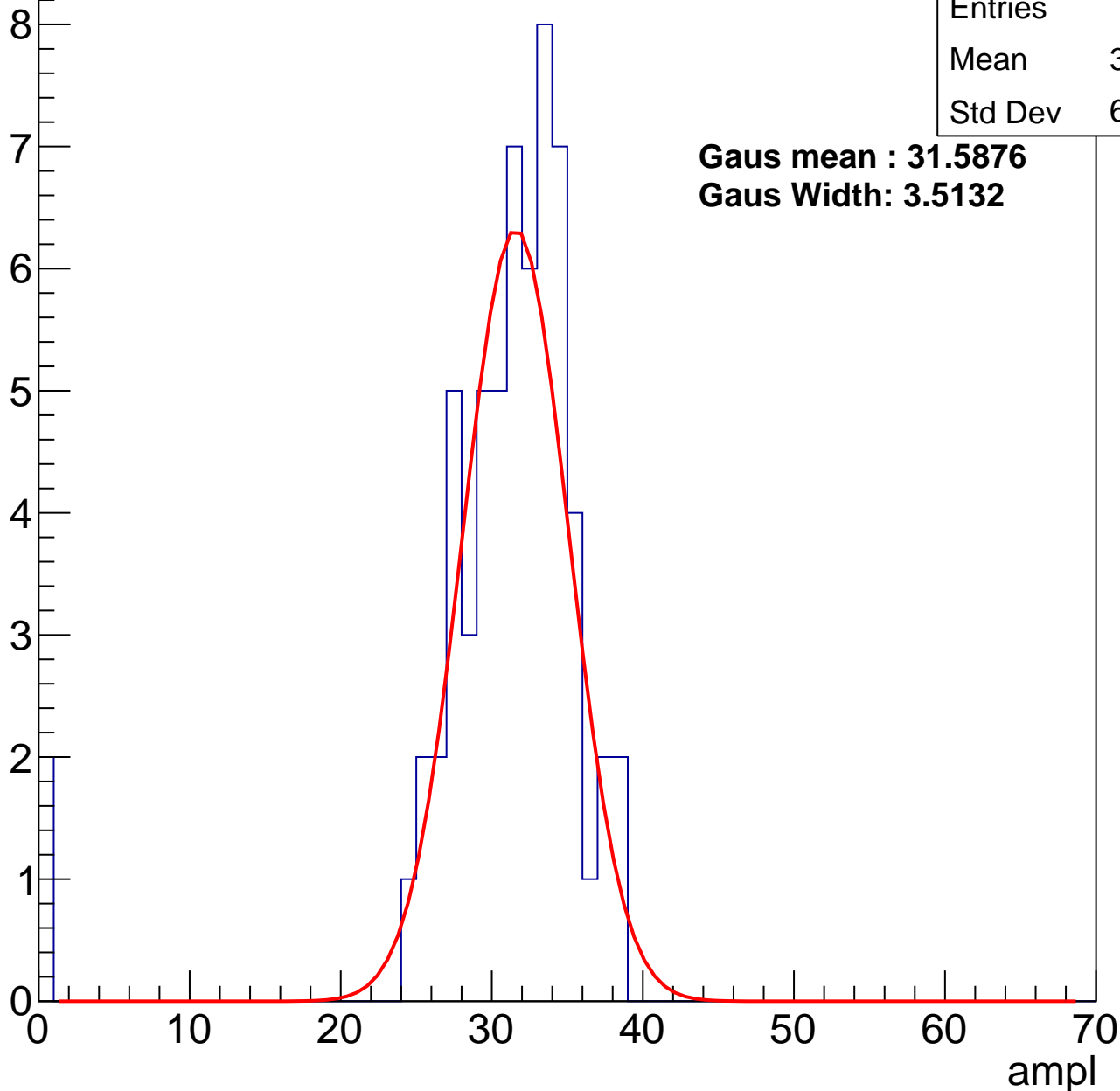
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.27
Std Dev	6.416

Gaus mean : 31.5876

Gaus Width: 3.5132



B1L003S, U13-ch123, adc1

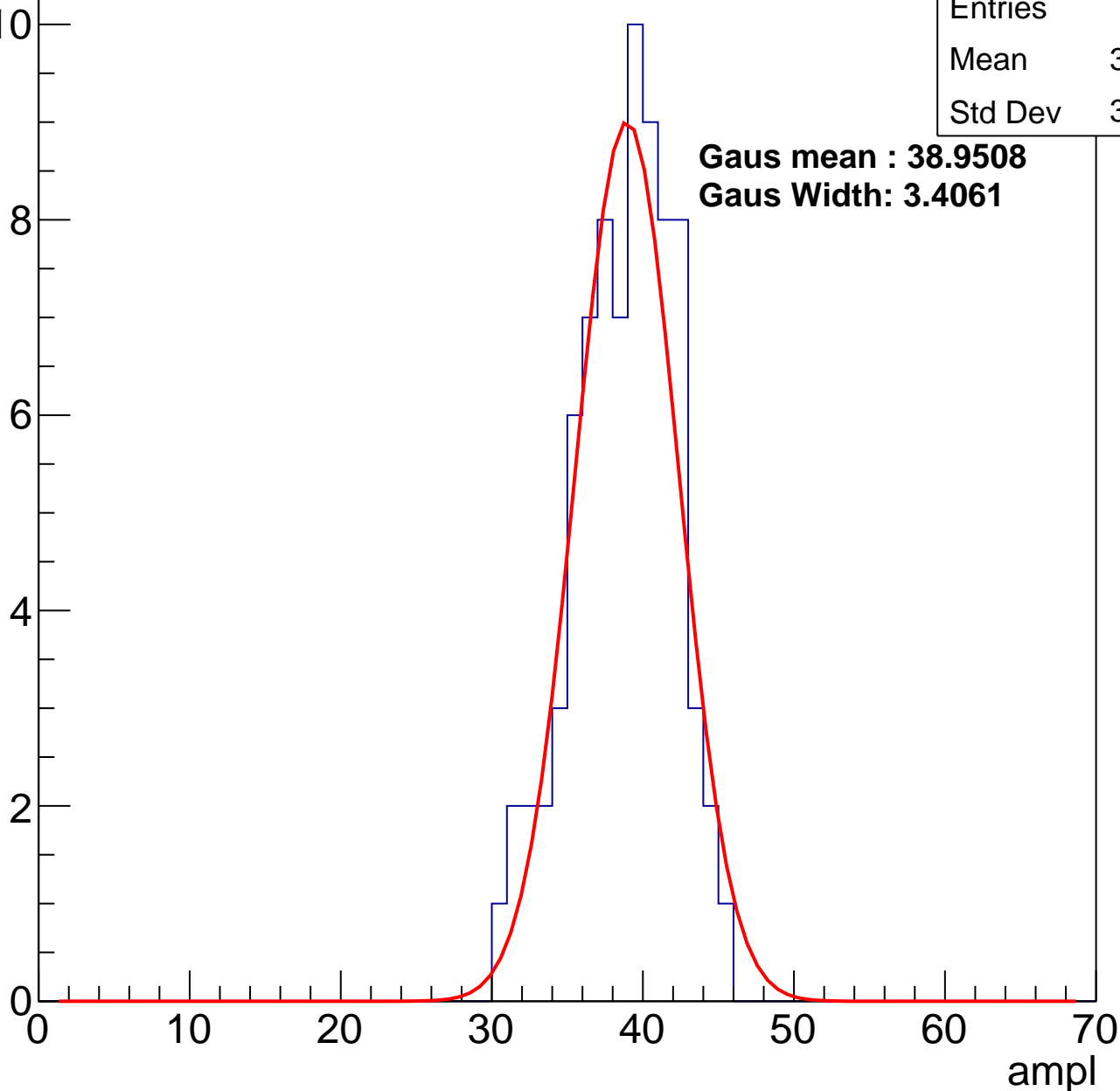
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	38.28
Std Dev	3.295

Gaus mean : 38.9508

Gaus Width: 3.4061



B1L003S, U13-ch123, adc2

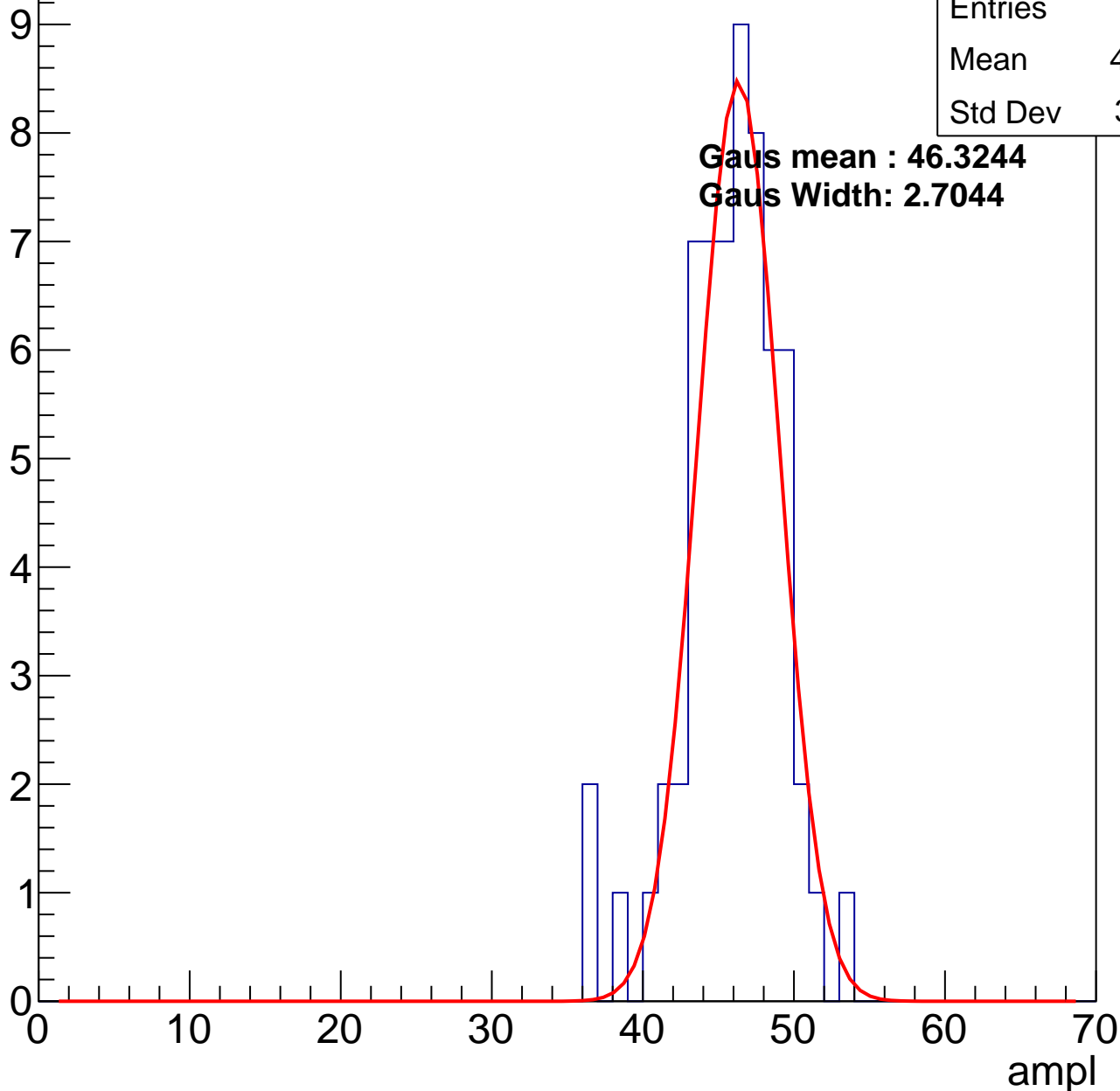
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	45.42
Std Dev	3.241

Gaus mean : 46.3244

Gaus Width: 2.7044

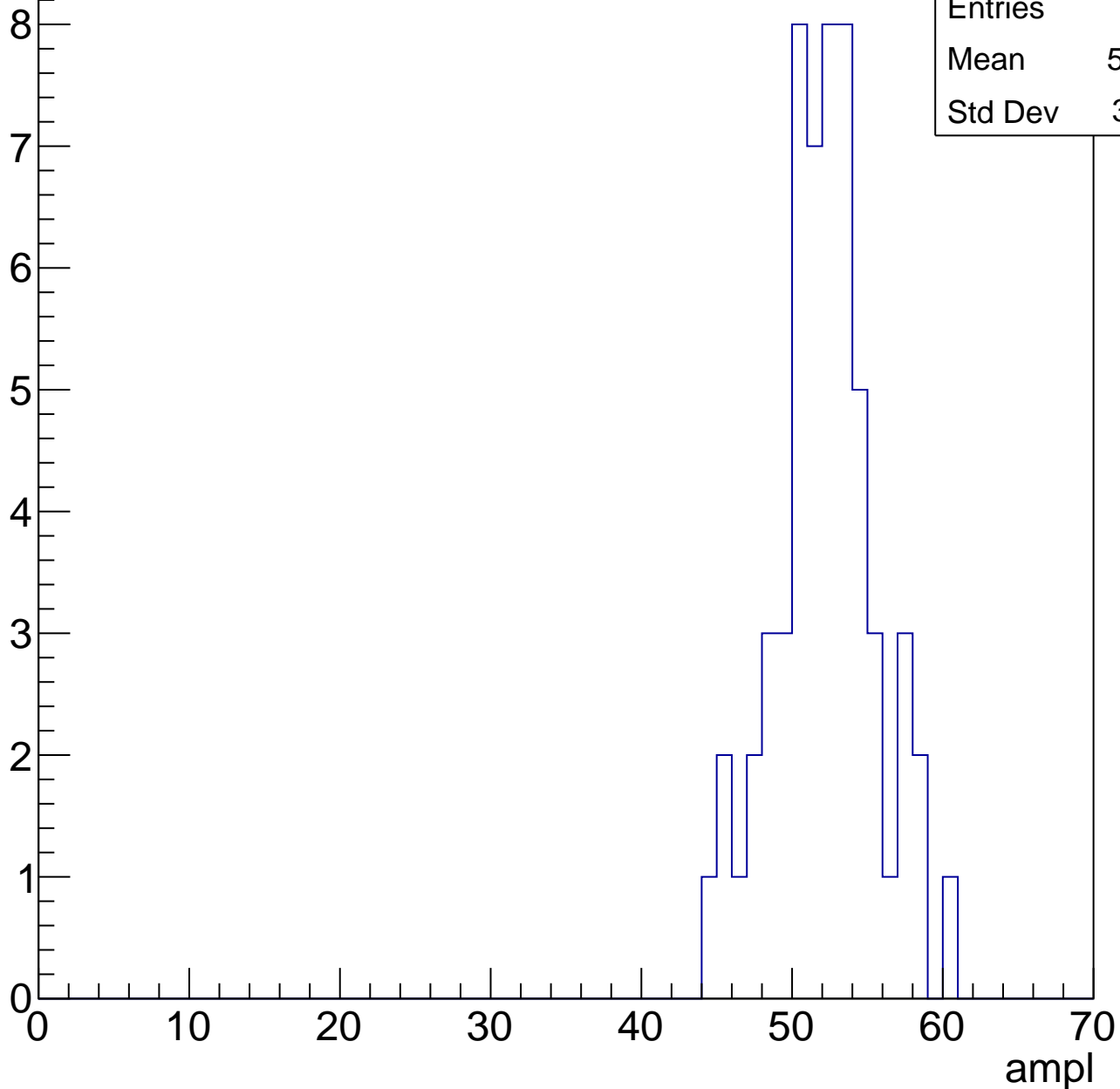


B1L003S, U13-ch123, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	51.72
Std Dev	3.321

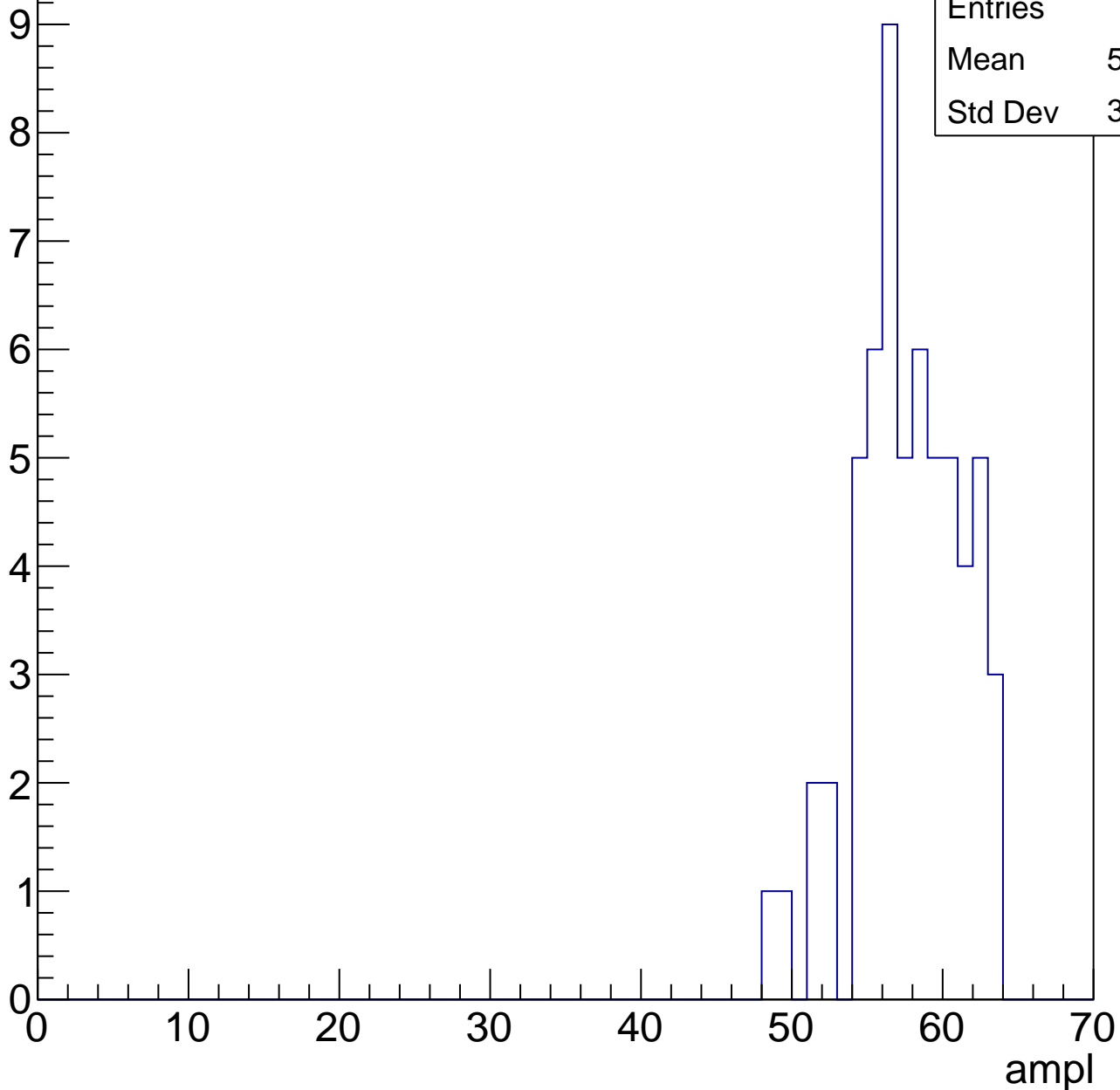


B1L003S, U13-ch123, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.25
Std Dev	3.467

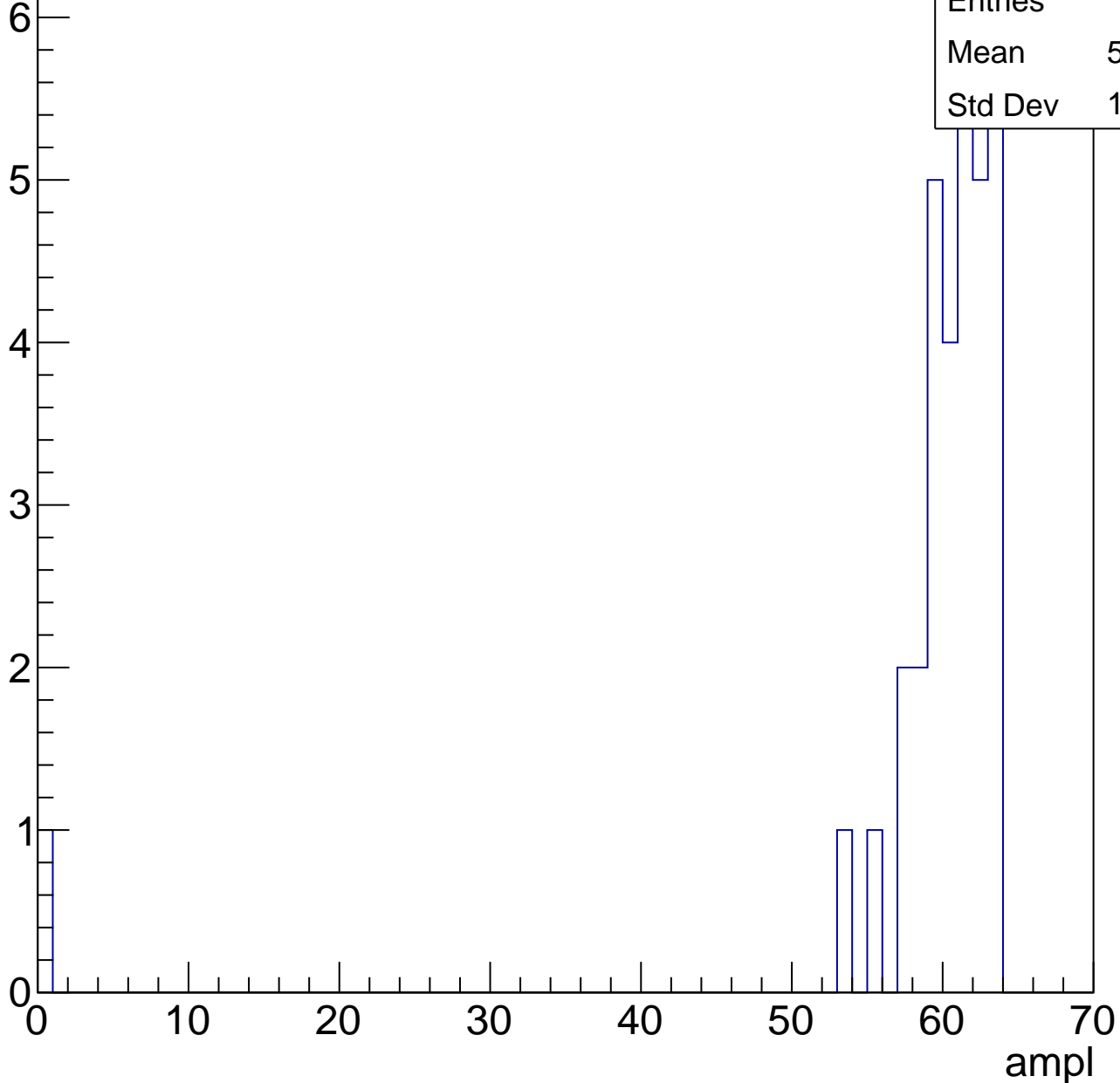


B1L003S, U13-ch123, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.39
Std Dev	10.59



B1L003S, U13-ch123, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U13-ch123, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch124, adc0

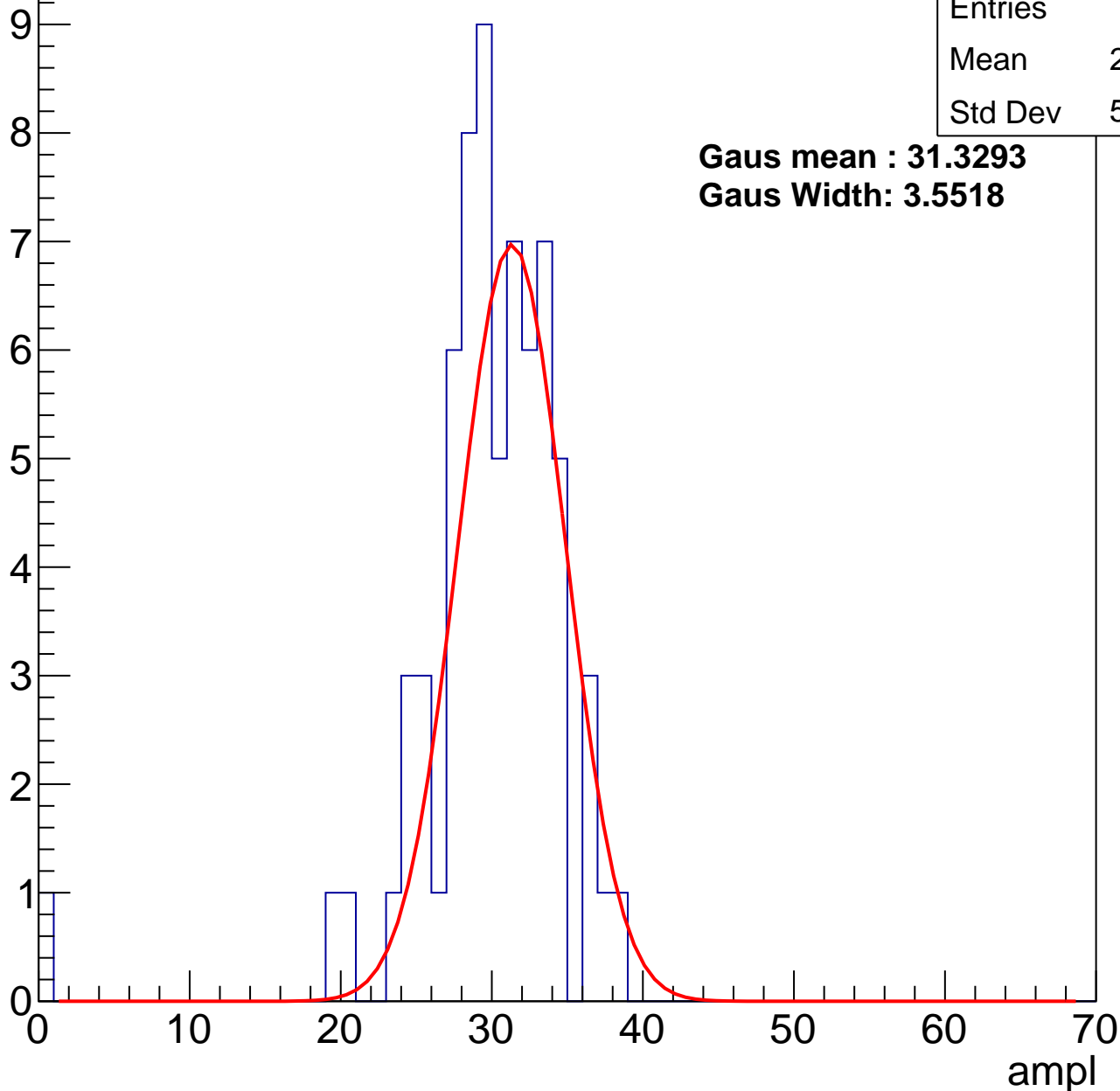
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.35
Std Dev	5.149

Gaus mean : 31.3293

Gaus Width: 3.5518



B1L003S, U13-ch124, adc1

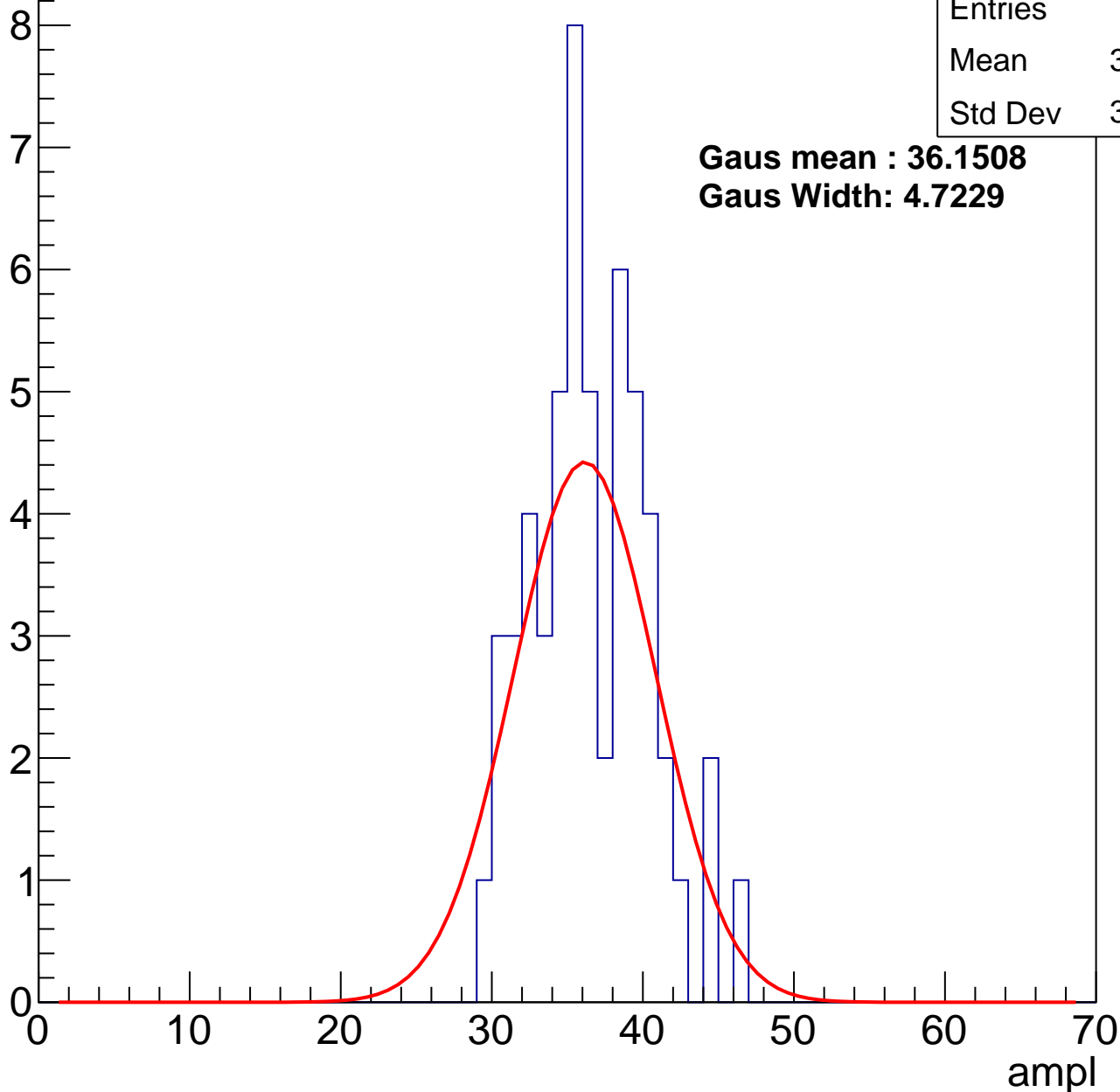
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.07
Std Dev	3.813

Gaus mean : 36.1508

Gaus Width: 4.7229

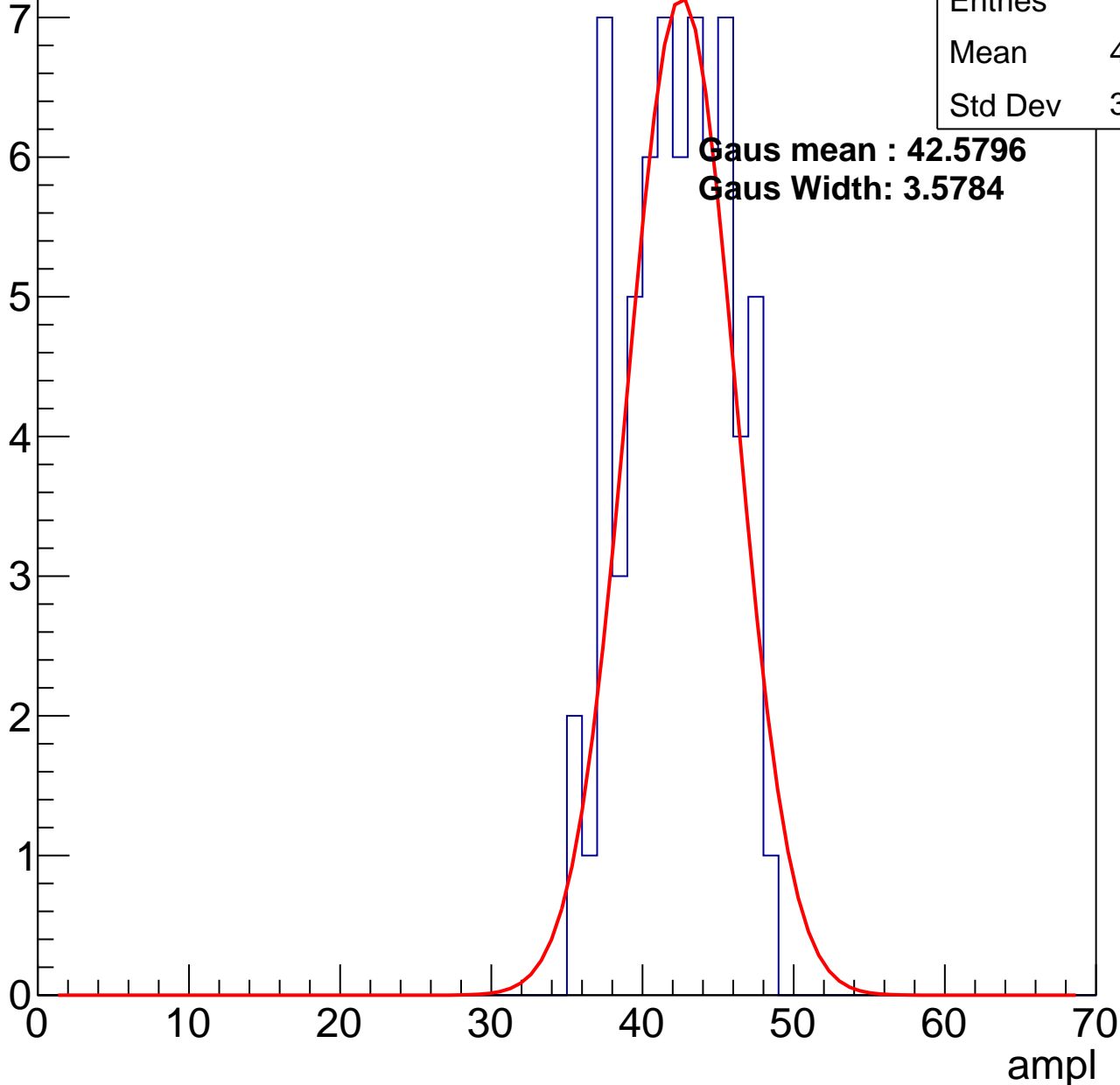


B1L003S, U13-ch124, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.79
Std Dev	3.344

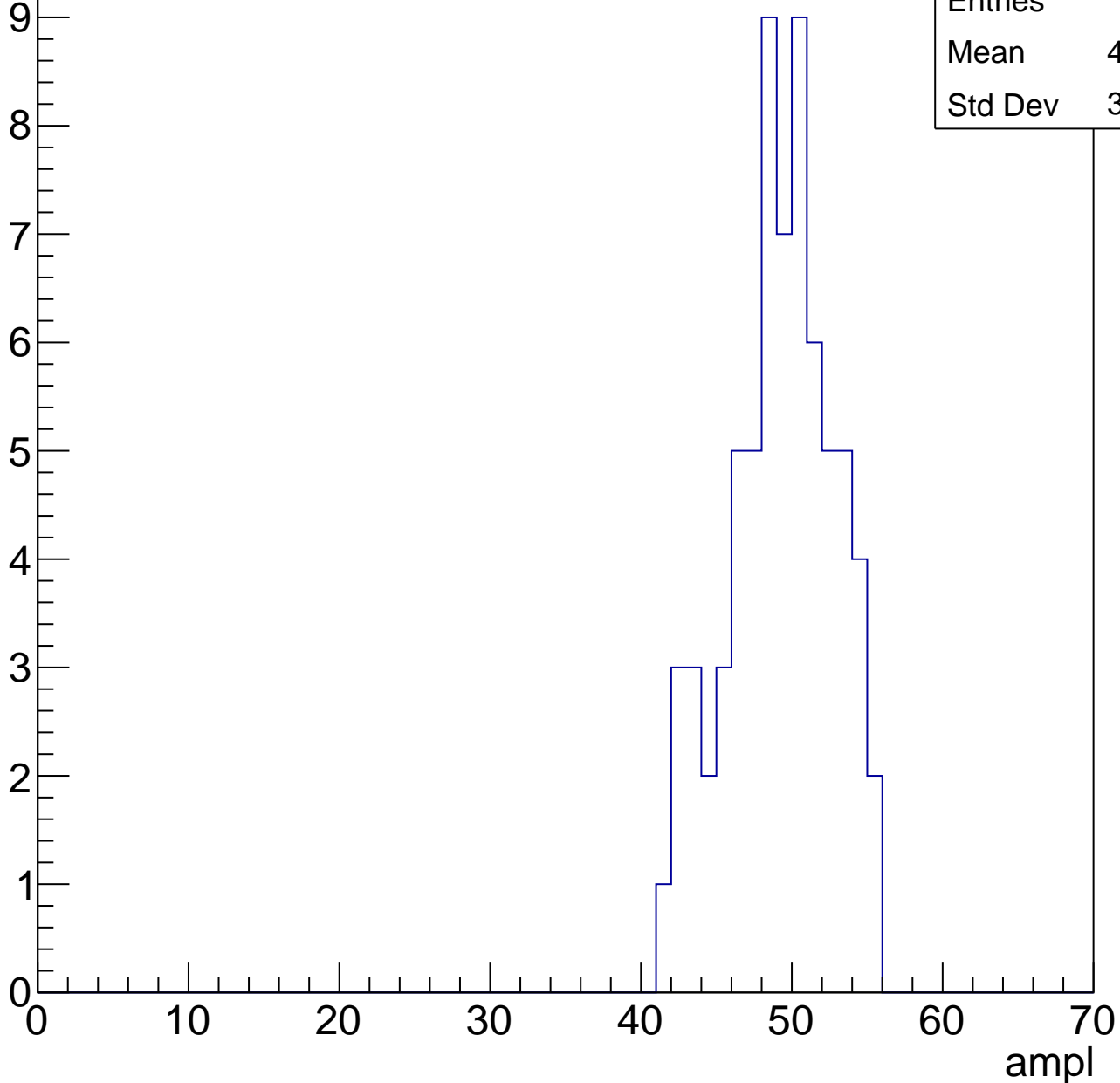


B1L003S, U13-ch124, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	48.78
Std Dev	3.443

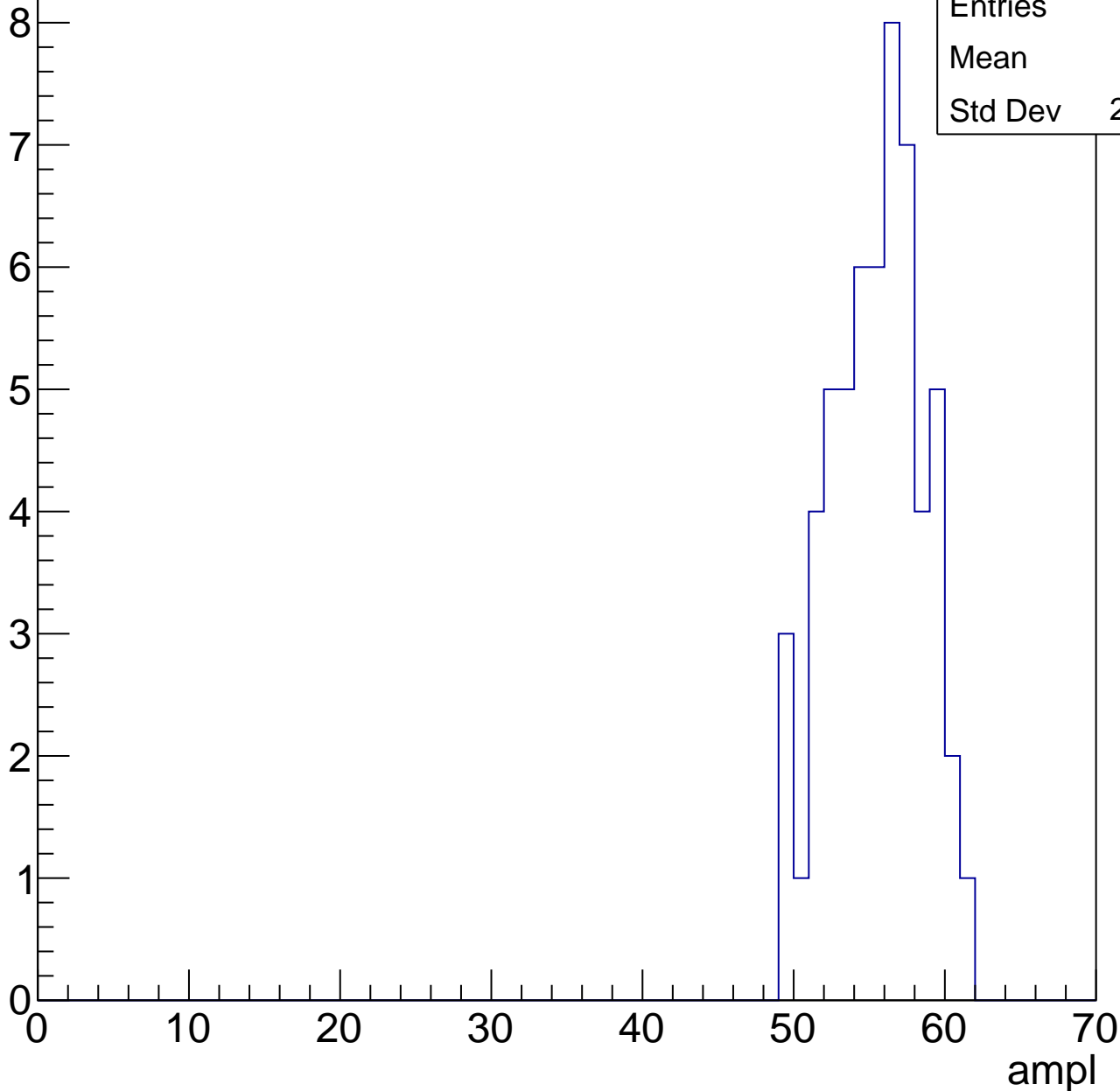


B1L003S, U13-ch124, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	55
Std Dev	2.979

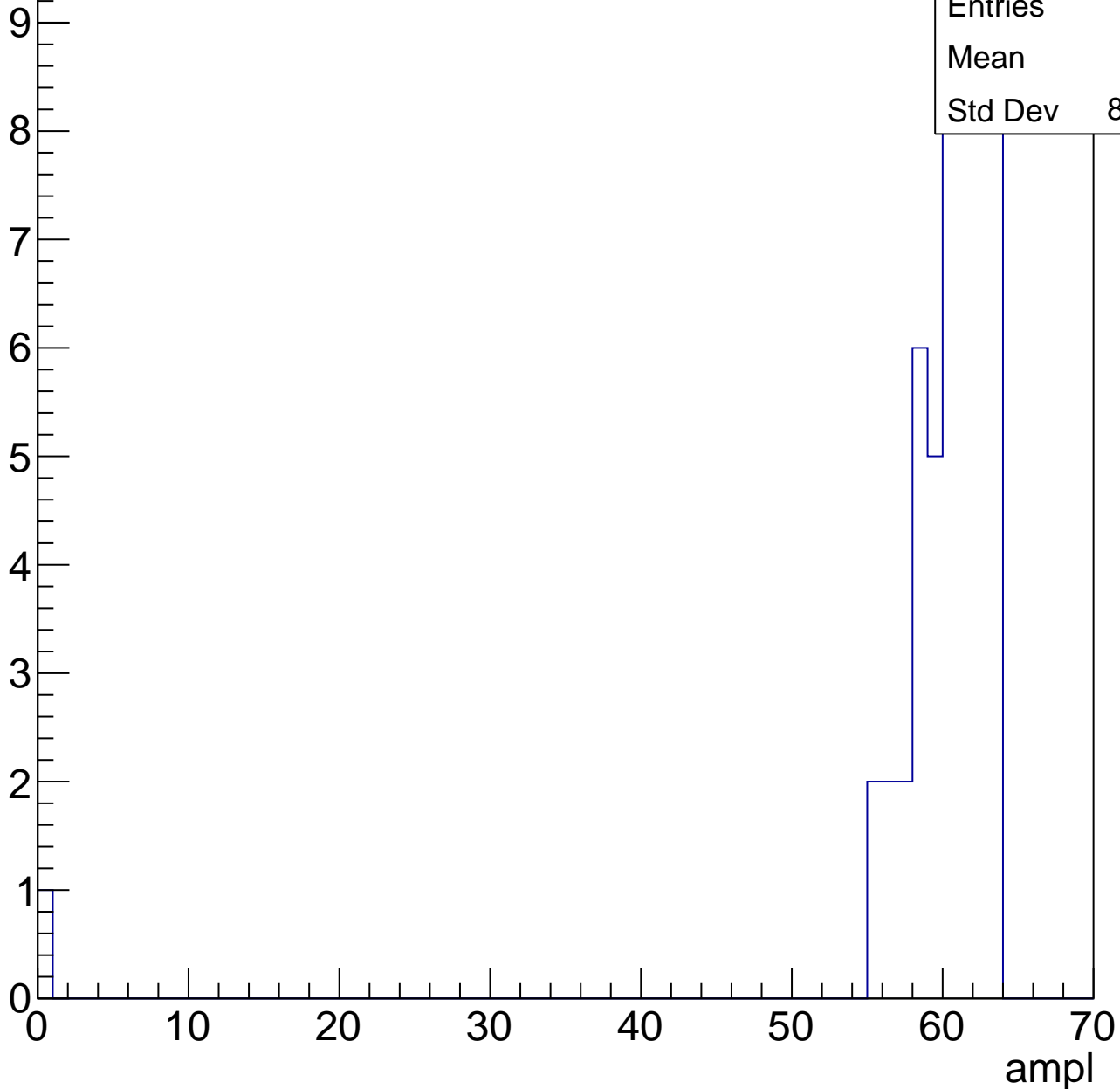


B1L003S, U13-ch124, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59
Std Dev	8.625



B1L003S, U13-ch124, adc6

calib_packv5_042523_0143.root, FC#13, port D2

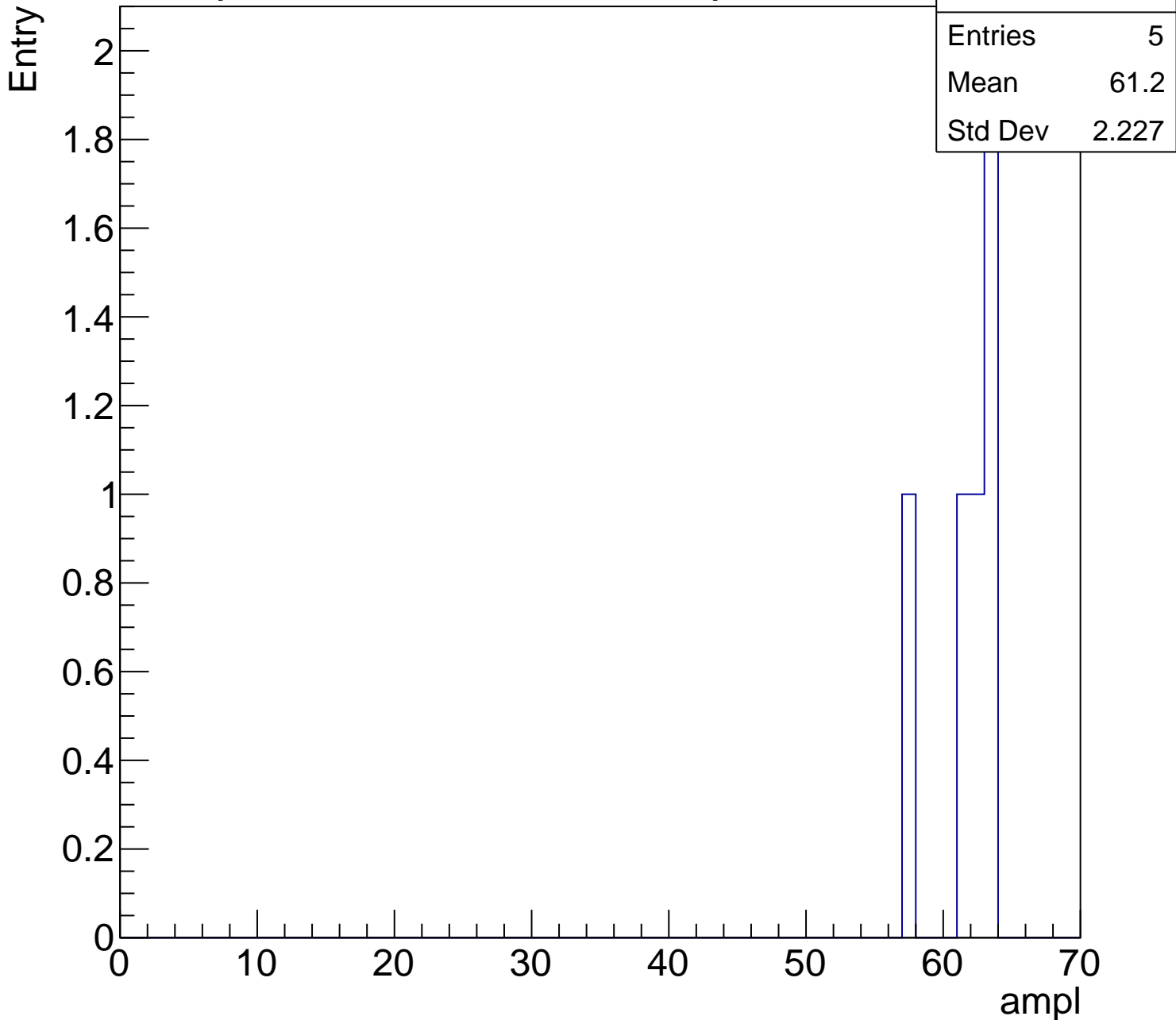
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	2.227

0 10 20 30 40 50 60 70

ampl



B1L003S, U13-ch124, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch125, adc0

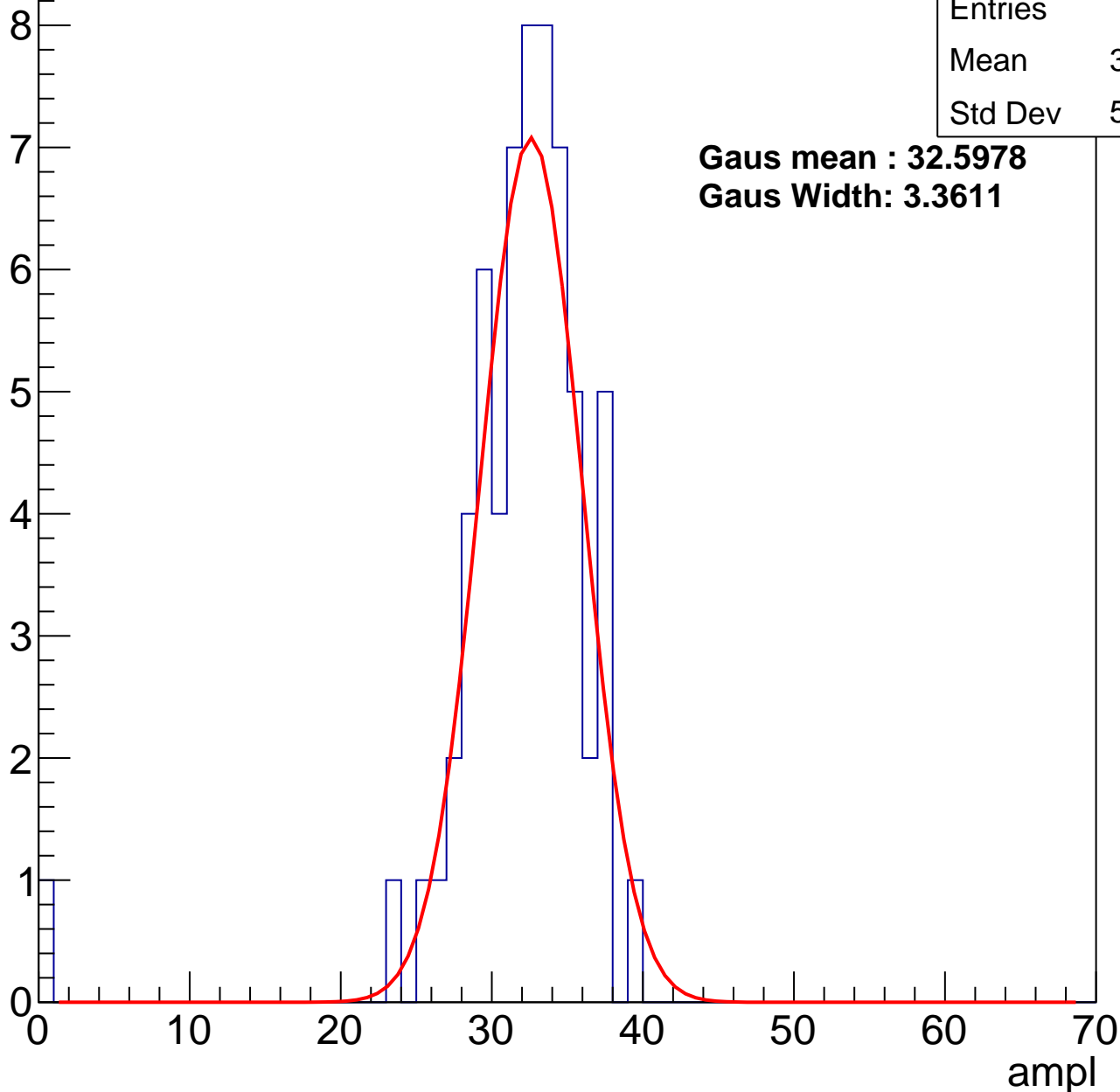
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	31.43
Std Dev	5.114

Gaus mean : 32.5978

Gaus Width: 3.3611



B1L003S, U13-ch125, adc1

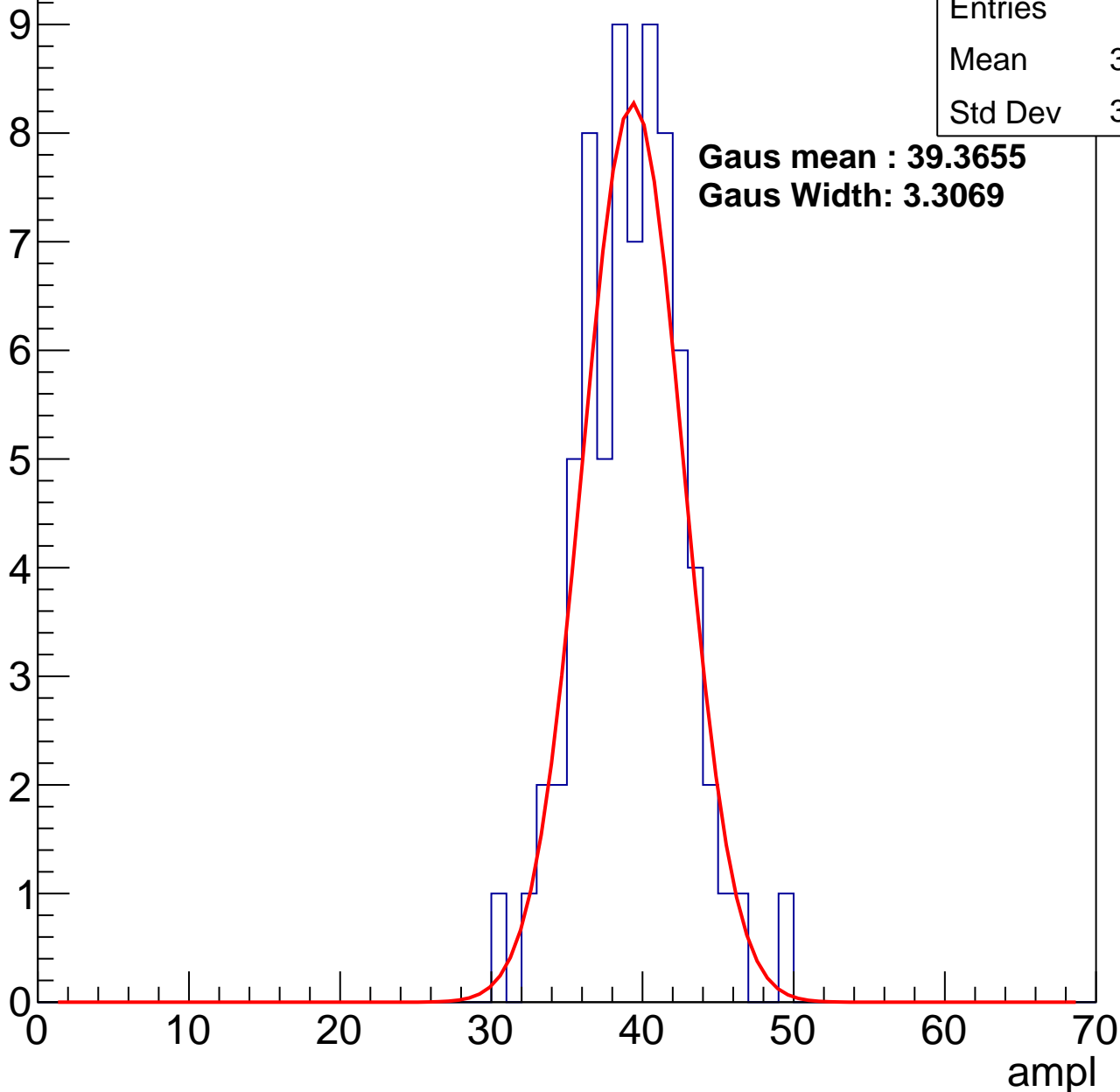
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	38.88
Std Dev	3.387

Gaus mean : 39.3655

Gaus Width: 3.3069



B1L003S, U13-ch125, adc2

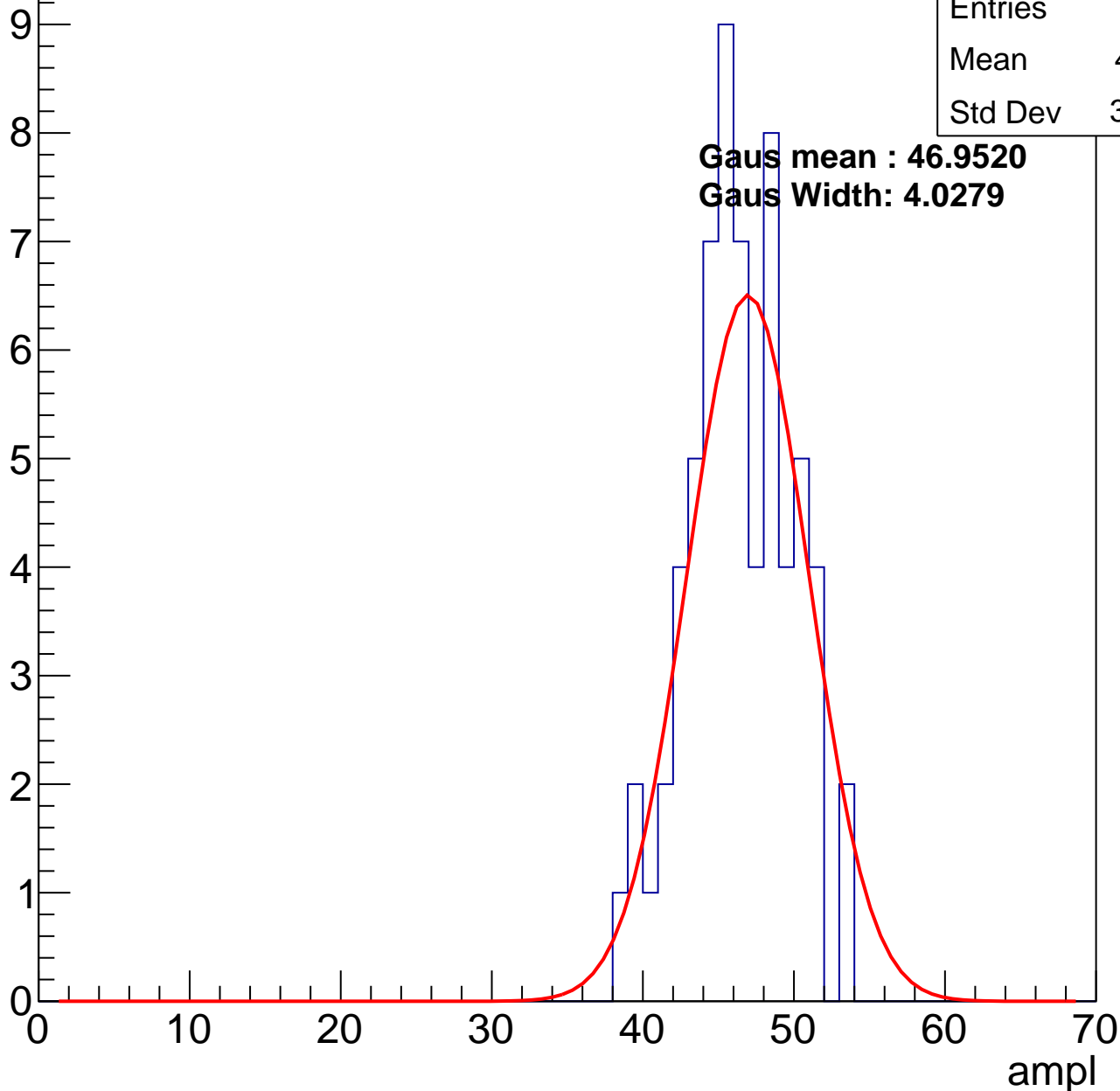
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	45.91
Std Dev	3.382

Gaus mean : 46.9520

Gaus Width: 4.0279

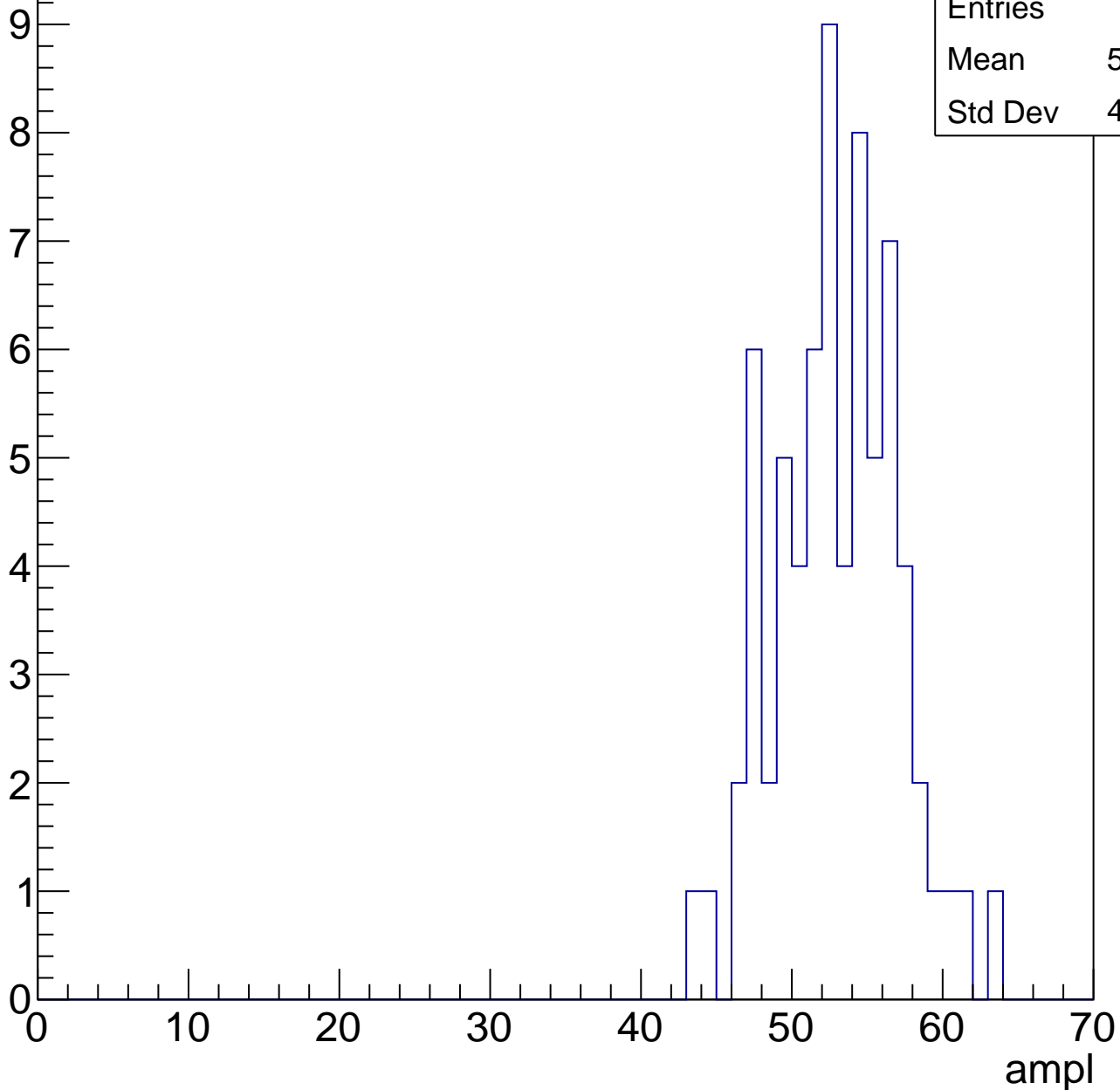


B1L003S, U13-ch125, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	52.49
Std Dev	4.017

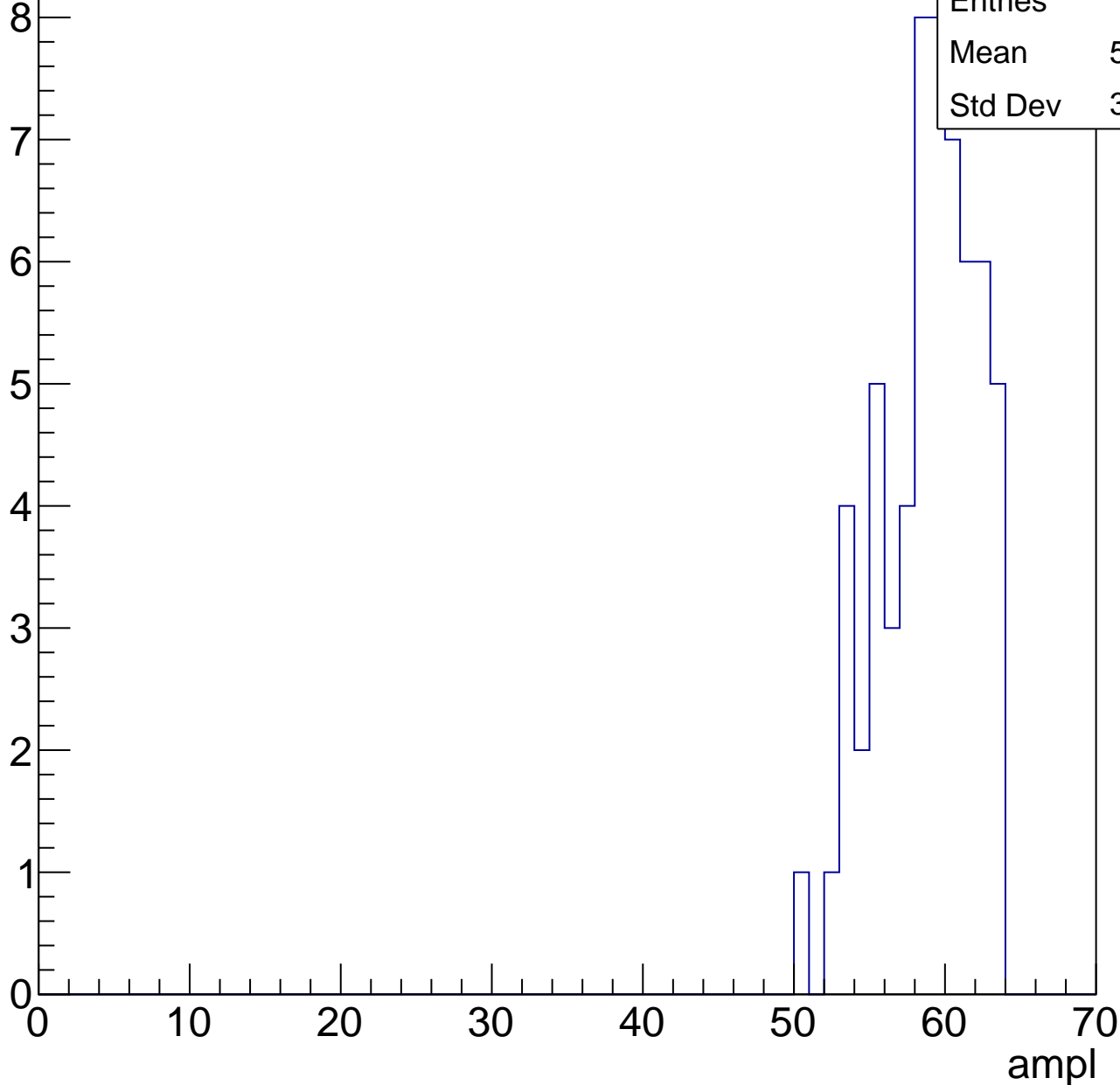


B1L003S, U13-ch125, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

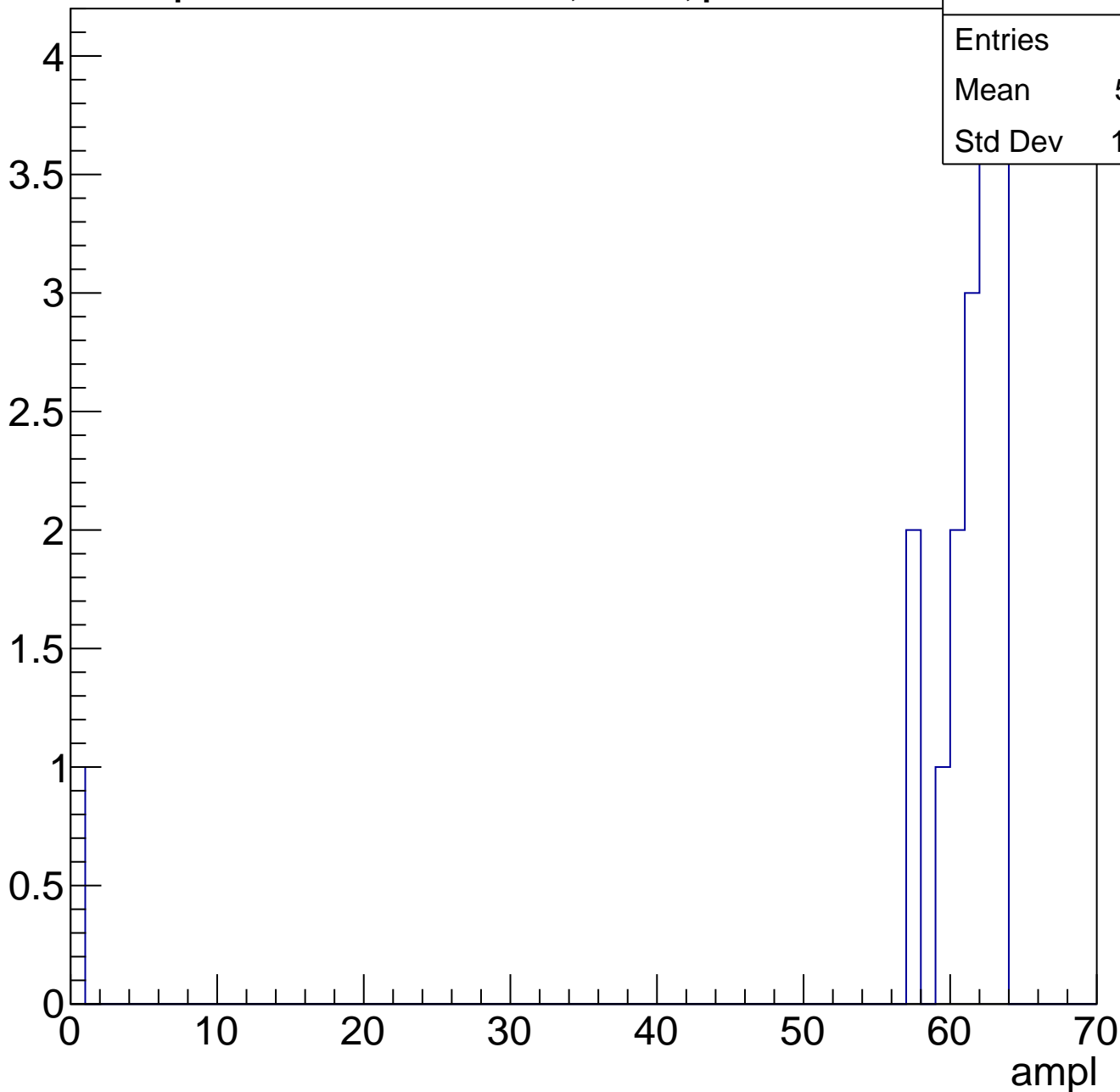
Entries	60
Mean	58.37
Std Dev	3.152



B1L003S, U13-ch125, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch125, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U13-ch125, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch126, adc0

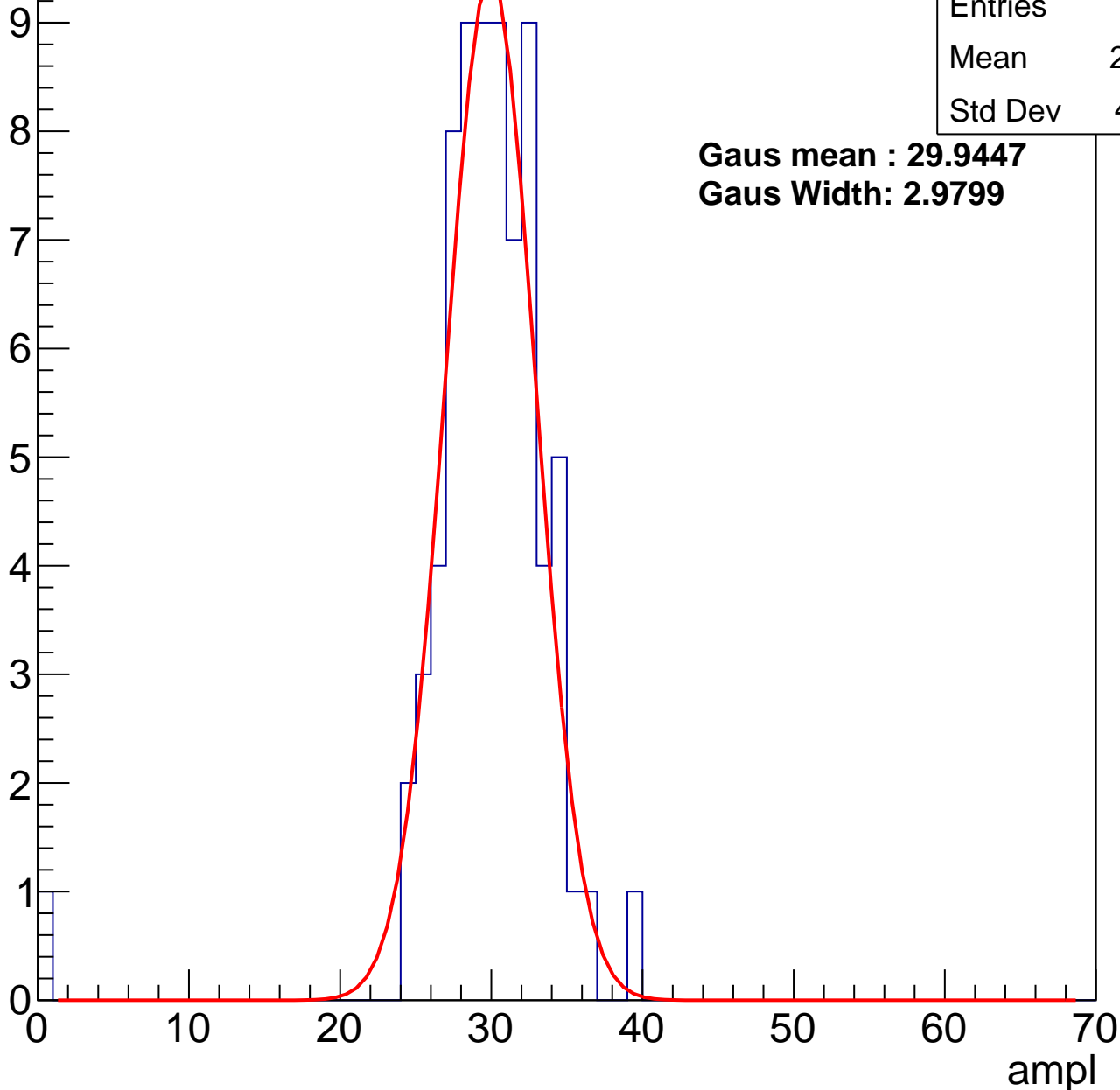
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.36
Std Dev	4.531

Gaus mean : 29.9447

Gaus Width: 2.9799



B1L003S, U13-ch126, adc1

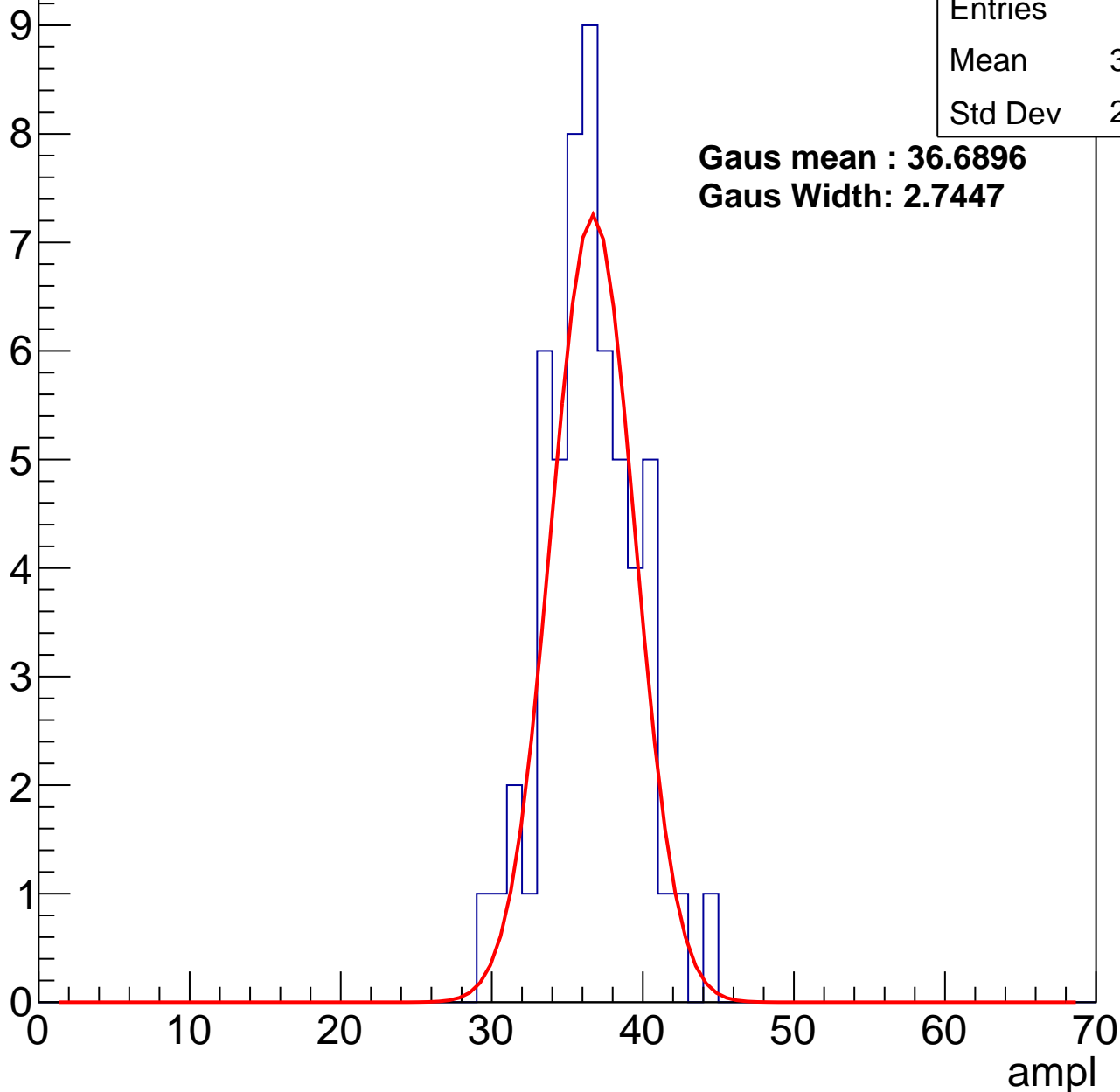
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.07
Std Dev	2.987

Gaus mean : 36.6896

Gaus Width: 2.7447



B1L003S, U13-ch126, adc2

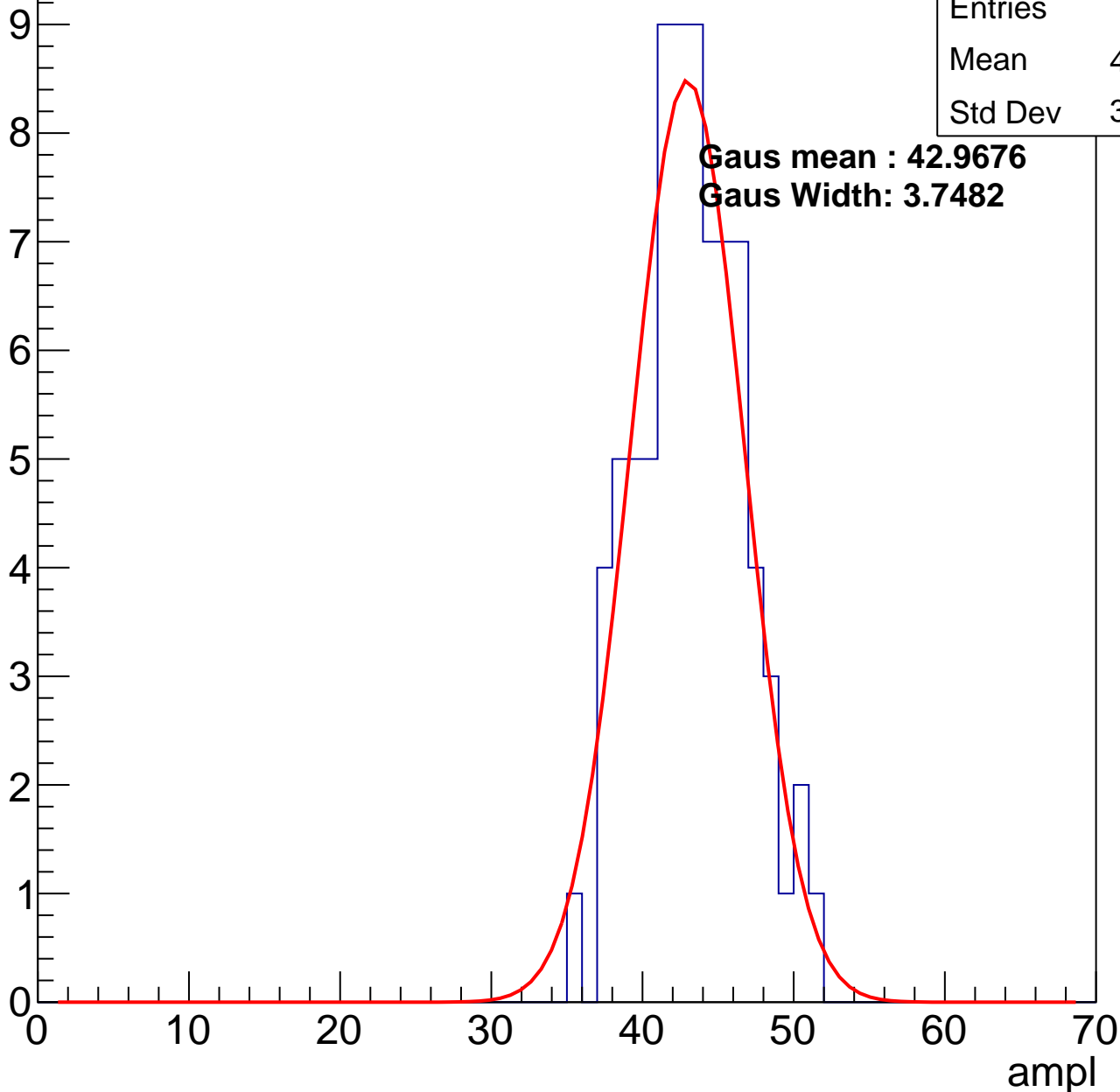
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	42.77
Std Dev	3.438

Gaus mean : 42.9676

Gaus Width: 3.7482

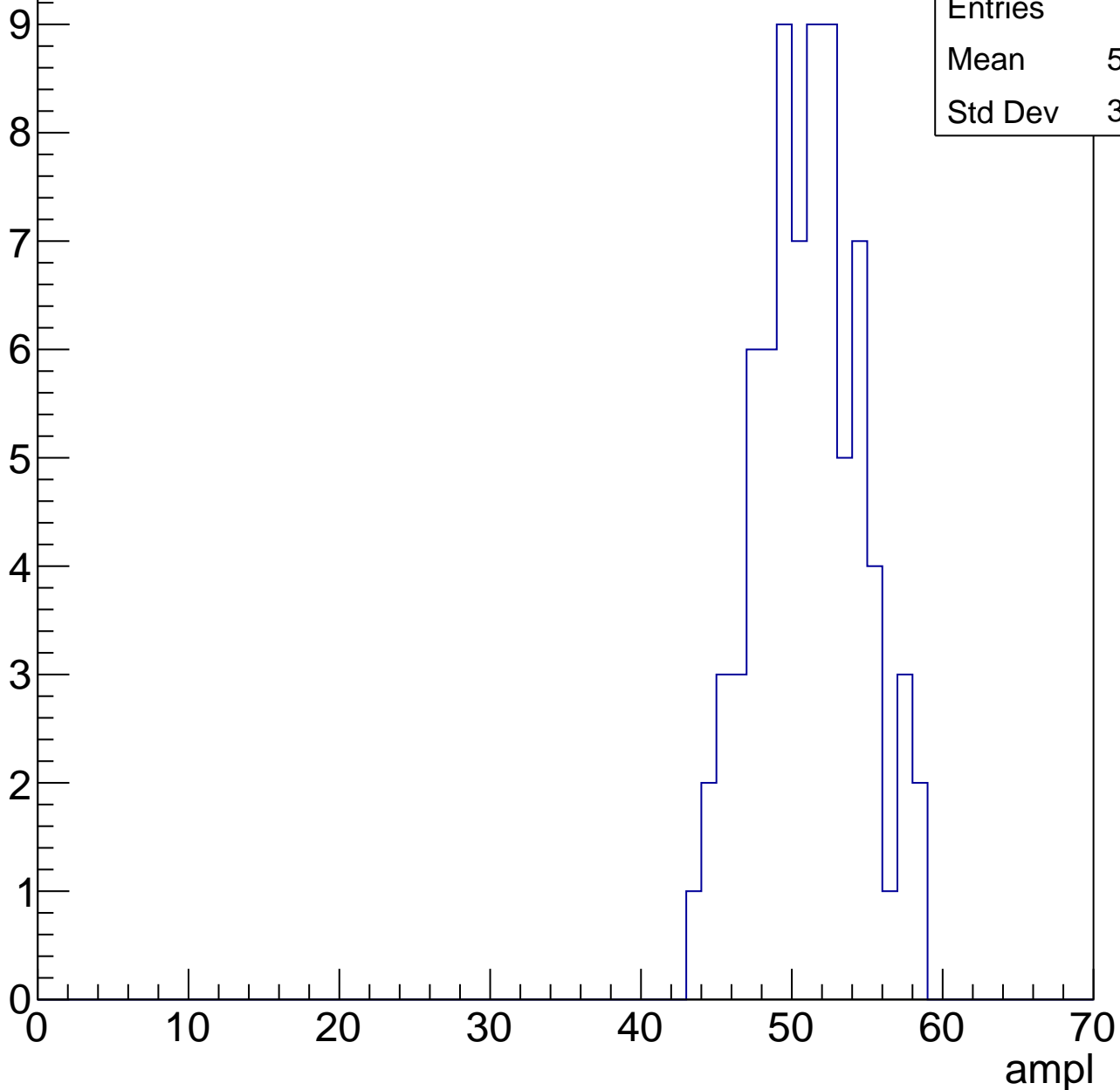


B1L003S, U13-ch126, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	50.62
Std Dev	3.445

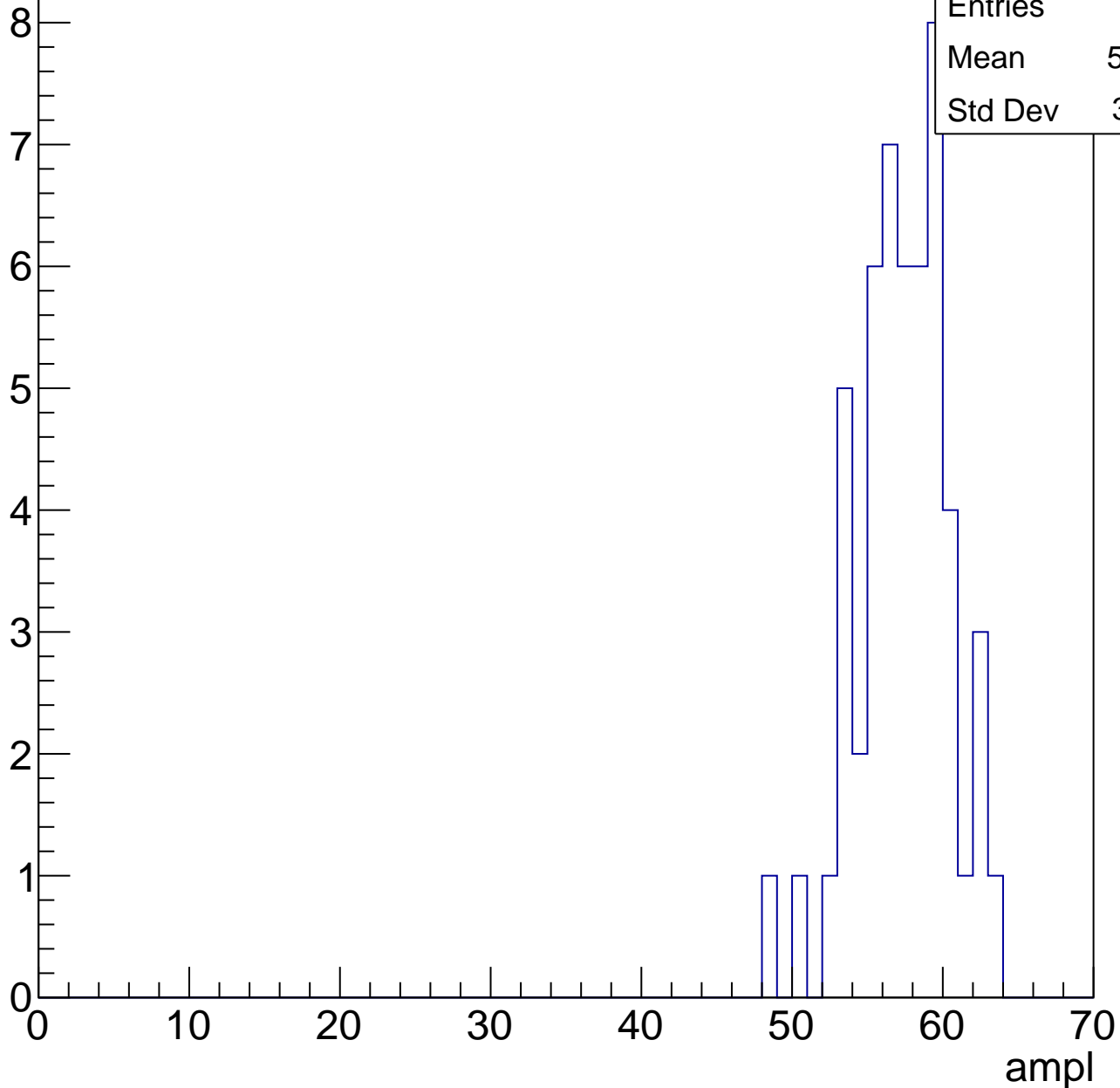


B1L003S, U13-ch126, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	56.87
Std Dev	3.051

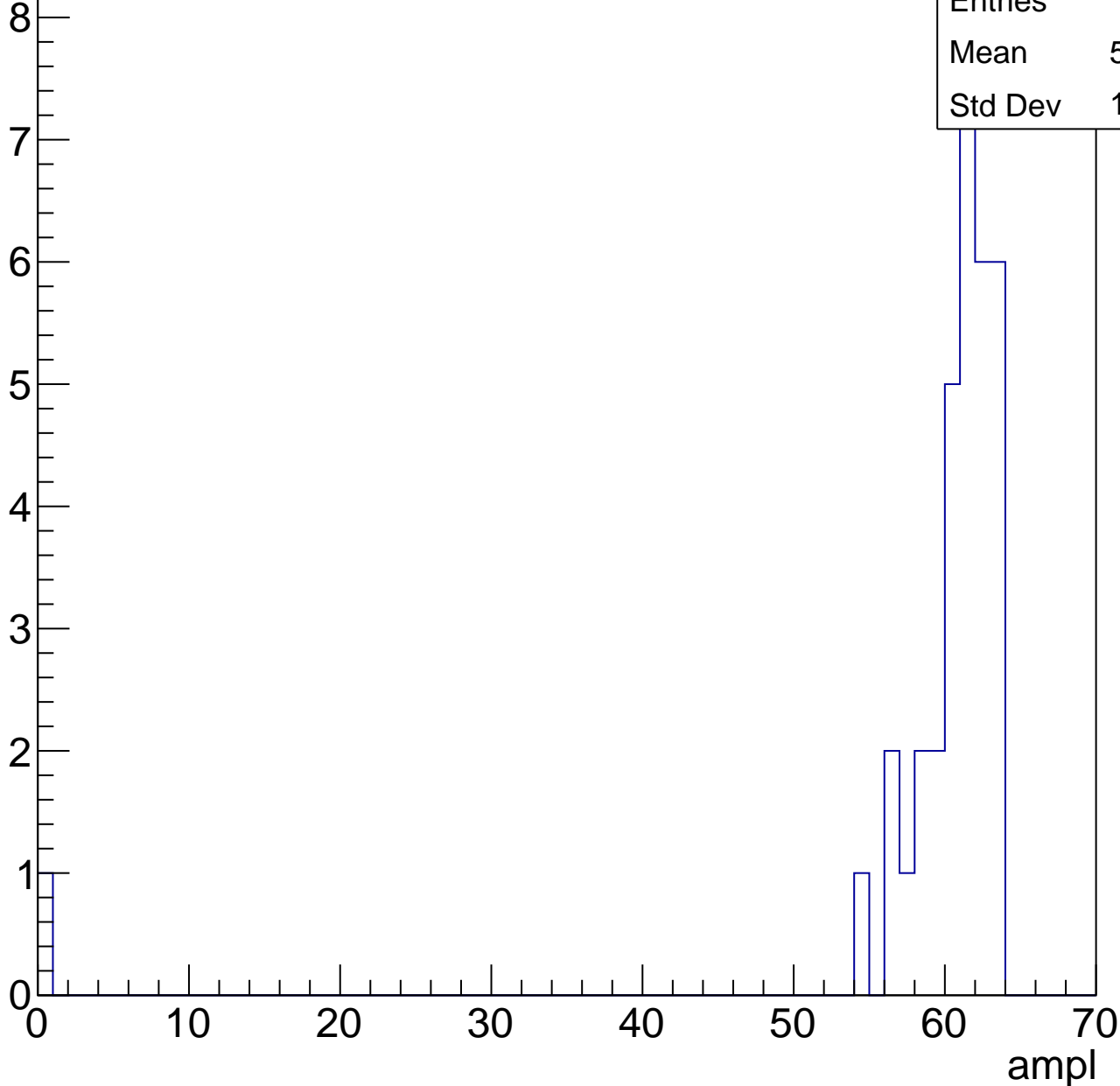


B1L003S, U13-ch126, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

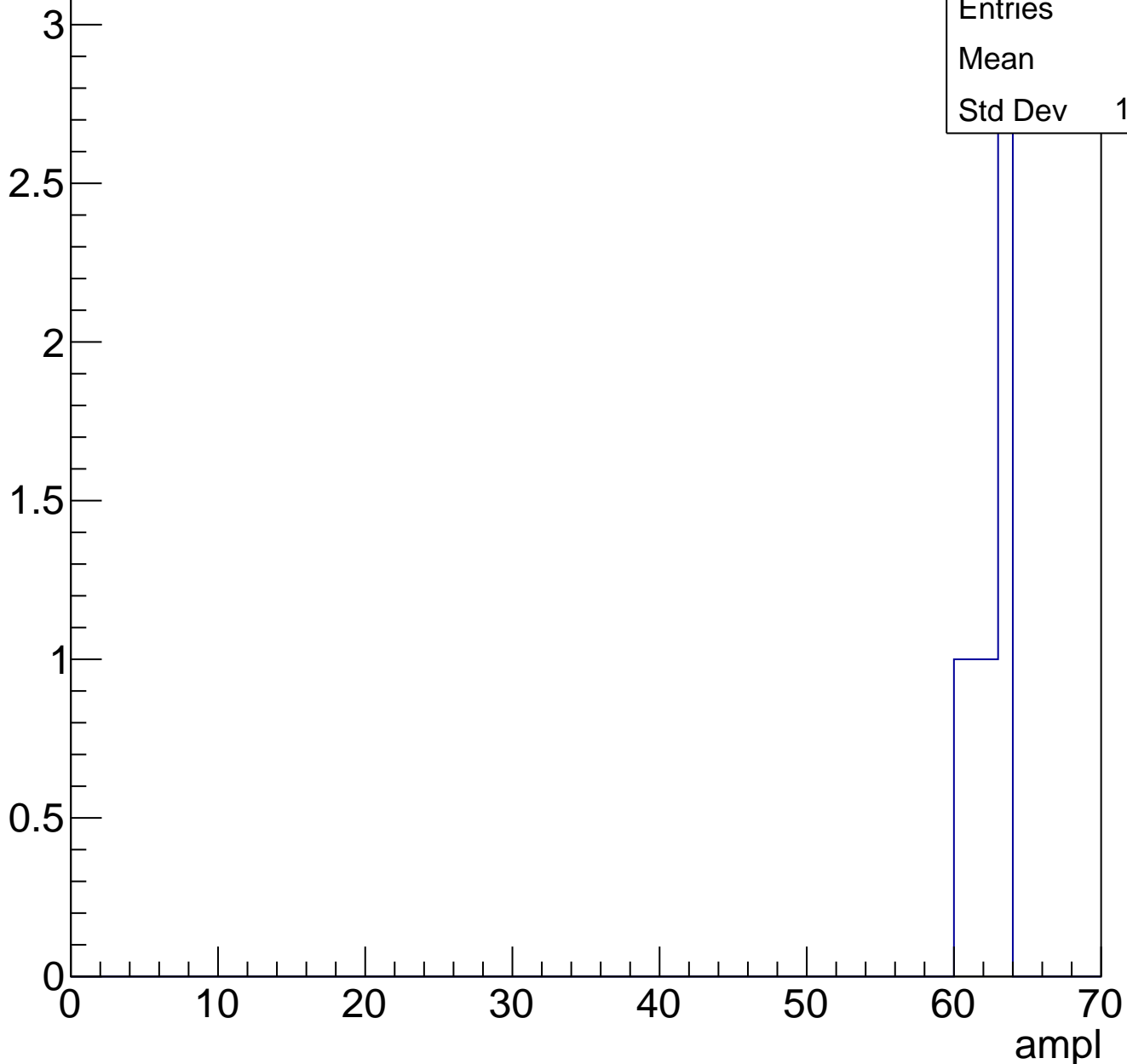
Entries	34
Mean	58.68
Std Dev	10.45



B1L003S, U13-ch126, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch126, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch127, adc0

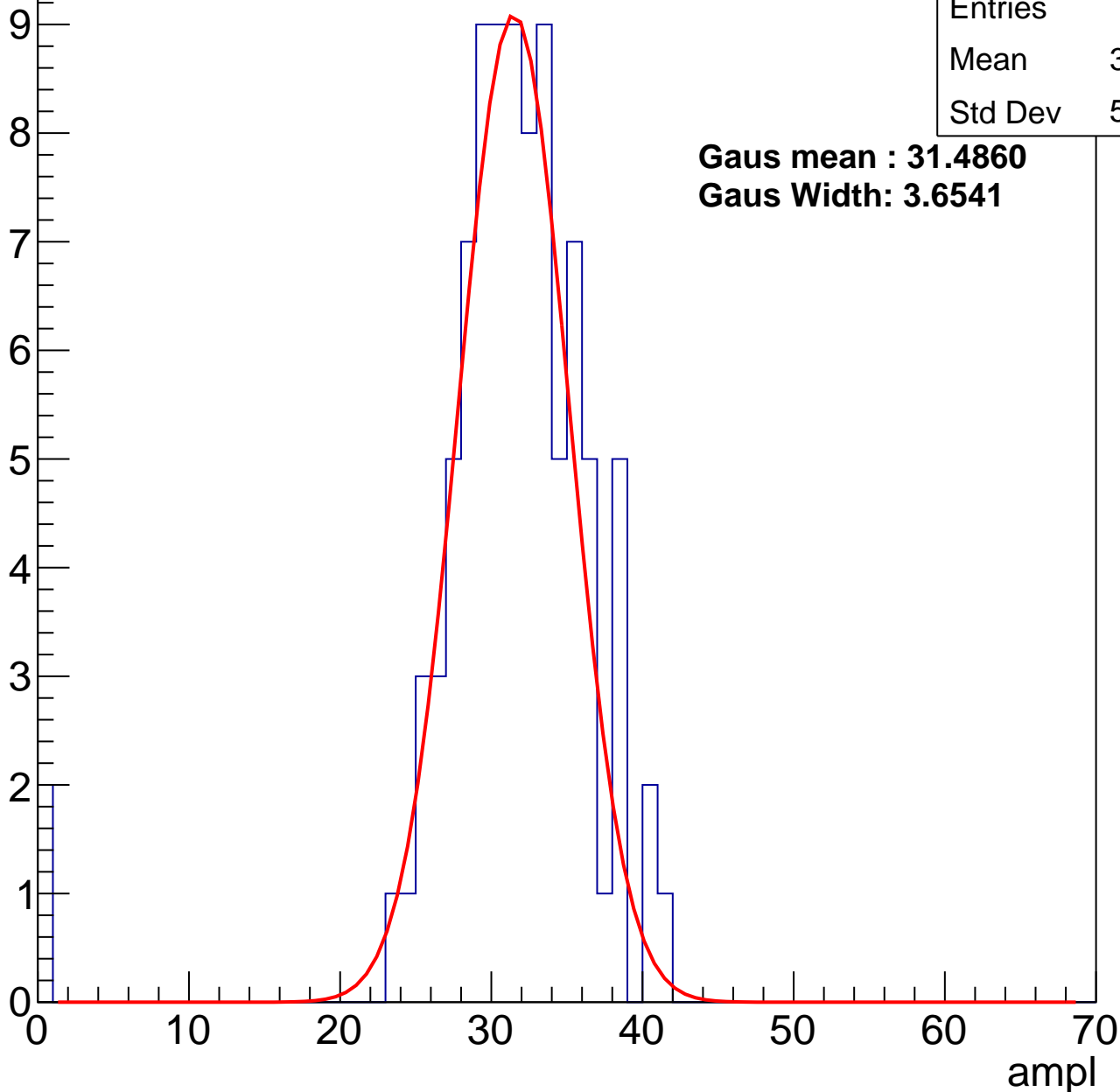
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	92
Mean	30.84
Std Dev	5.964

Gaus mean : 31.4860

Gaus Width: 3.6541



B1L003S, U13-ch127, adc1

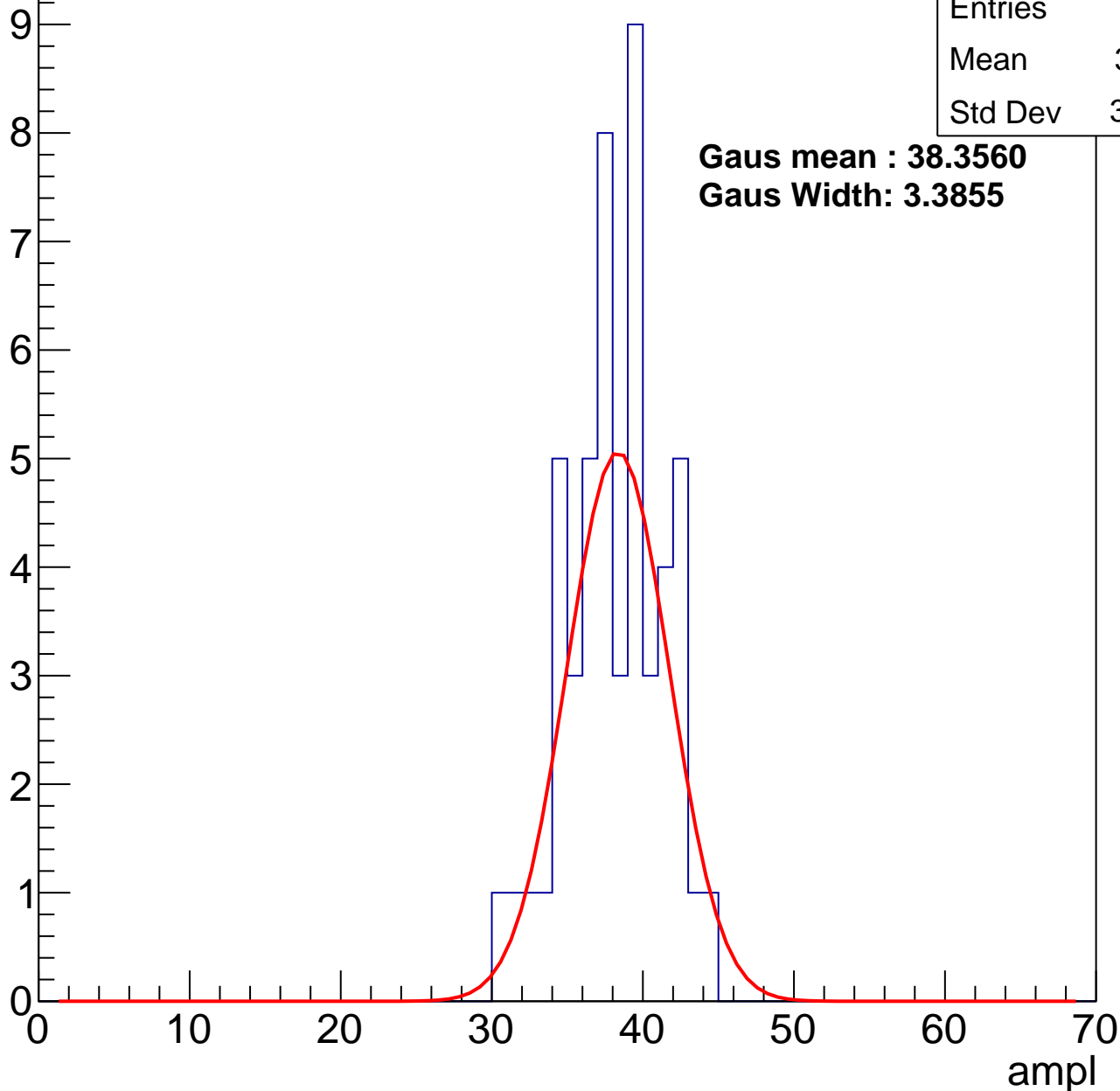
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	37.71
Std Dev	3.139

Gaus mean : 38.3560

Gaus Width: 3.3855



B1L003S, U13-ch127, adc2

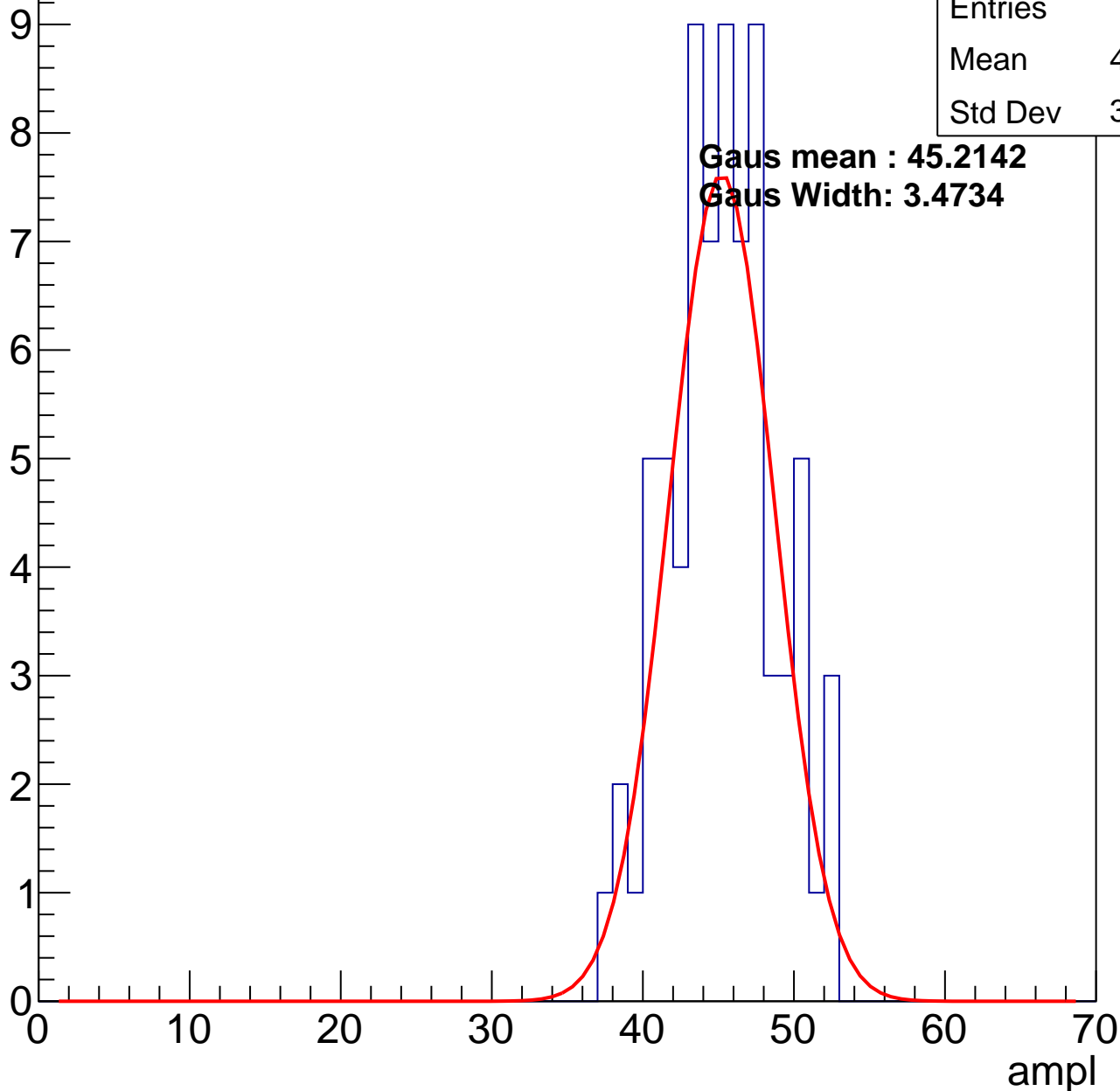
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	44.84
Std Dev	3.499

Gaus mean : 45.2142

Gaus Width: 3.4734

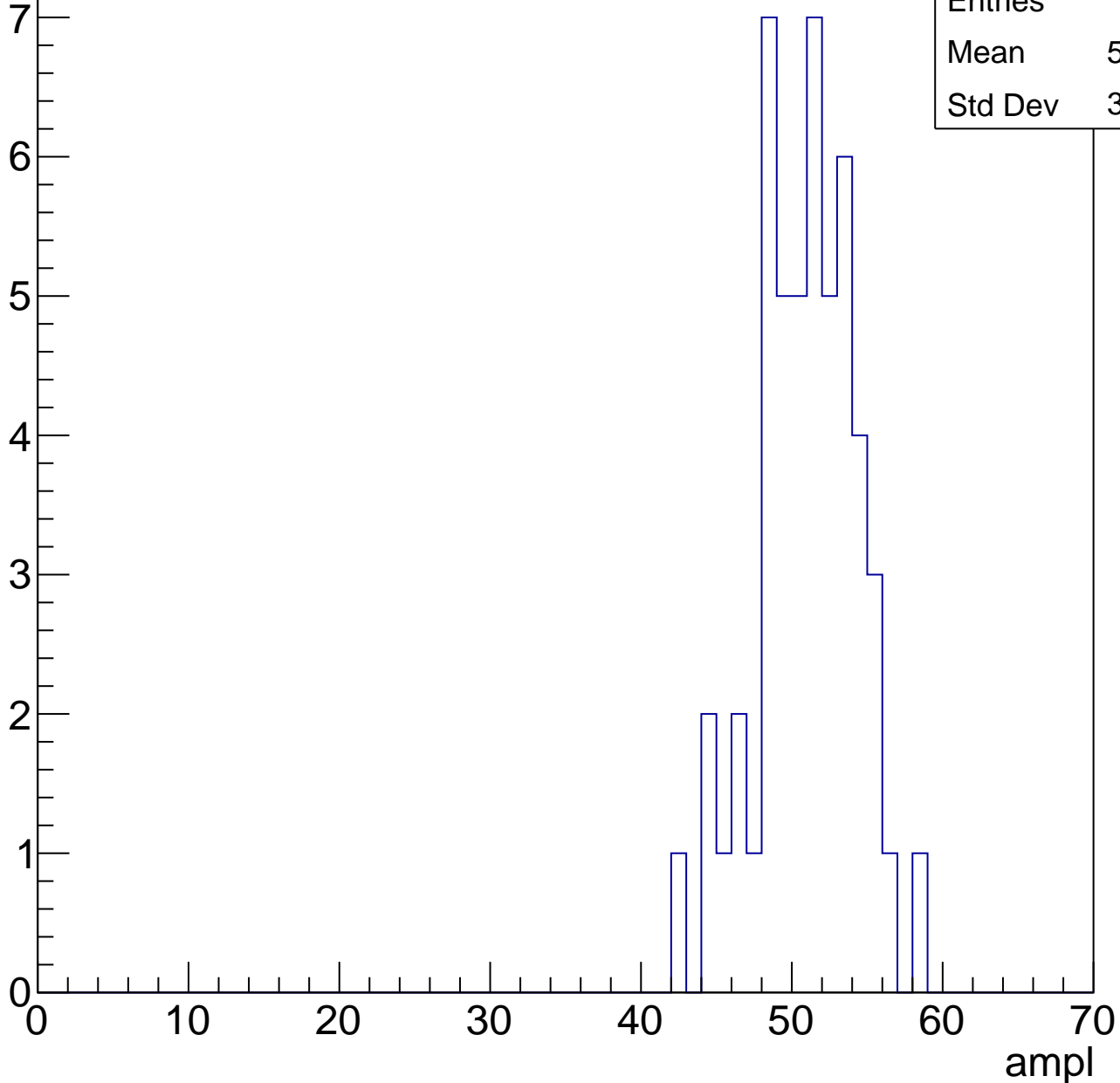


B1L003S, U13-ch127, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	50.49
Std Dev	3.262

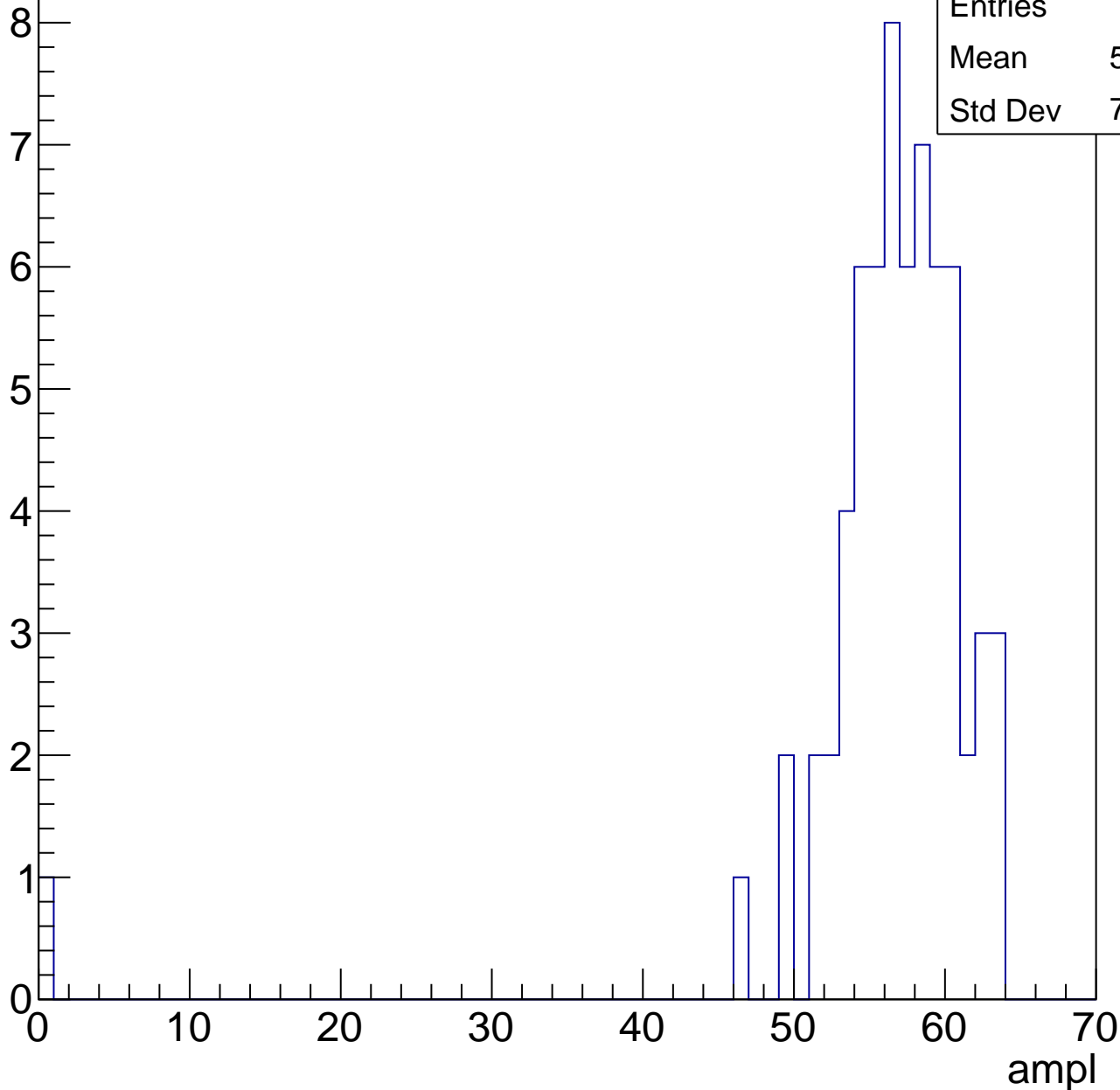


B1L003S, U13-ch127, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.74
Std Dev	7.812

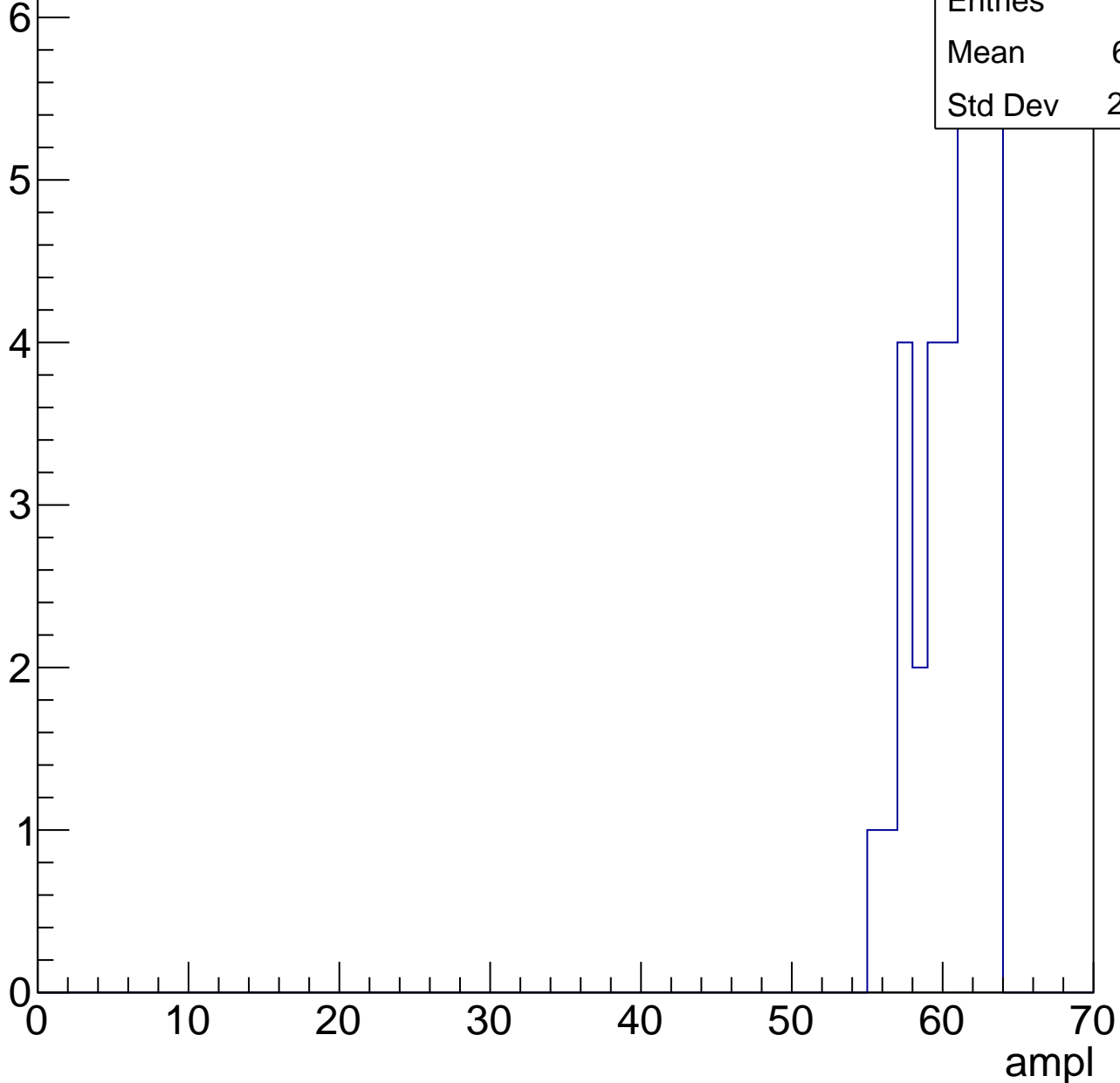


B1L003S, U13-ch127, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	60.21
Std Dev	2.246



B1L003S, U13-ch127, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L003S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

