

B1L101S, U14-ch0, adc0

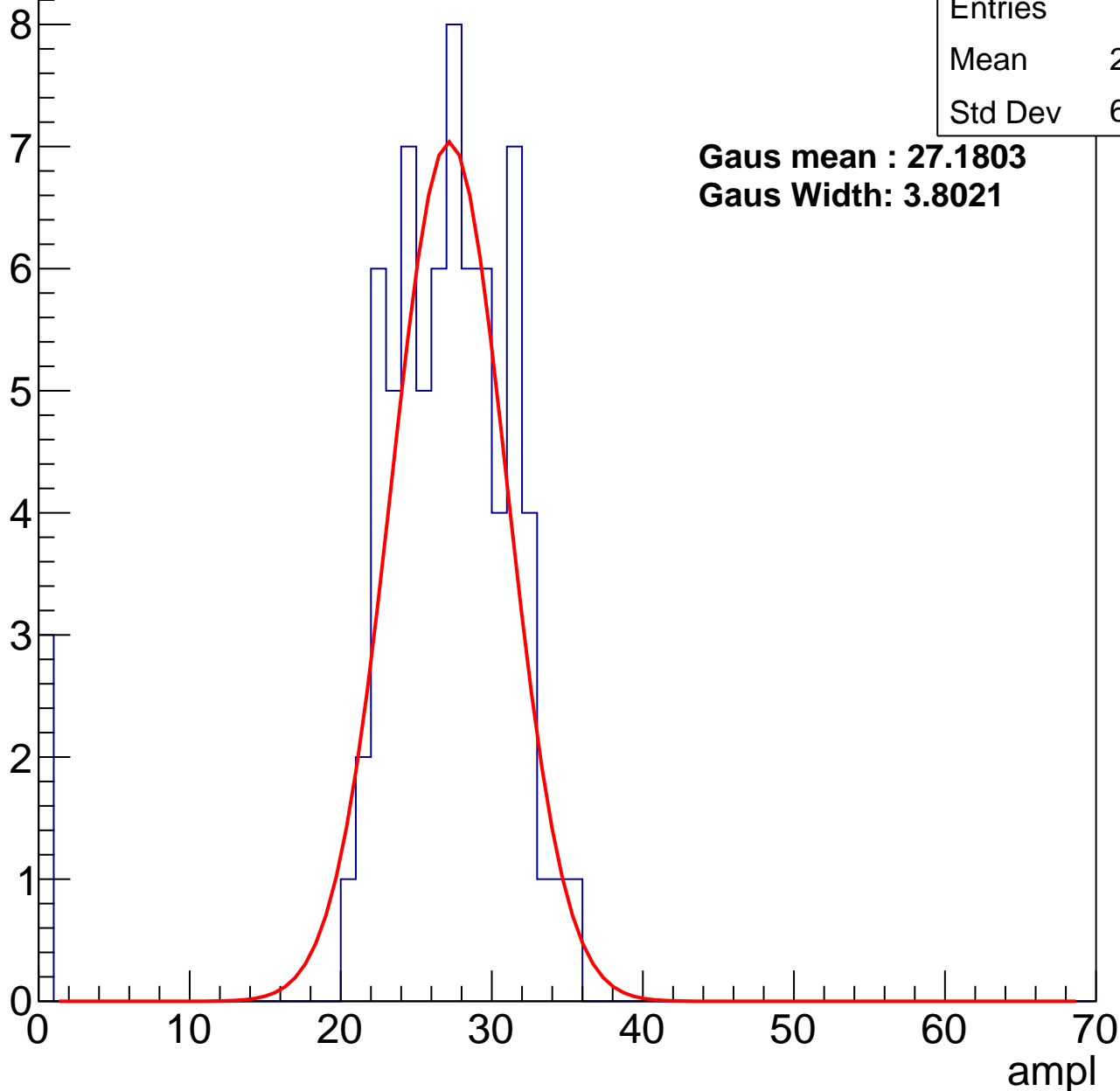
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	25.79
Std Dev	6.355

Gaus mean : 27.1803

Gaus Width: 3.8021



B1L101S, U14-ch0, adc1

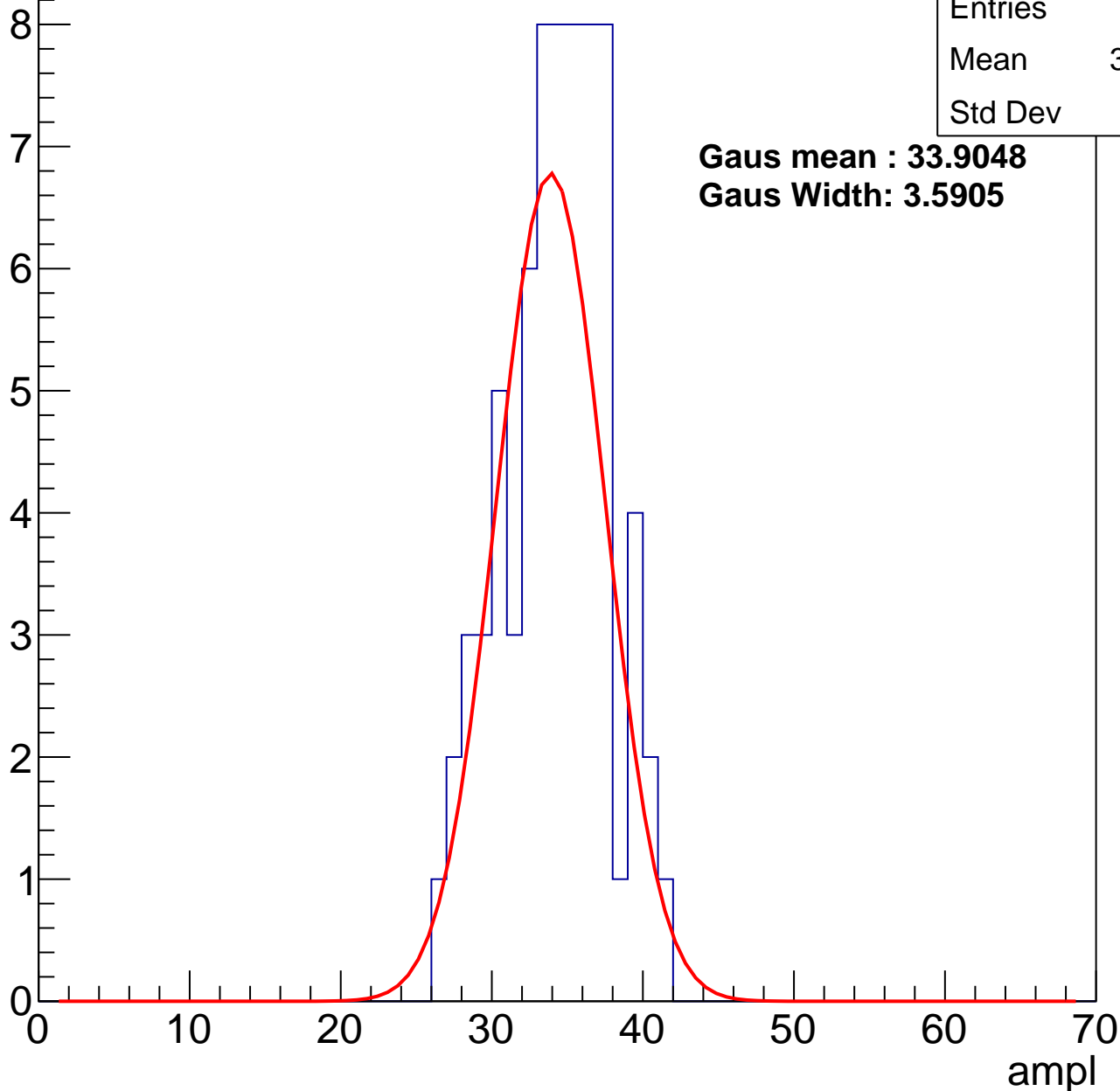
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	33.82
Std Dev	3.42

Gaus mean : 33.9048

Gaus Width: 3.5905



B1L101S, U14-ch0, adc2

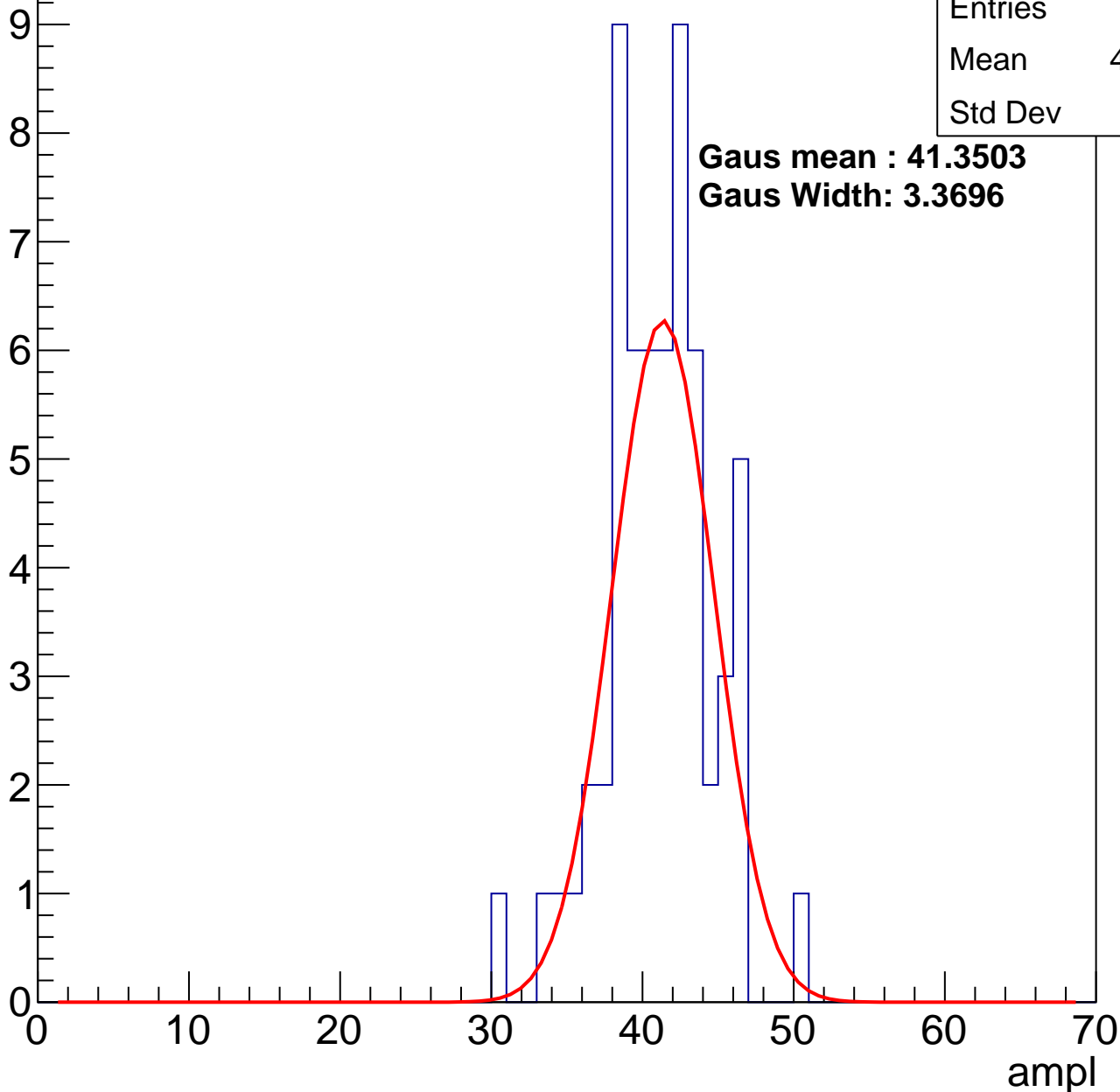
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	40.64
Std Dev	3.53

Gaus mean : 41.3503

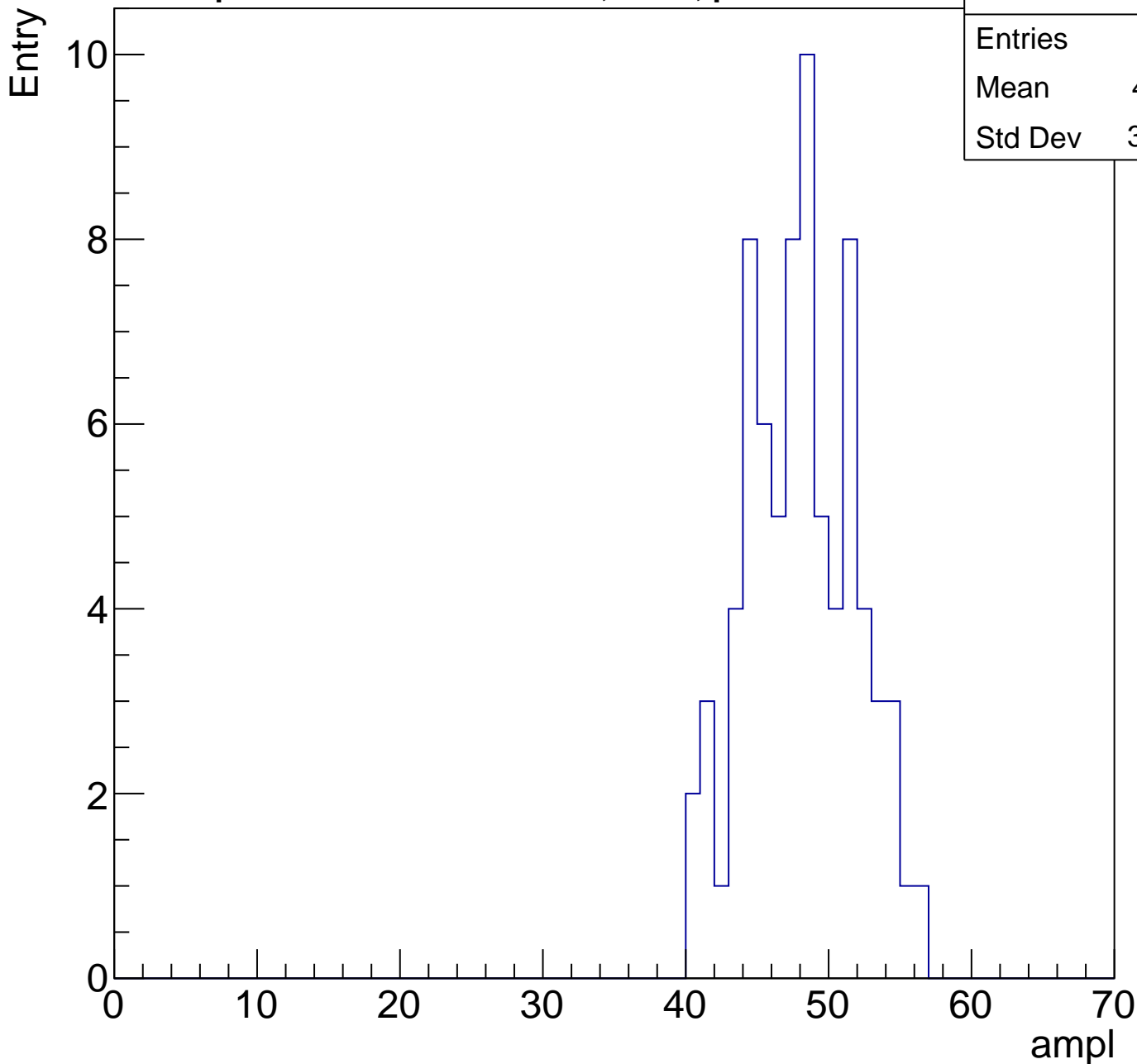
Gaus Width: 3.3696



B1L101S, U14-ch0, adc3

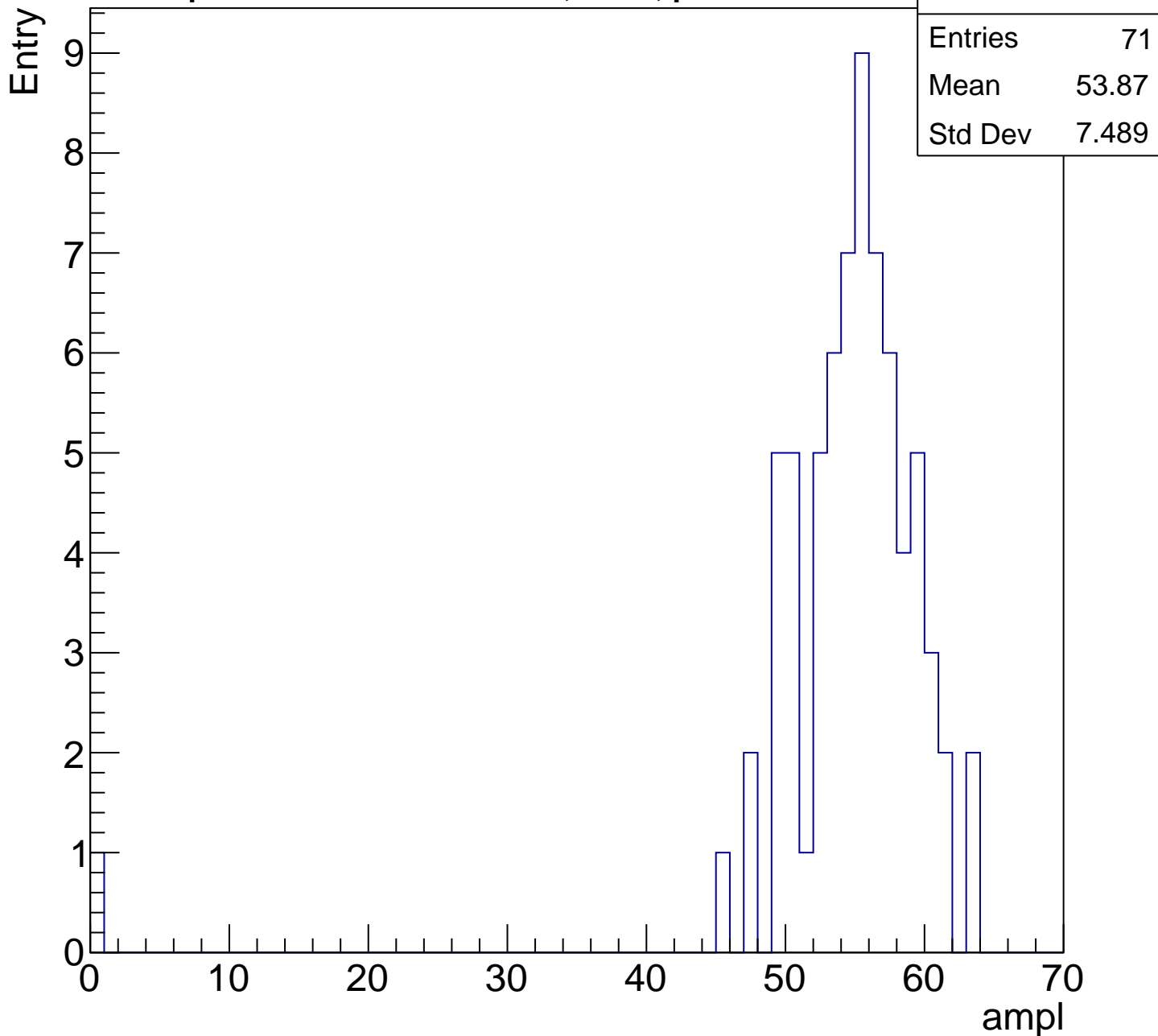
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	47.61
Std Dev	3.738



B1L101S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

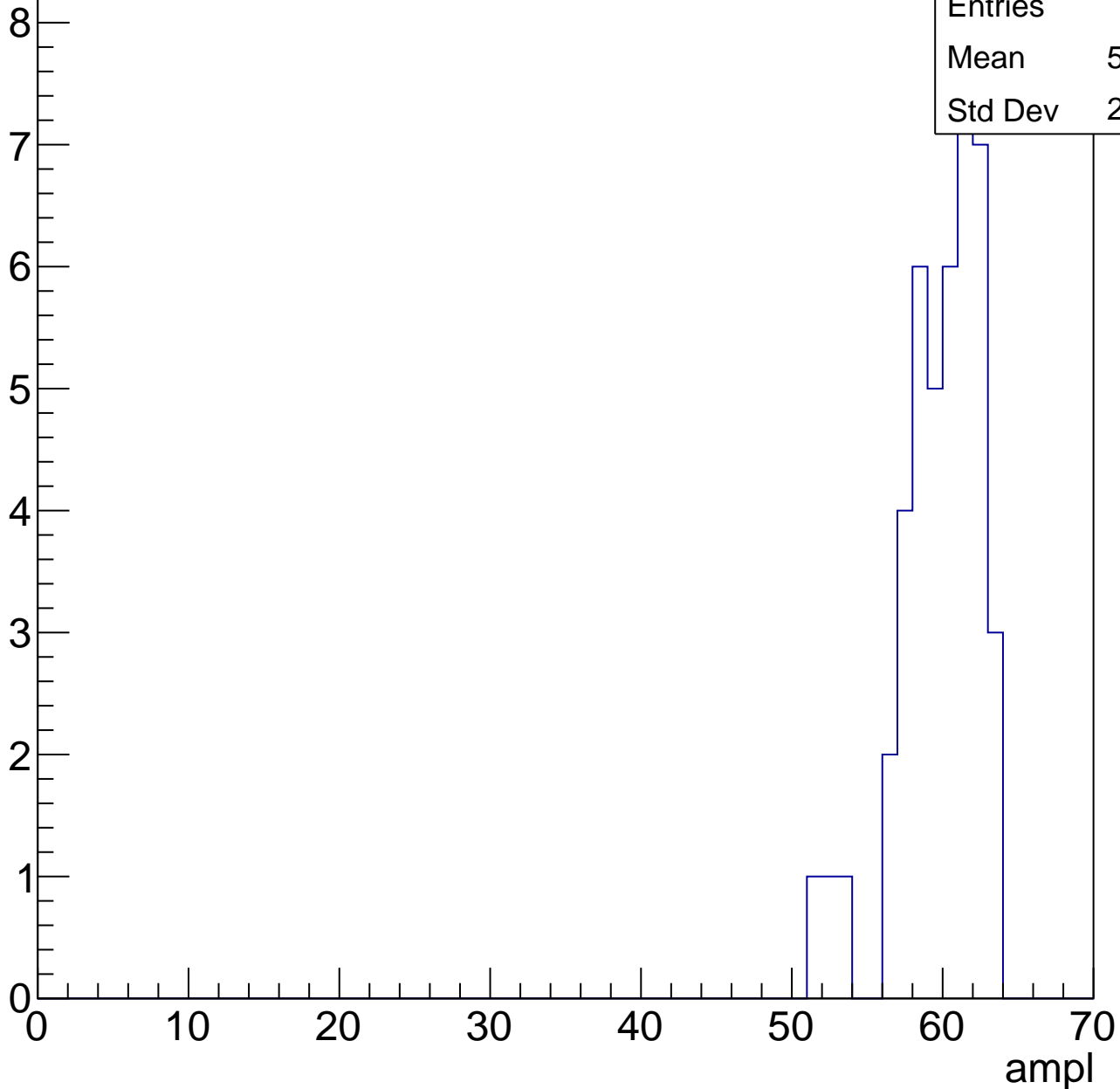


B1L101S, U14-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	59.32
Std Dev	2.753



B1L101S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch1, adc0

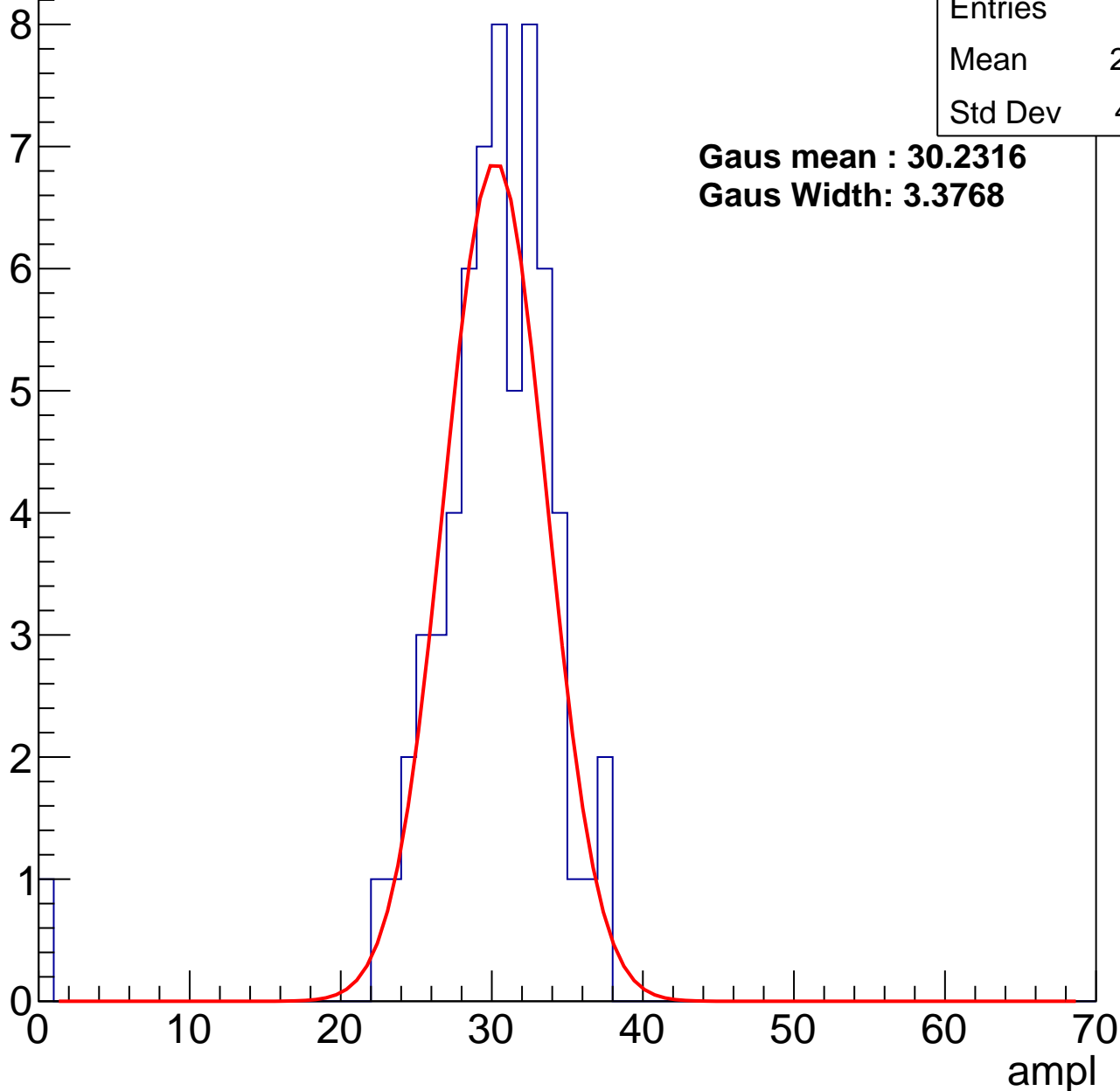
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.44
Std Dev	4.991

Gaus mean : 30.2316

Gaus Width: 3.3768



B1L101S, U14-ch1, adc1

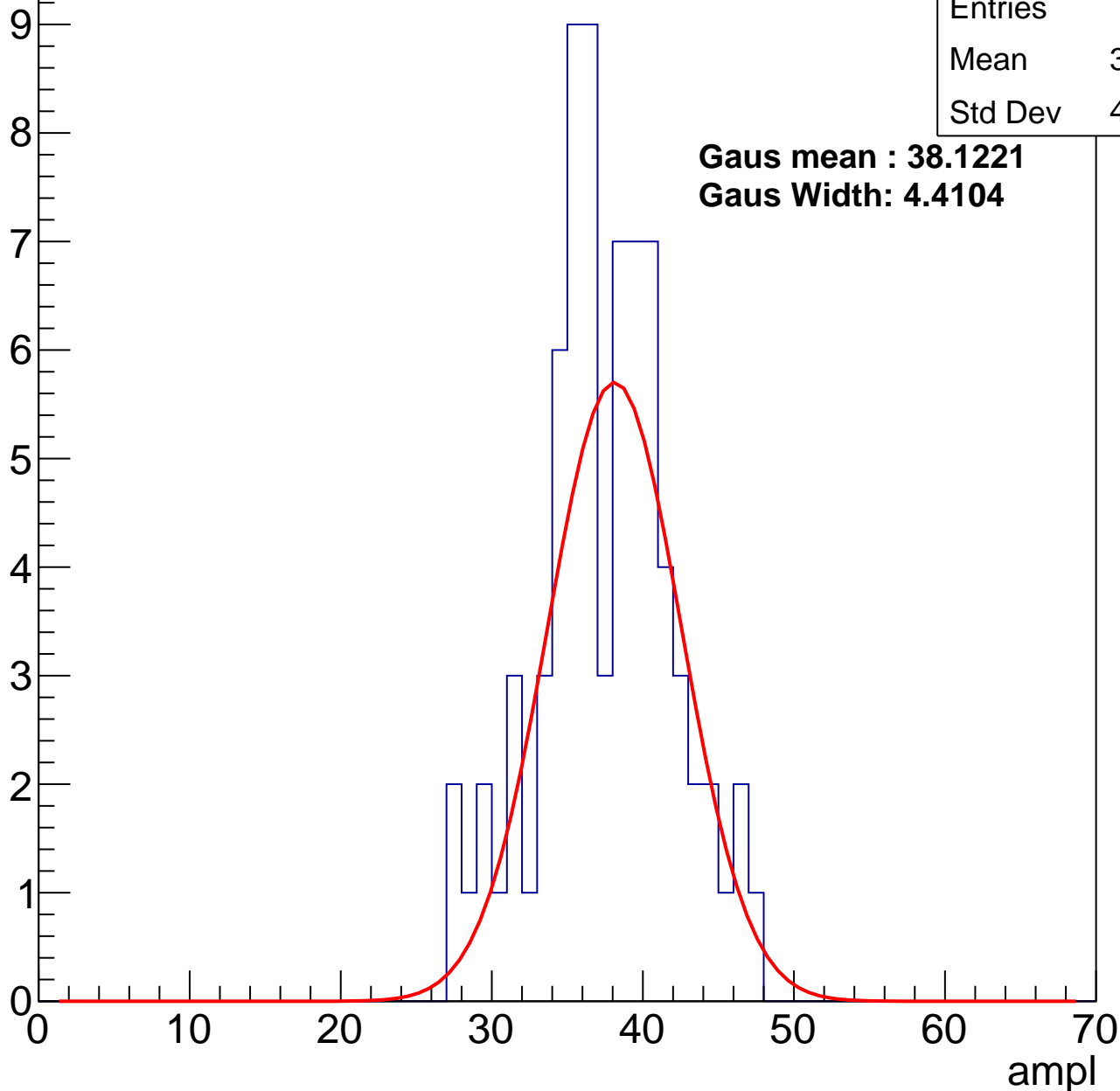
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.04
Std Dev	4.399

Gaus mean : 38.1221

Gaus Width: 4.4104



B1L101S, U14-ch1, adc2

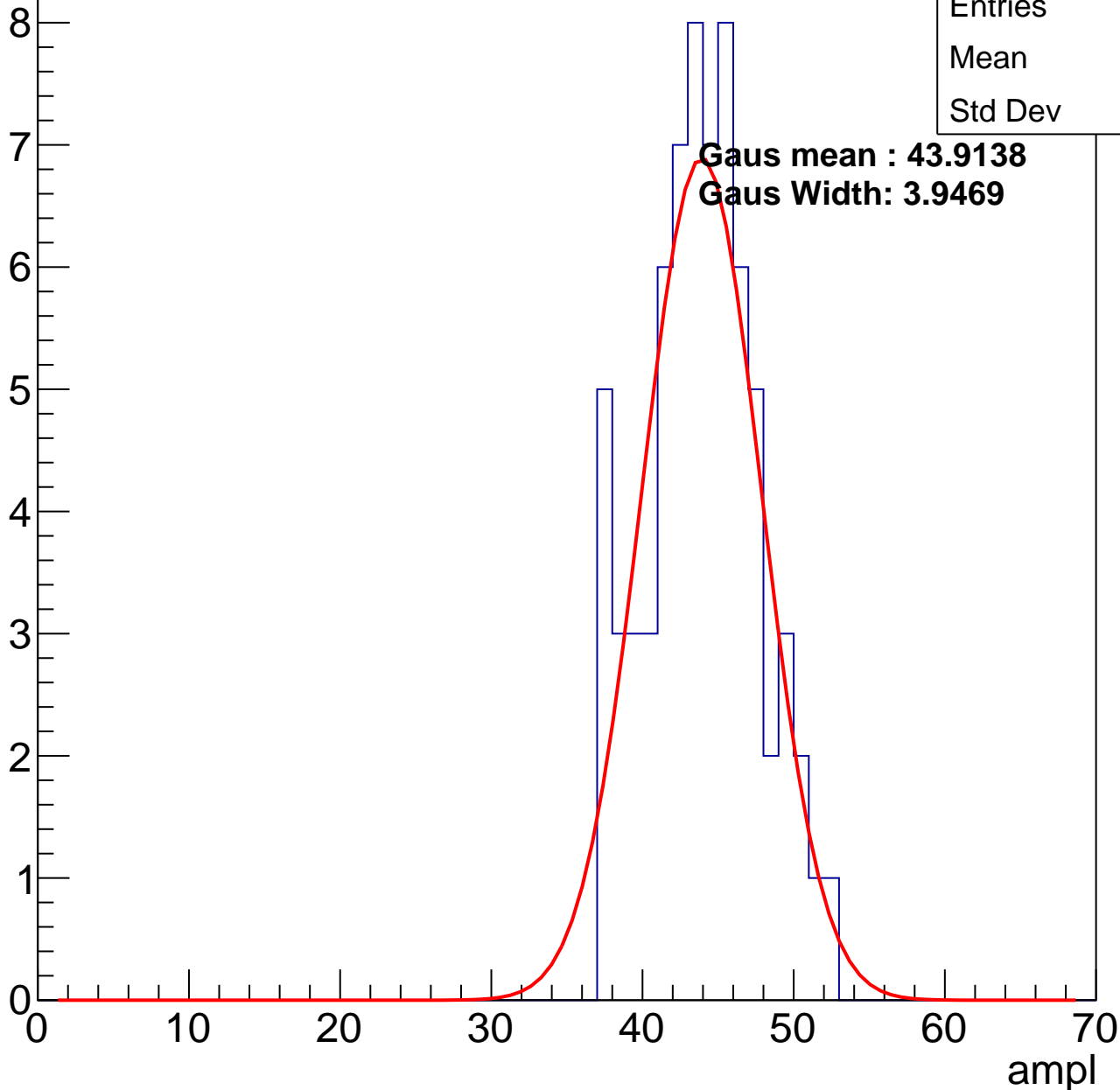
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.5
Std Dev	3.62

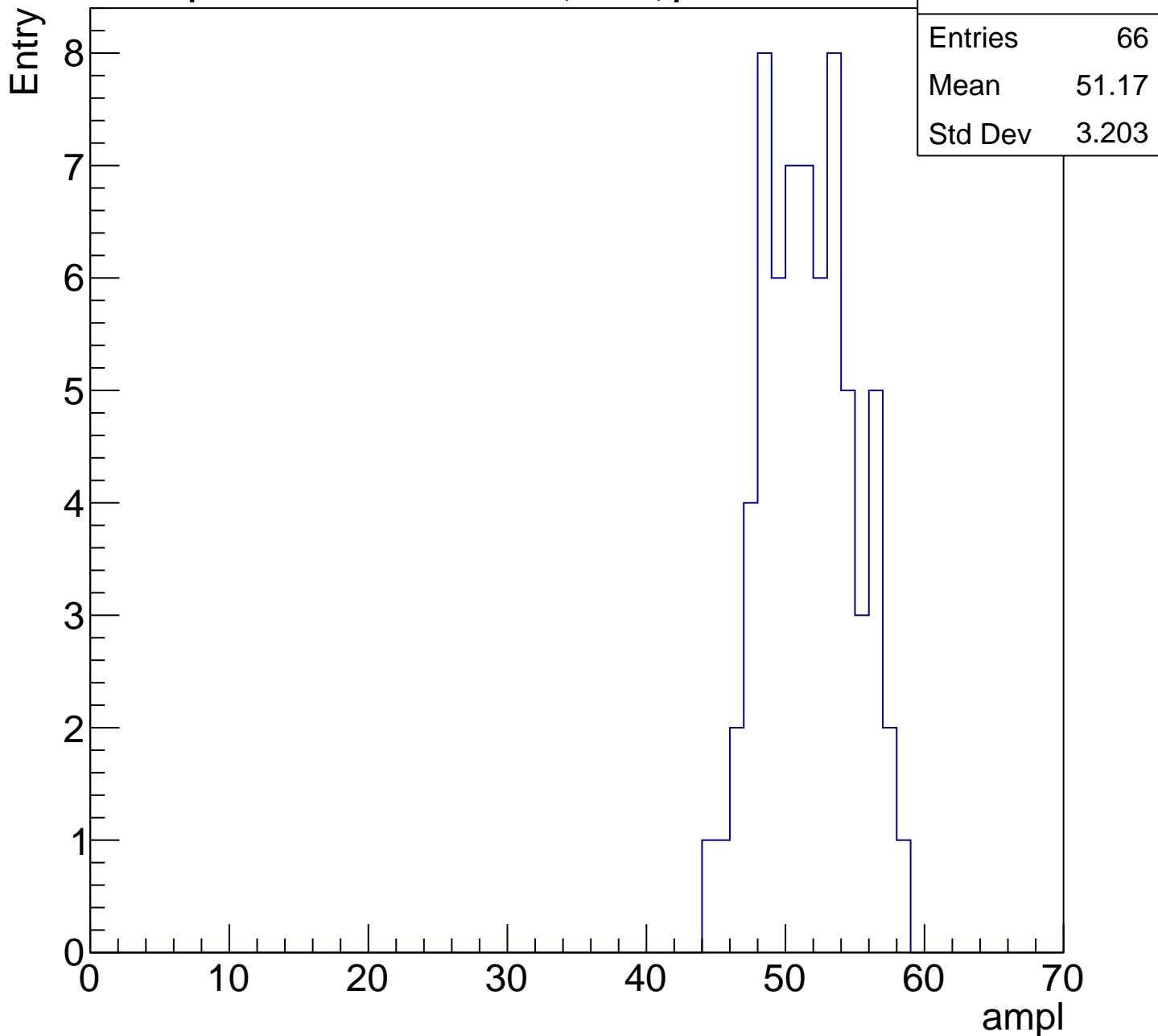
Gaus mean : 43.9138

Gaus Width: 3.9469



B1L101S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch1, adc4

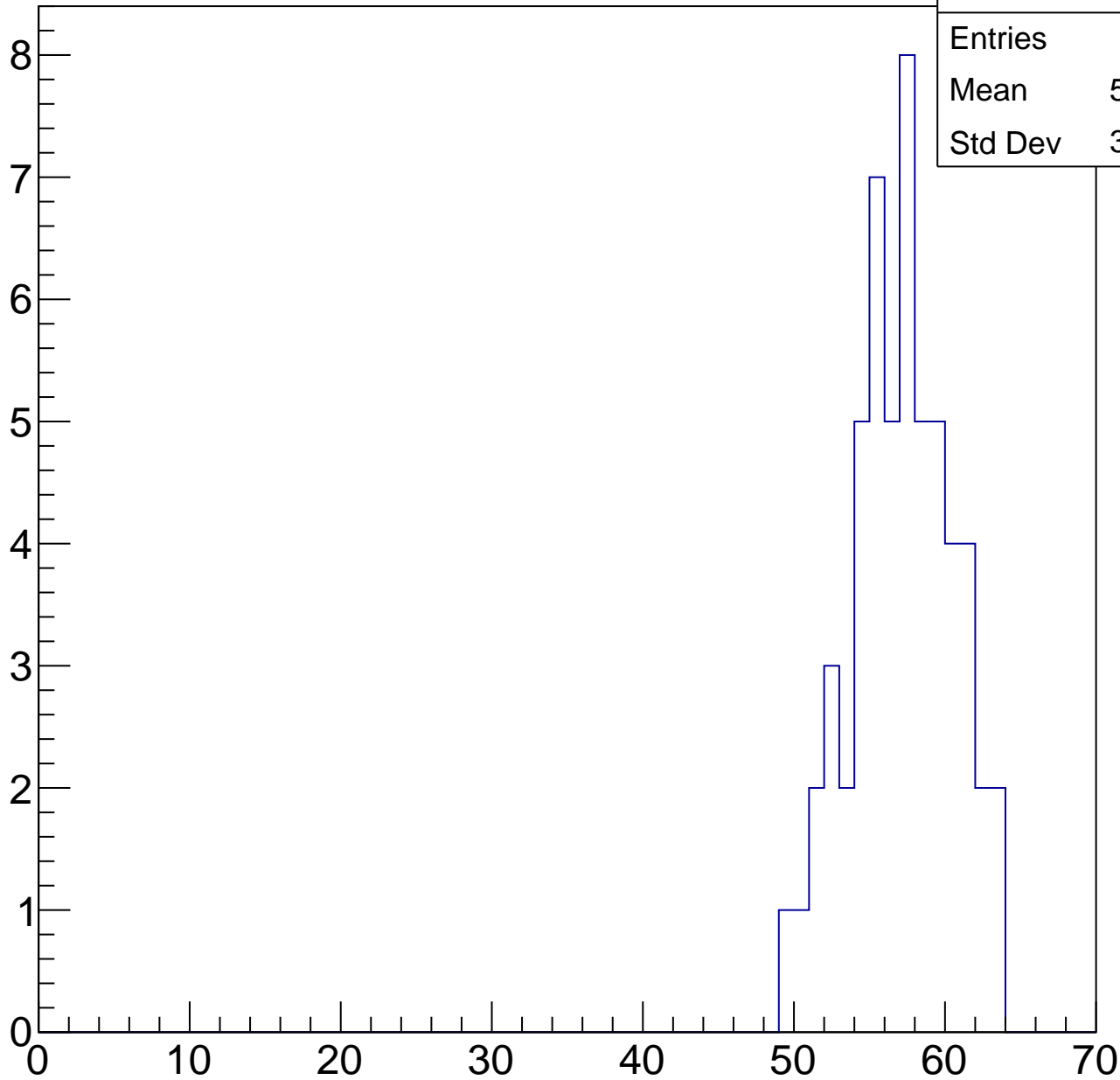
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	56.66
Std Dev	3.302

ampl

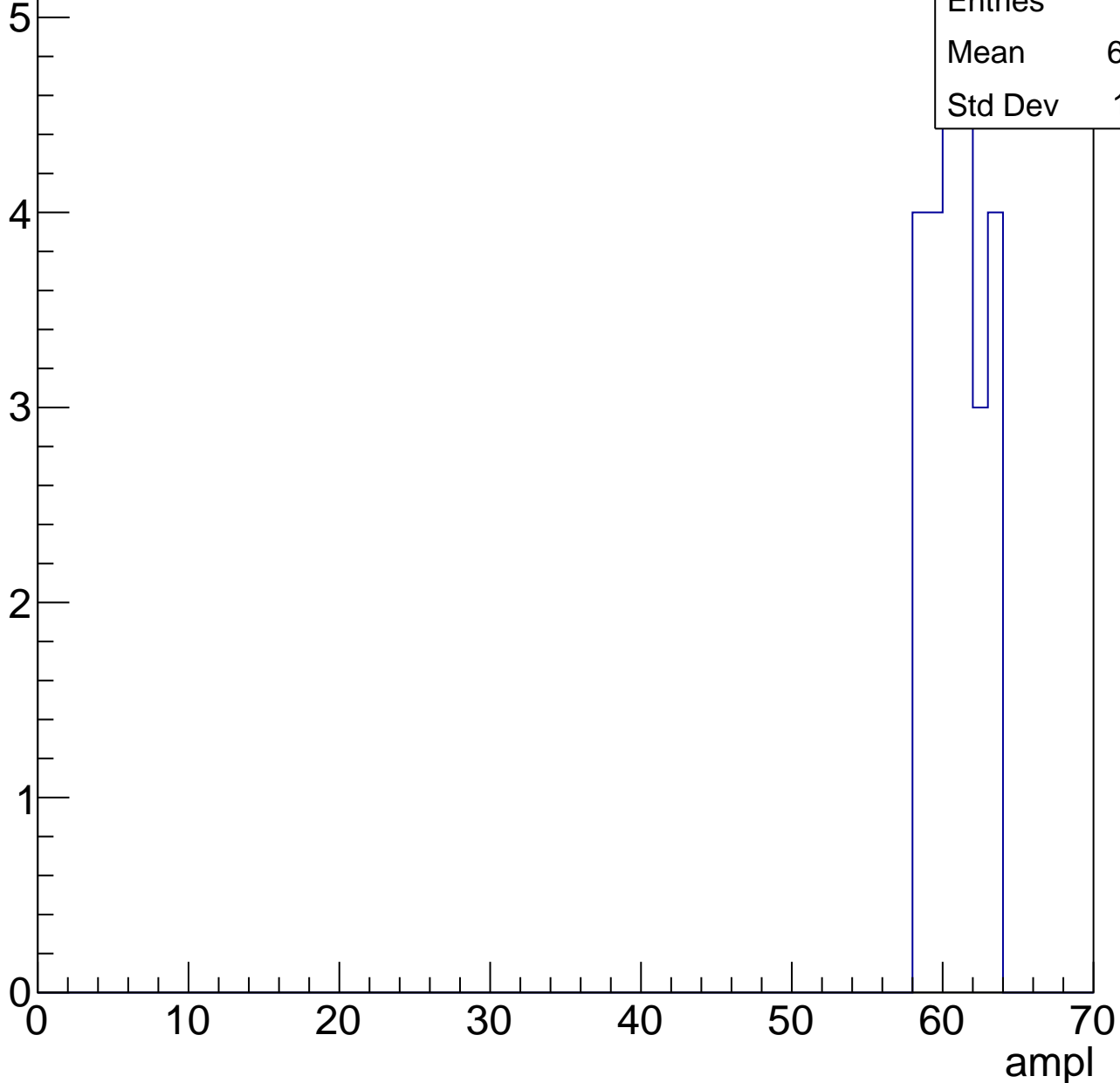


B1L101S, U14-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	60.44
Std Dev	1.651

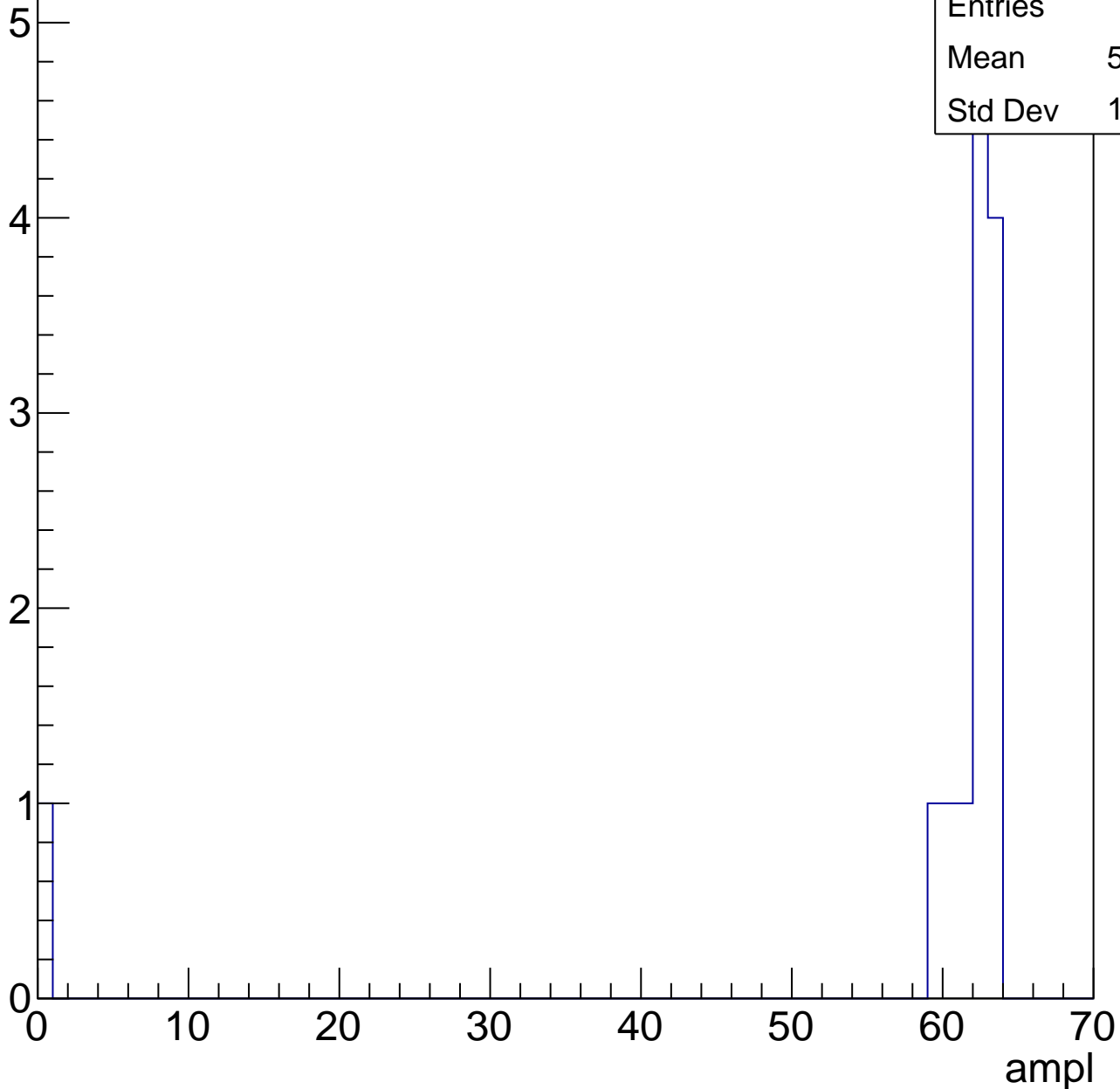


B1L101S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	57.08
Std Dev	16.52



B1L101S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U14-ch2, adc0

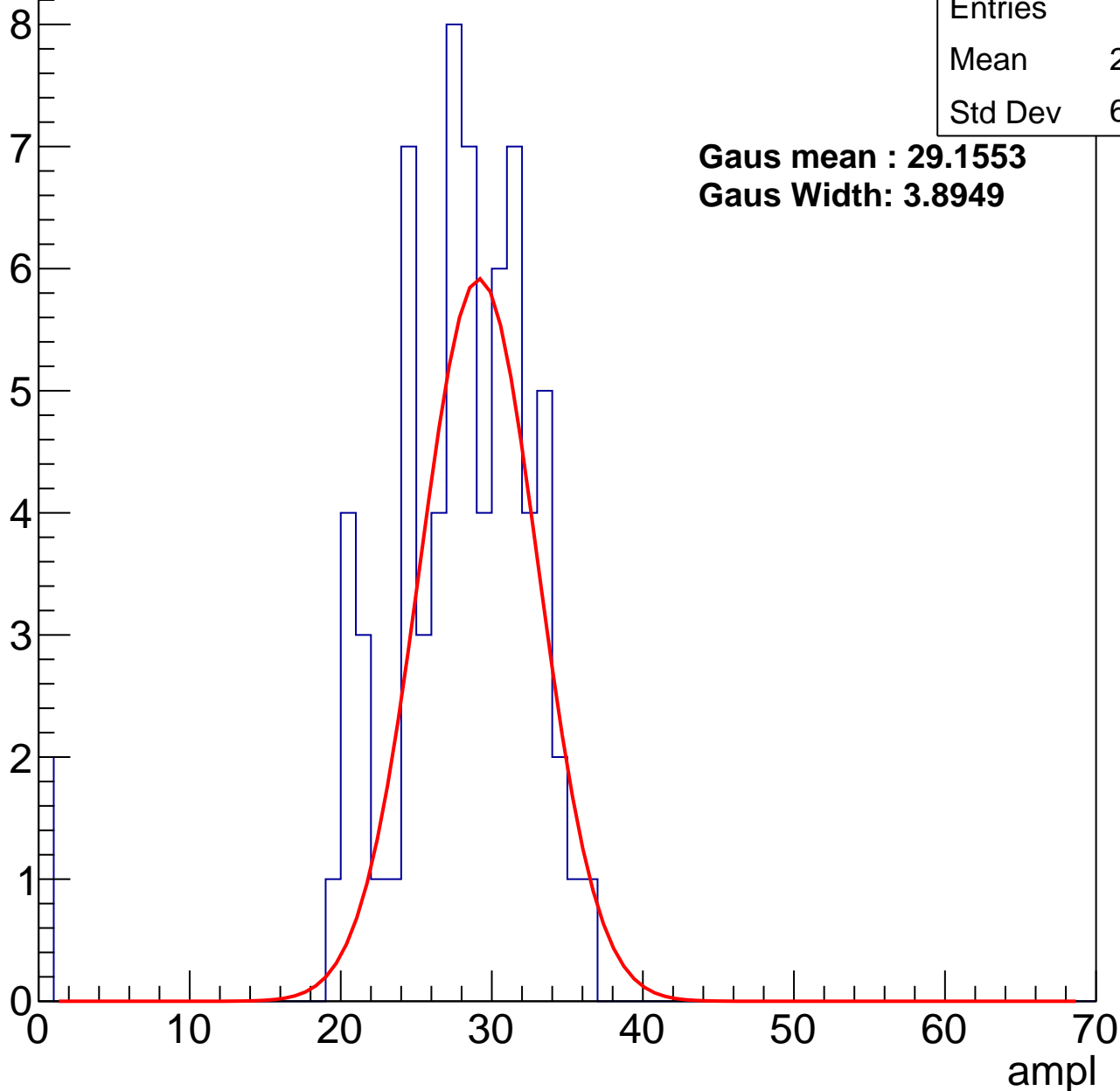
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	26.92
Std Dev	6.118

Gaus mean : 29.1553

Gaus Width: 3.8949



B1L101S, U14-ch2, adc1

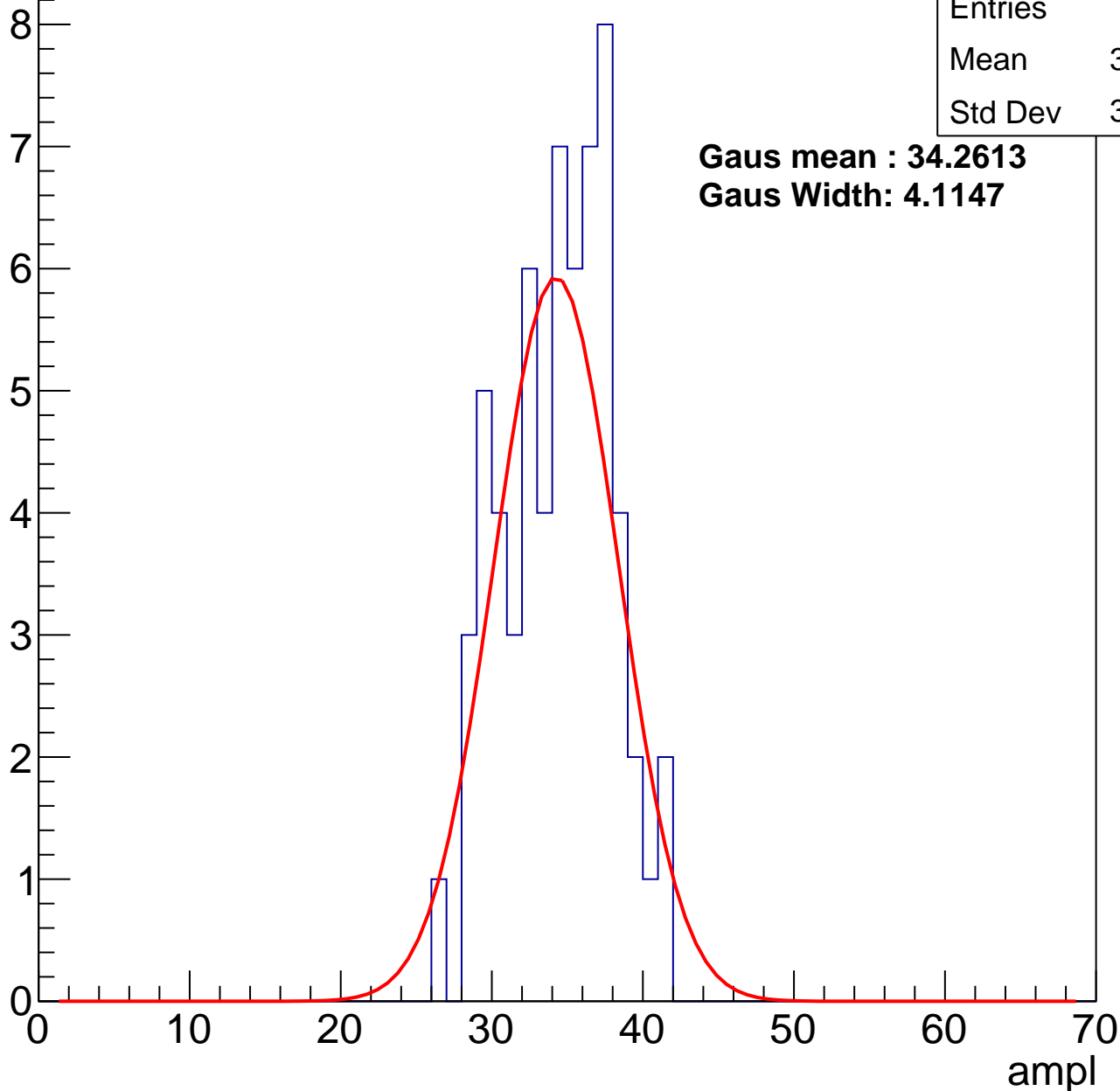
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	33.97
Std Dev	3.496

Gaus mean : 34.2613

Gaus Width: 4.1147



B1L101S, U14-ch2, adc2

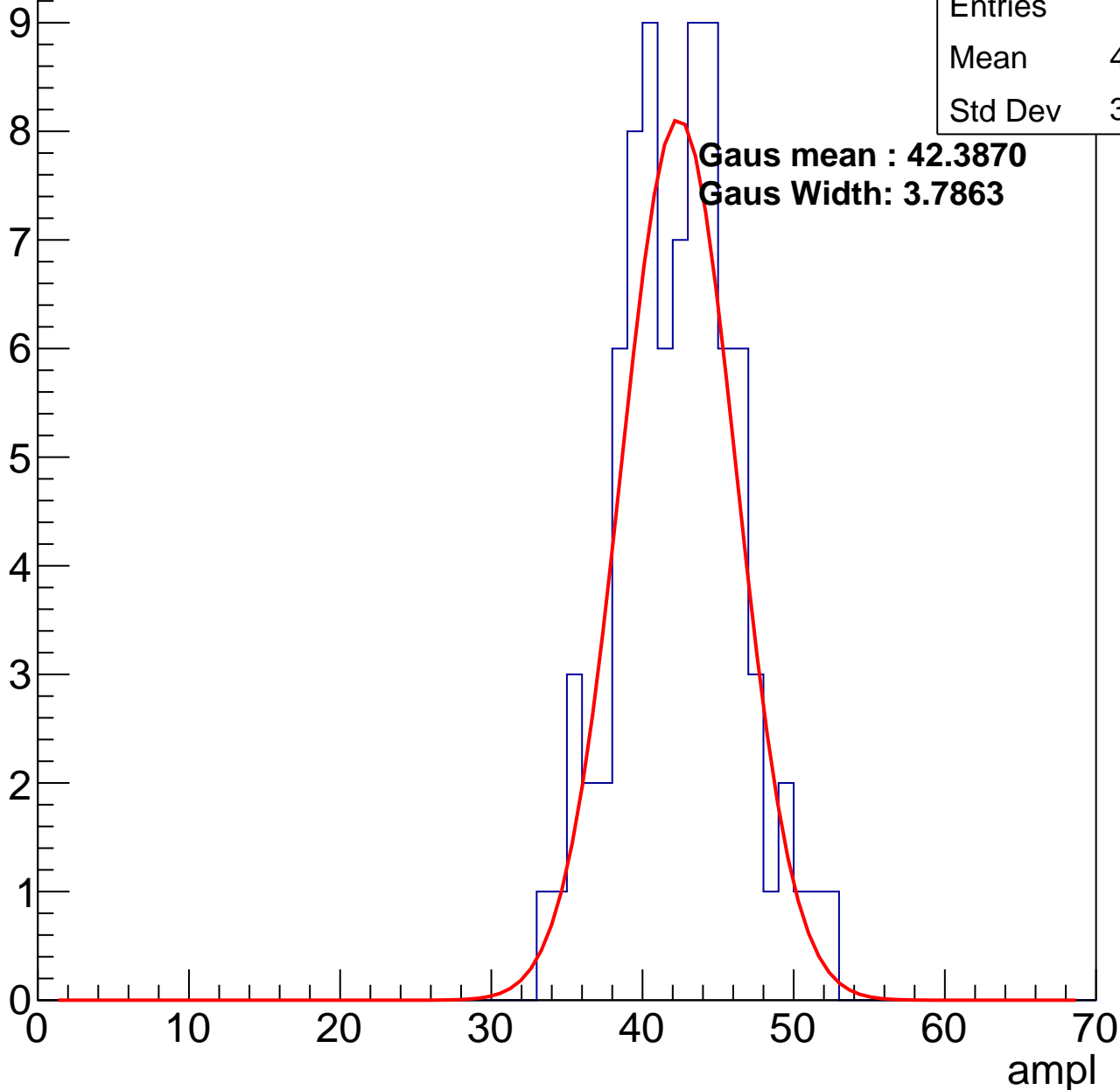
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	41.99
Std Dev	3.893

Gaus mean : 42.3870

Gaus Width: 3.7863

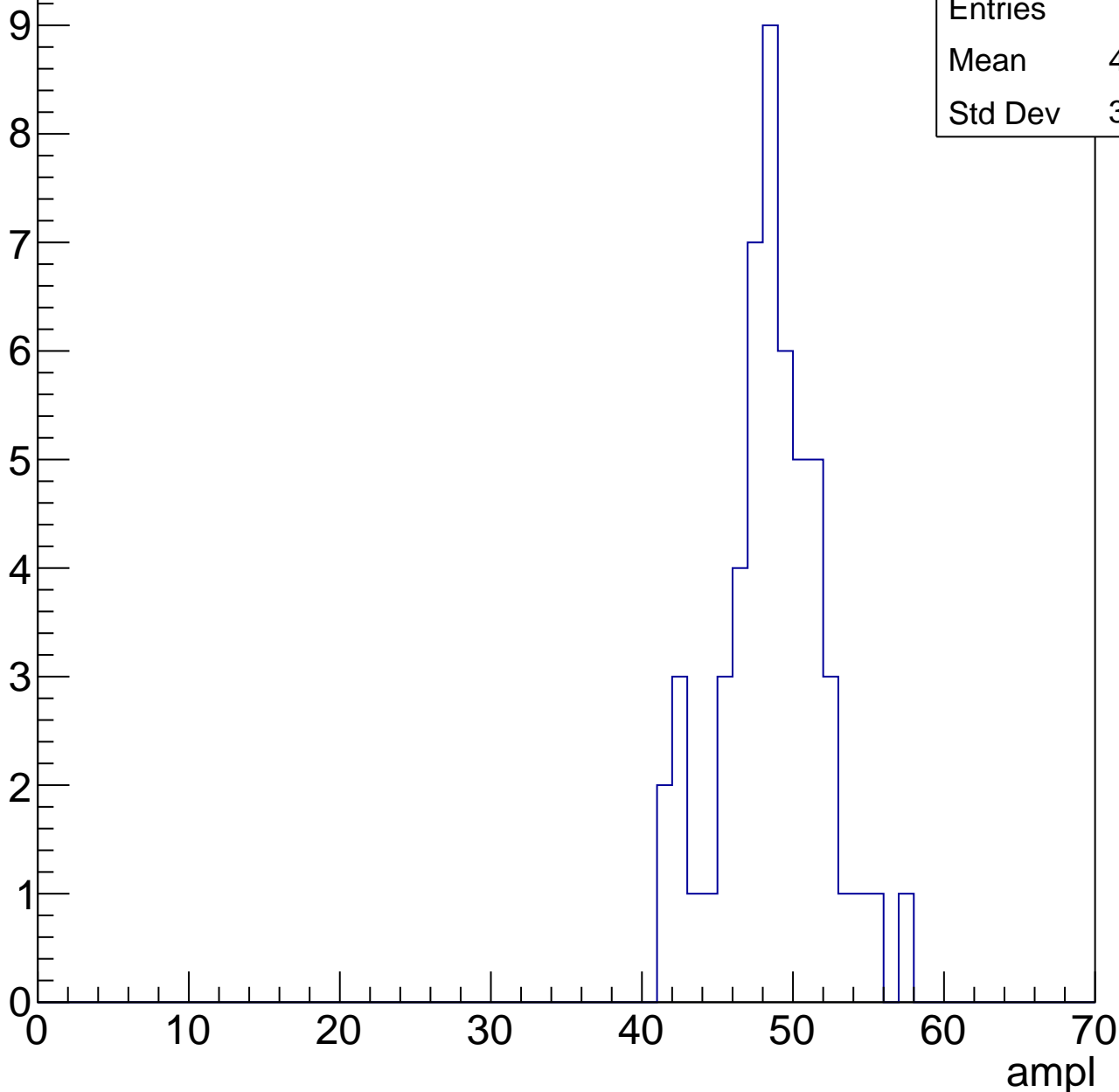


B1L101S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	48.09
Std Dev	3.383

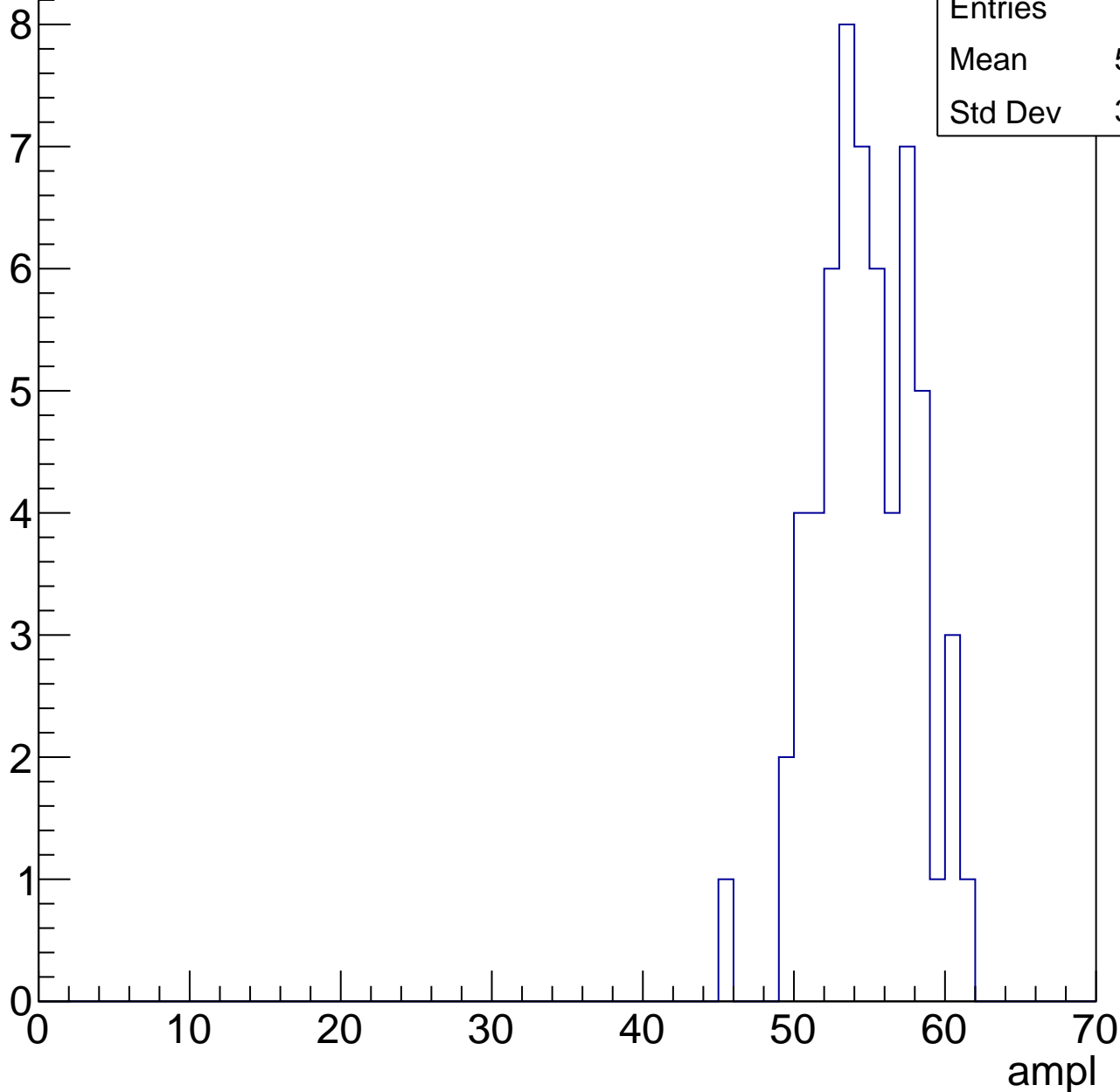


B1L101S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

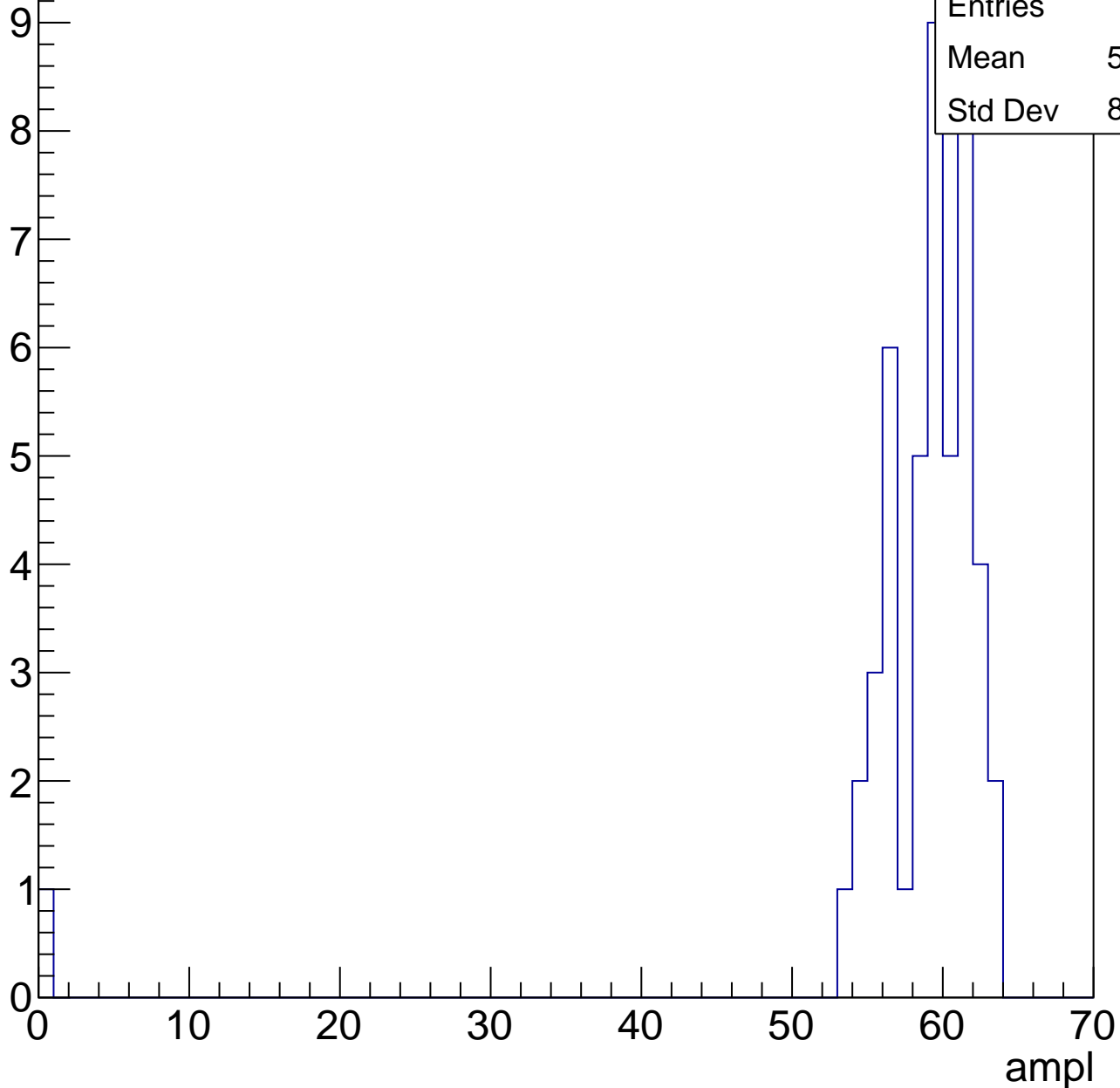
Entries	59
Mean	54.31
Std Dev	3.201



B1L101S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

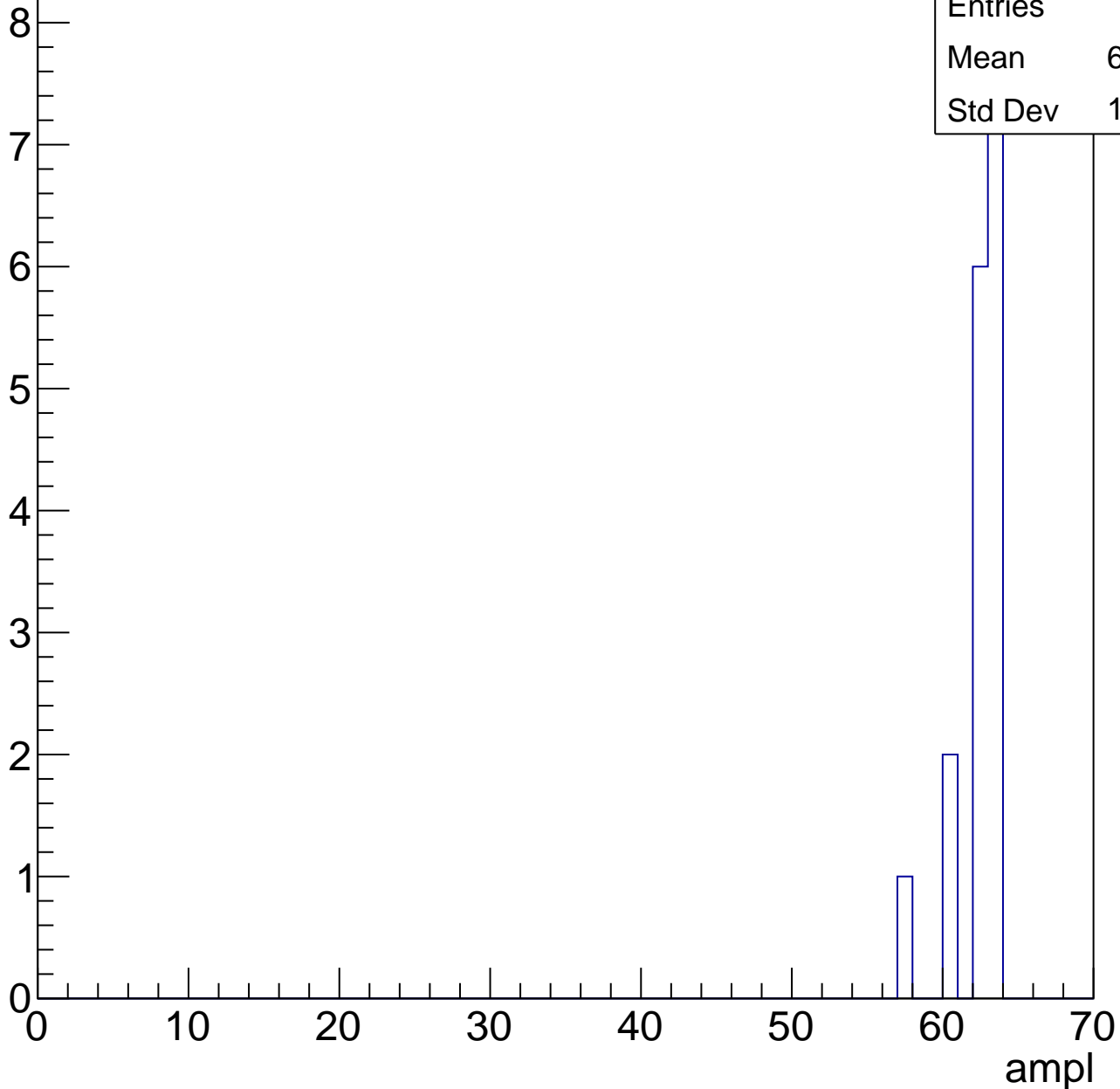


B1L101S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61.94
Std Dev	1.552



B1L101S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch3, adc0

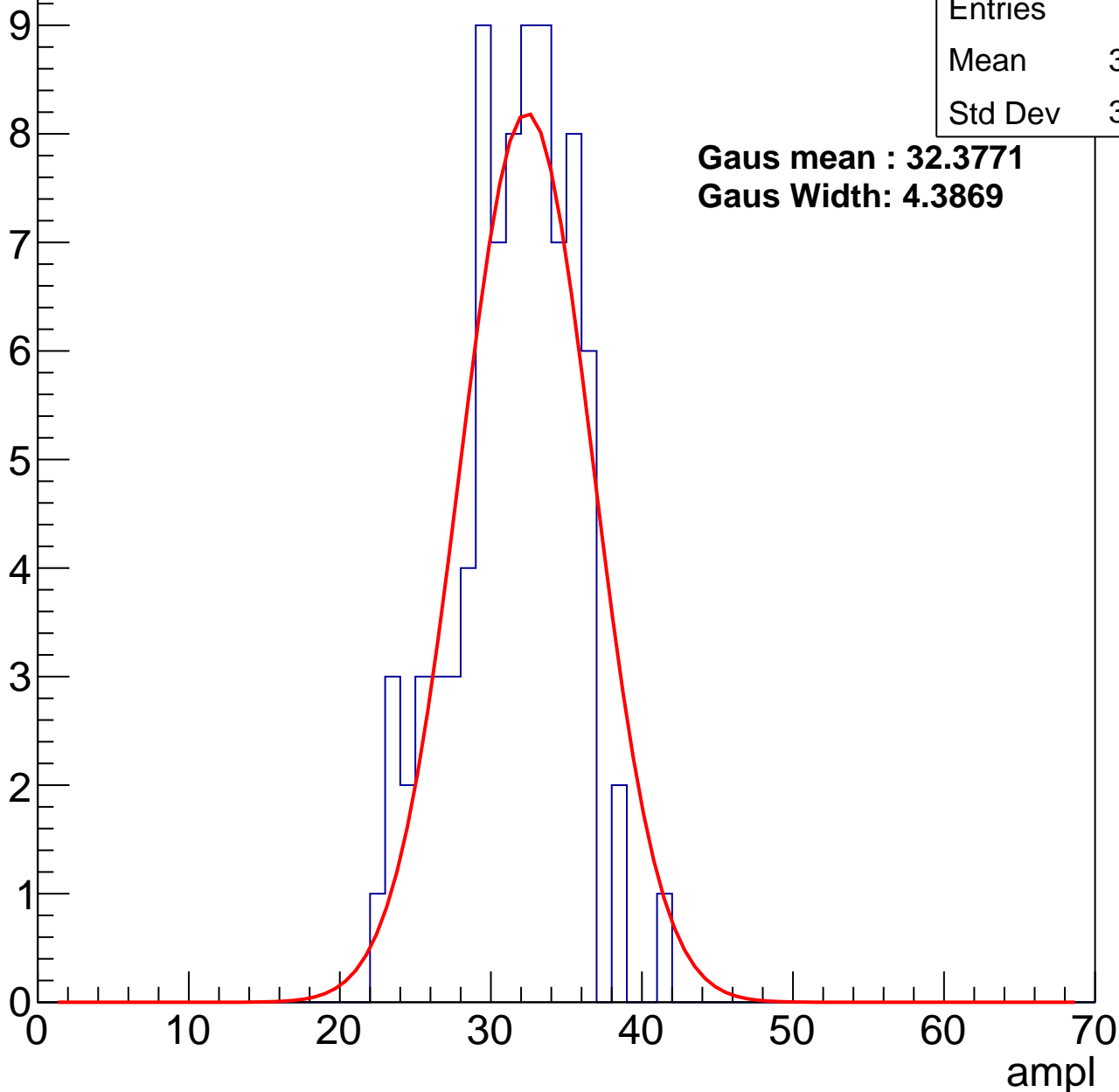
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	31.06
Std Dev	3.857

Gaus mean : 32.3771

Gaus Width: 4.3869



B1L101S, U14-ch3, adc1

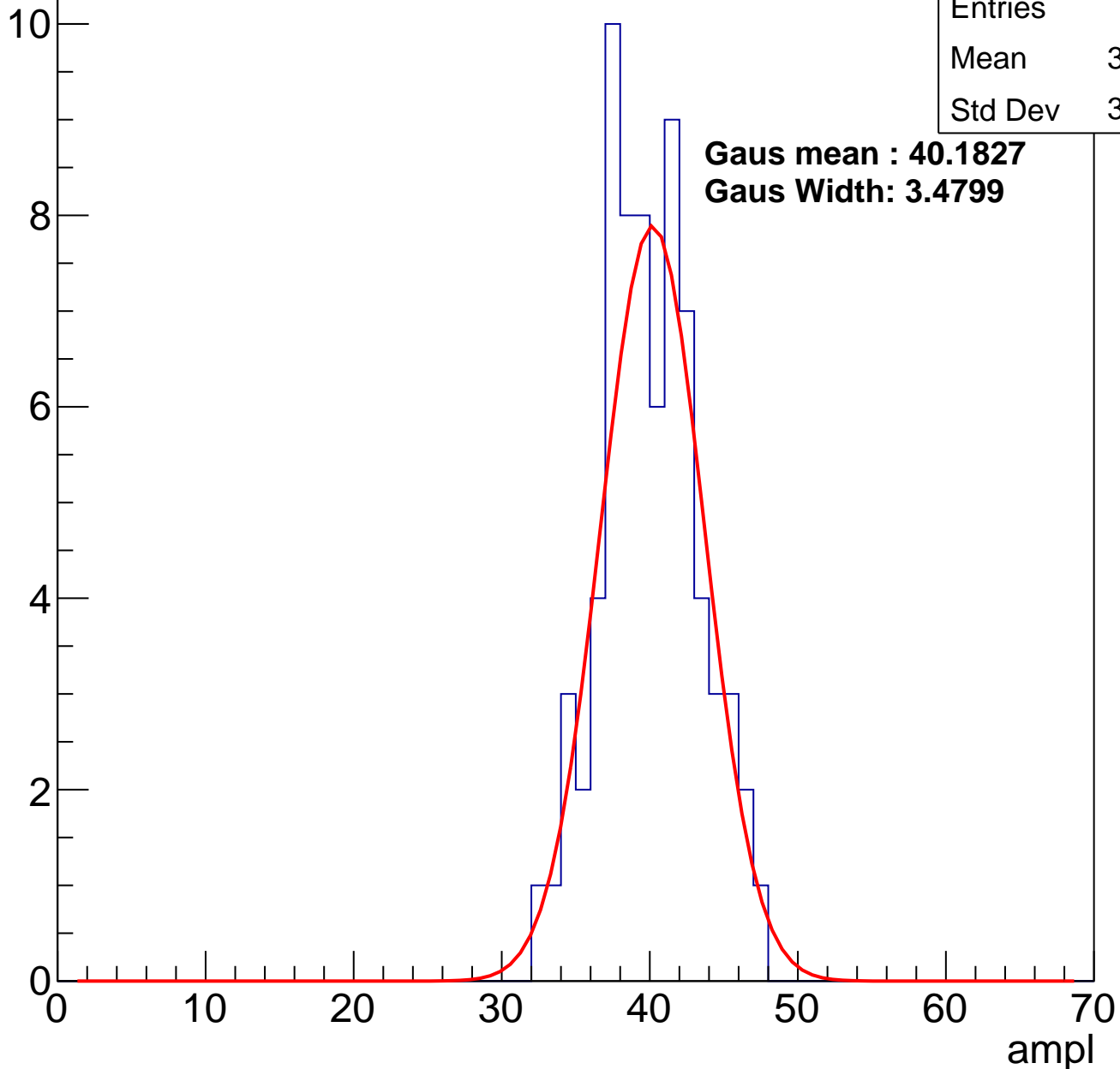
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	39.56
Std Dev	3.266

Gaus mean : 40.1827

Gaus Width: 3.4799

Entry



B1L101S, U14-ch3, adc2

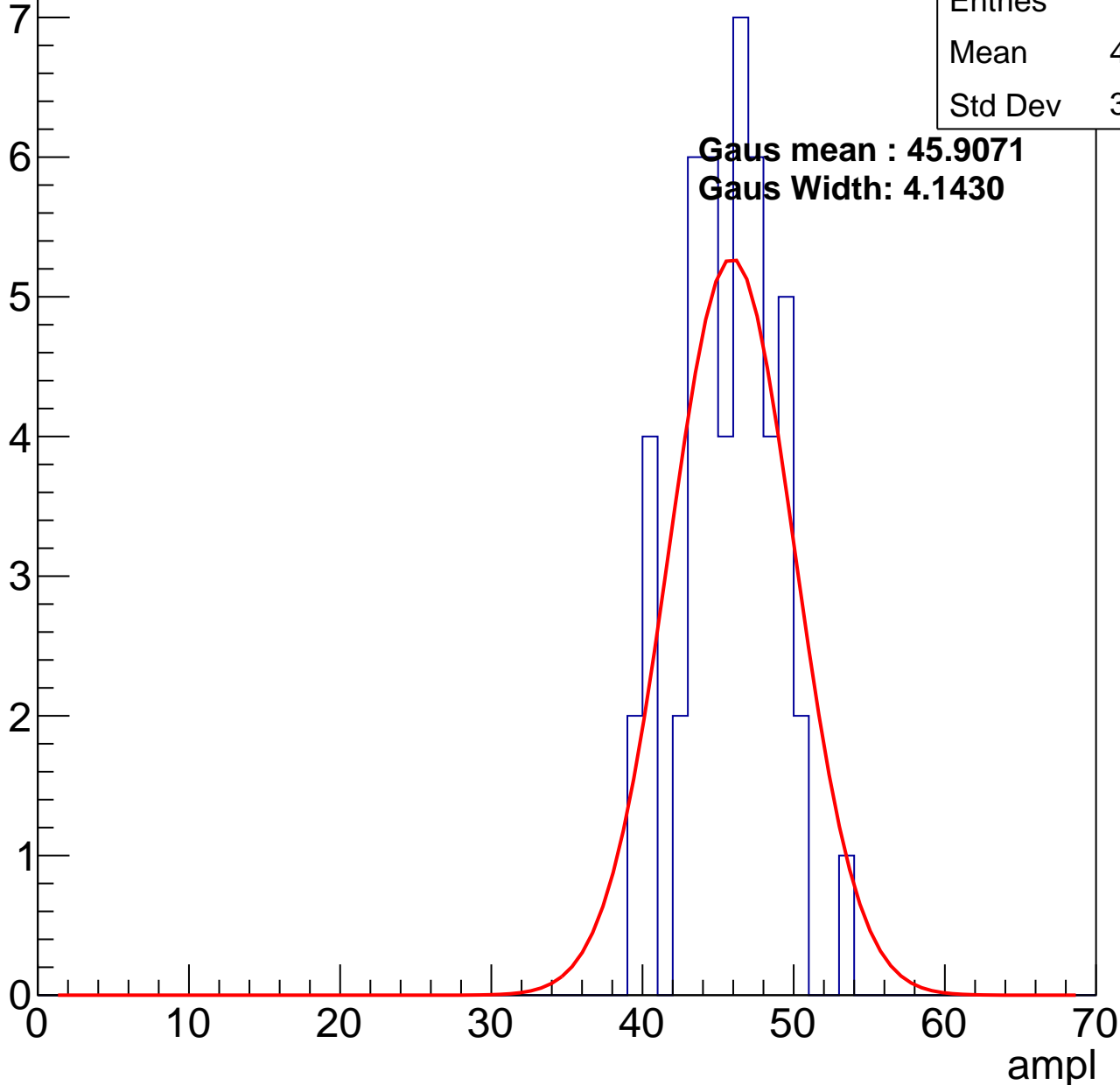
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	45.27
Std Dev	3.122

Gaus mean : 45.9071

Gaus Width: 4.1430

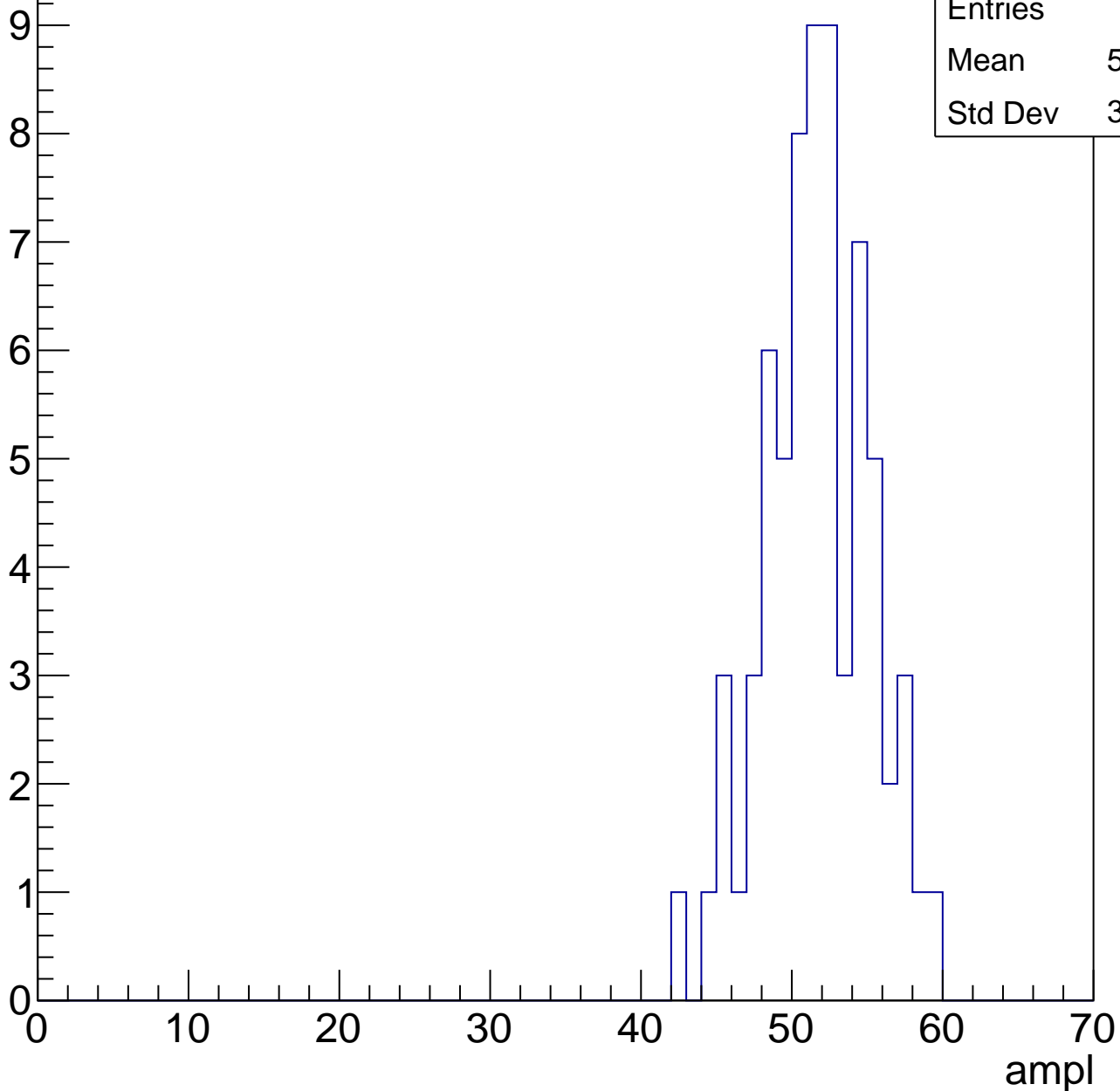


B1L101S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

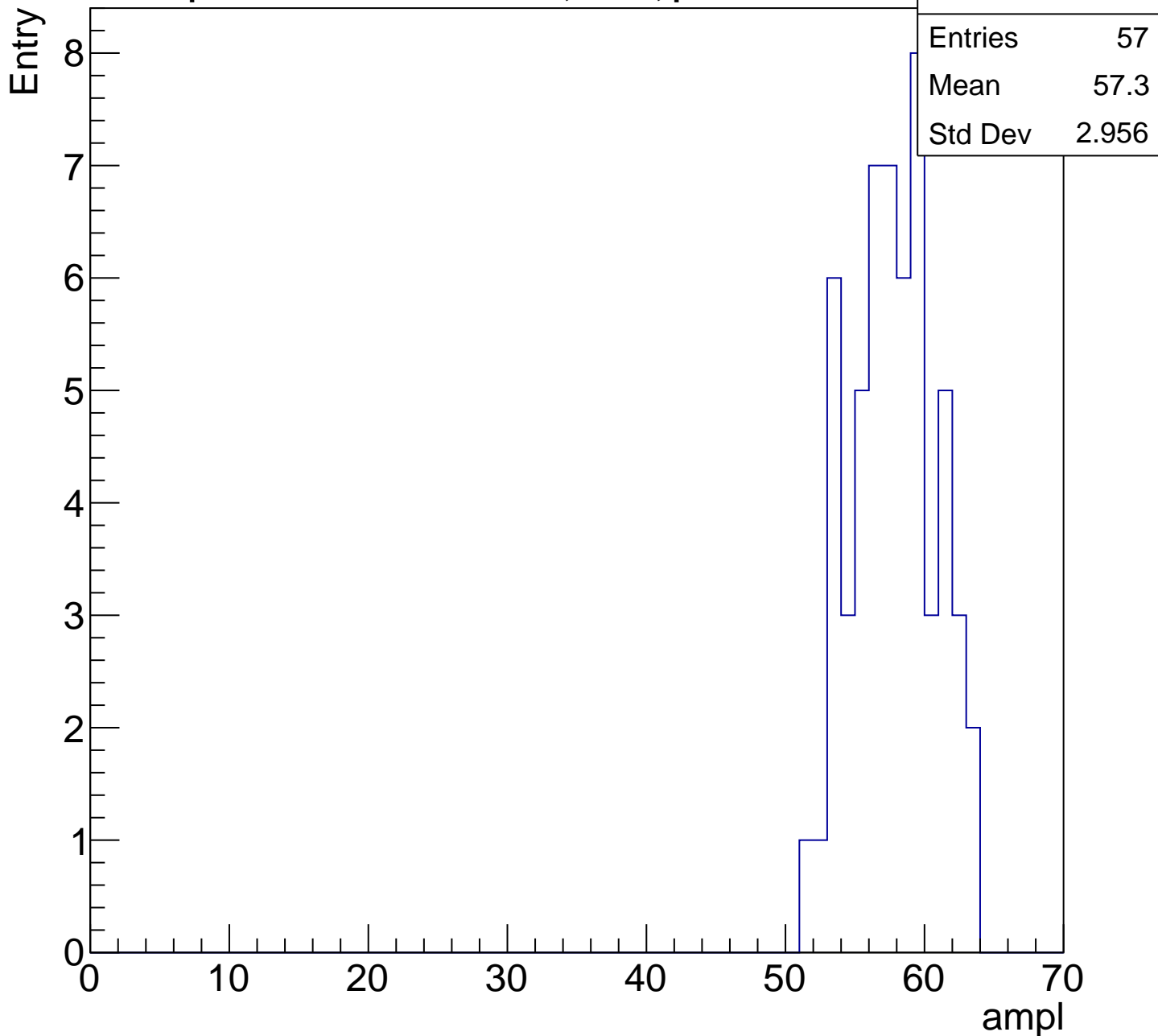
Entry

Entries	68
Mean	51.18
Std Dev	3.485



B1L101S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

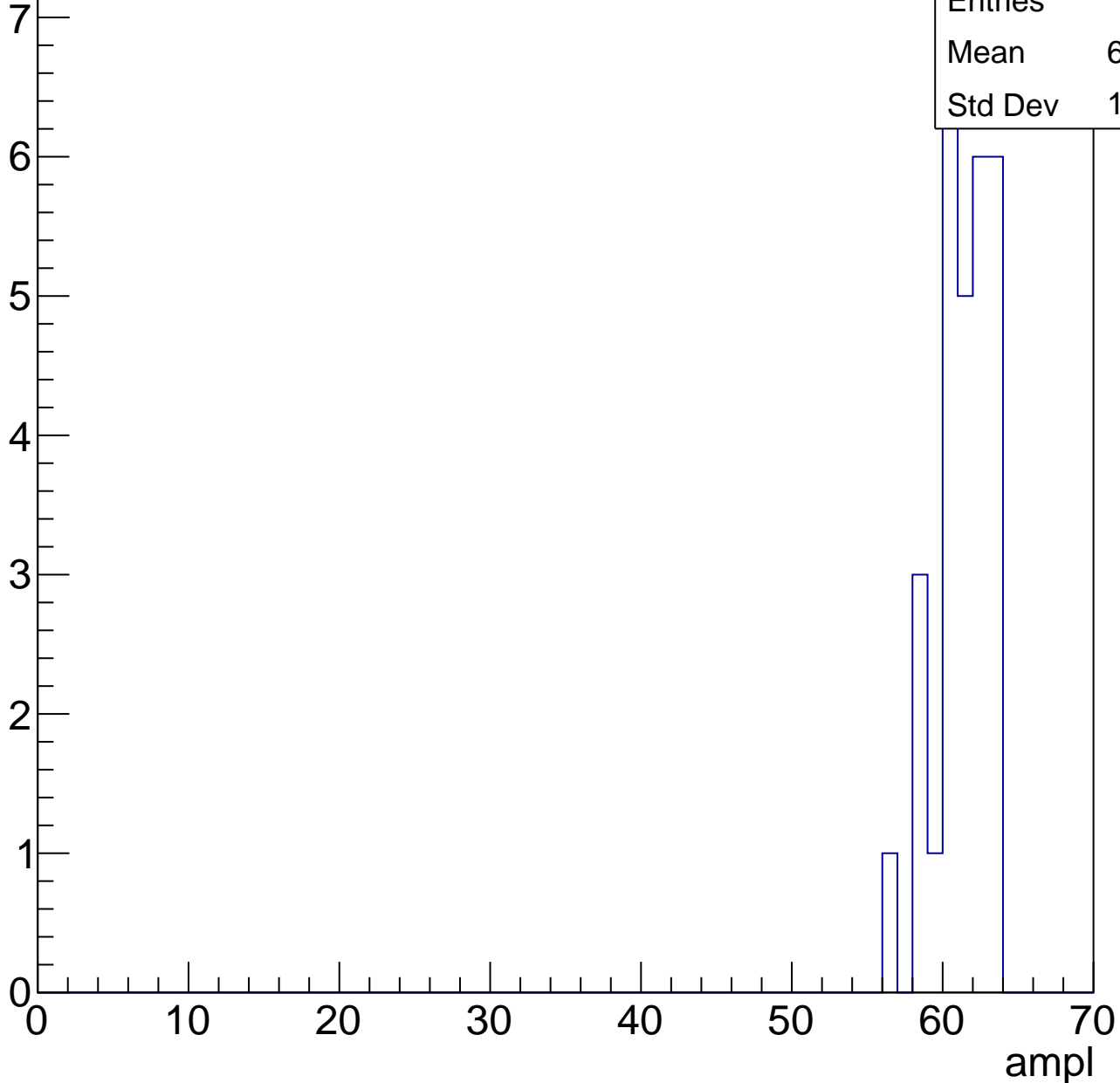


B1L101S, U14-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	60.83
Std Dev	1.782



B1L101S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	47
Std Dev	27.14

ampl

B1L101S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch4, adc0

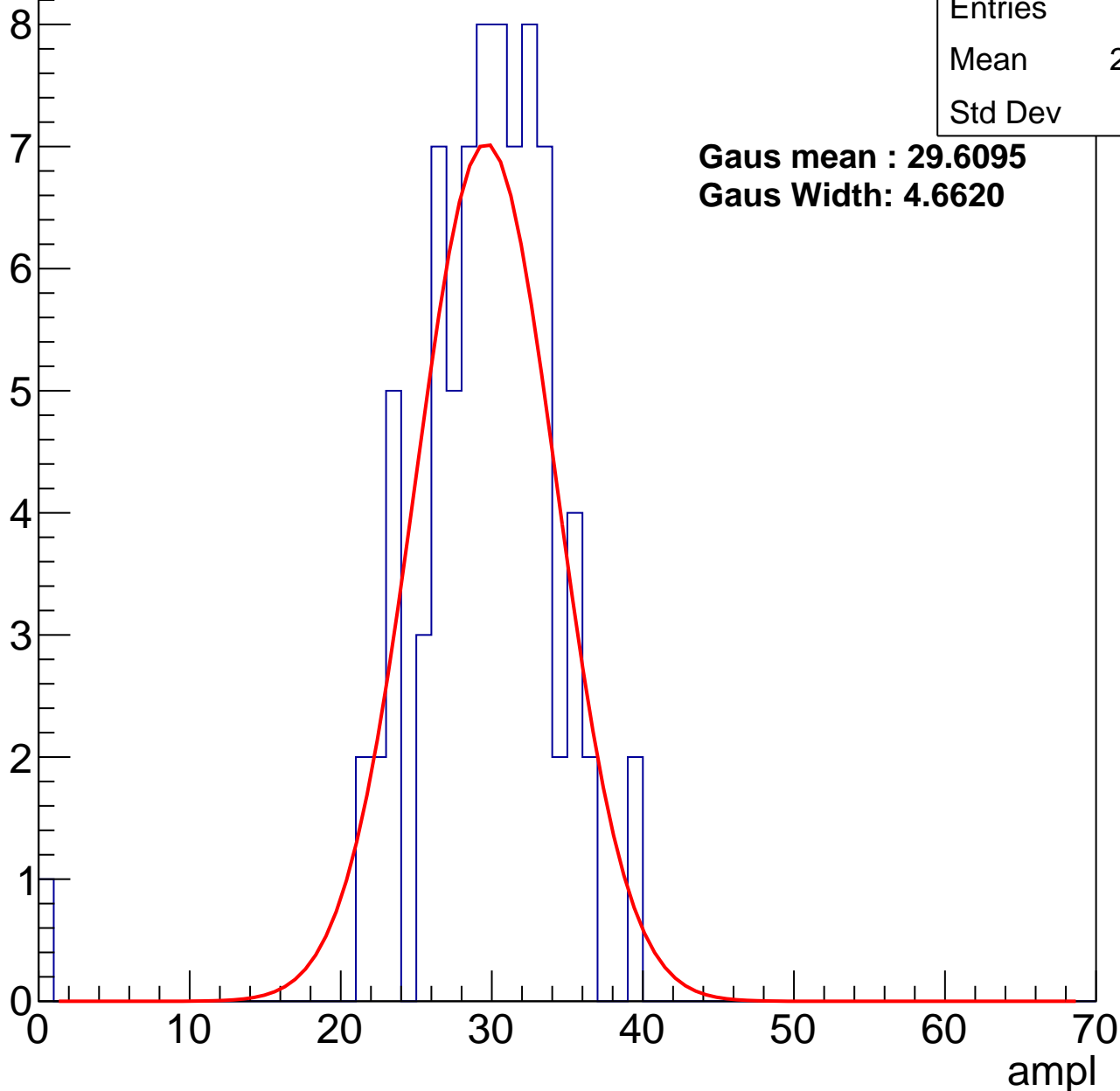
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	29.04
Std Dev	5.12

Gaus mean : 29.6095

Gaus Width: 4.6620



B1L101S, U14-ch4, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	37.53
Std Dev	3.895

Gaus mean : 37.7967

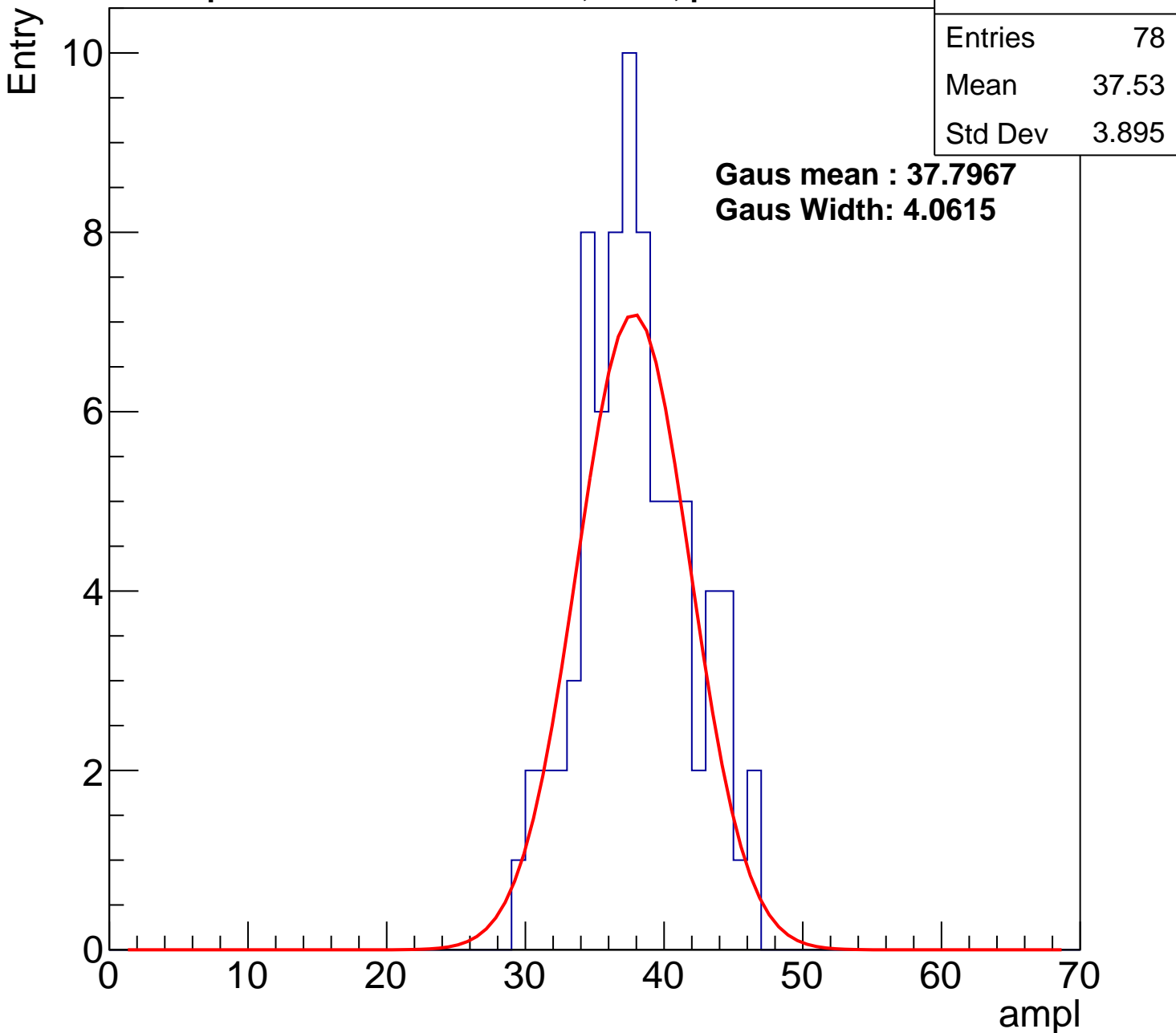
Gaus Width: 4.0615

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch4, adc2

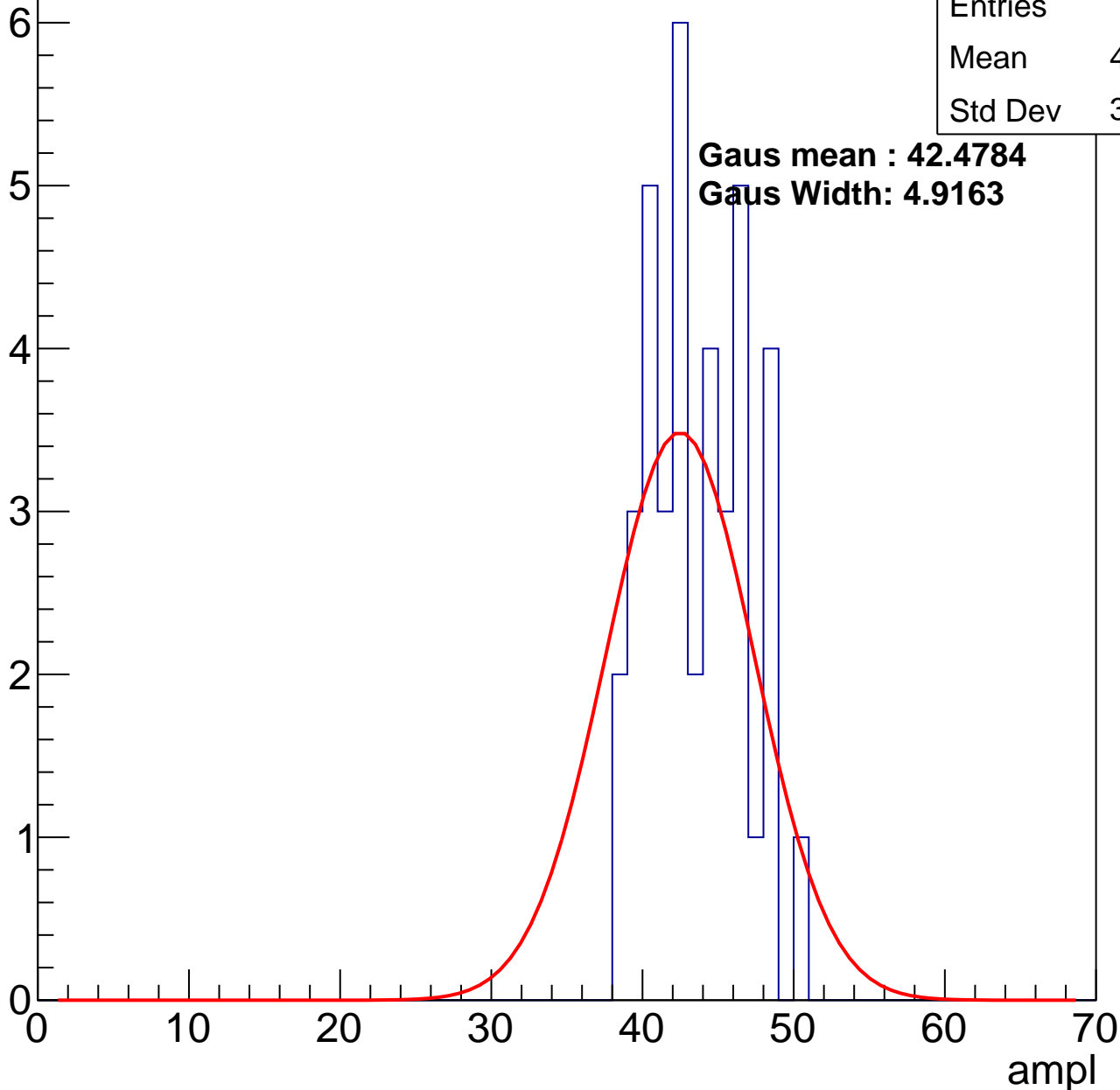
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	43.18
Std Dev	3.145

Gaus mean : 42.4784

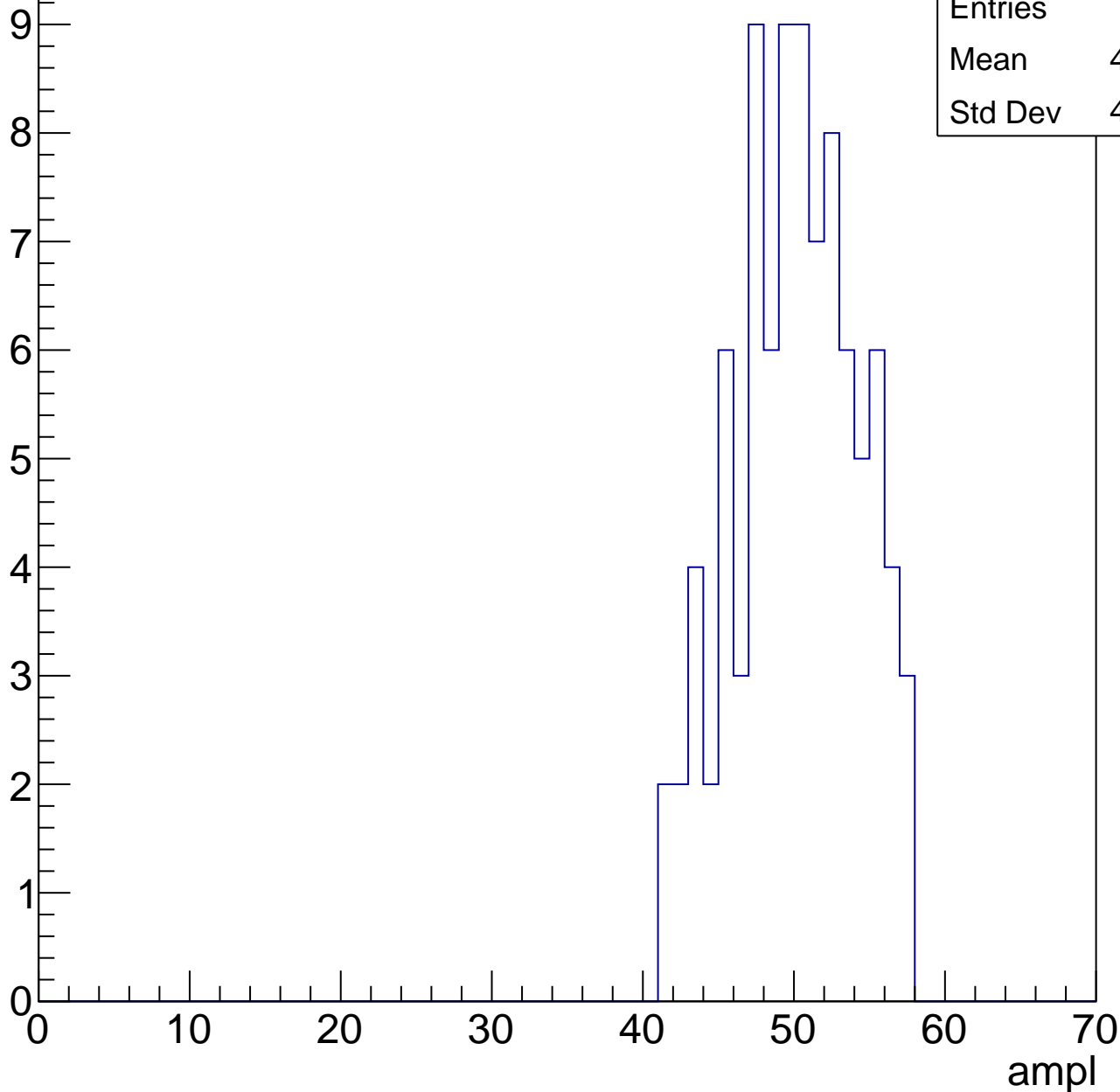
Gaus Width: 4.9163



B1L101S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



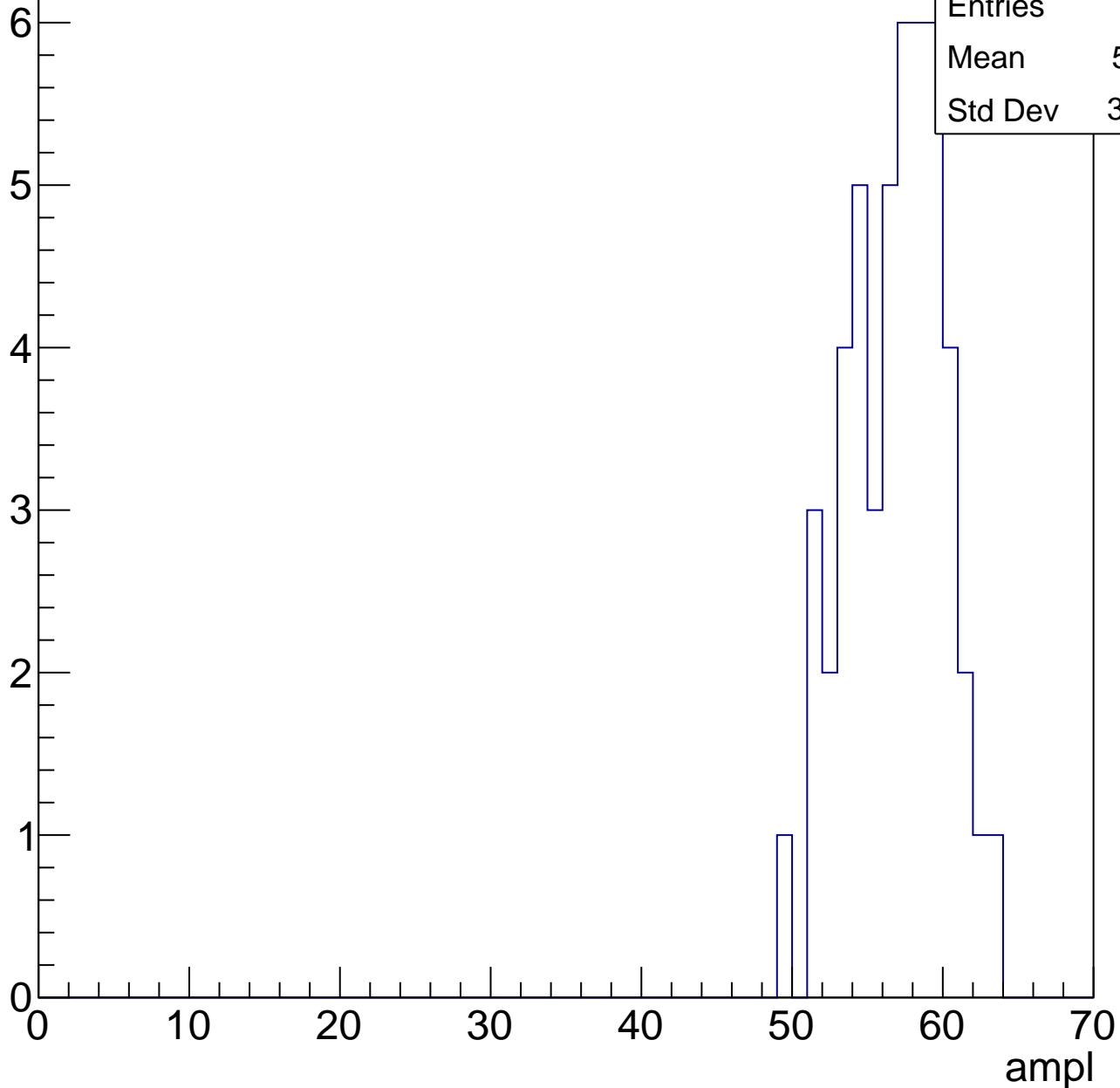
Entries	91
Mean	49.69
Std Dev	4.027

B1L101S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.41
Std Dev	3.162

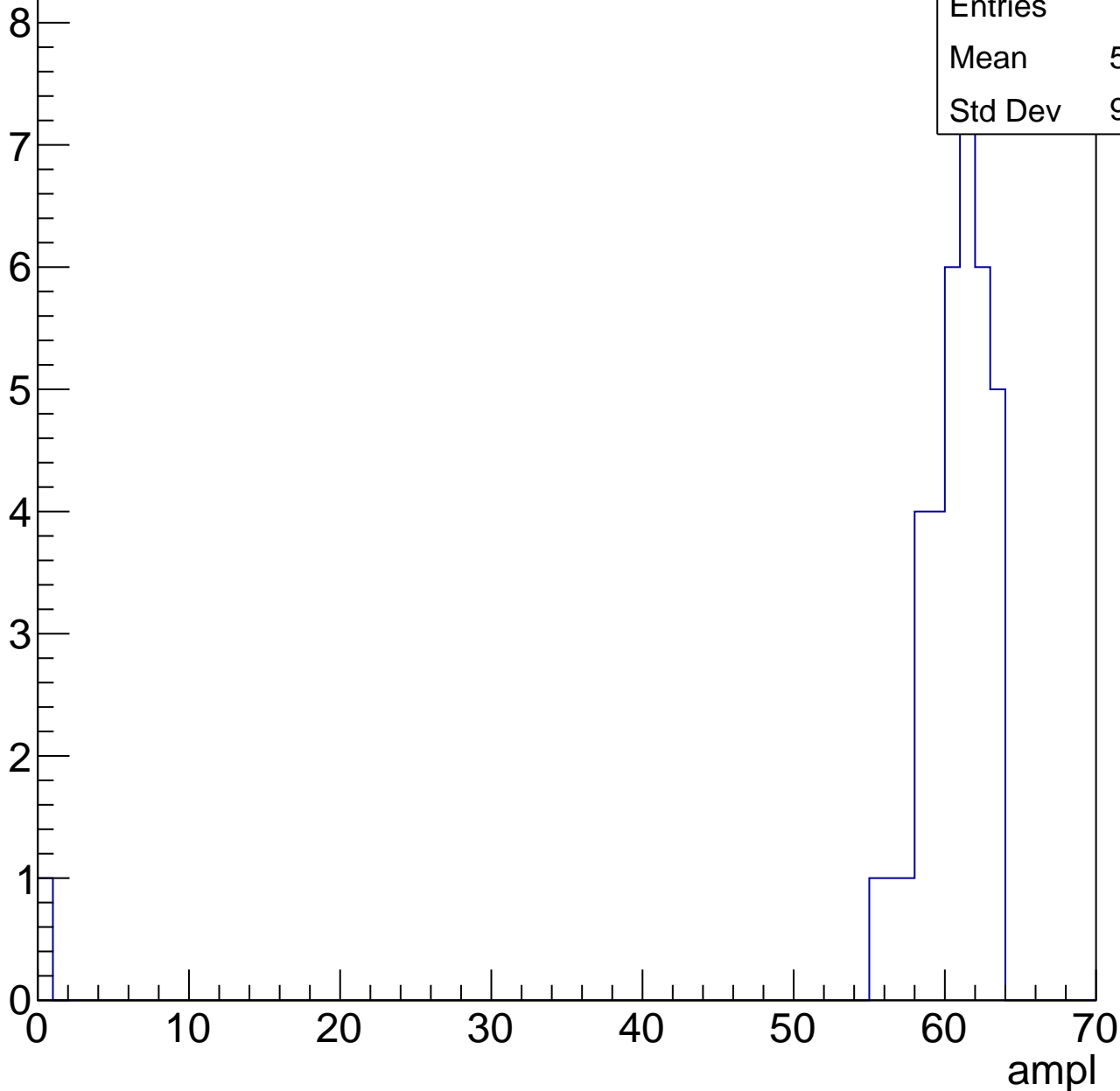


B1L101S, U14-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

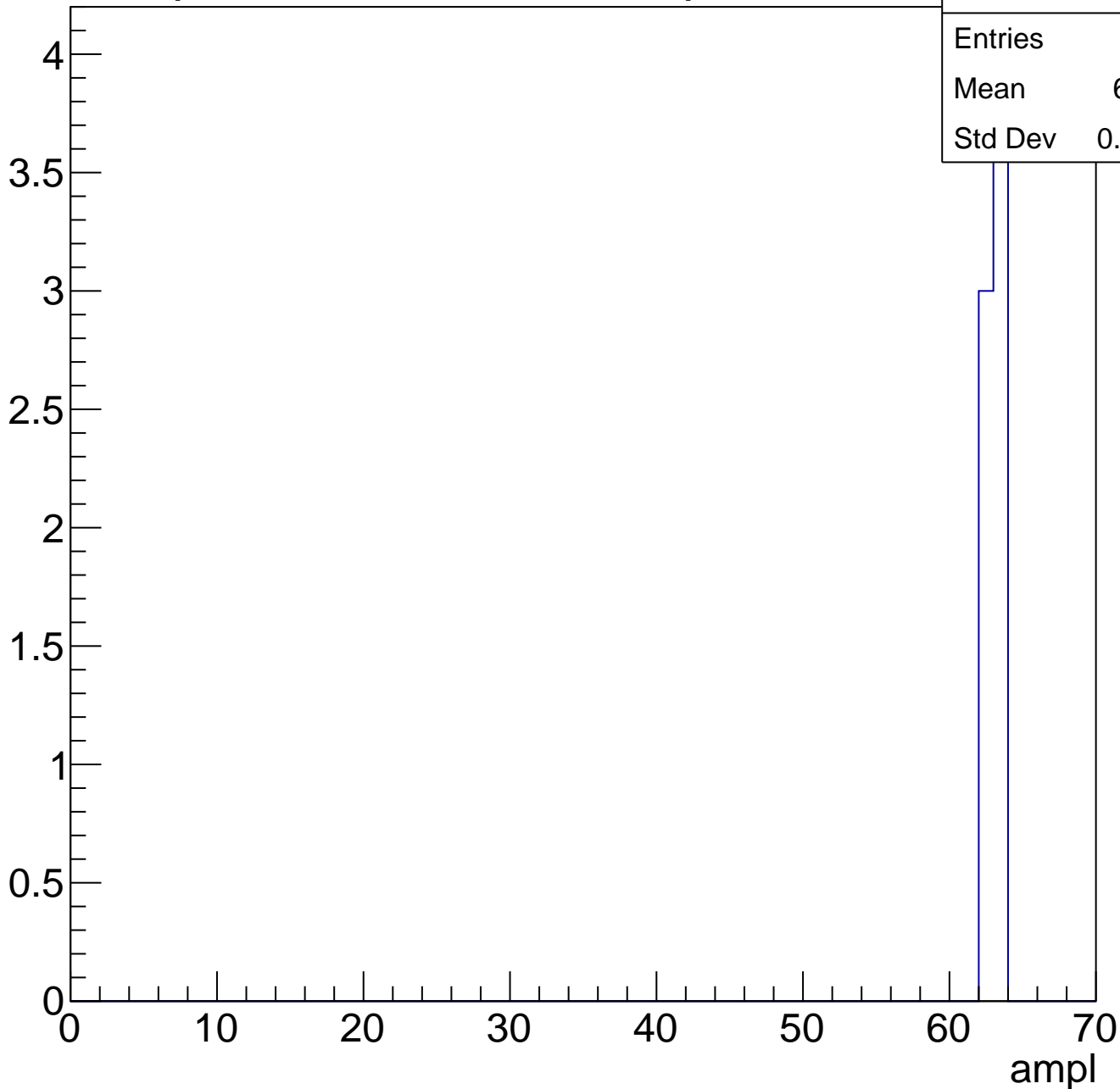
Entries	37
Mean	58.68
Std Dev	9.976



B1L101S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch4, adc7

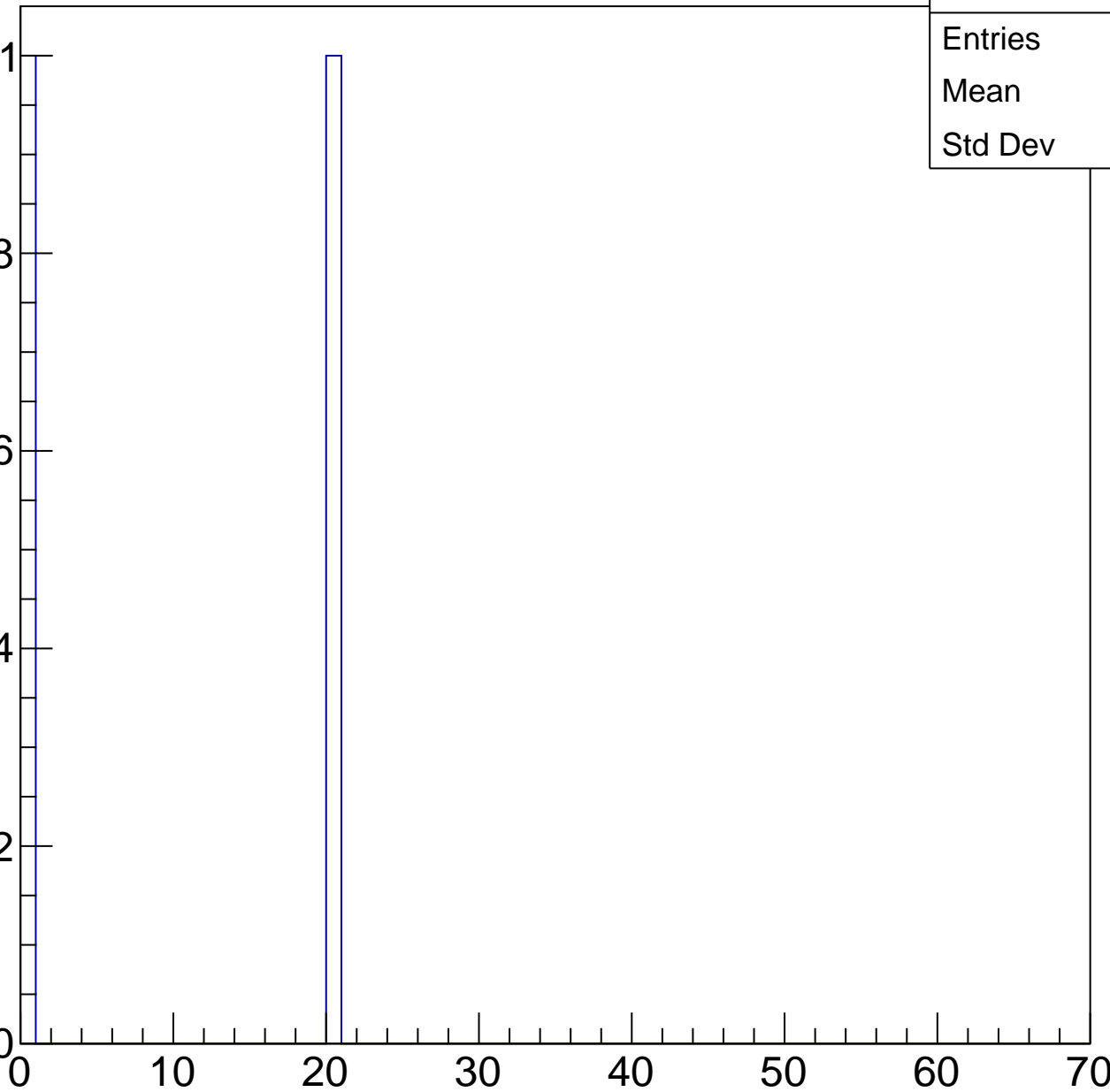
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B1L101S, U14-ch5, adc0

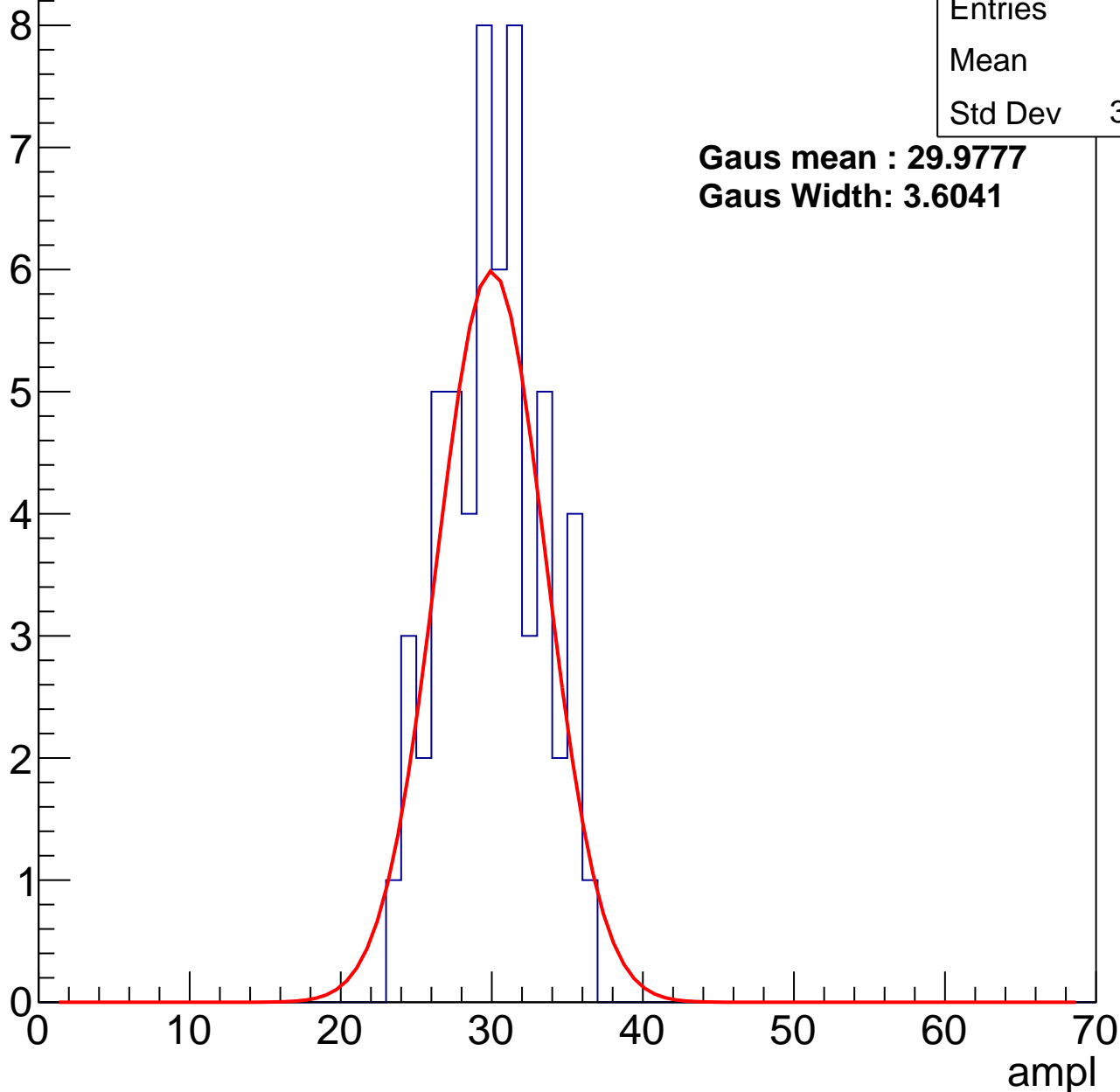
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	29.6
Std Dev	3.189

Gaus mean : 29.9777

Gaus Width: 3.6041



B1L101S, U14-ch5, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	37.18
Std Dev	4.441

Gaus mean : 38.0988

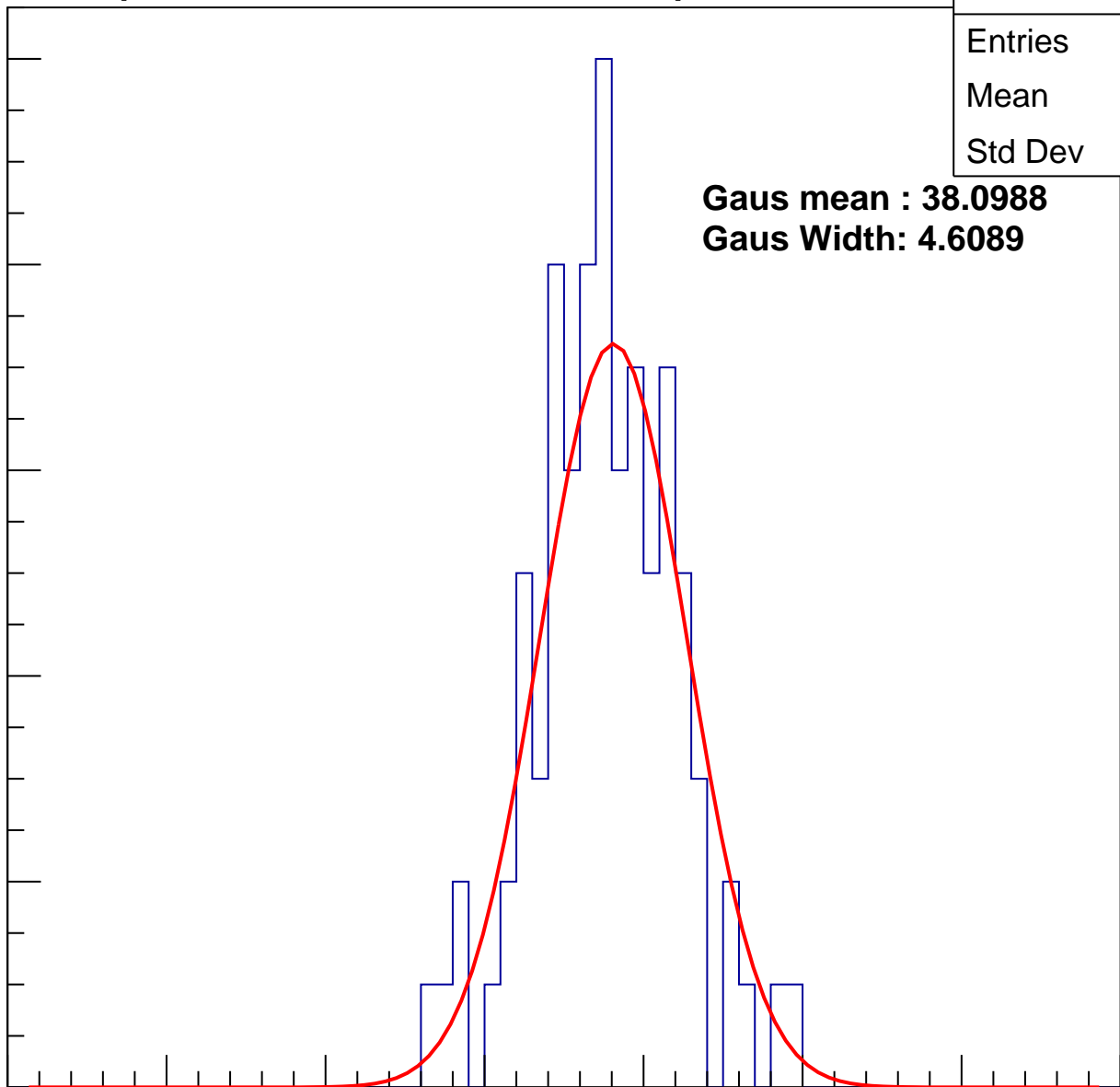
Gaus Width: 4.6089

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch5, adc2

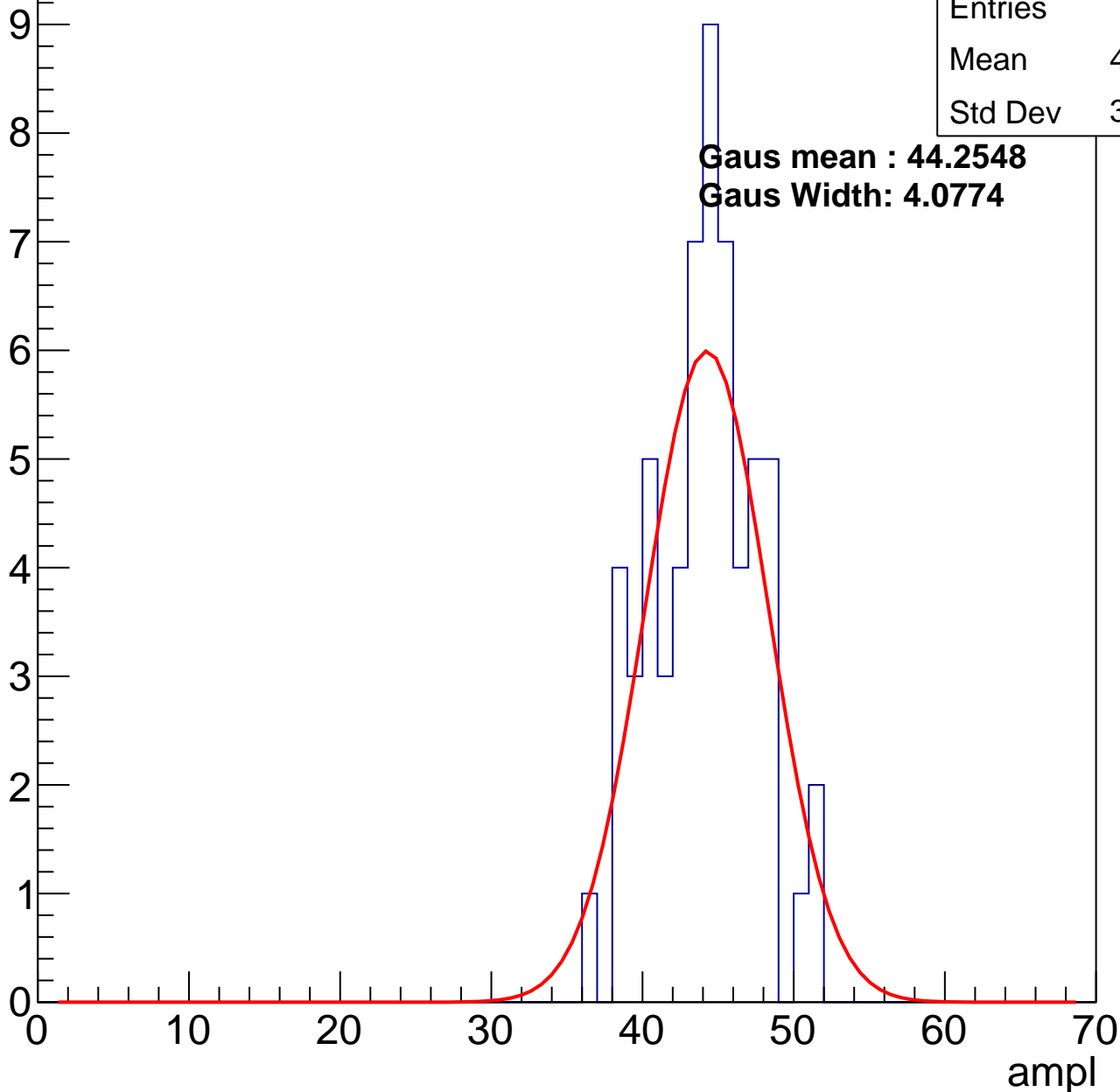
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.65
Std Dev	3.385

Gaus mean : 44.2548

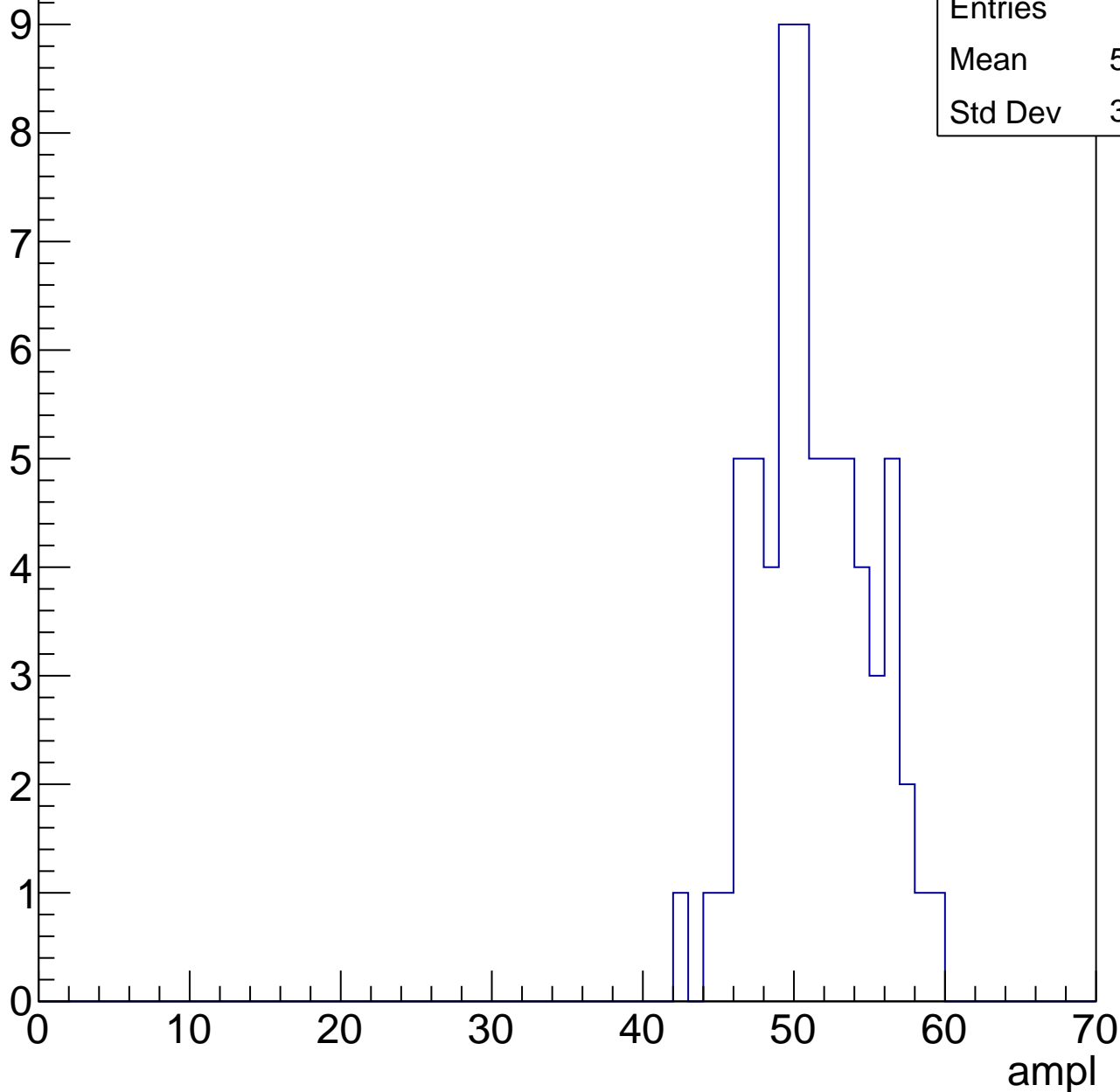
Gaus Width: 4.0774



B1L101S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

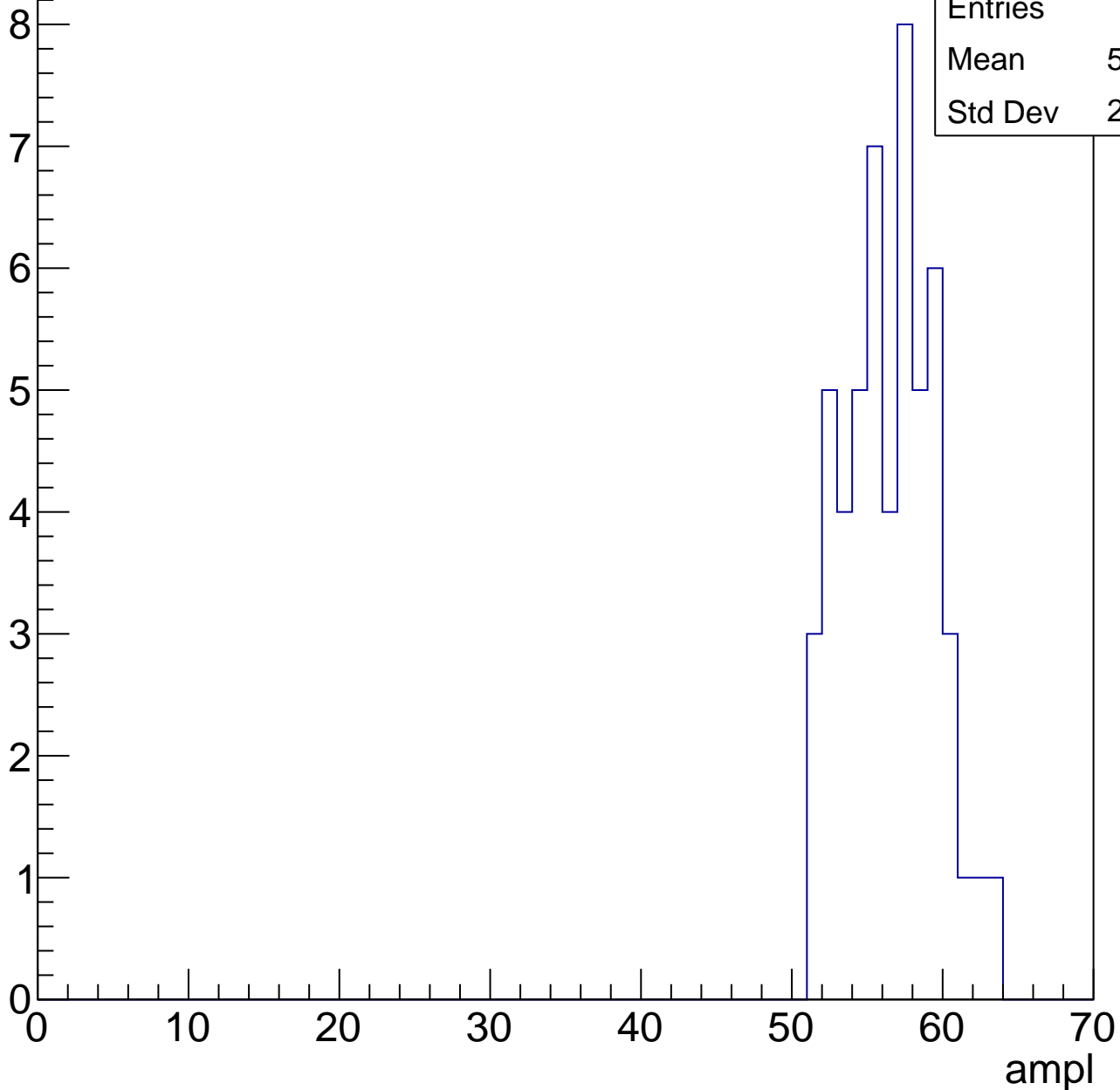


B1L101S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.04
Std Dev	2.939

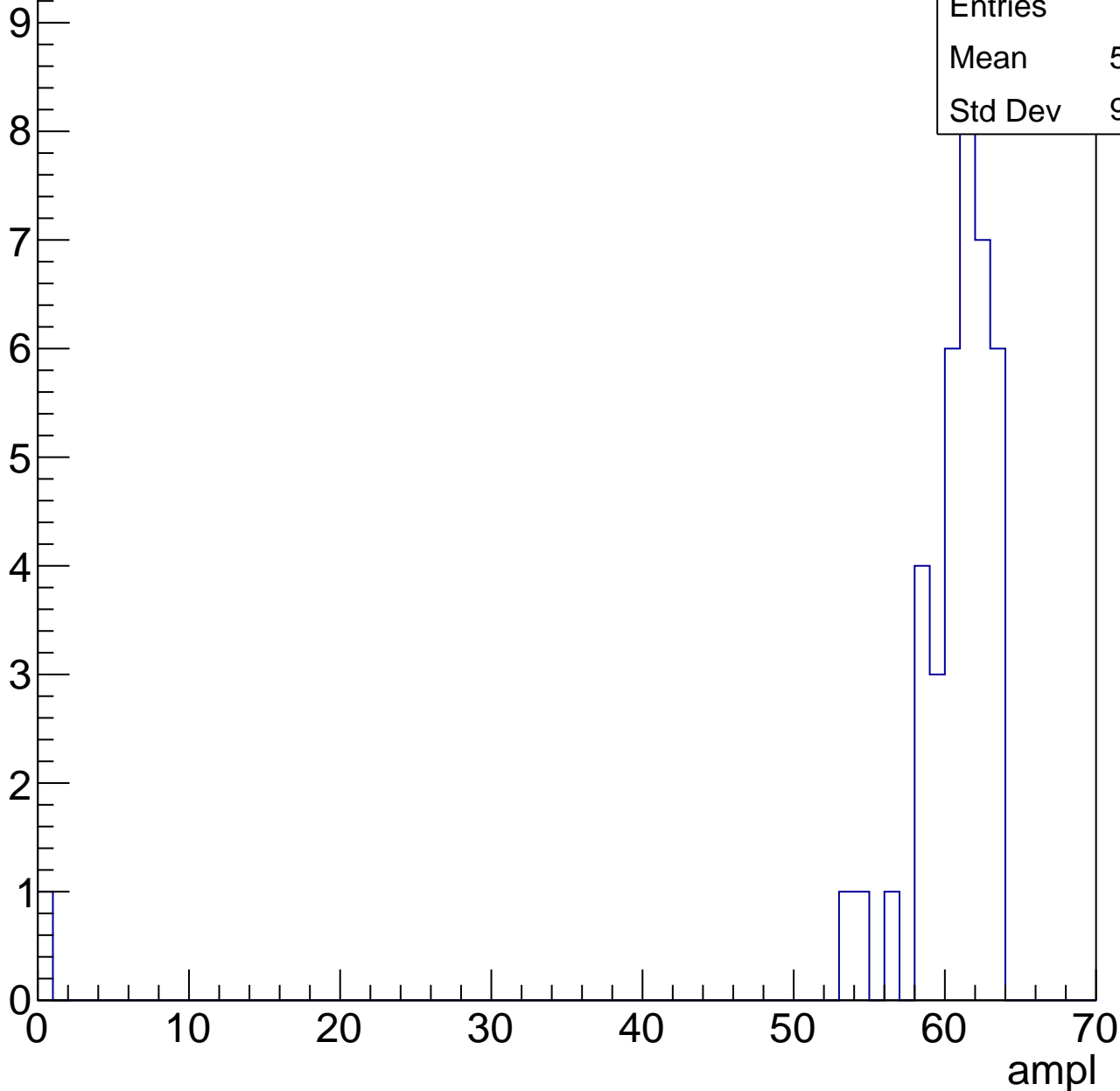


B1L101S, U14-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.79
Std Dev	9.812



B1L101S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

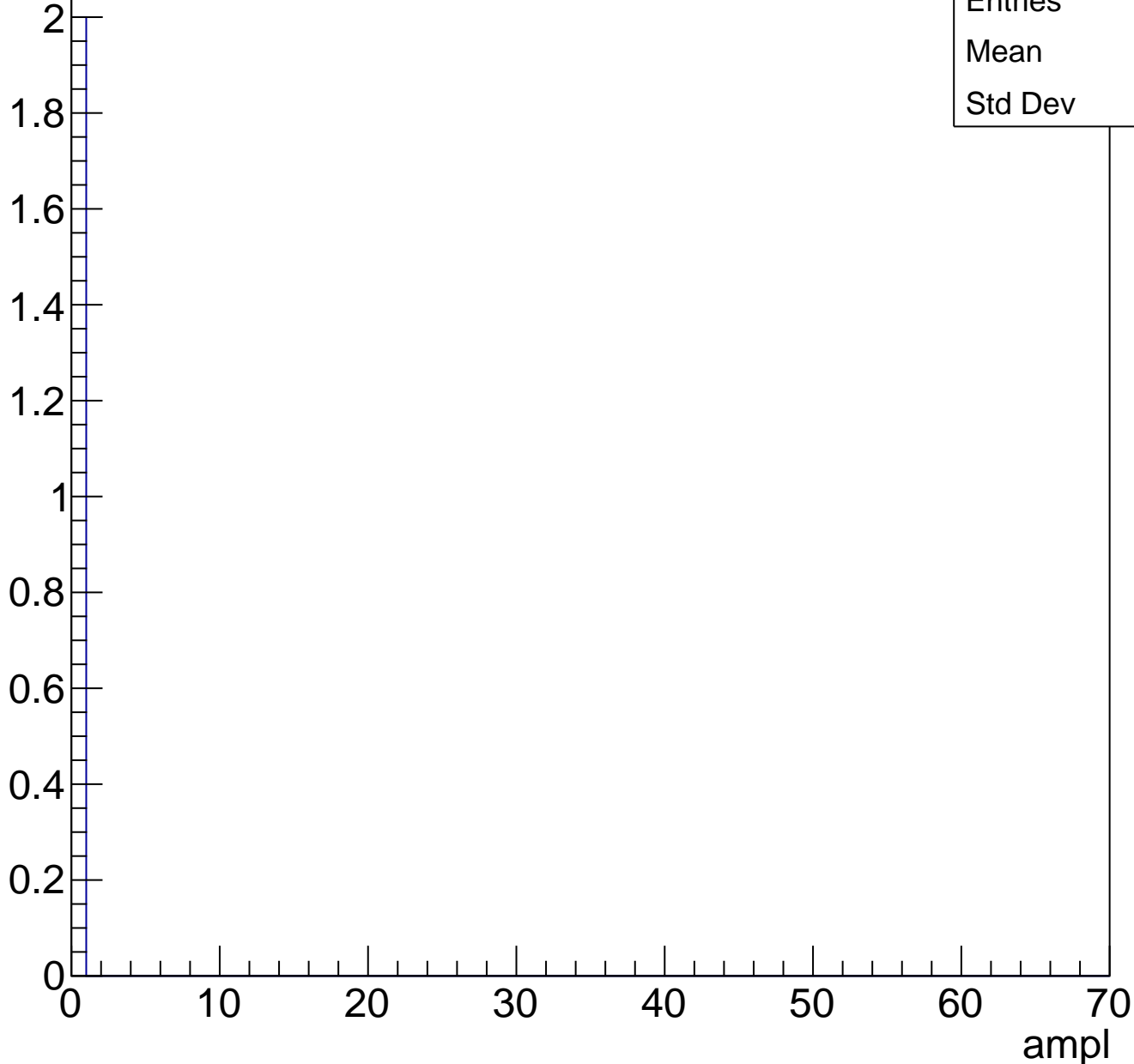
Entry



B1L101S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch6, adc0

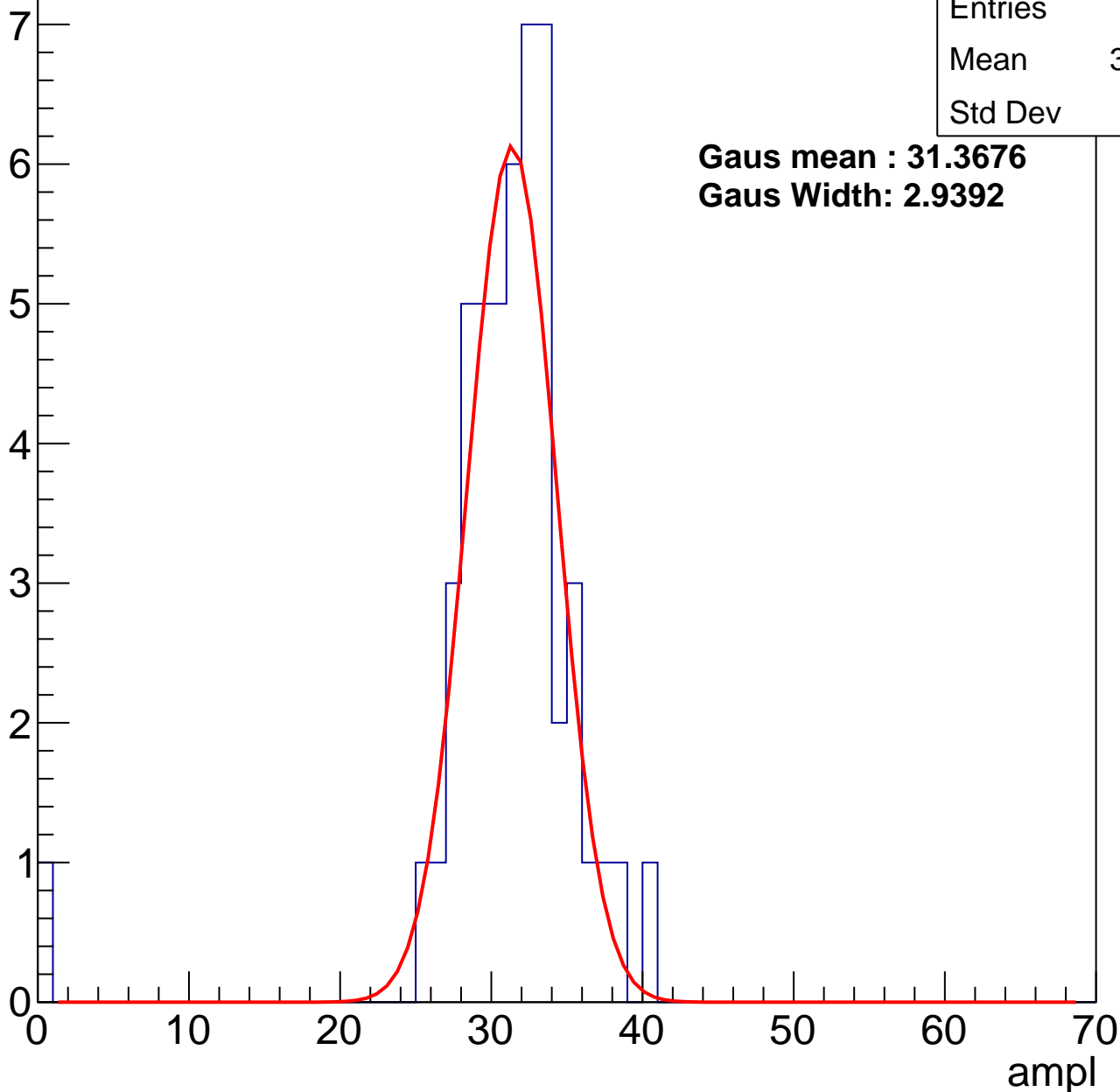
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	30.64
Std Dev	5.34

Gaus mean : 31.3676

Gaus Width: 2.9392



B1L101S, U14-ch6, adc1

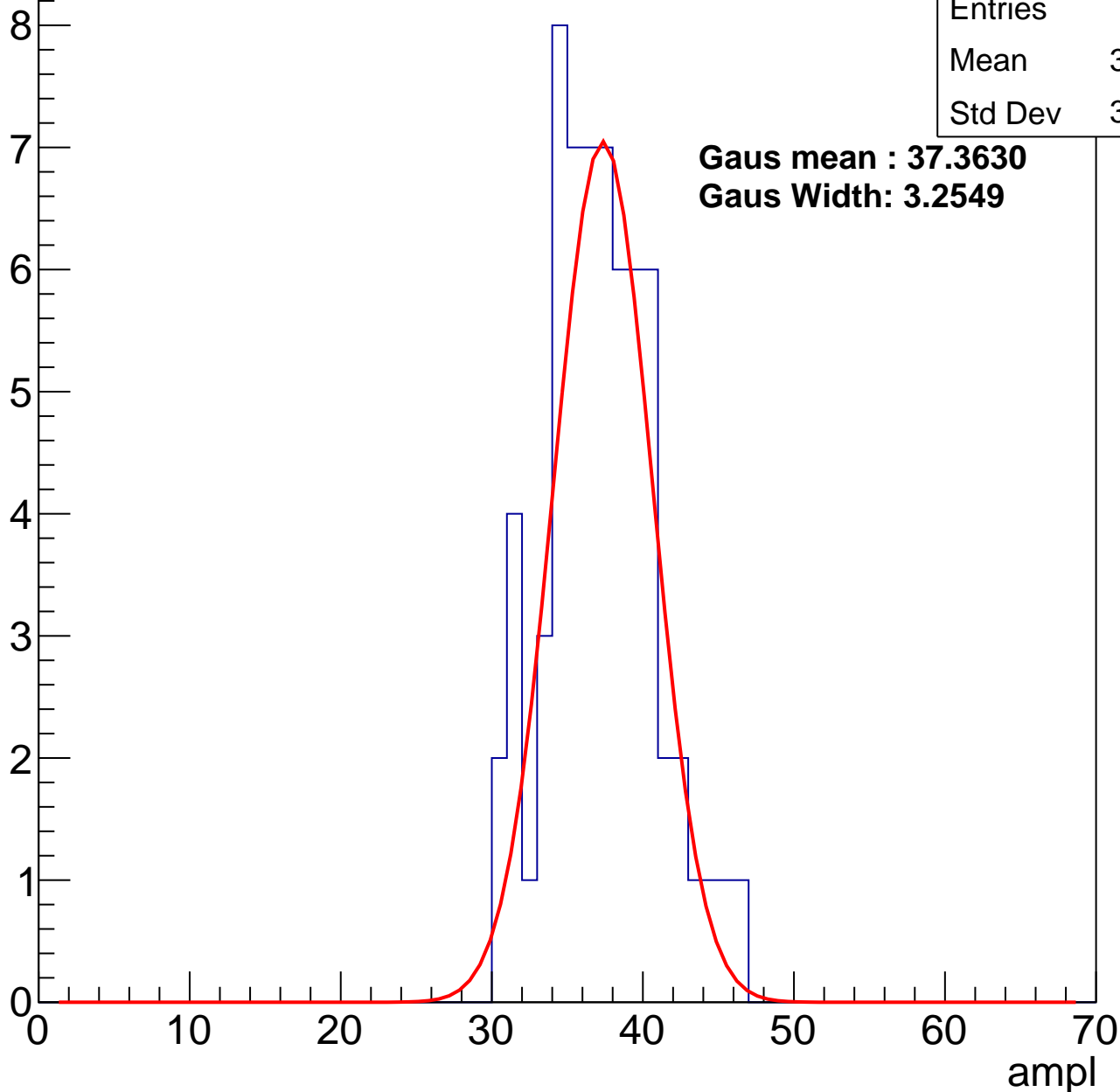
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.75
Std Dev	3.539

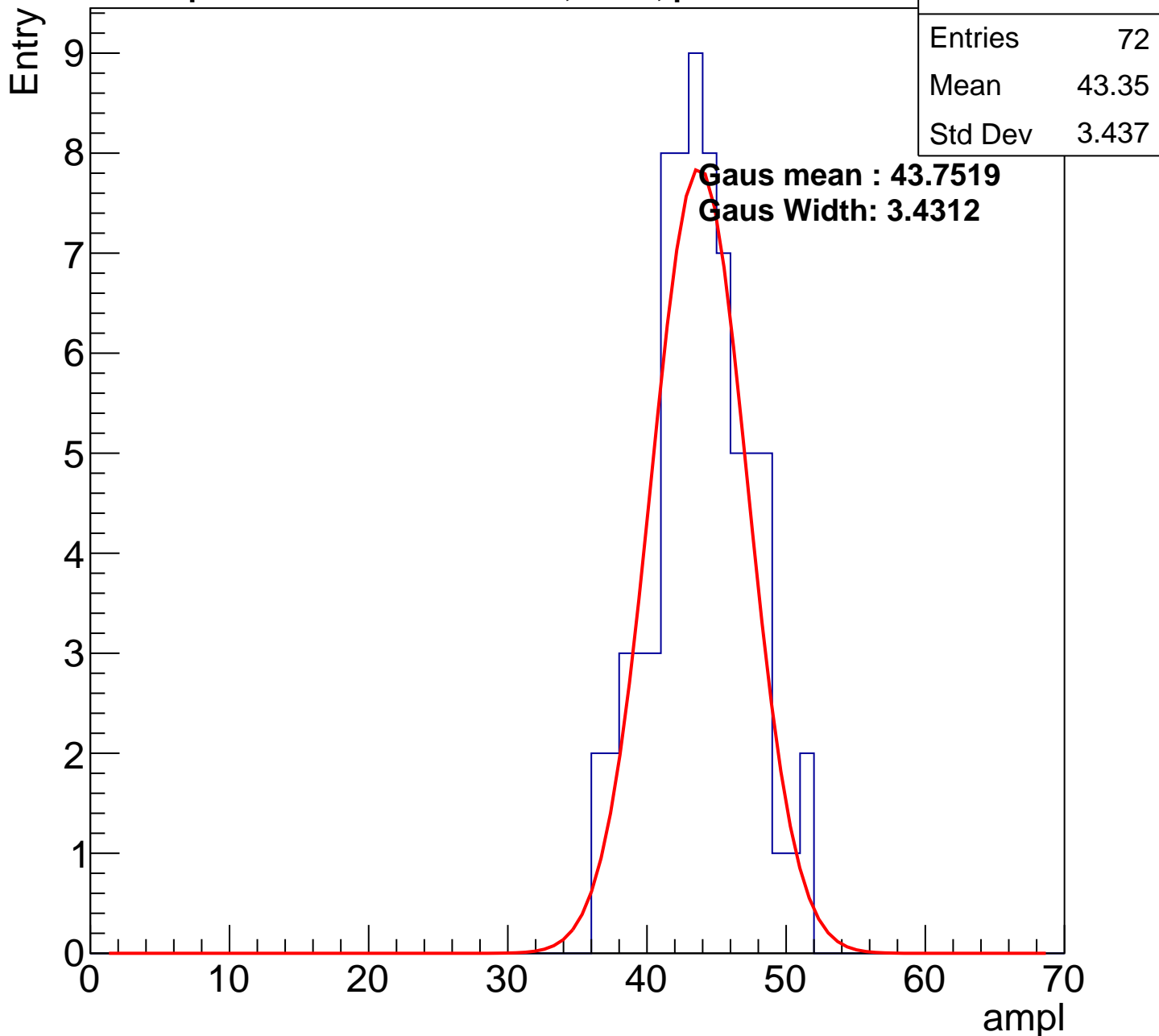
Gaus mean : 37.3630

Gaus Width: 3.2549



B1L101S, U14-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2

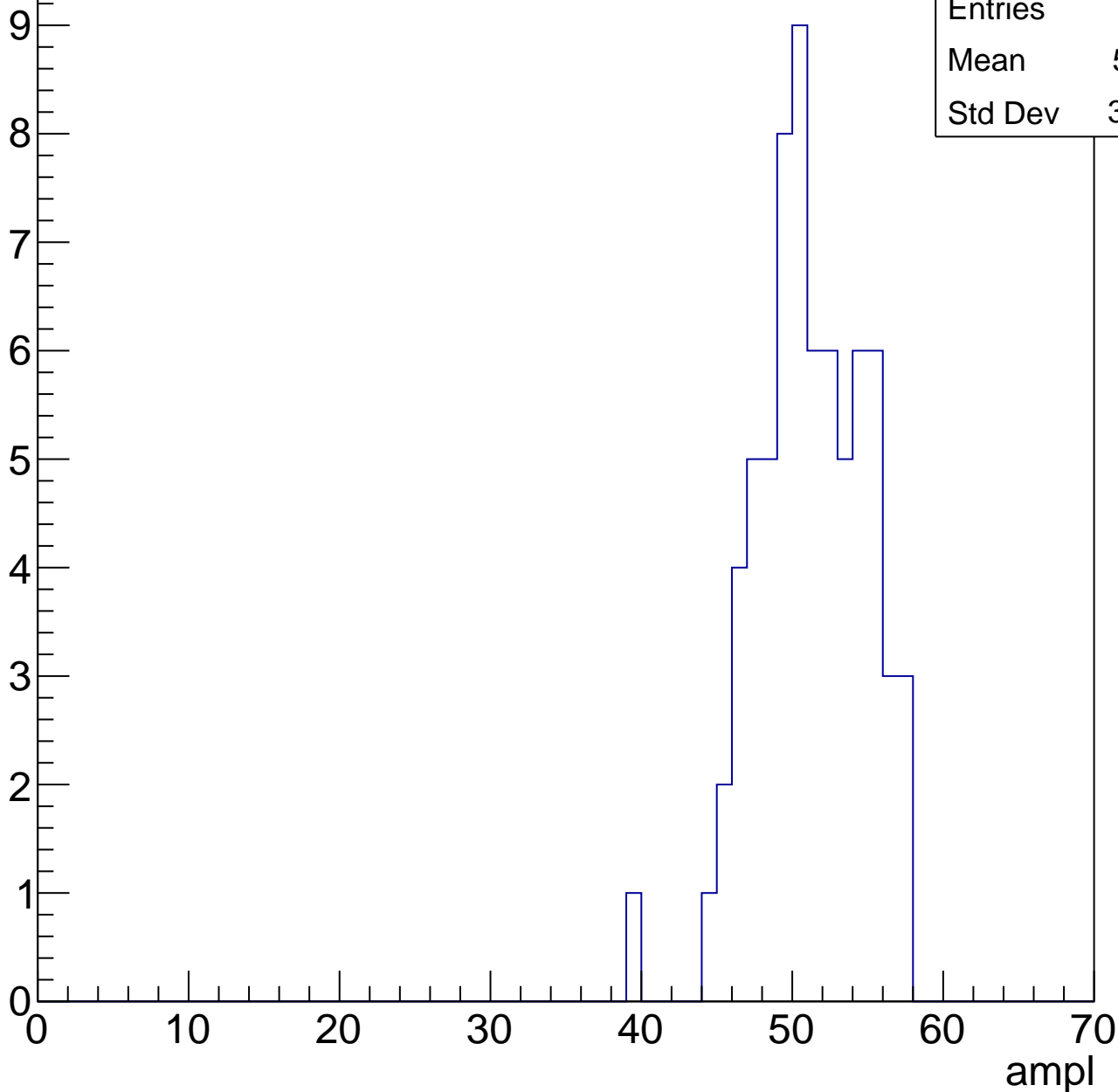


B1L101S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.71
Std Dev	3.554

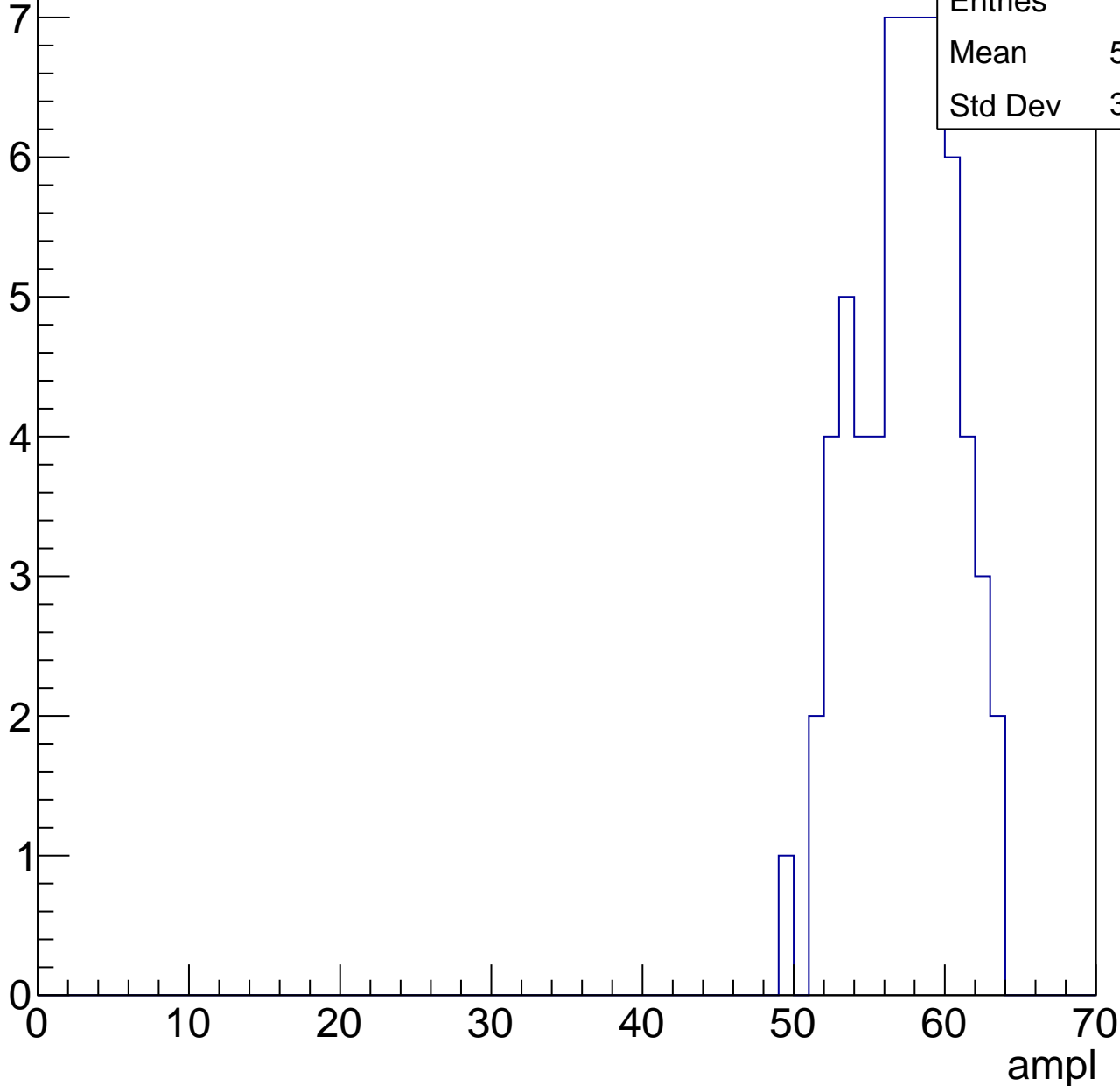


B1L101S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	56.92
Std Dev	3.272

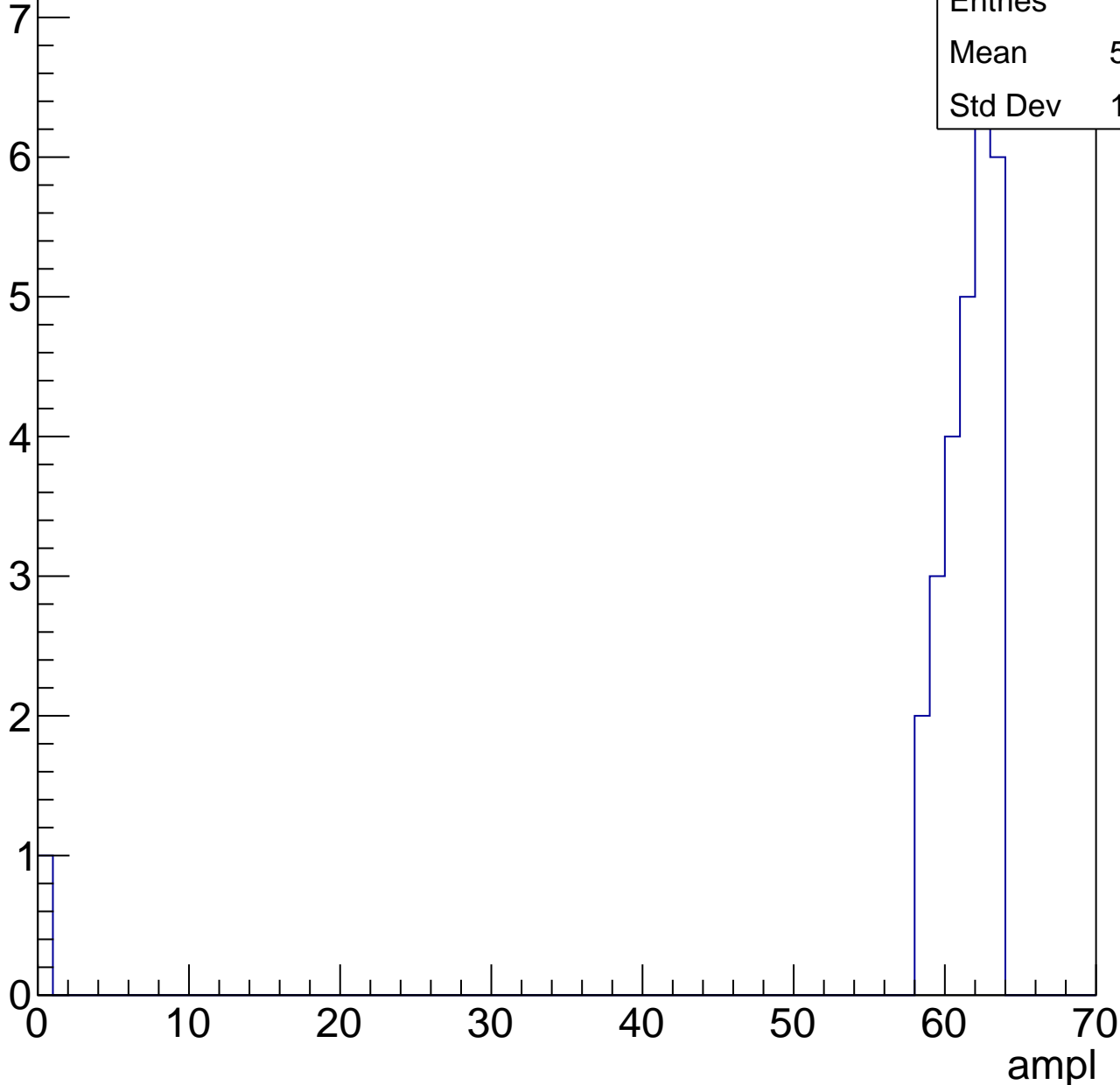


B1L101S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

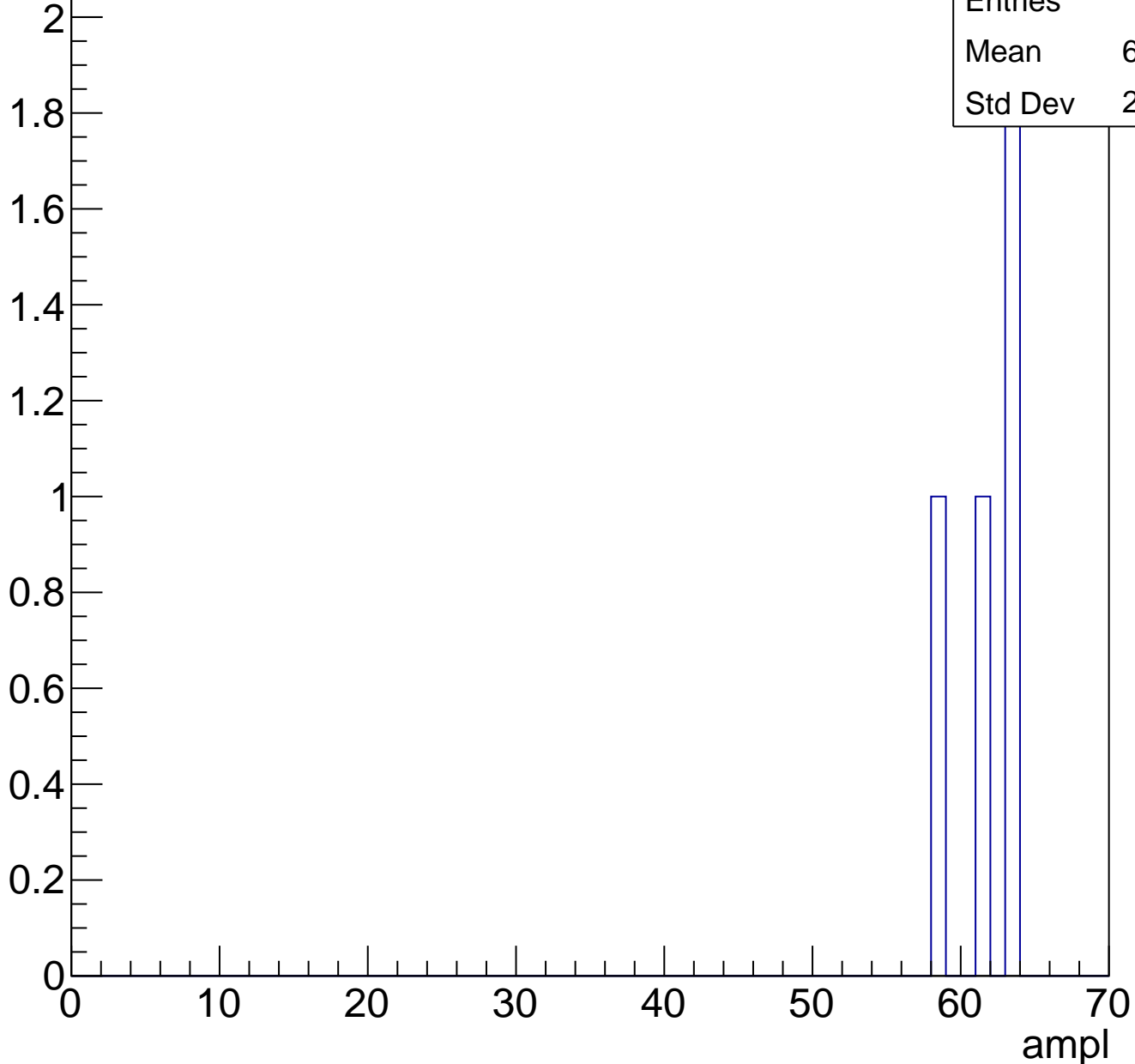
Entries	28
Mean	58.93
Std Dev	11.44



B1L101S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L101S, U14-ch7, adc0

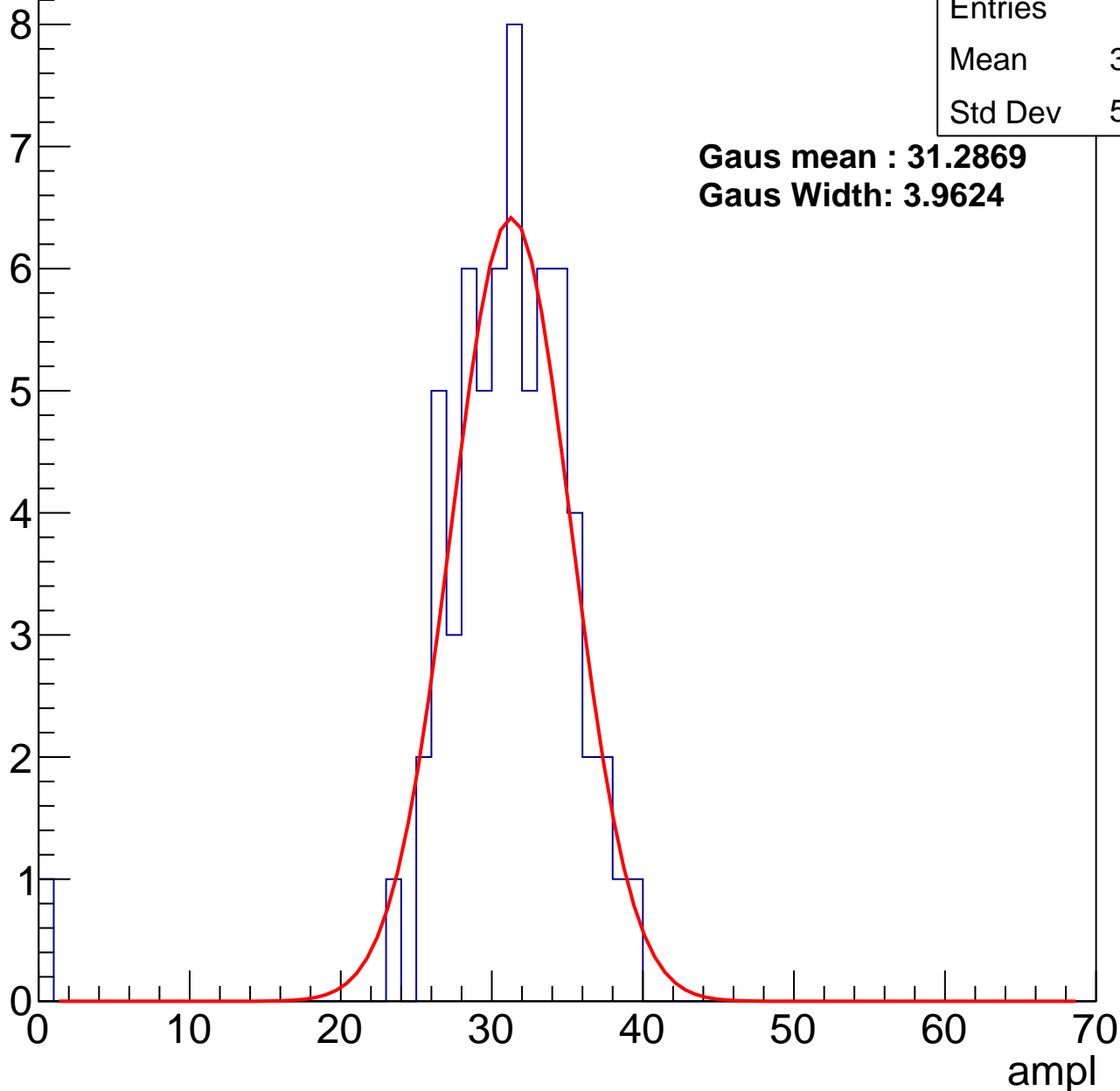
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.47
Std Dev	5.175

Gaus mean : 31.2869

Gaus Width: 3.9624



B1L101S, U14-ch7, adc1

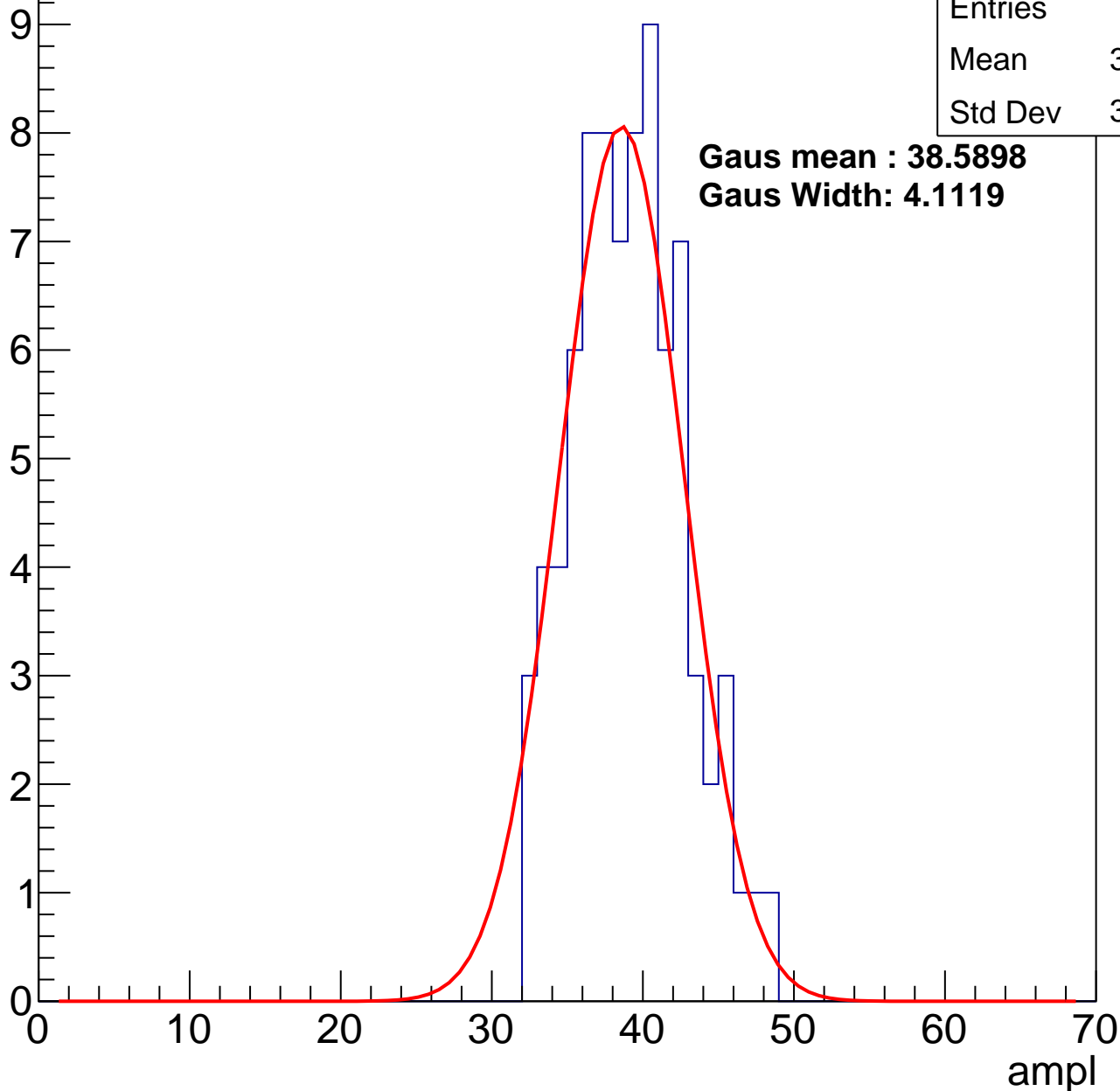
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	38.63
Std Dev	3.656

Gaus mean : 38.5898

Gaus Width: 4.1119



B1L101S, U14-ch7, adc2

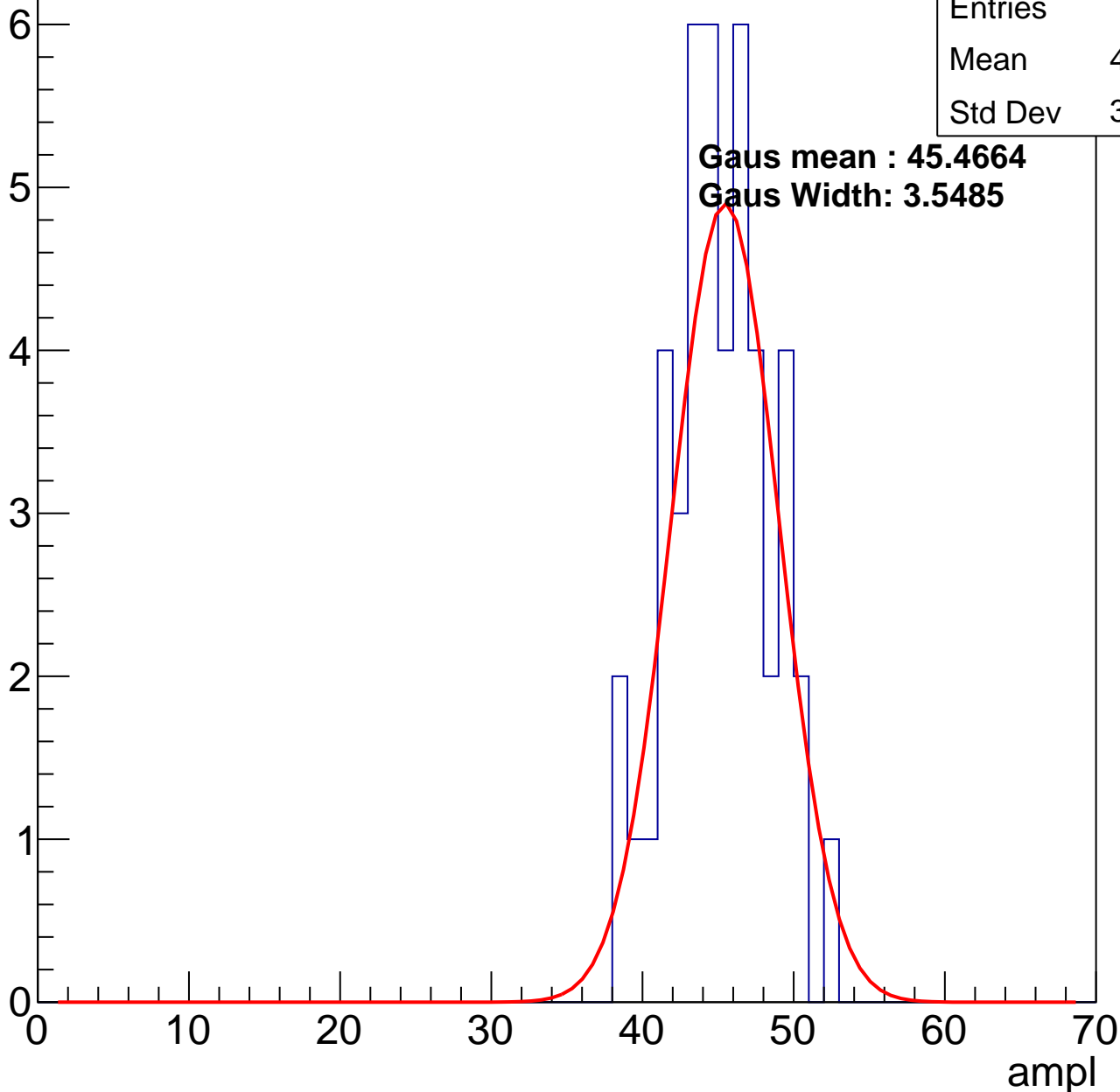
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	44.67
Std Dev	3.224

Gaus mean : 45.4664

Gaus Width: 3.5485

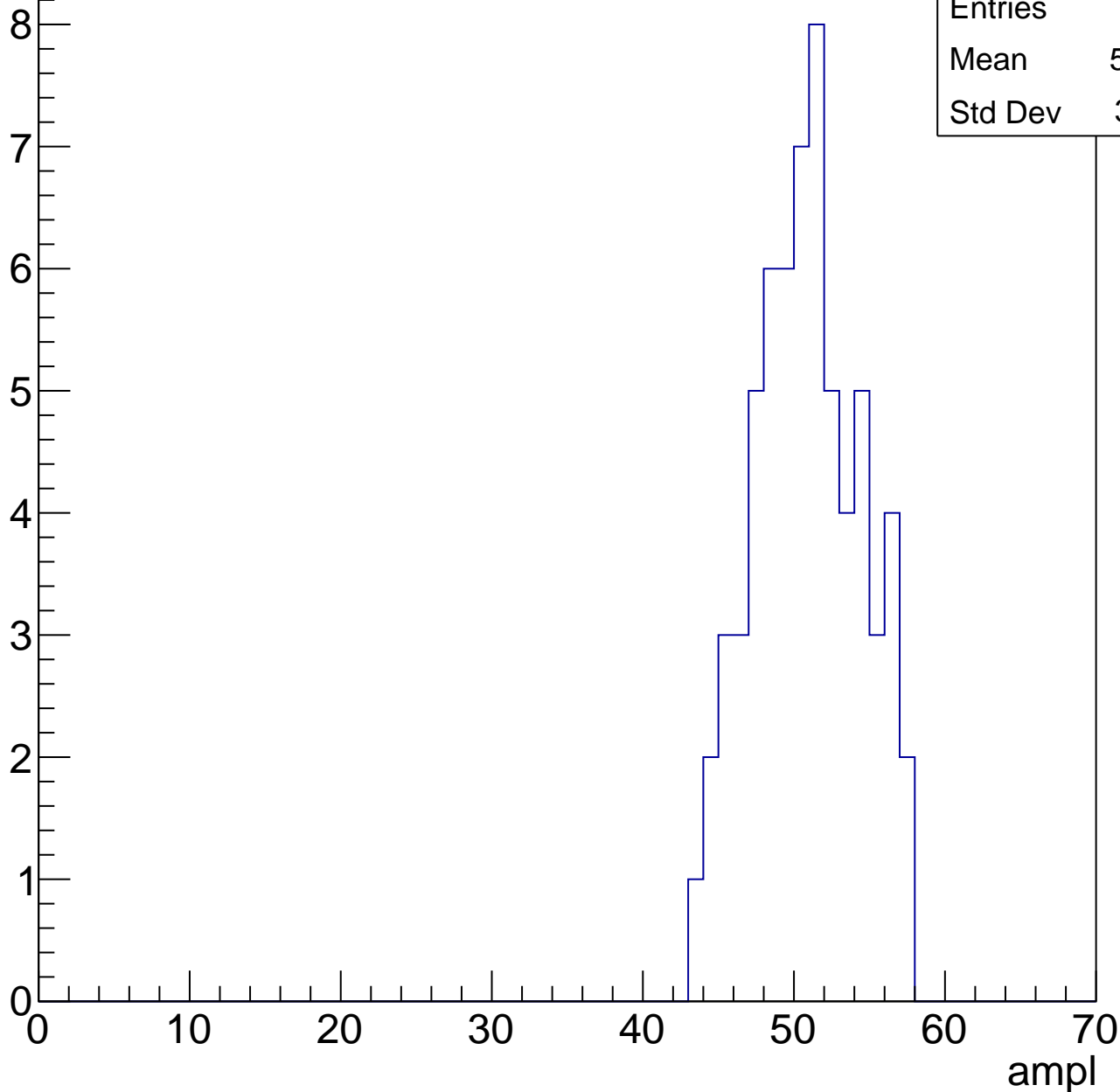


B1L101S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.38
Std Dev	3.471

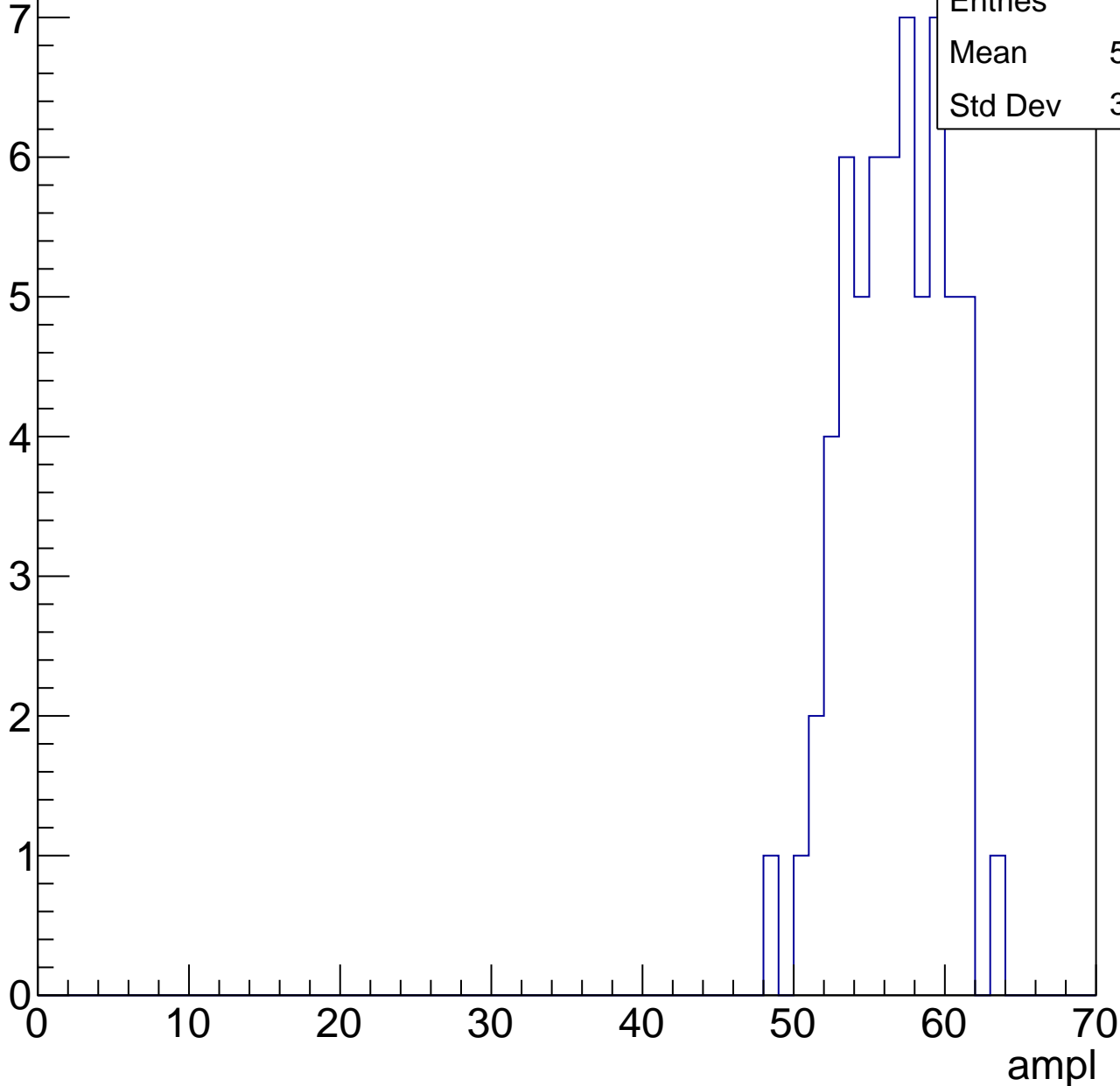


B1L101S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.26
Std Dev	3.223

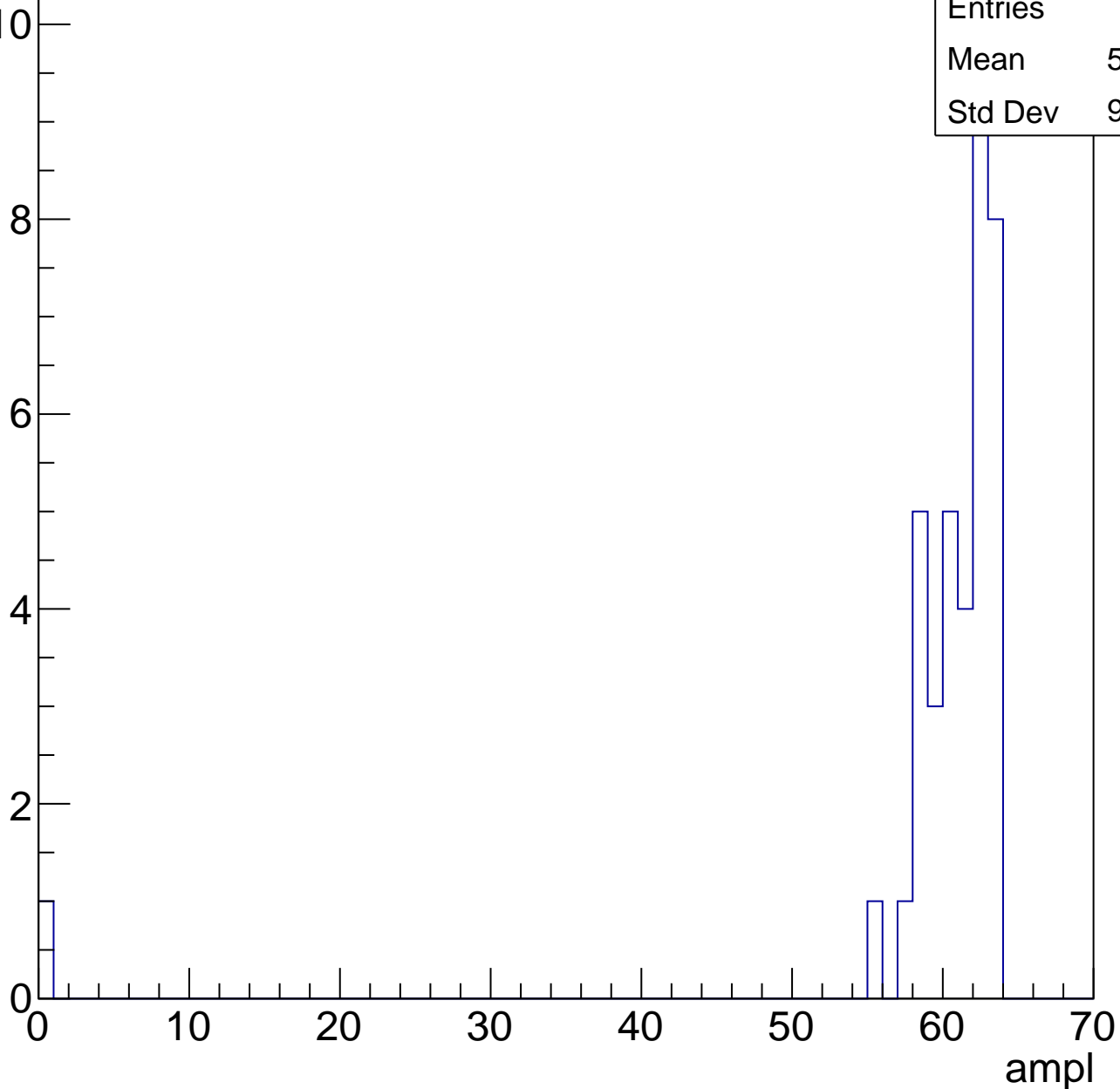


B1L101S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59.13
Std Dev	9.926



B1L101S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch8, adc0

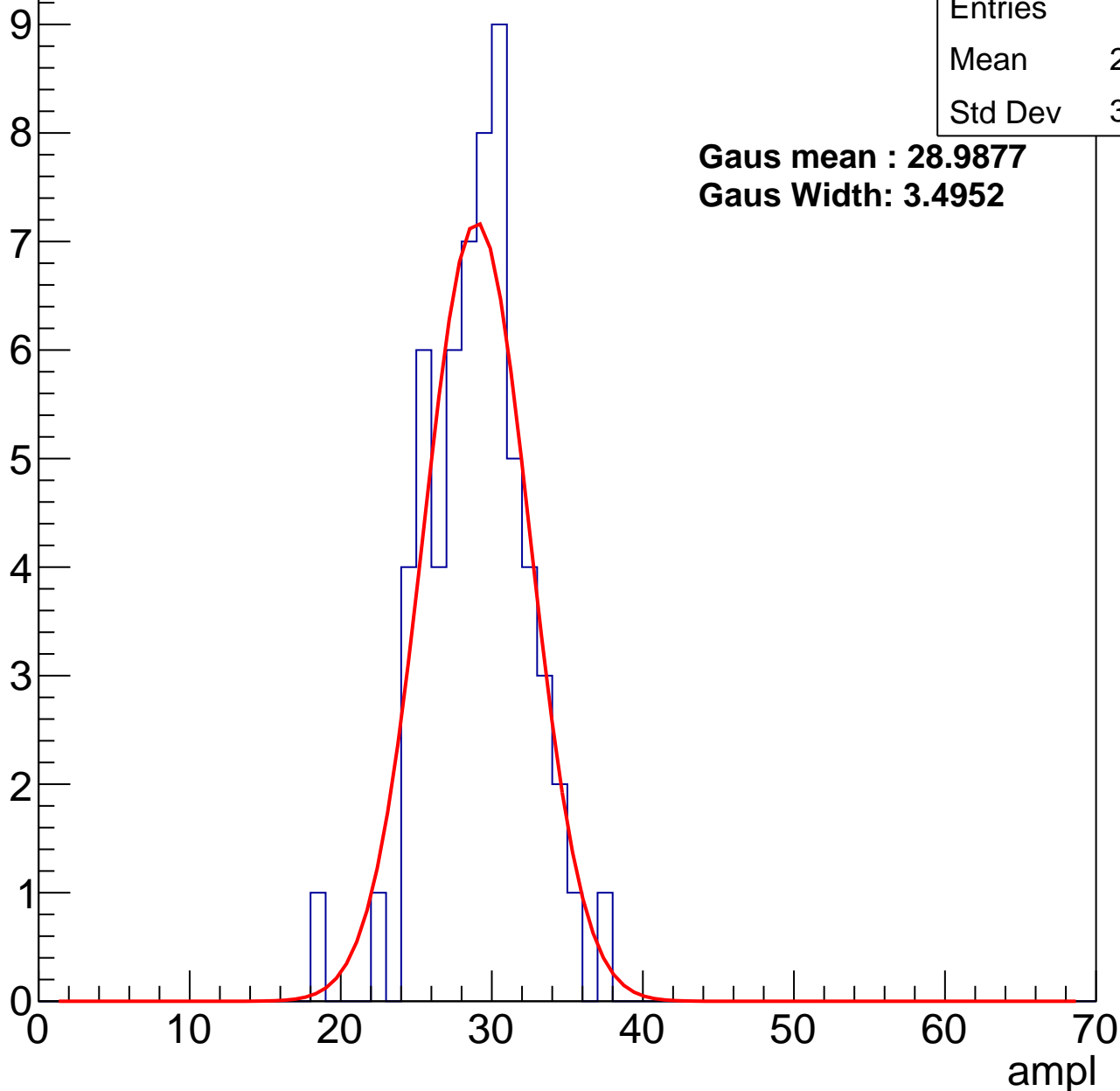
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.58
Std Dev	3.324

Gaus mean : 28.9877

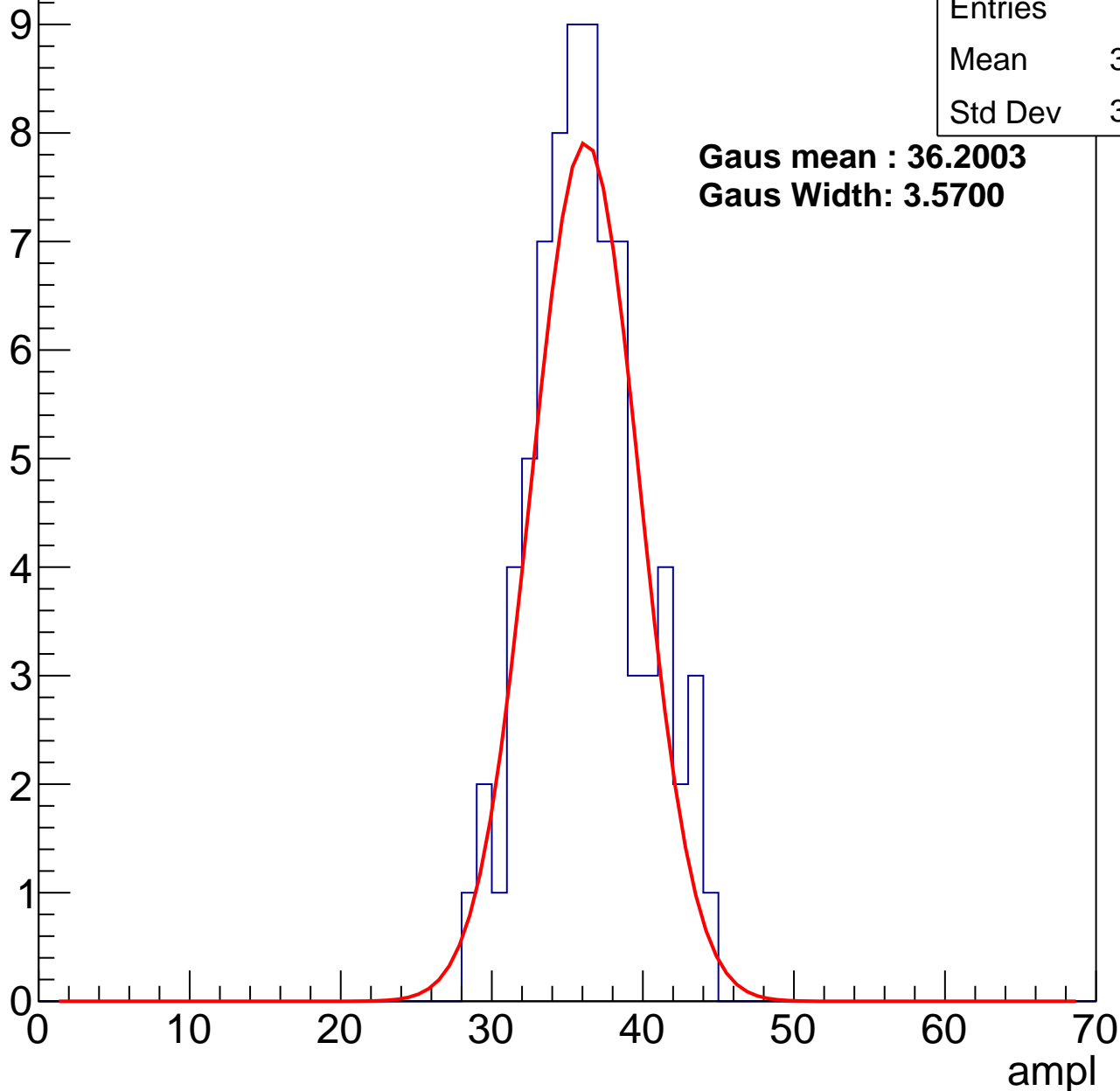
Gaus Width: 3.4952



B1L101S, U14-ch8, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch8, adc2

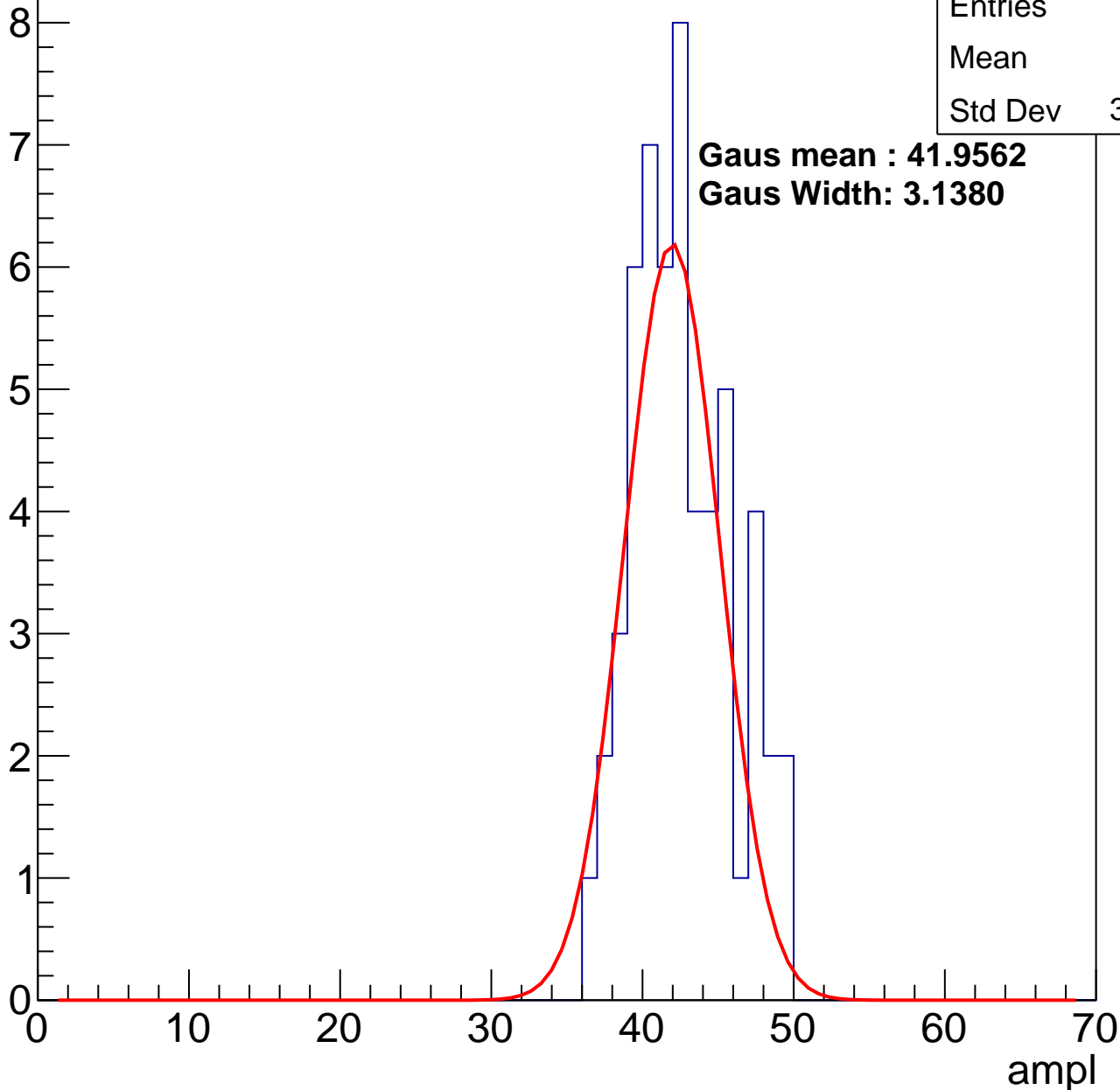
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.2
Std Dev	3.222

Gaus mean : 41.9562

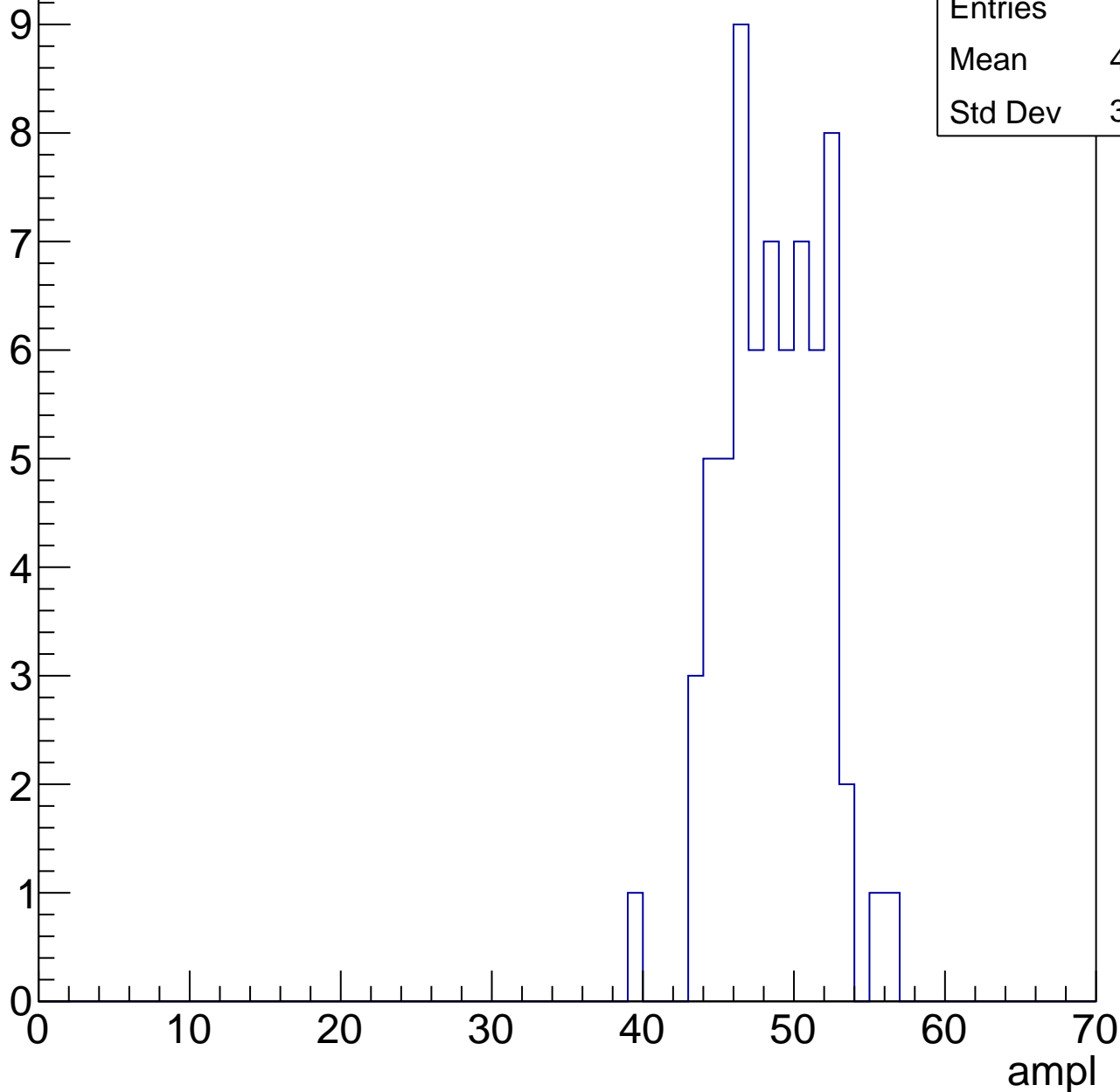
Gaus Width: 3.1380



B1L101S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

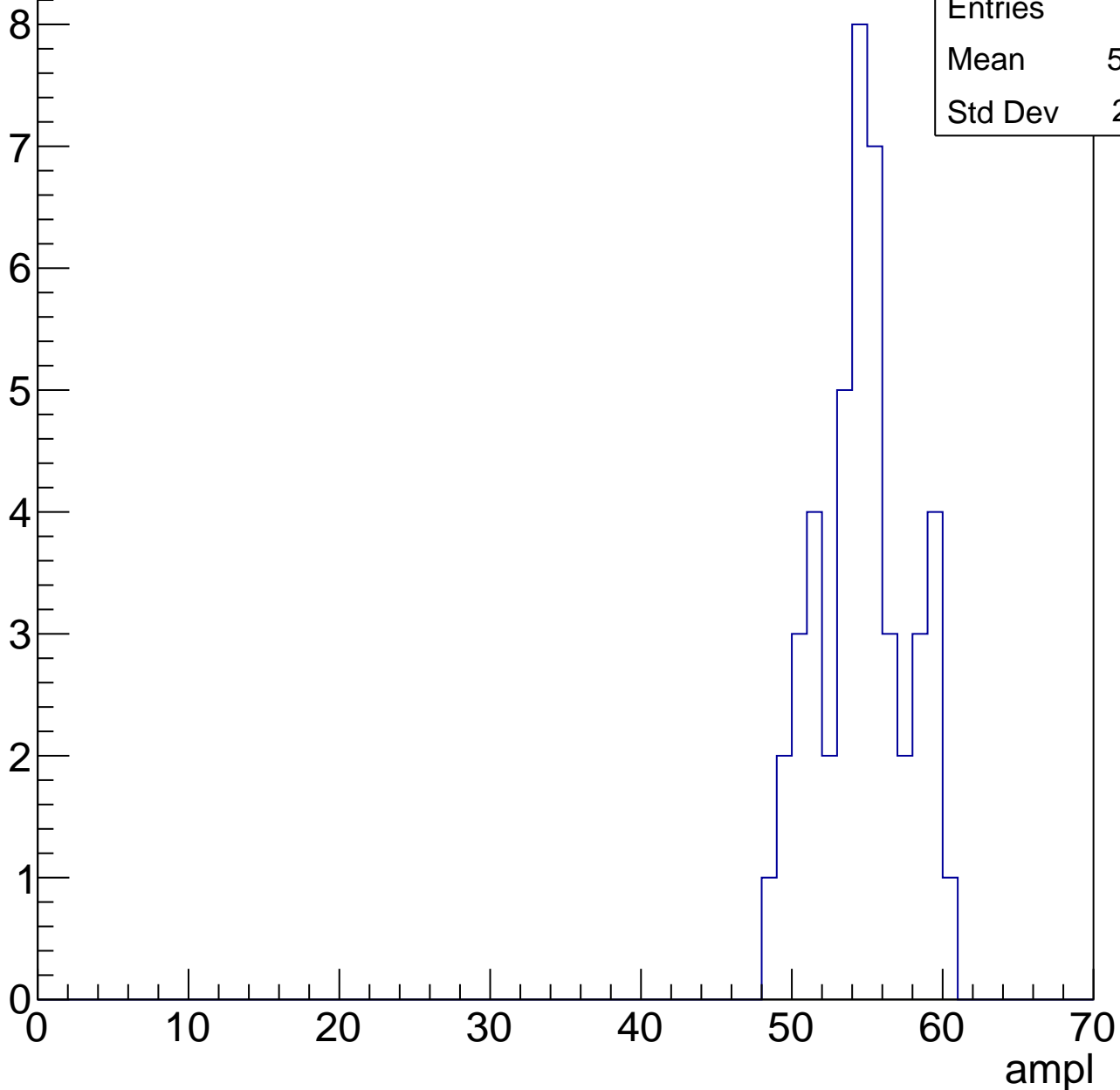


B1L101S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	54.18
Std Dev	2.991

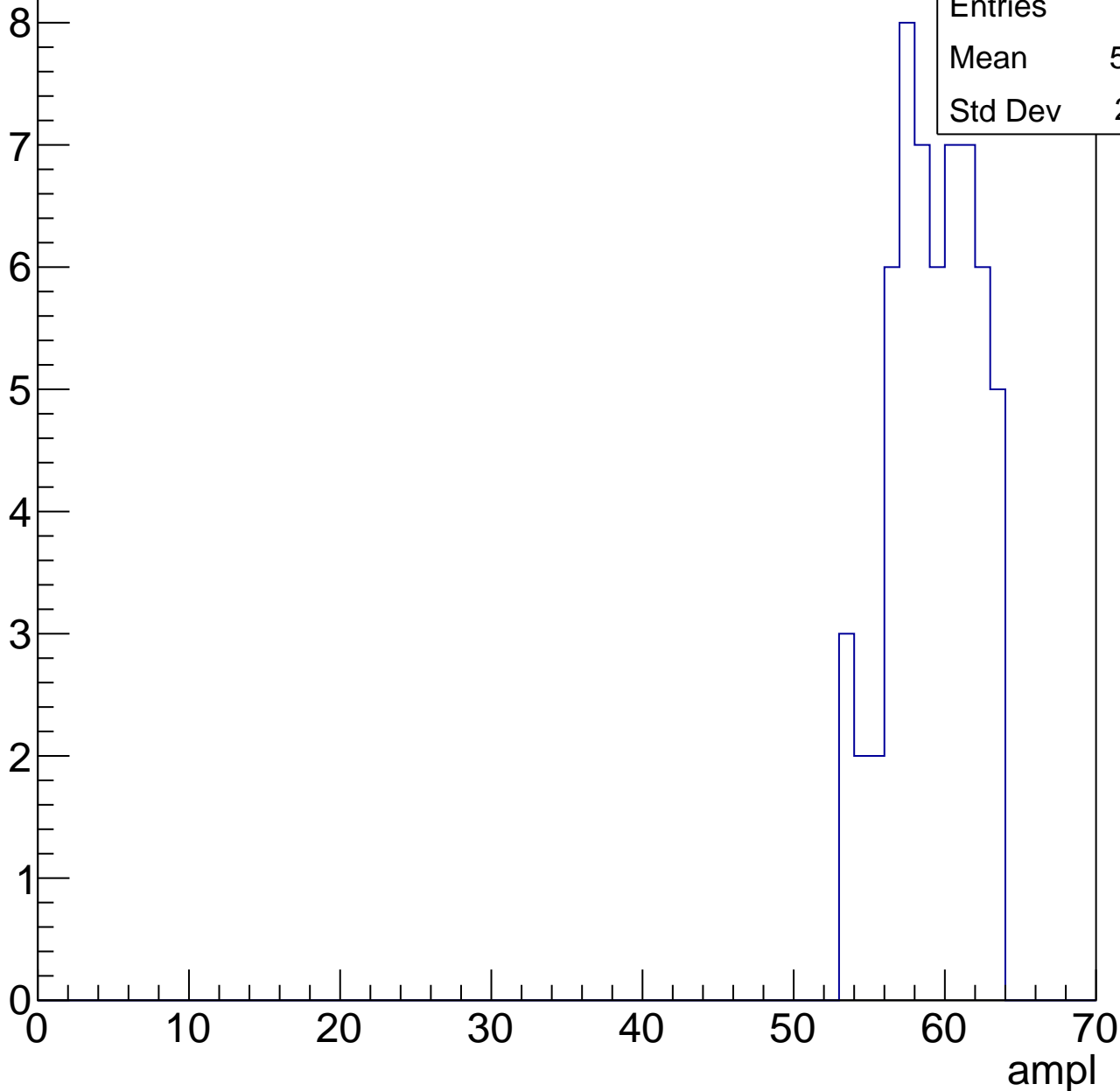


B1L101S, U14-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	58.69
Std Dev	2.751

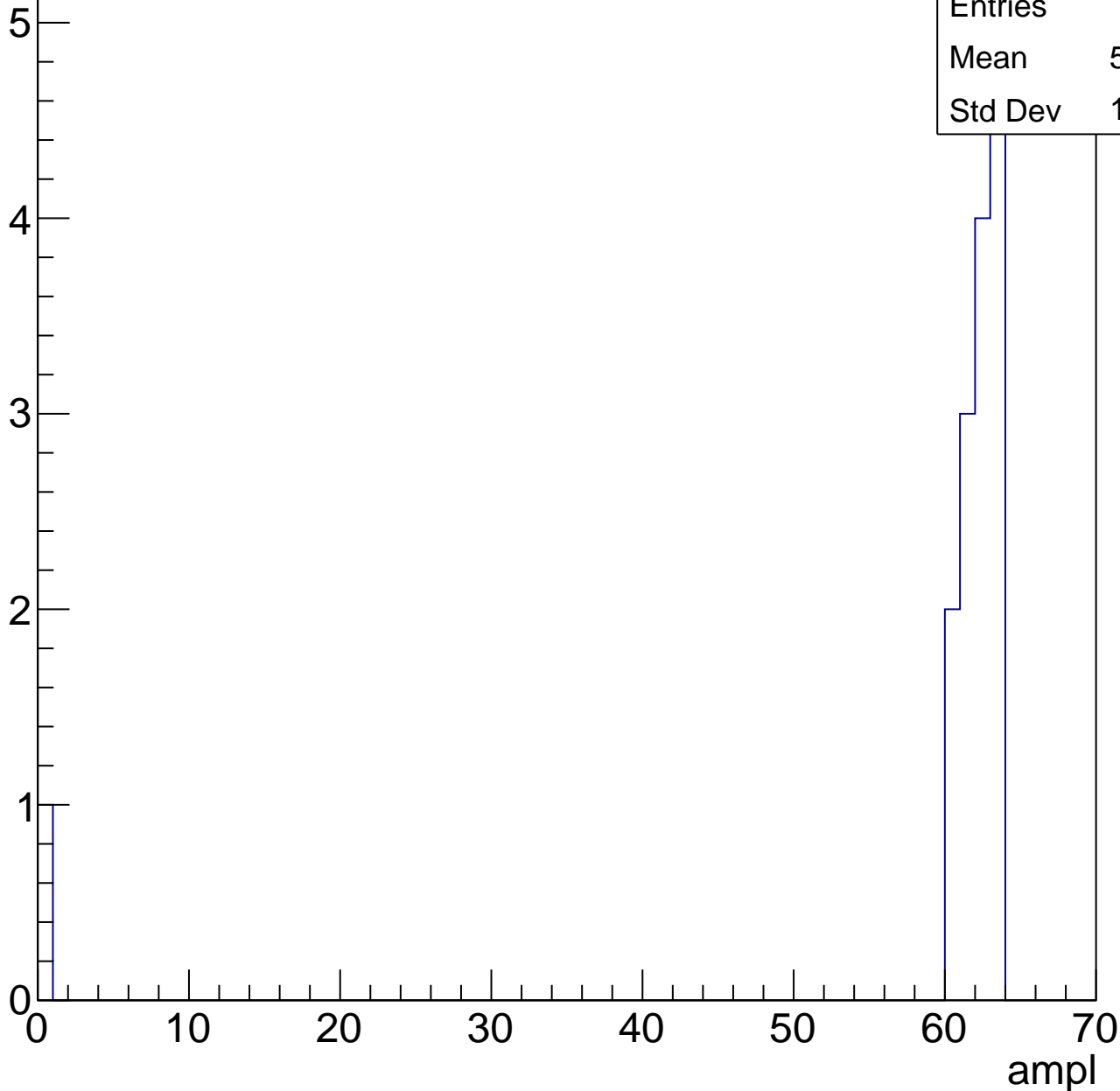


B1L101S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.73
Std Dev	15.46



B1L101S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch9, adc0

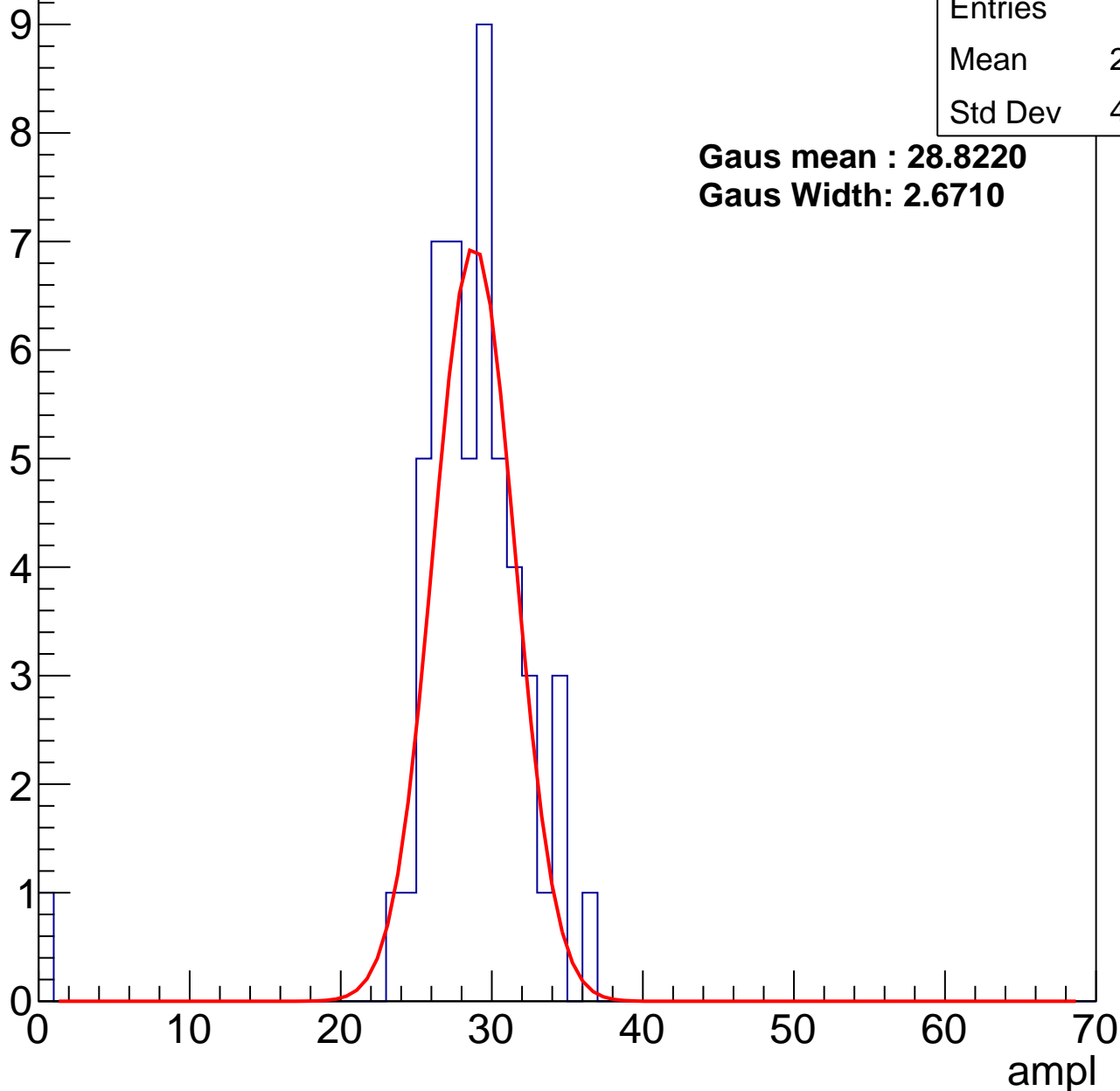
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	28.02
Std Dev	4.788

Gaus mean : 28.8220

Gaus Width: 2.6710



B1L101S, U14-ch9, adc1

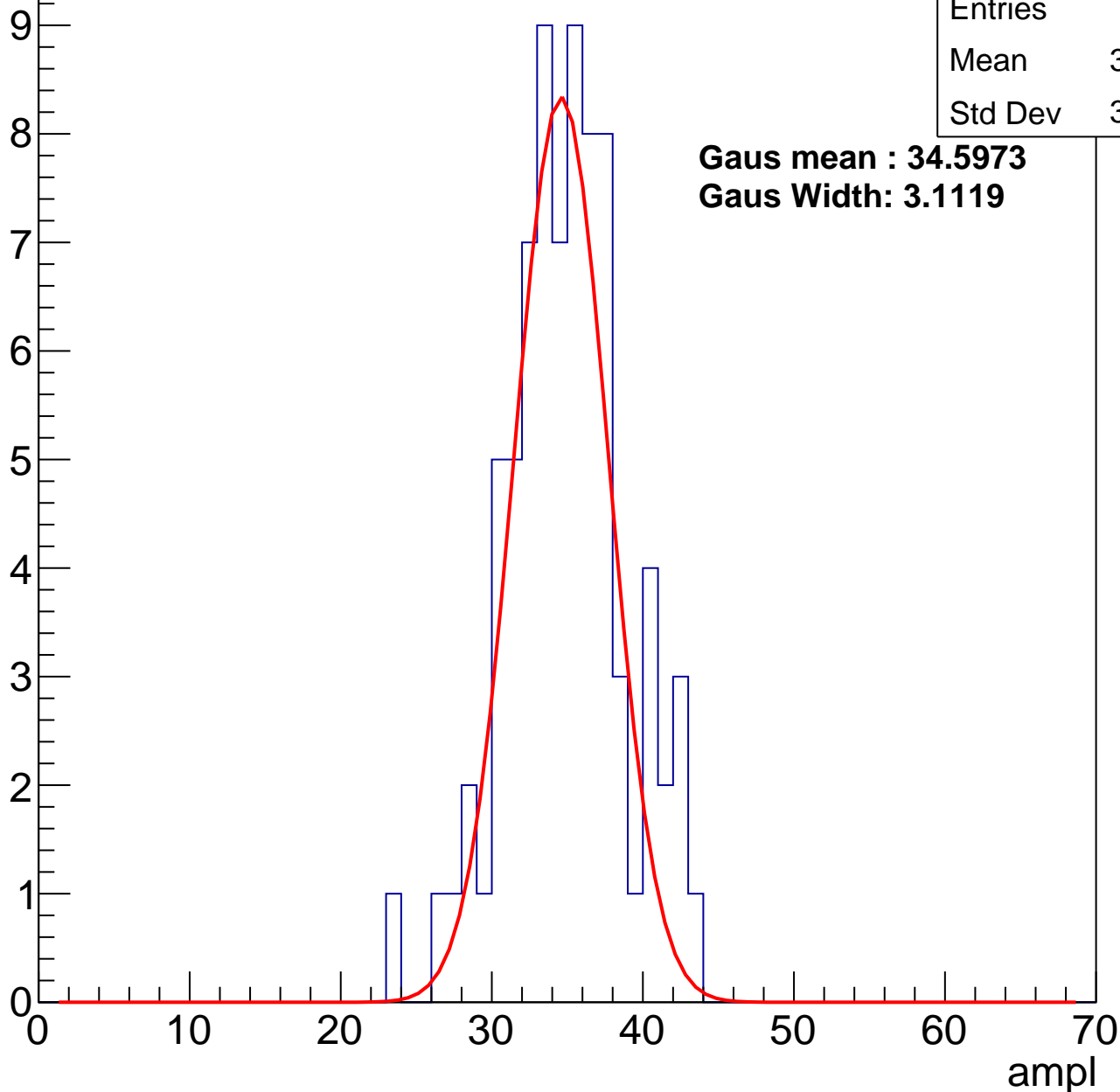
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	34.46
Std Dev	3.885

Gaus mean : 34.5973

Gaus Width: 3.1119



B1L101S, U14-ch9, adc2

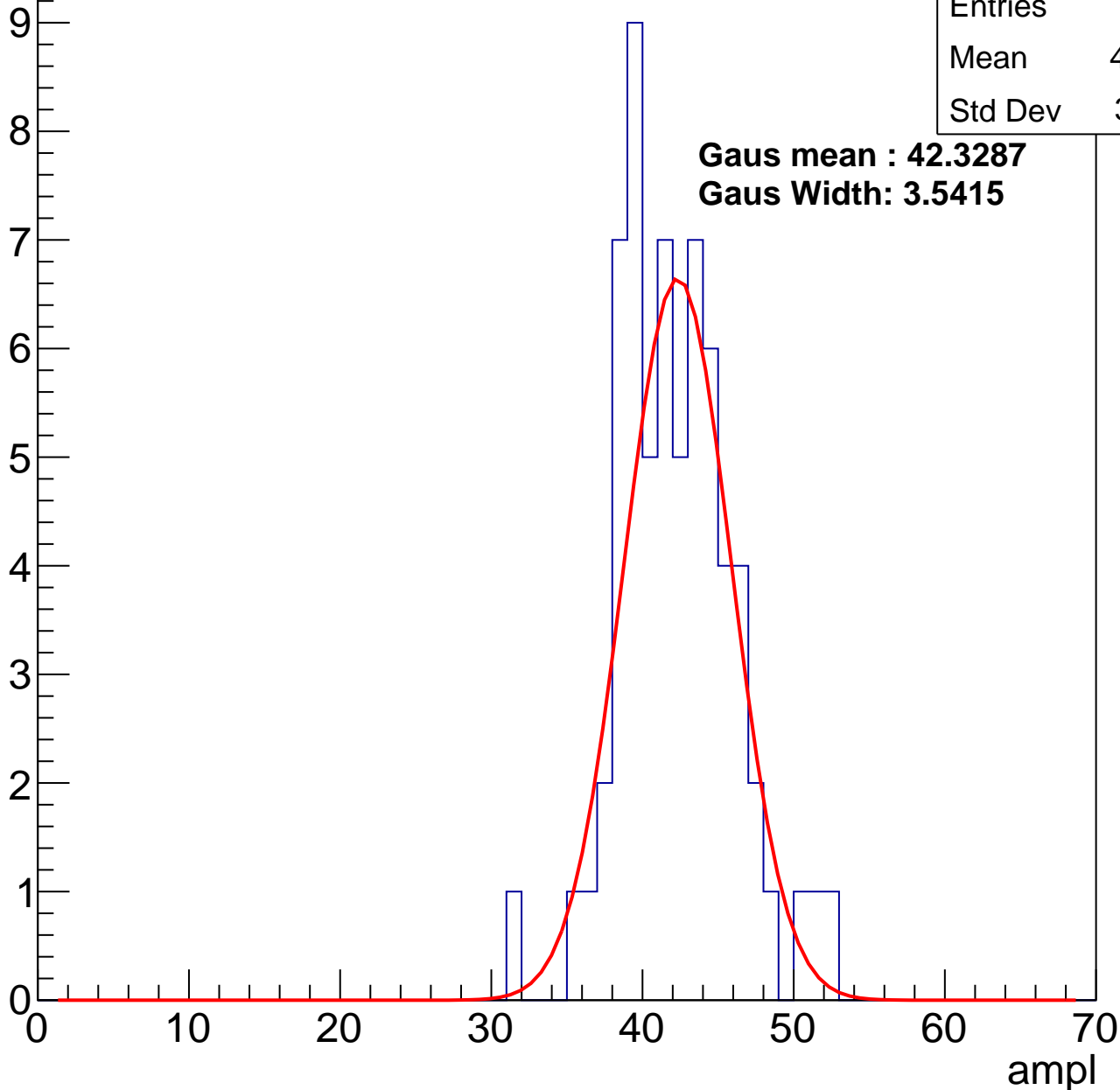
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.75
Std Dev	3.791

Gaus mean : 42.3287

Gaus Width: 3.5415

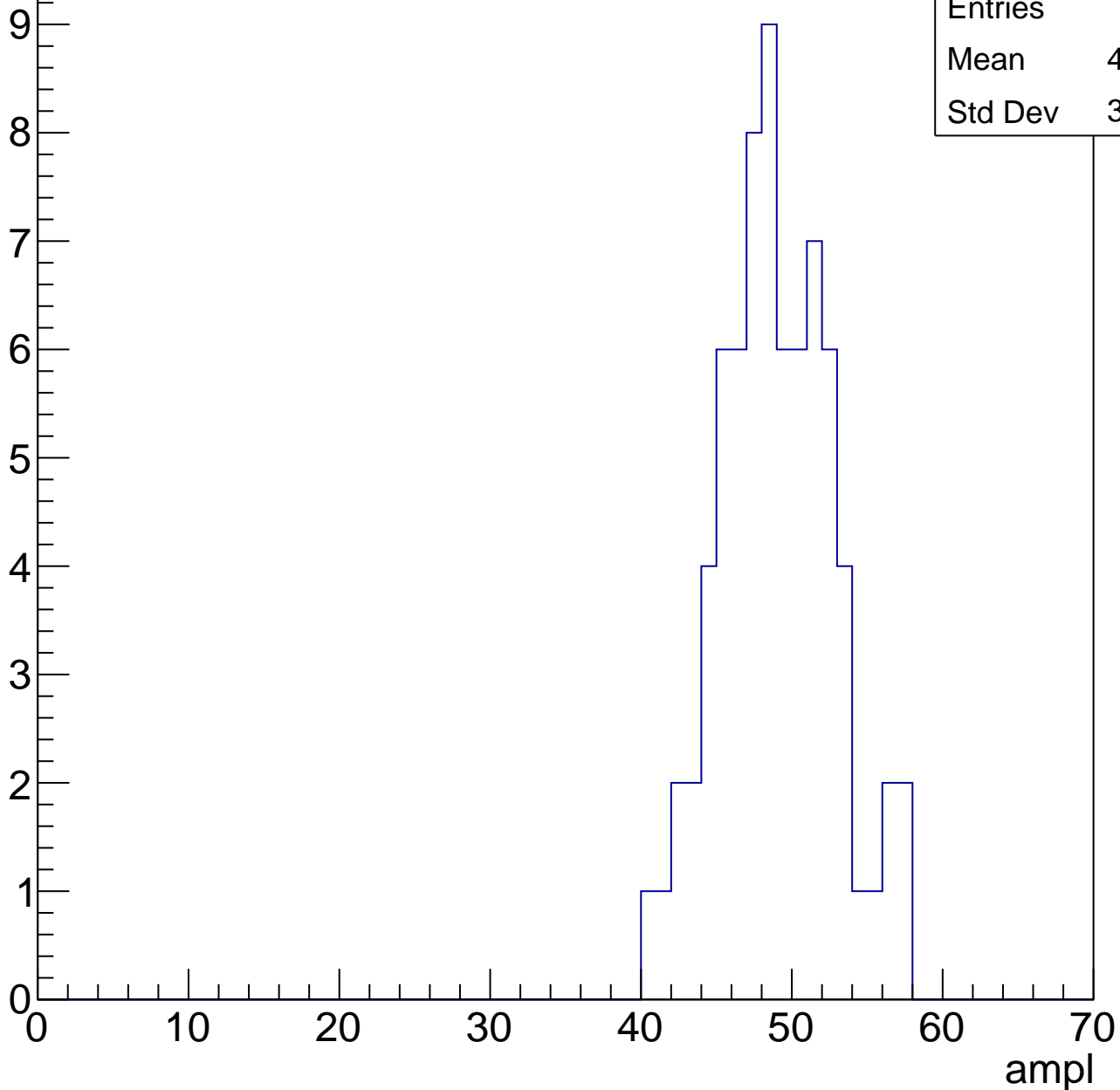


B1L101S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	48.53
Std Dev	3.728

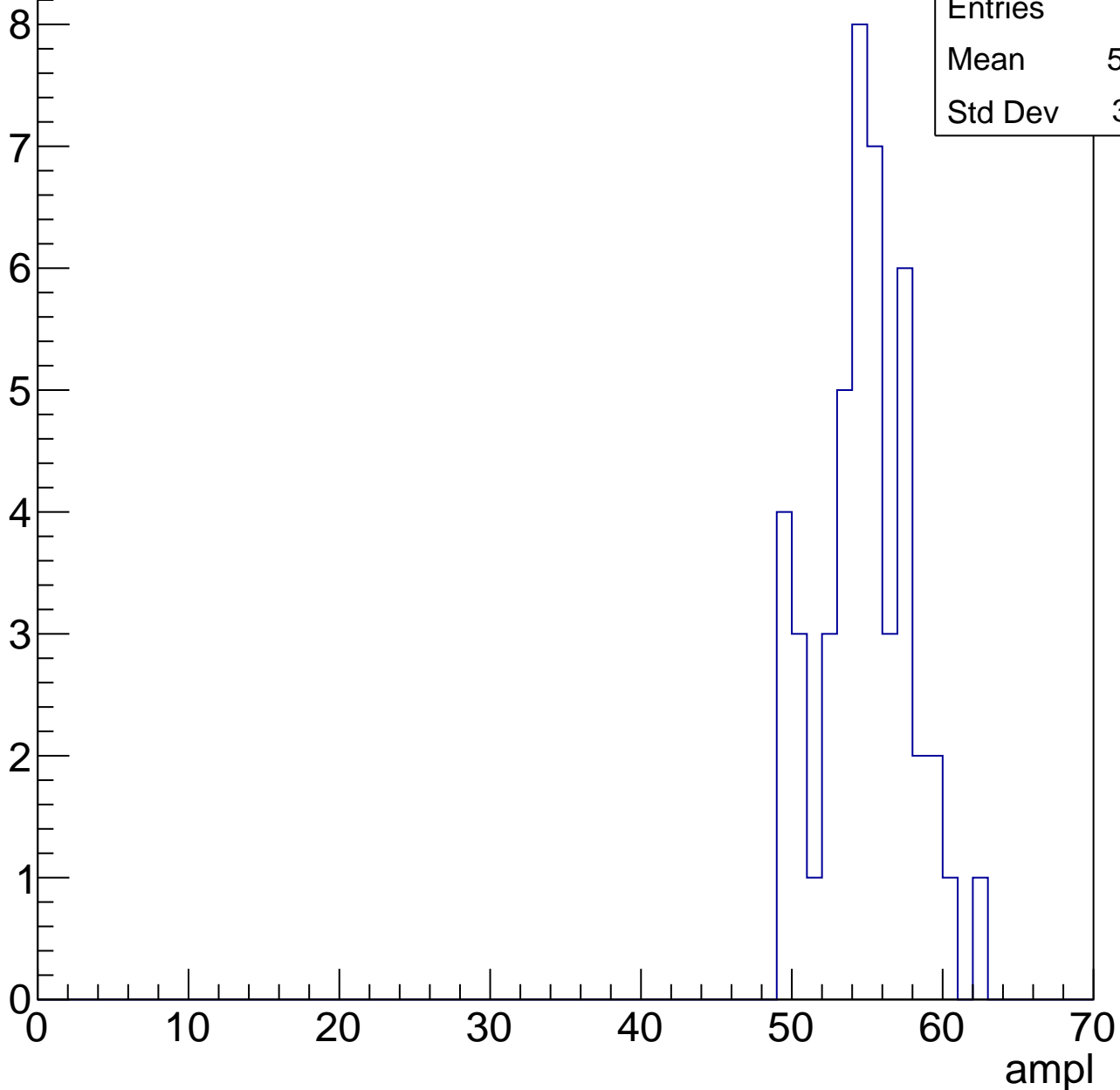


B1L101S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	54.37
Std Dev	3.031



B1L101S, U14-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 54

Mean 59.22

Std Dev 2.699

8

6

4

2

0

0

10

20

30

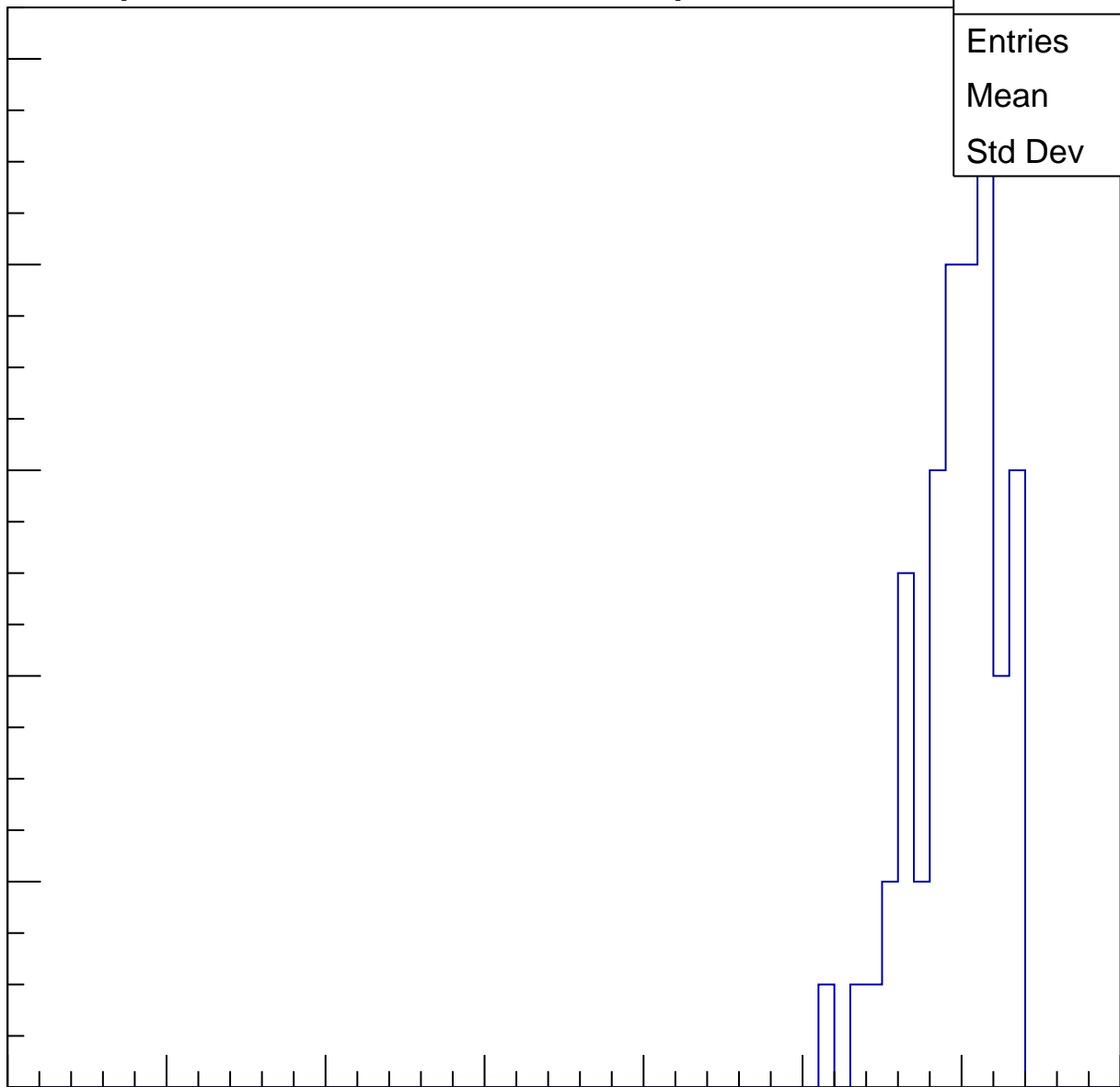
40

50

60

70

ampl

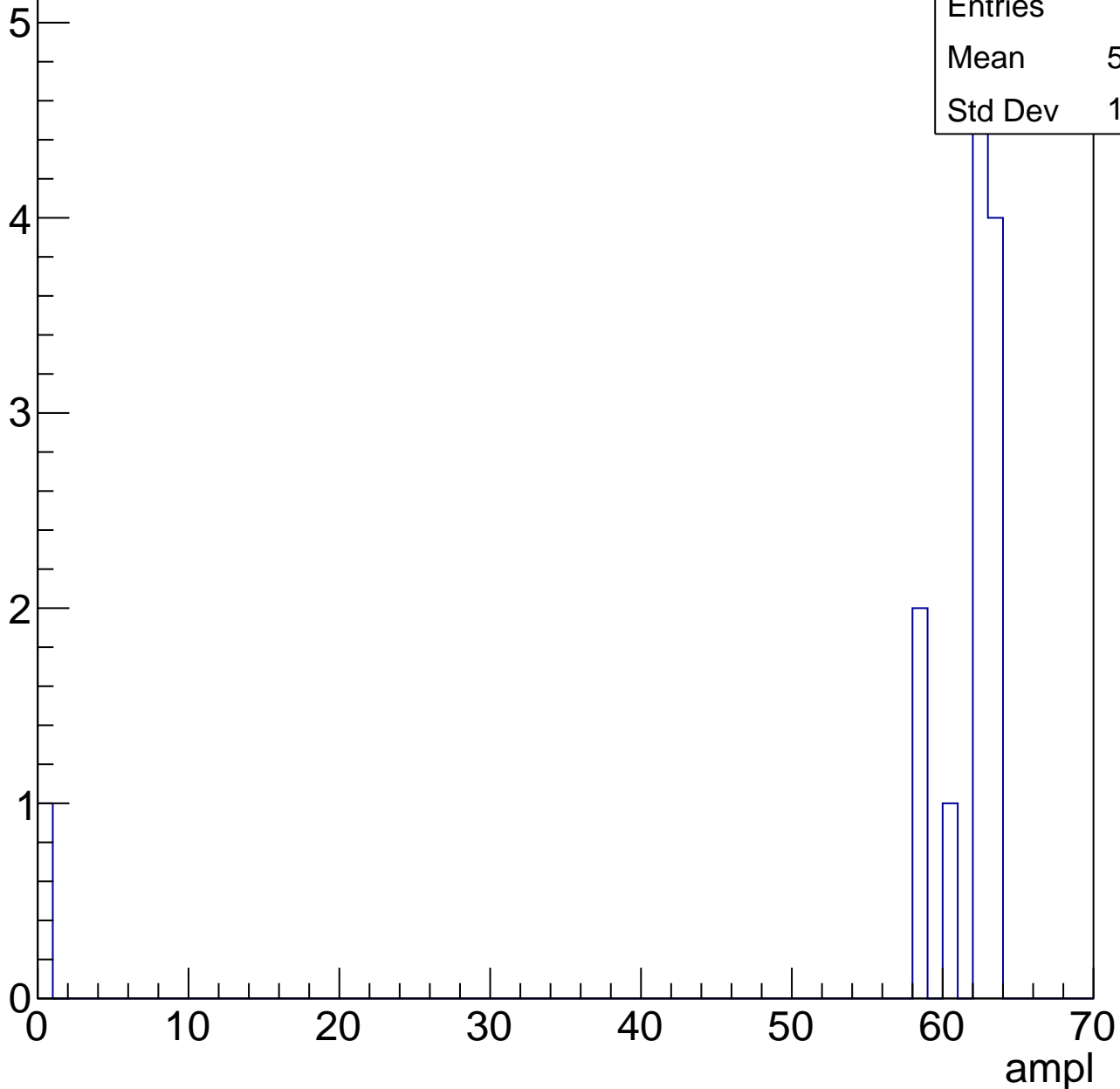


B1L101S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	56.77
Std Dev	16.47



B1L101S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch10, adc0

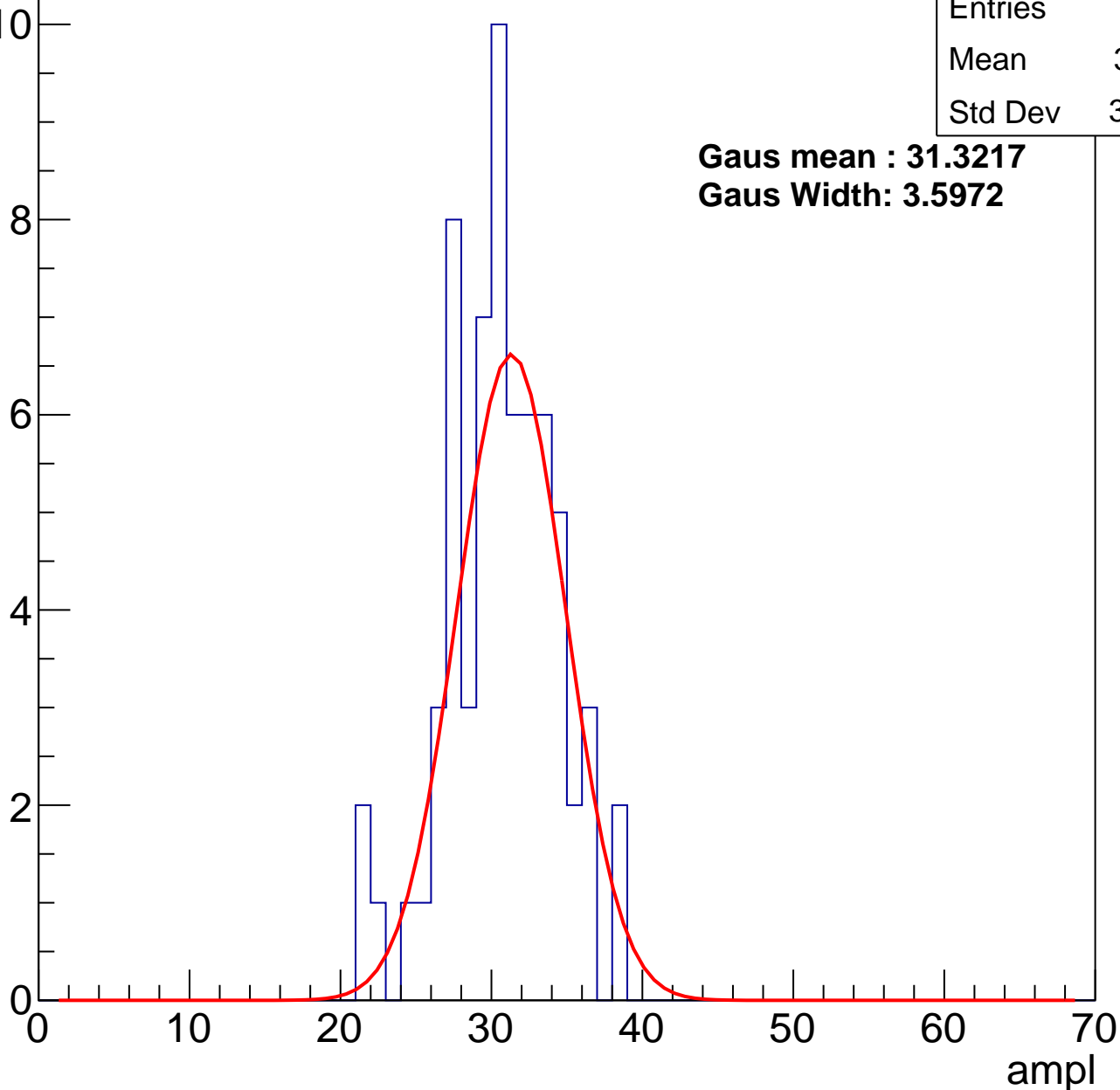
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.21
Std Dev	3.637

Gaus mean : 31.3217

Gaus Width: 3.5972



B1L101S, U14-ch10, adc1

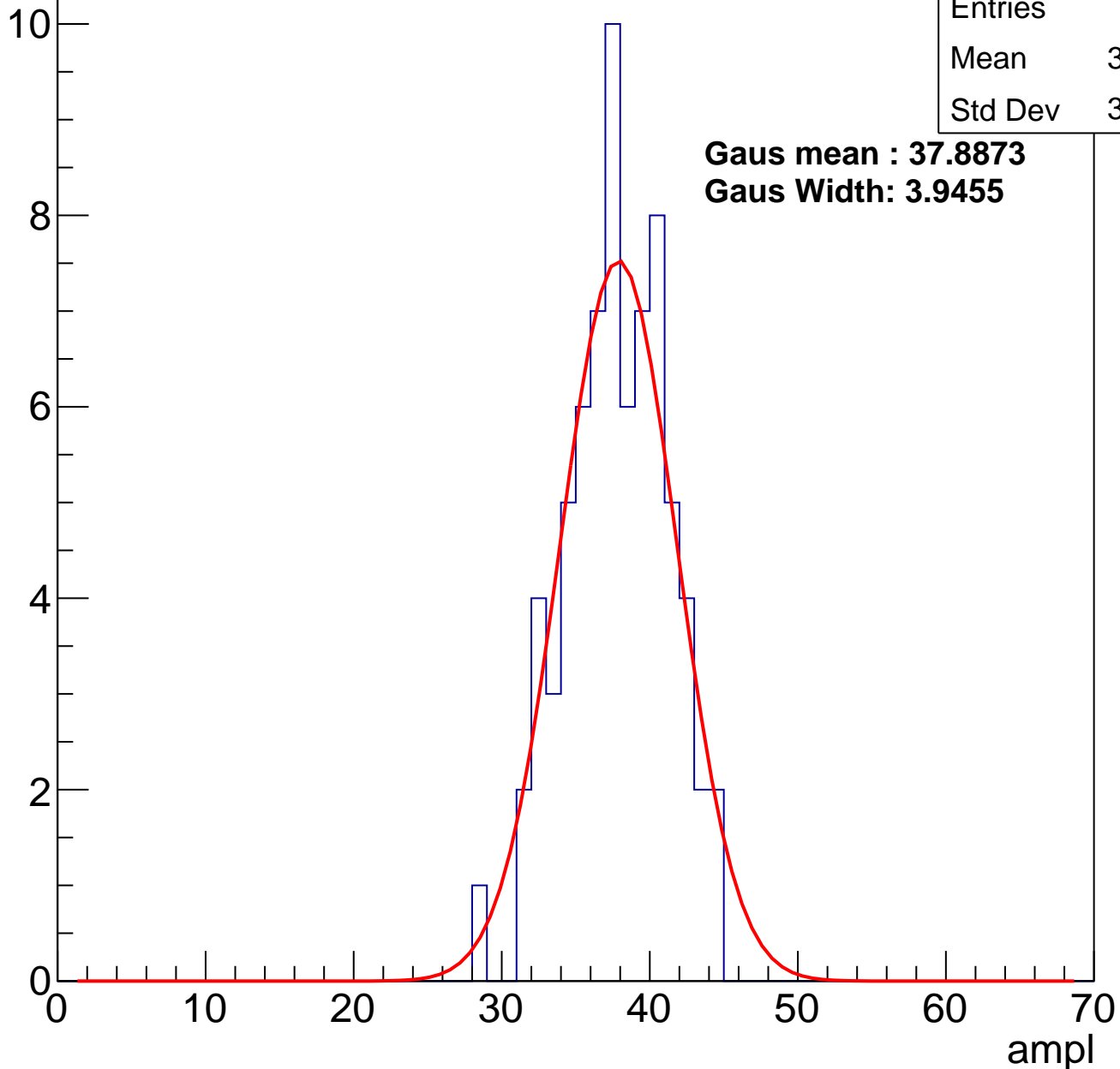
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	37.32
Std Dev	3.386

Gaus mean : 37.8873

Gaus Width: 3.9455

Entry



B1L101S, U14-ch10, adc2

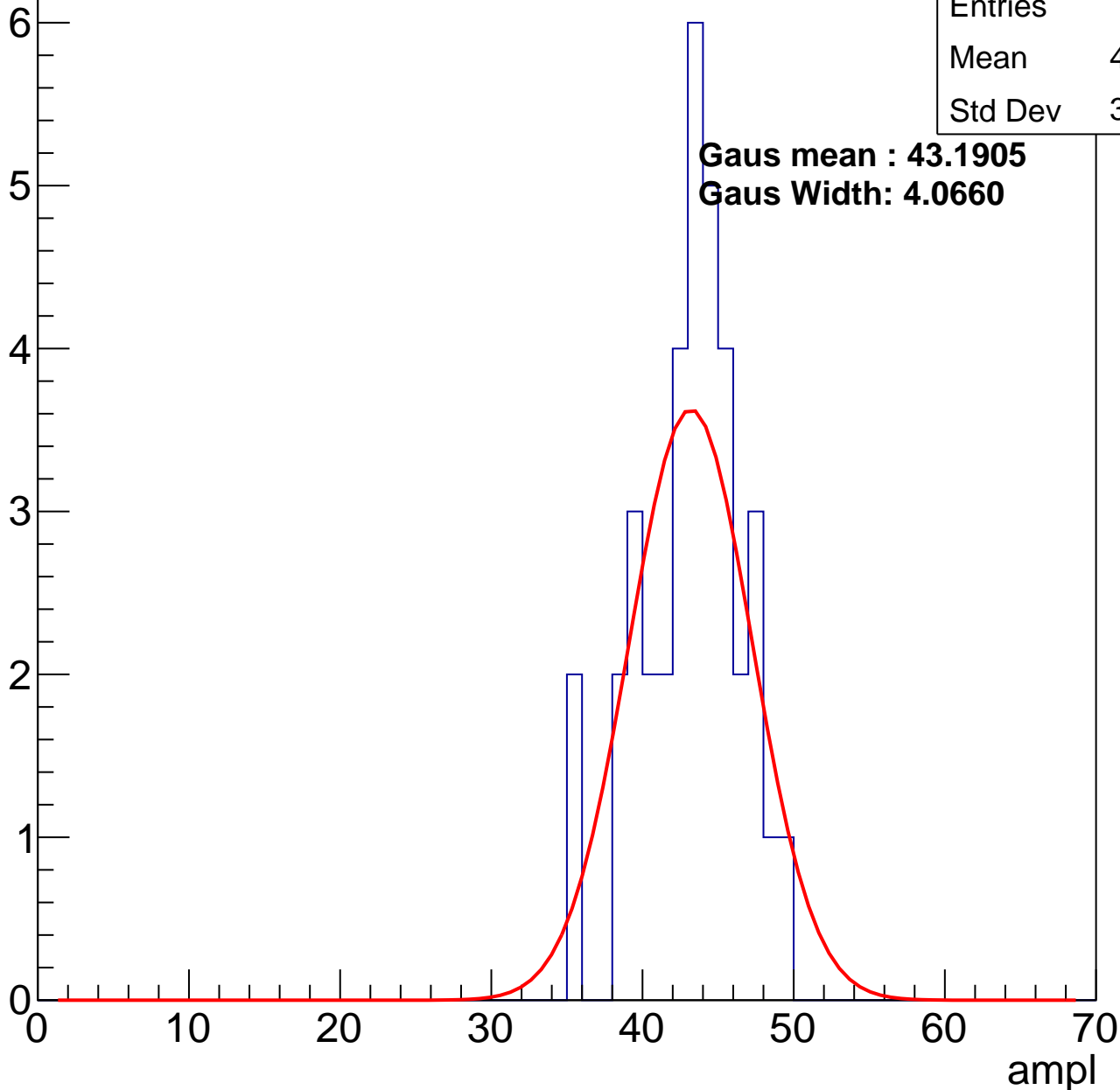
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	42.73
Std Dev	3.293

Gaus mean : 43.1905

Gaus Width: 4.0660

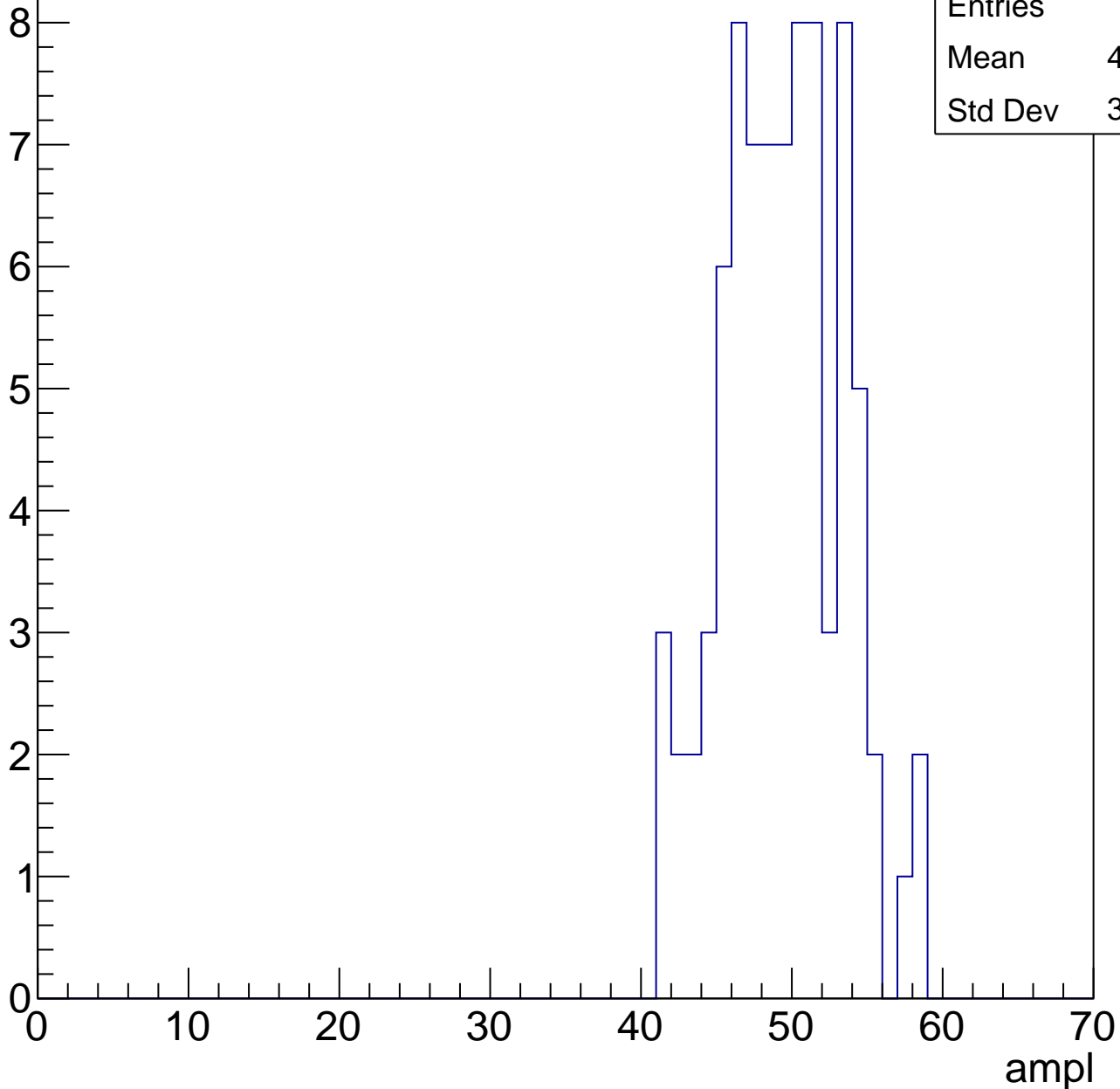


B1L101S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

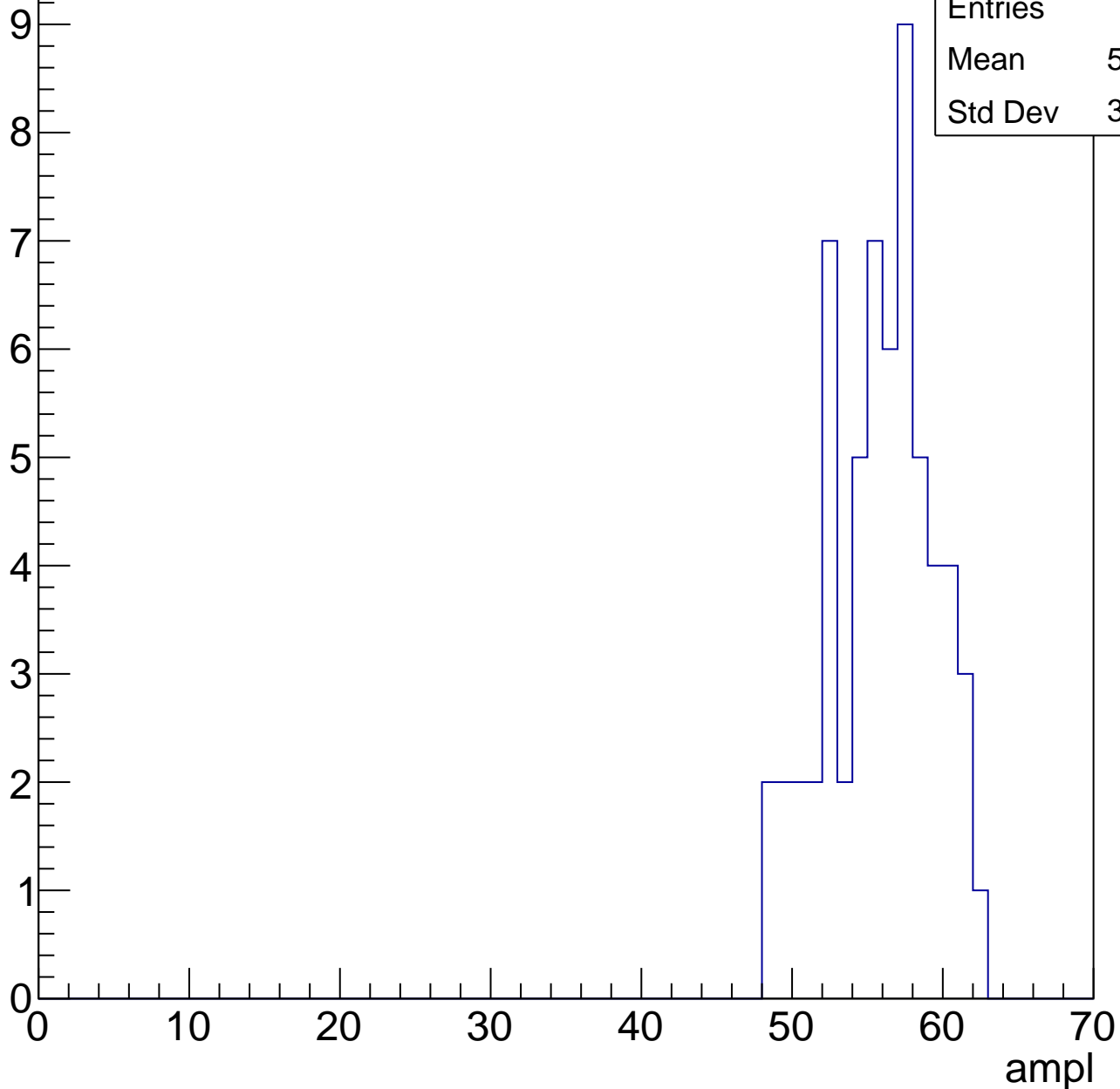
Entries	82
Mean	48.93
Std Dev	3.916



B1L101S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



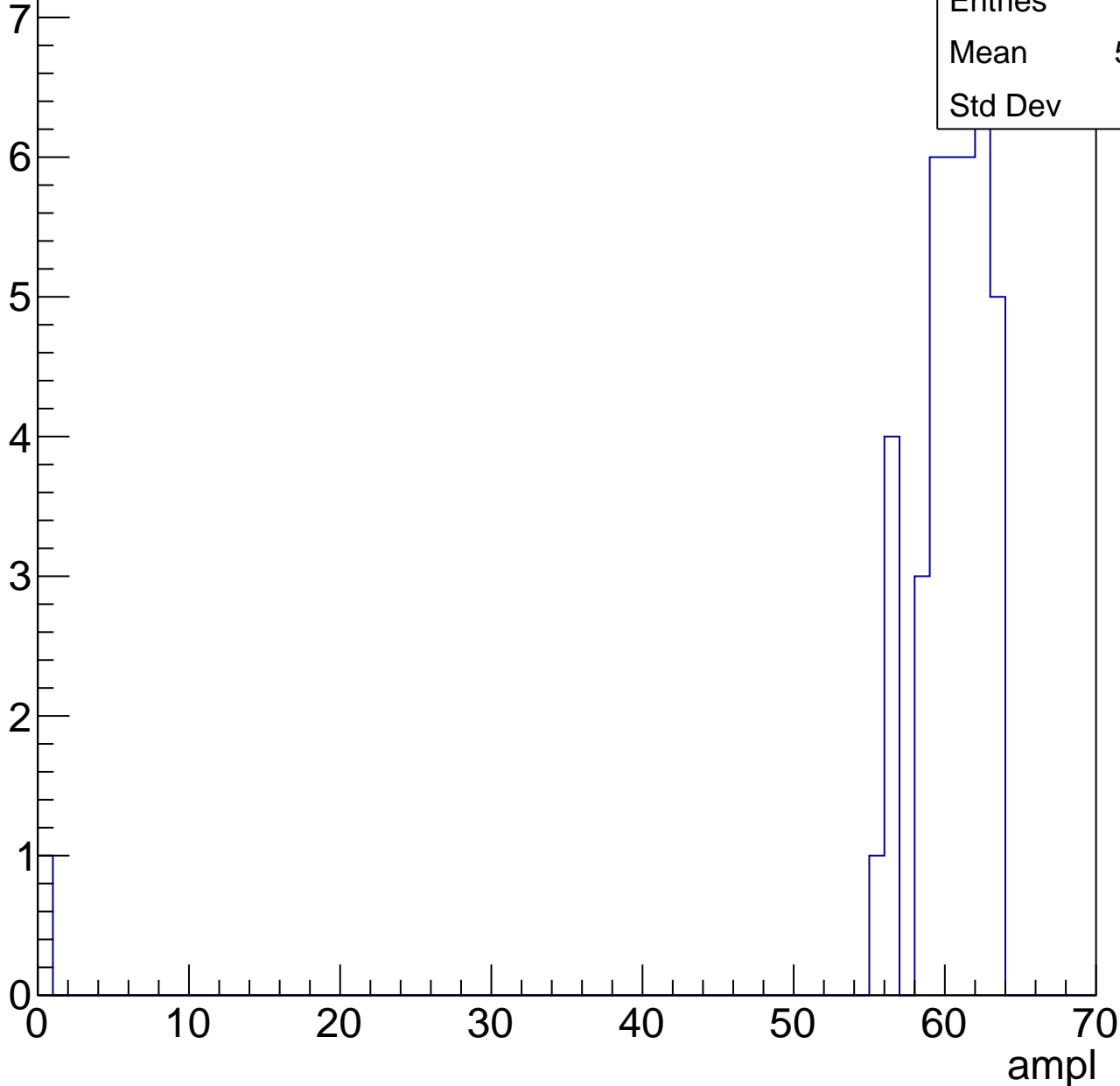
Entries	61
Mean	55.43
Std Dev	3.447

B1L101S, U14-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.51
Std Dev	9.74

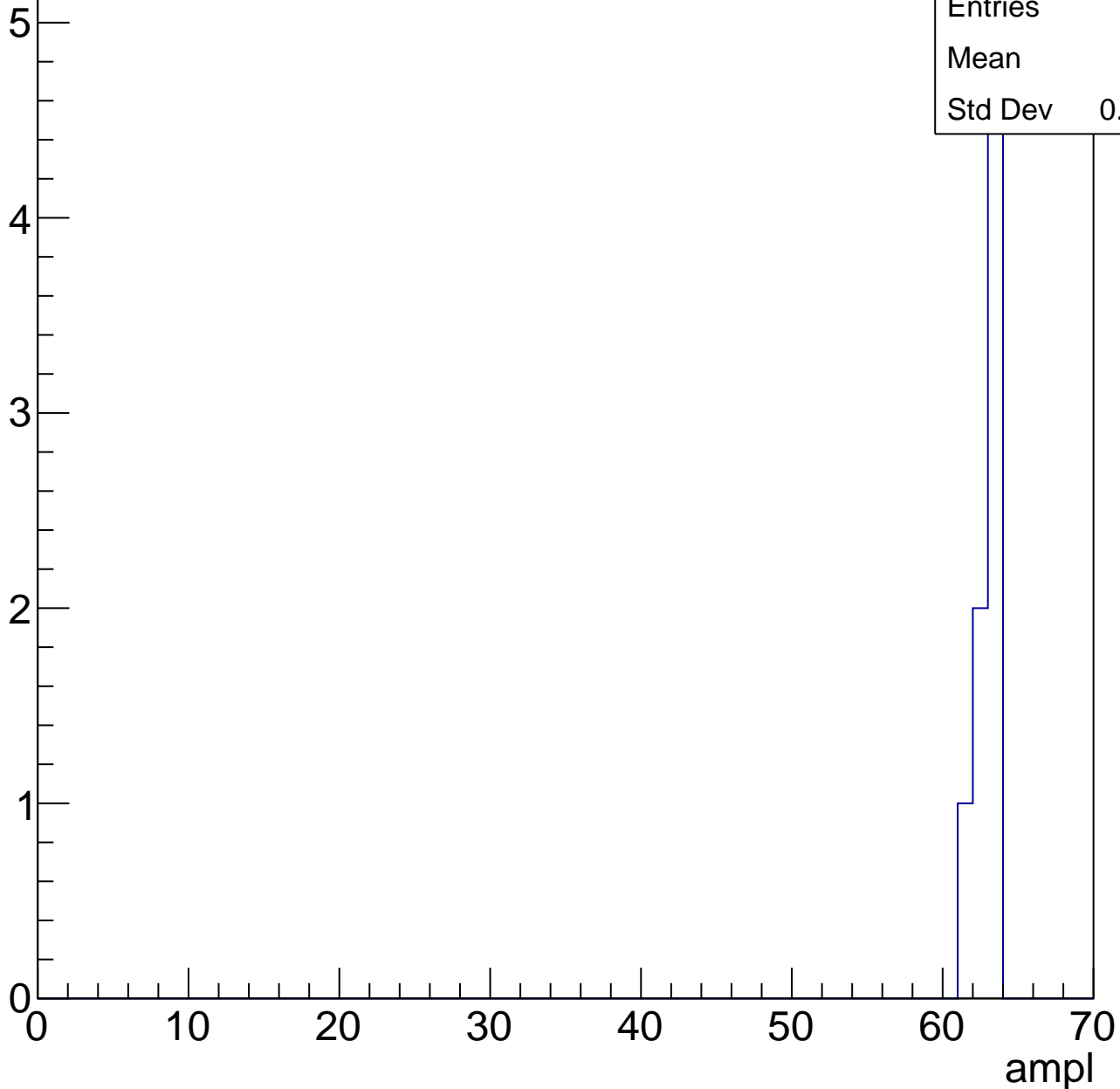


B1L101S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B1L101S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch11, adc0

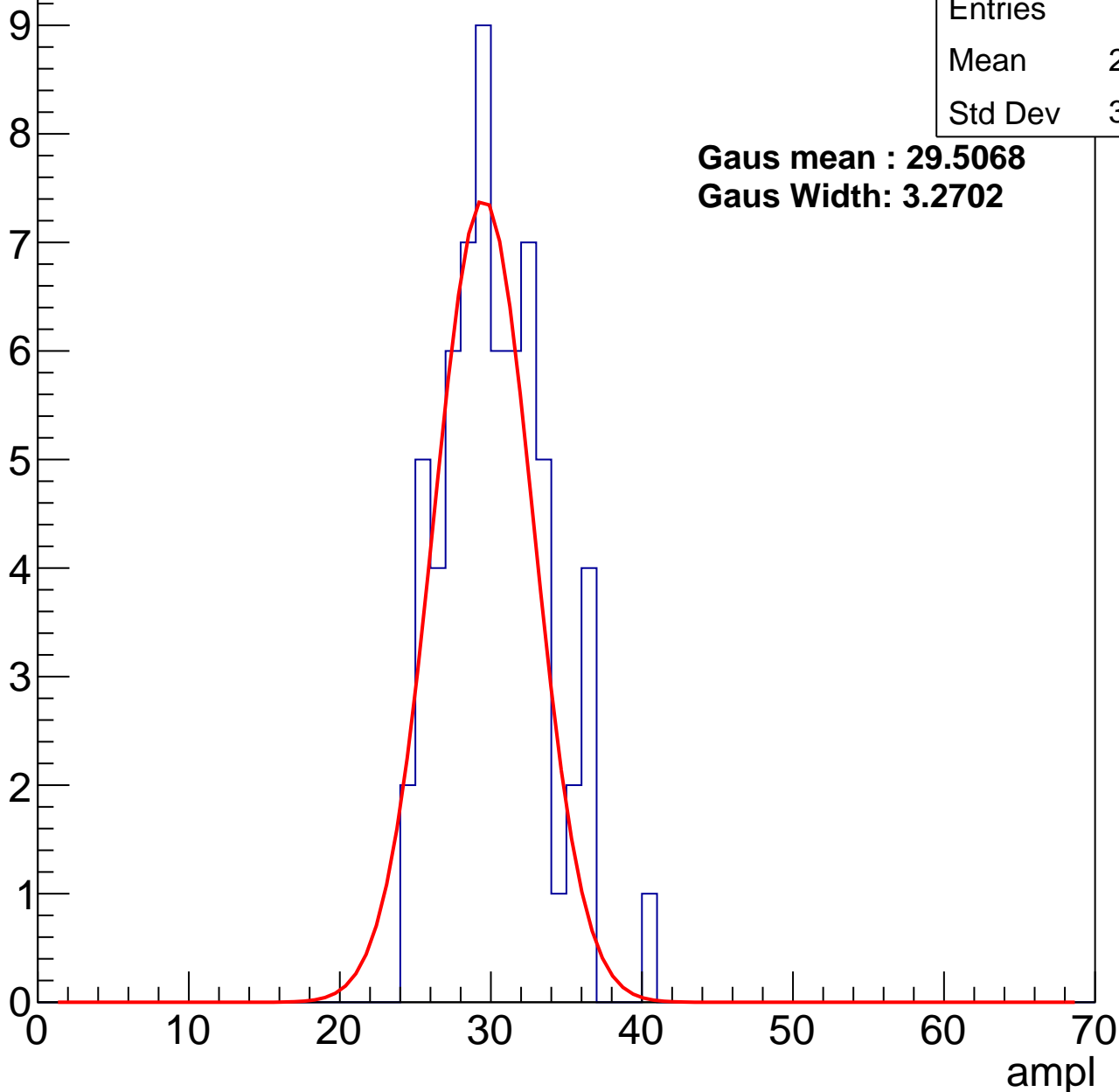
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.83
Std Dev	3.372

Gaus mean : 29.5068

Gaus Width: 3.2702



B1L101S, U14-ch11, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	37.47
Std Dev	3.753

Gaus mean : 37.6962

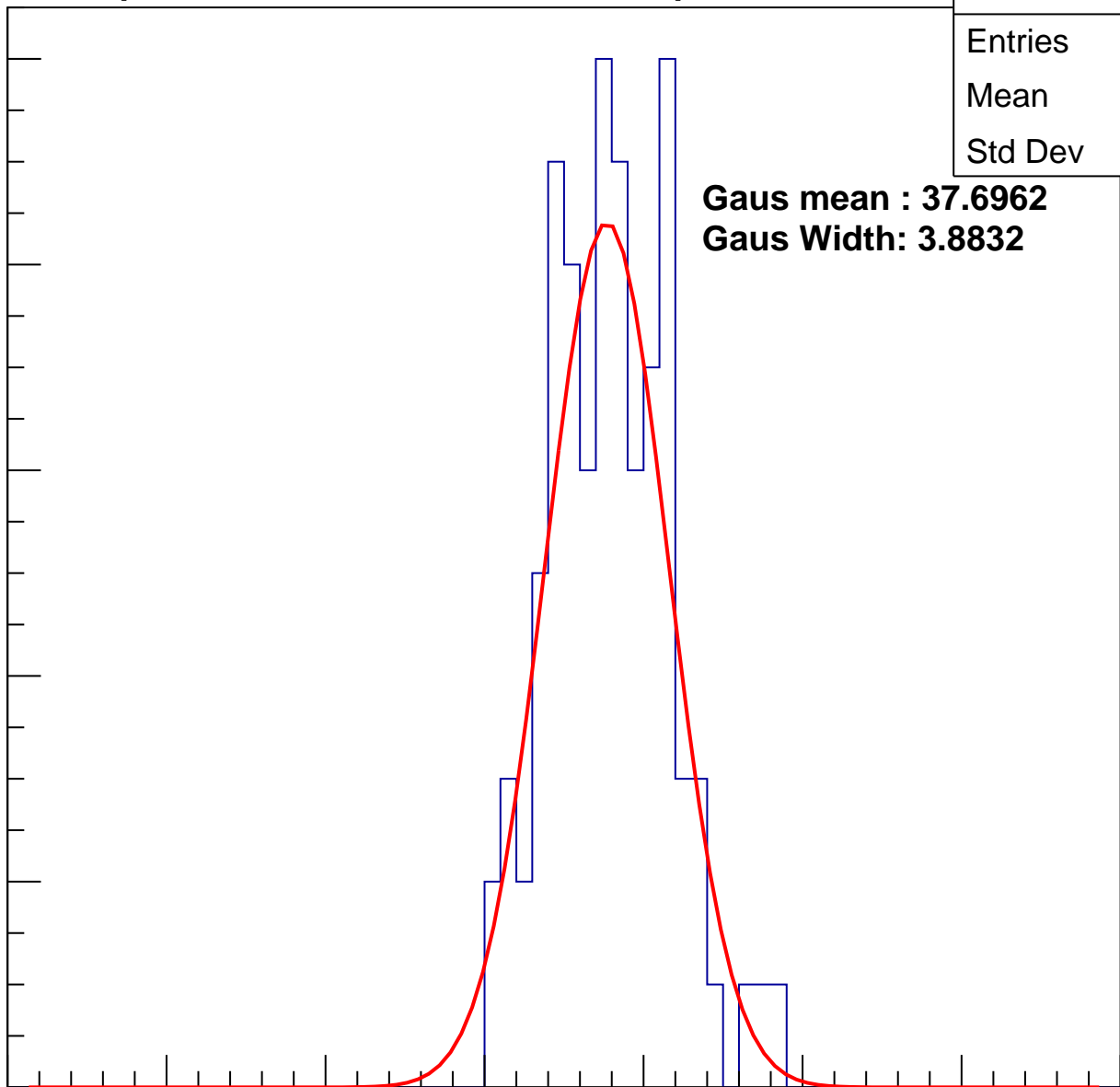
Gaus Width: 3.8832

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch11, adc2

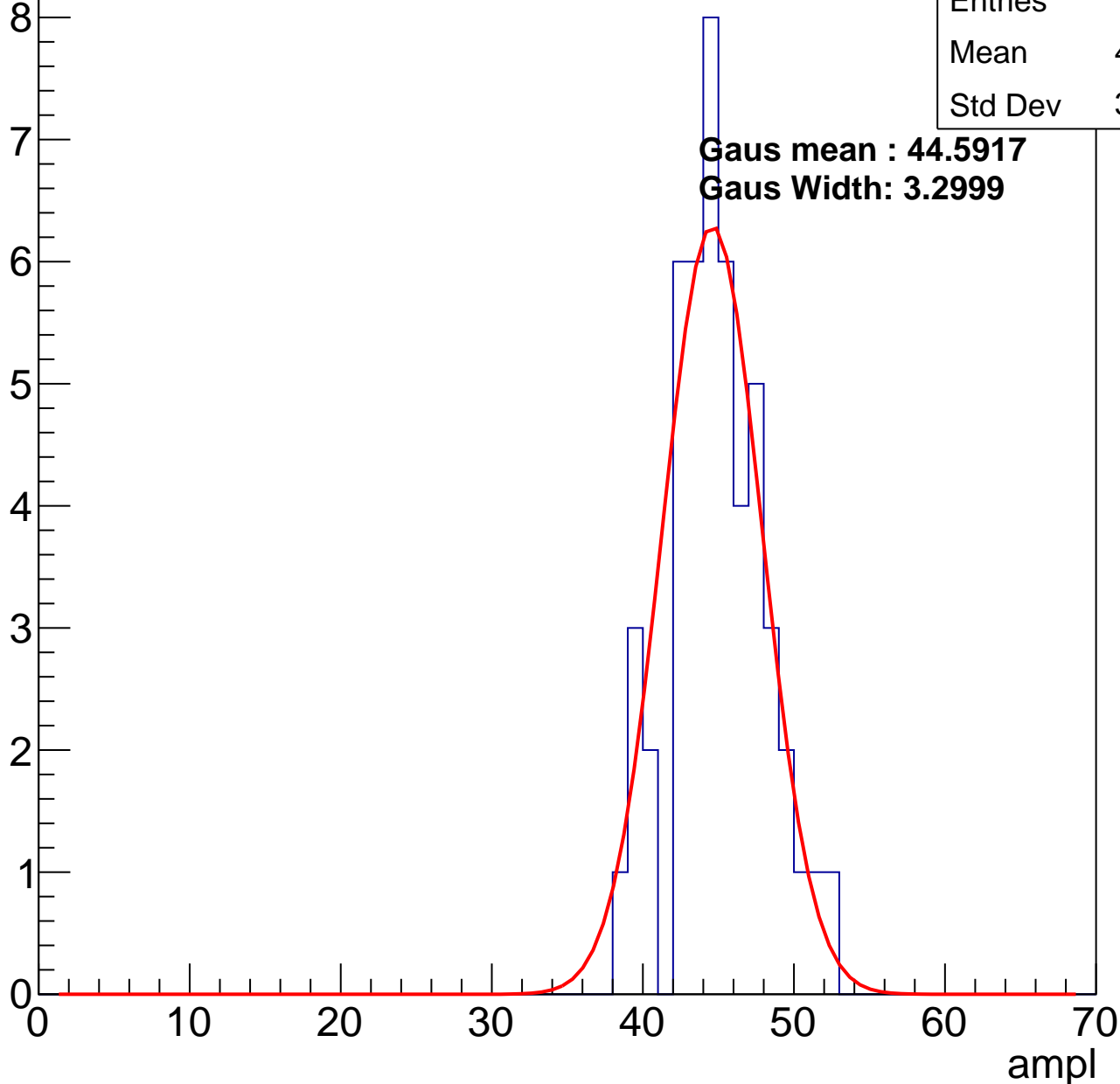
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	44.51
Std Dev	3.111

Gaus mean : 44.5917

Gaus Width: 3.2999

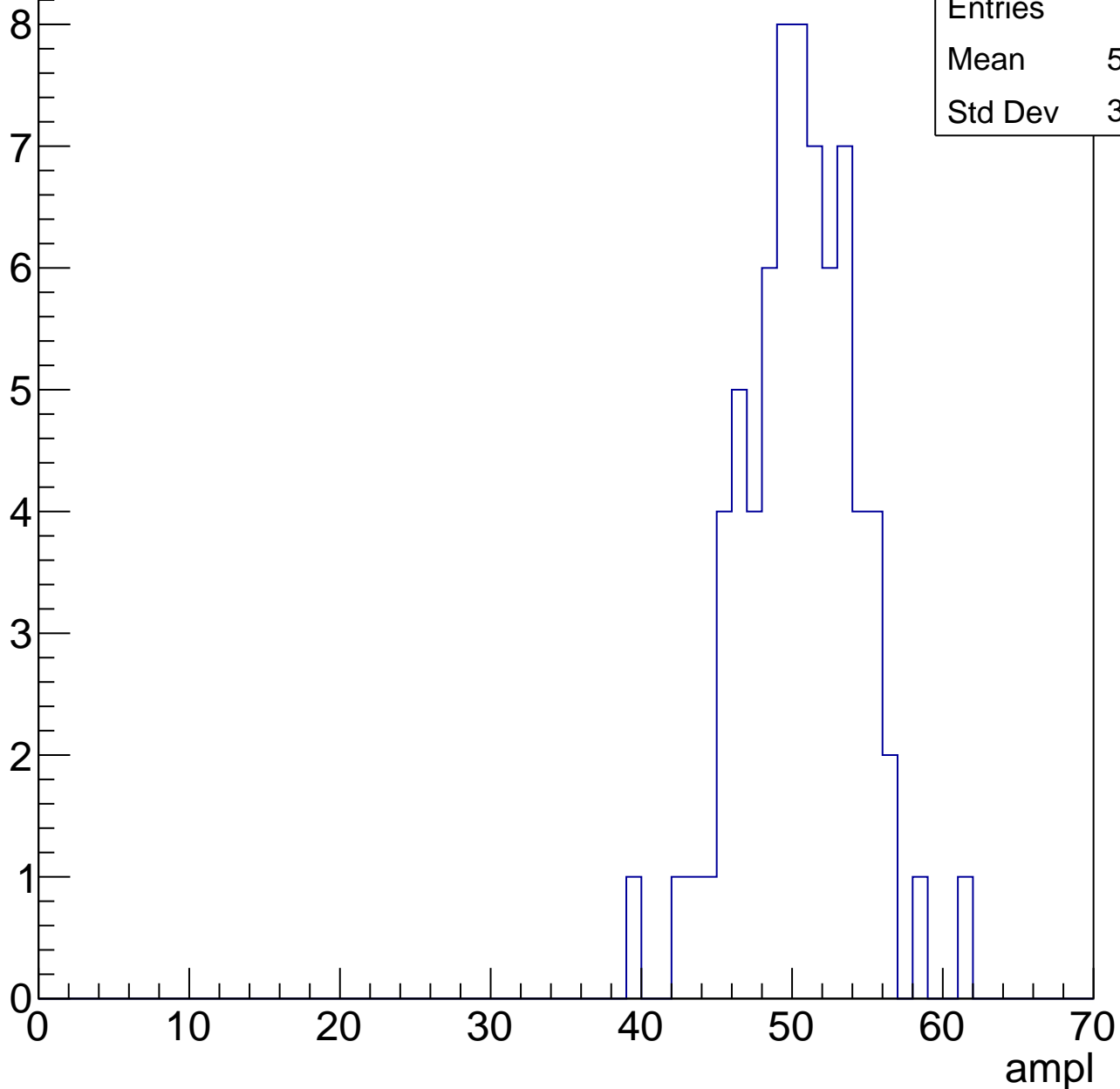


B1L101S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.04
Std Dev	3.814

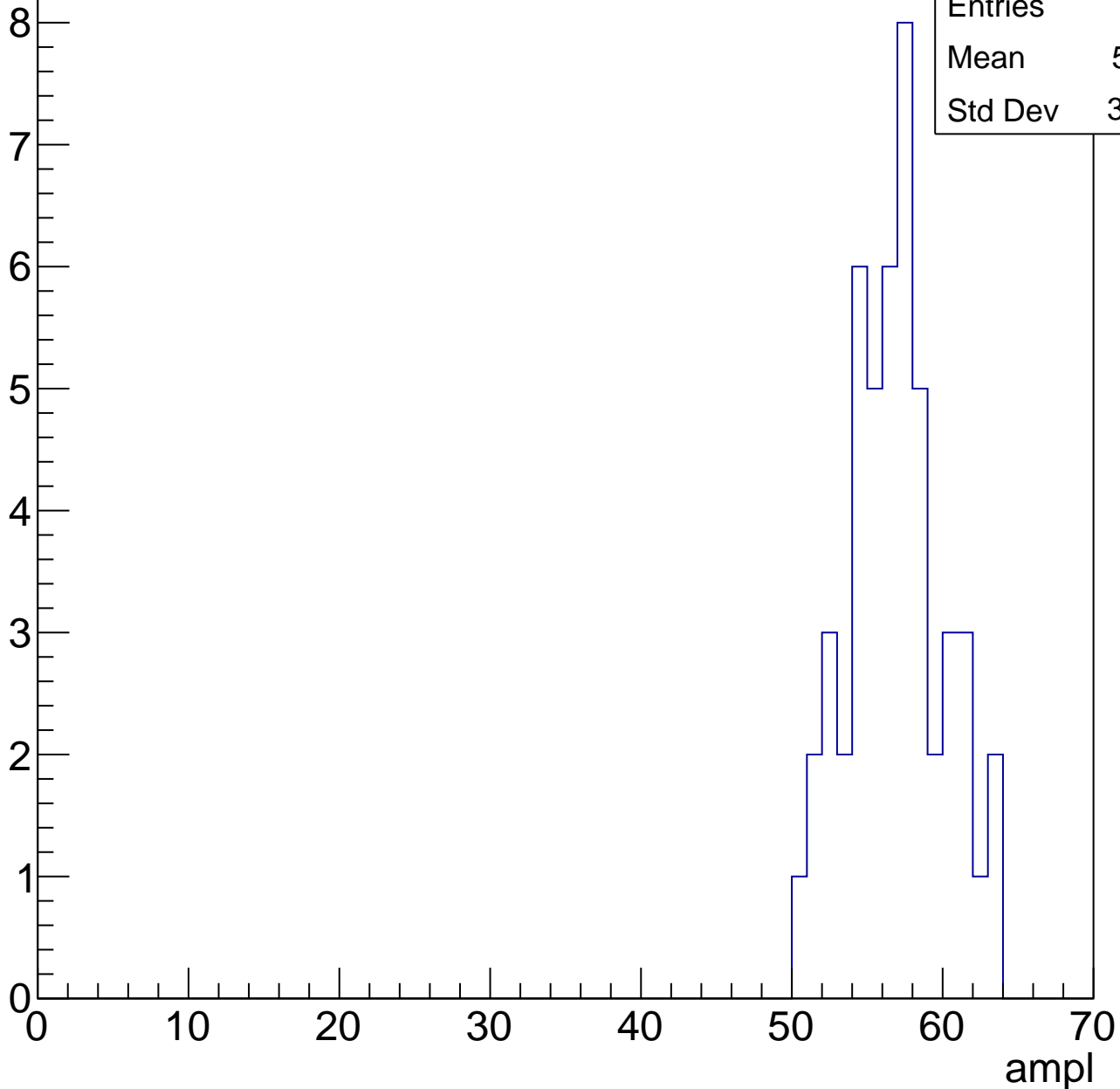


B1L101S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.41
Std Dev	3.116

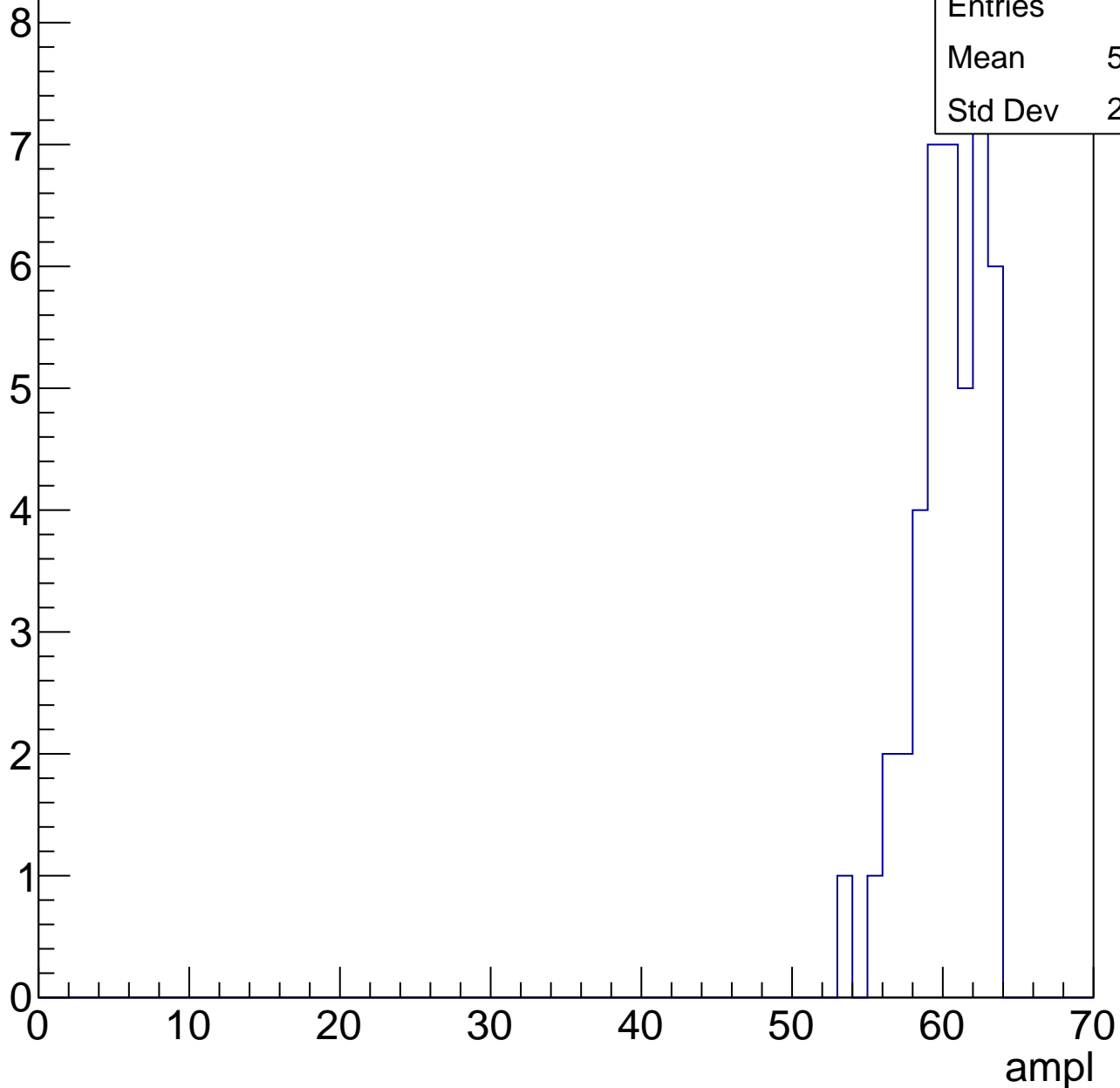


B1L101S, U14-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

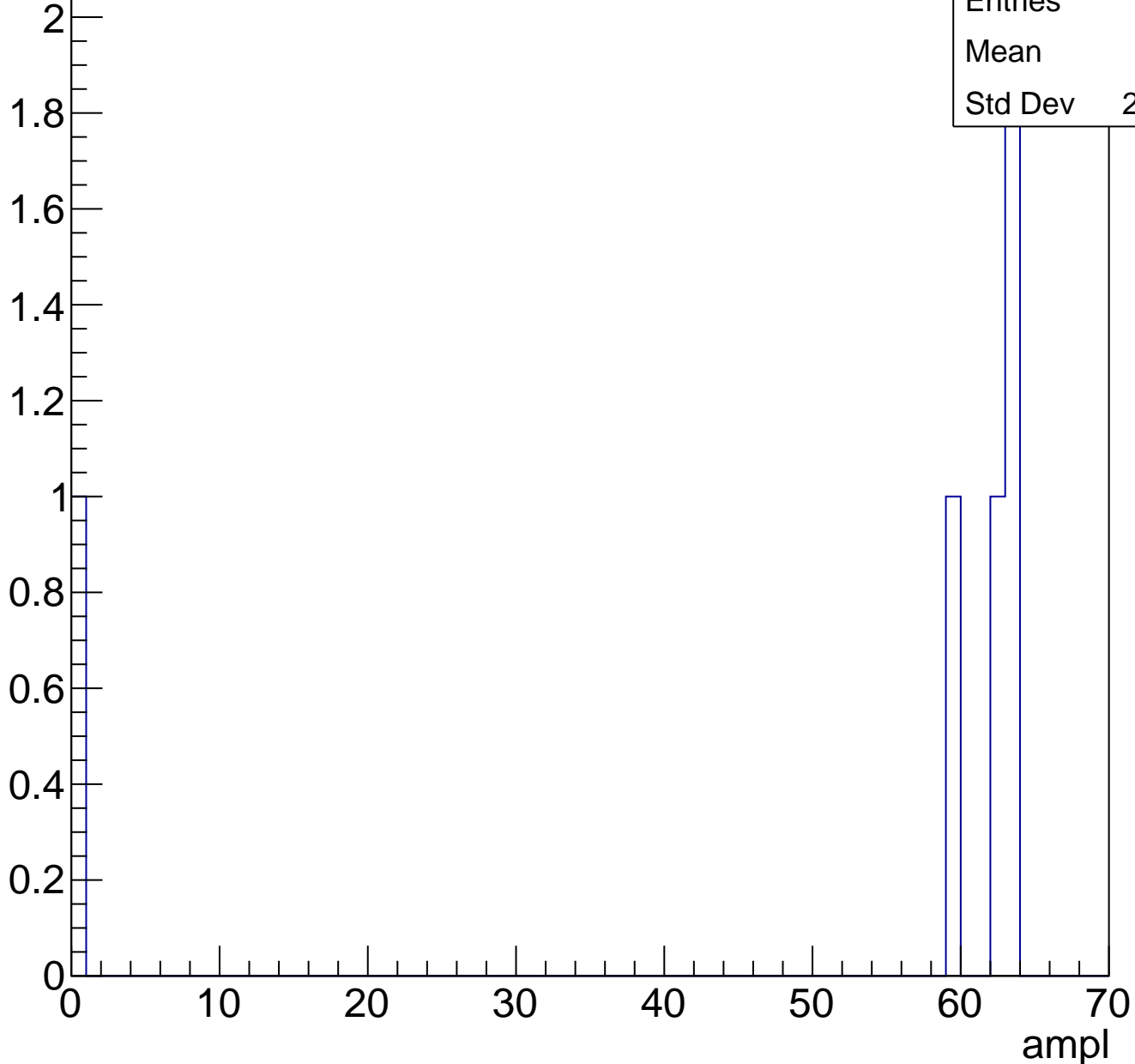
Entries	43
Mean	59.95
Std Dev	2.352



B1L101S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch12, adc0

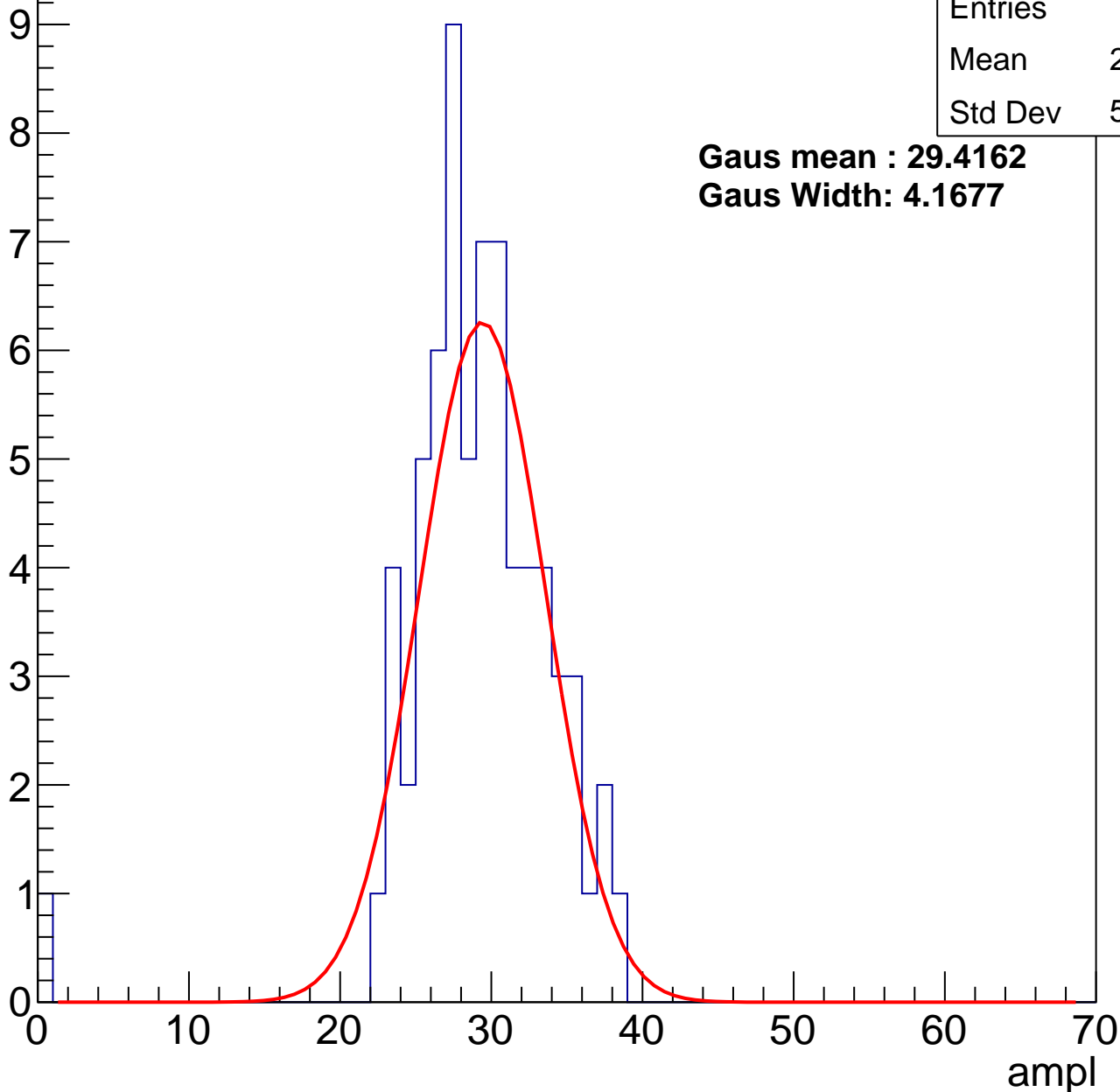
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.67
Std Dev	5.129

Gaus mean : 29.4162

Gaus Width: 4.1677



B1L101S, U14-ch12, adc1

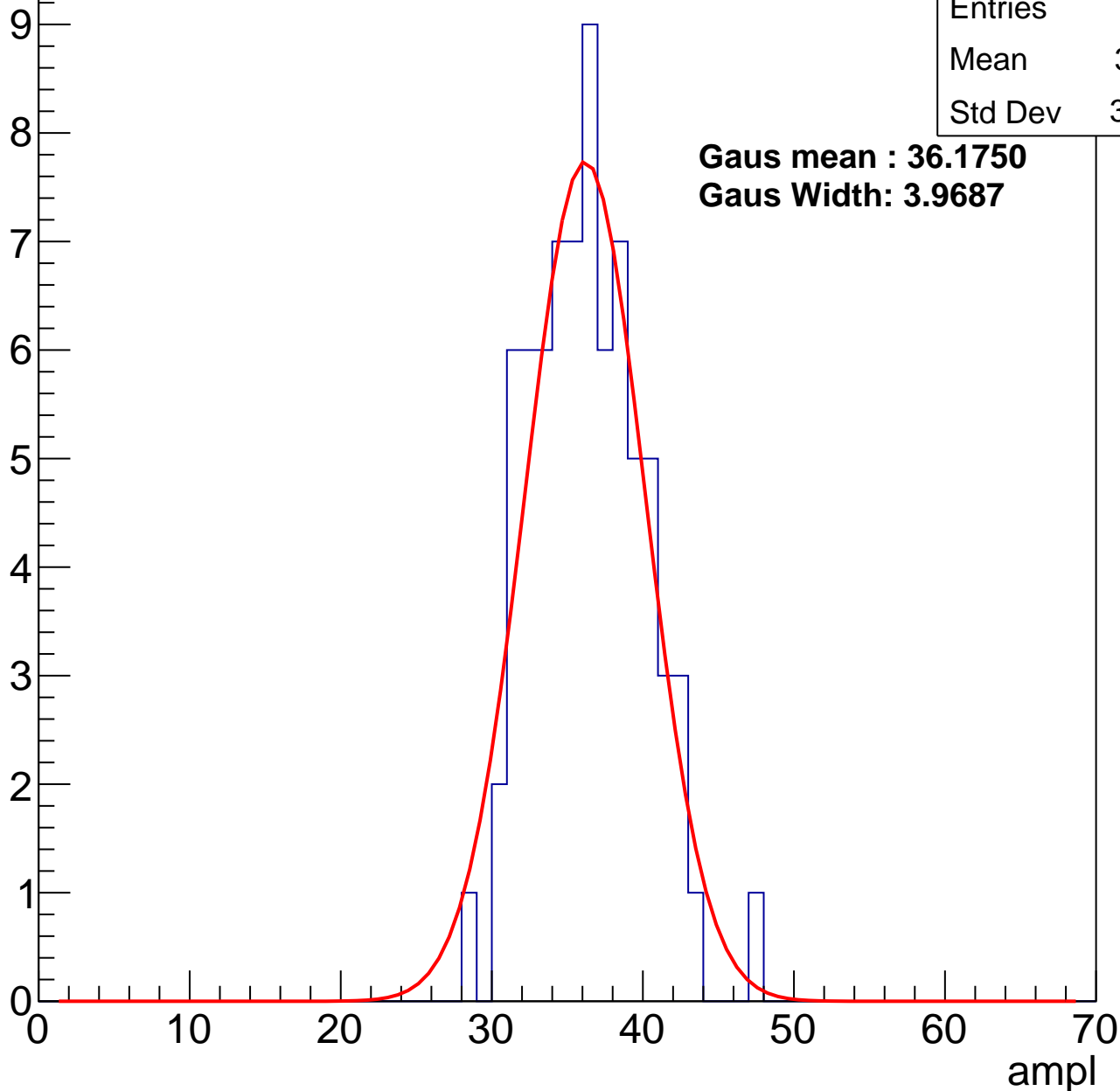
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.91
Std Dev	3.619

Gaus mean : 36.1750

Gaus Width: 3.9687



B1L101S, U14-ch12, adc2

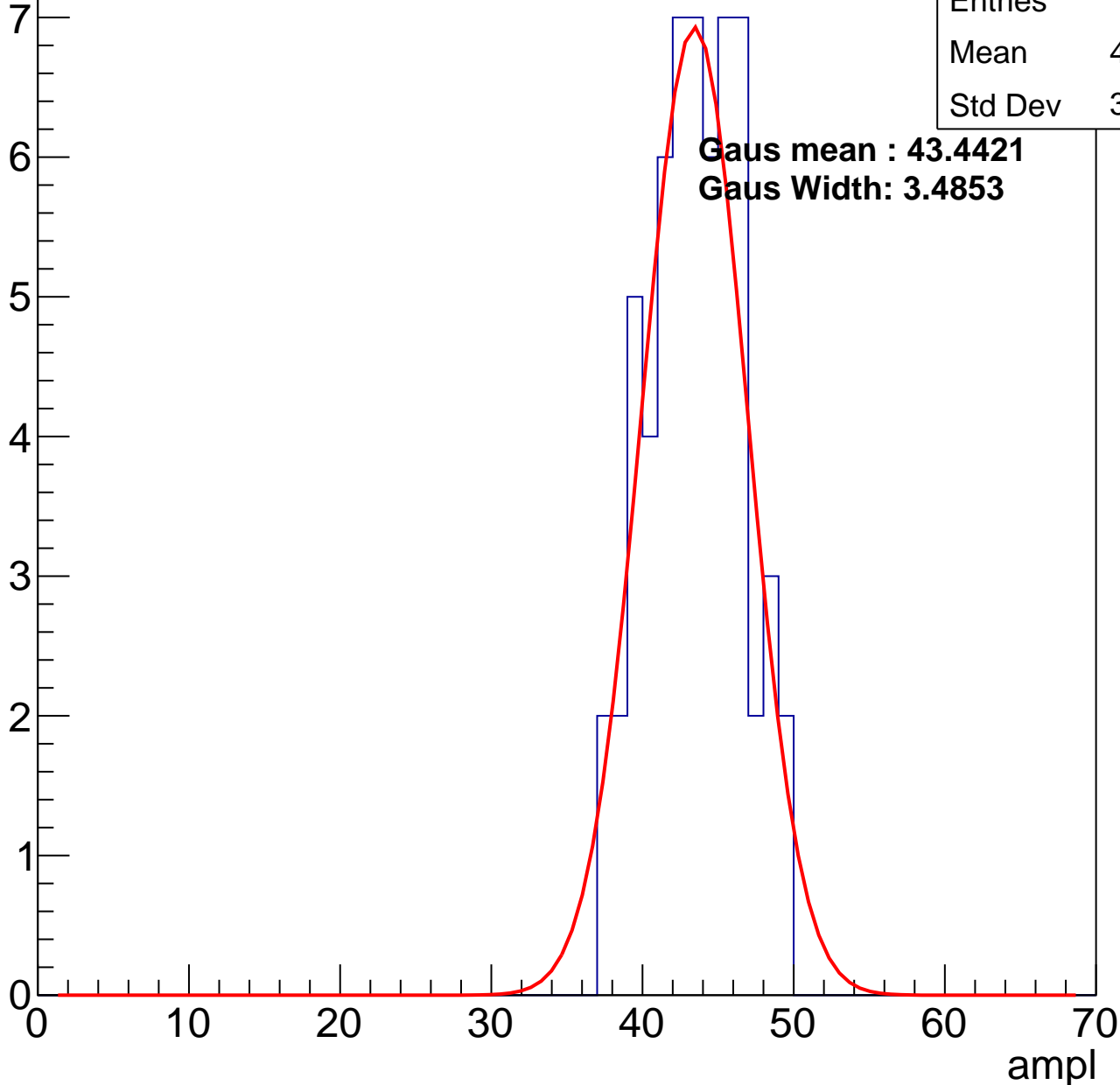
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.05
Std Dev	3.013

Gaus mean : 43.4421

Gaus Width: 3.4853

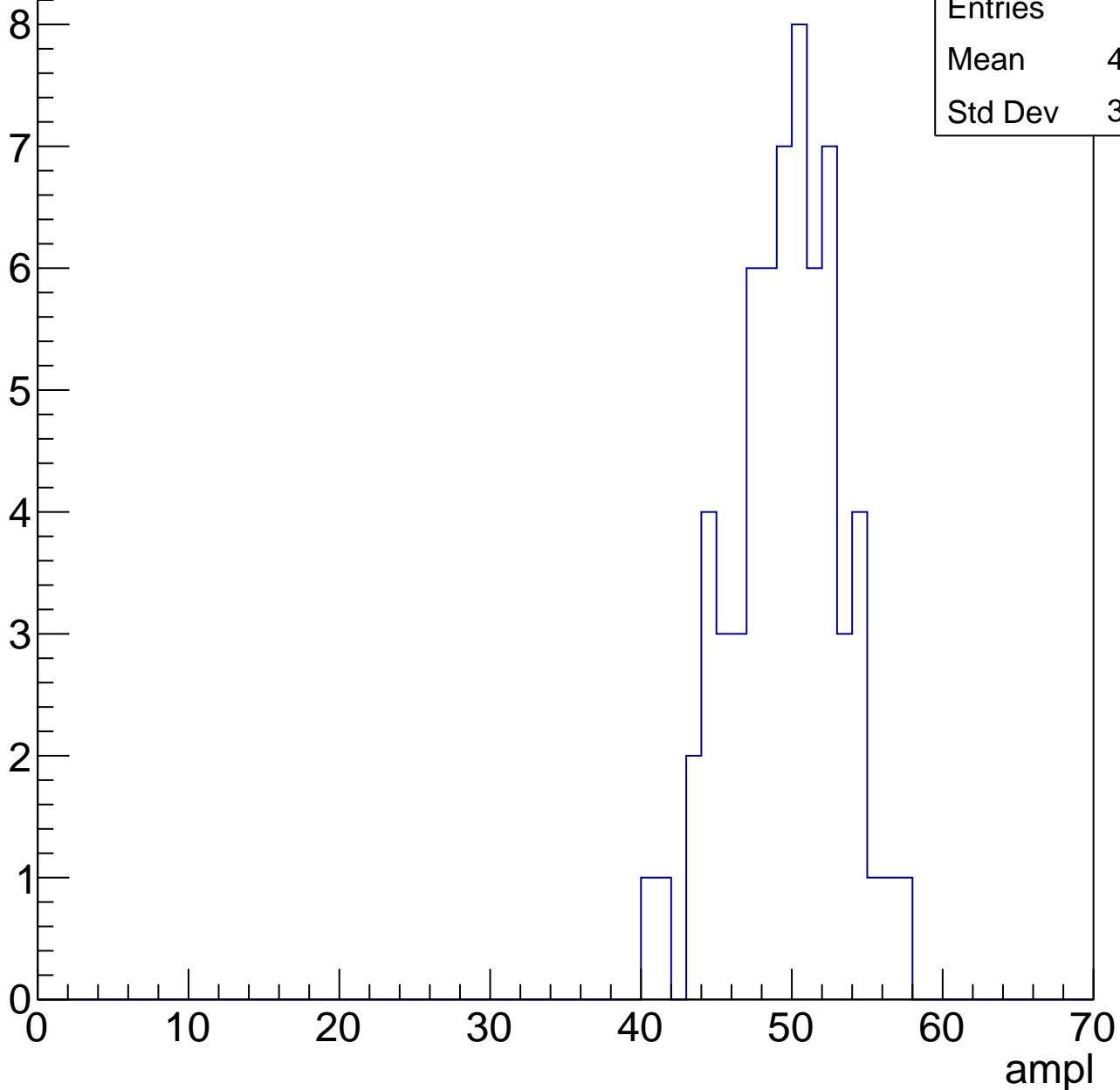


B1L101S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.09
Std Dev	3.565

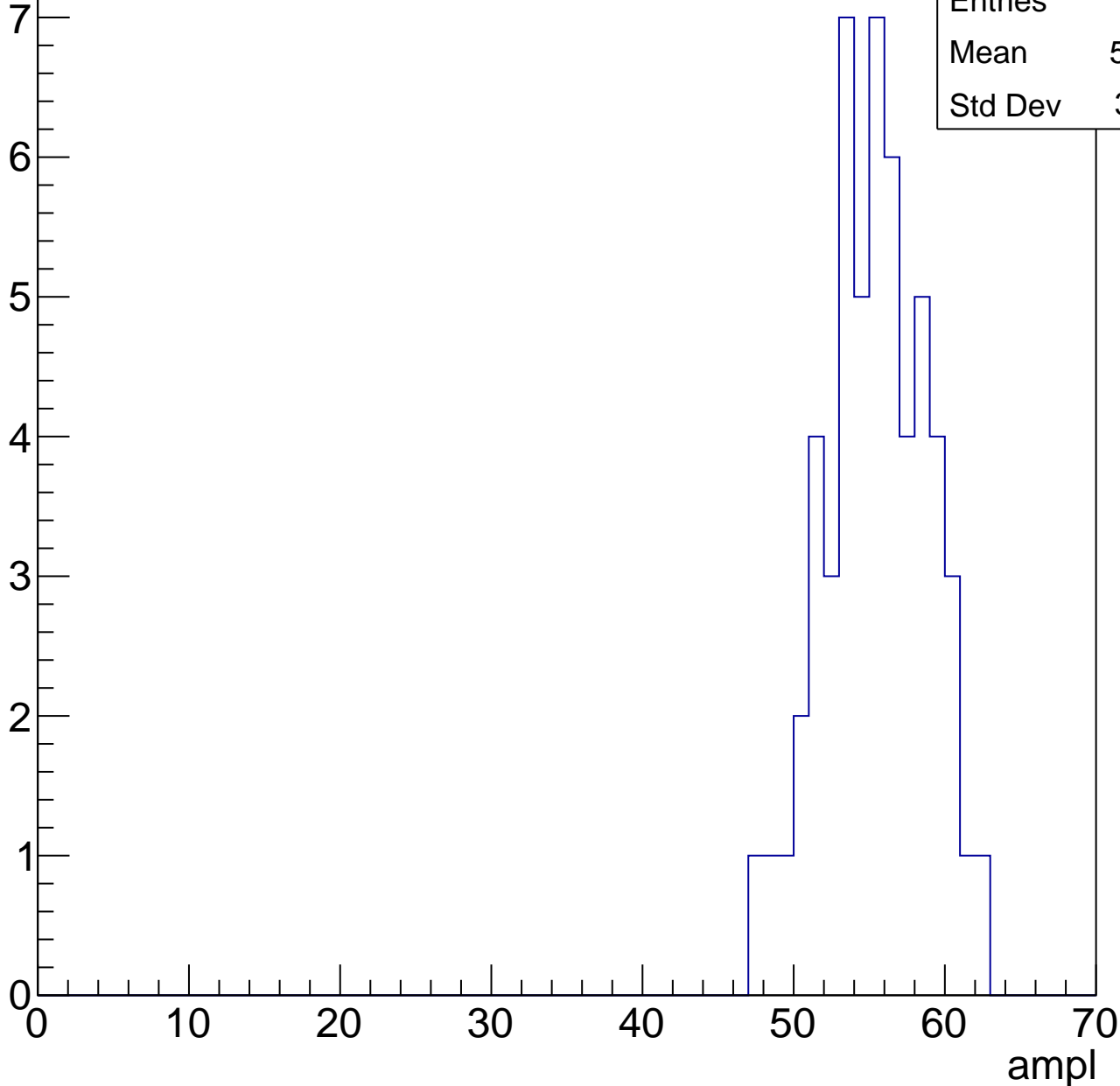


B1L101S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.96
Std Dev	3.341

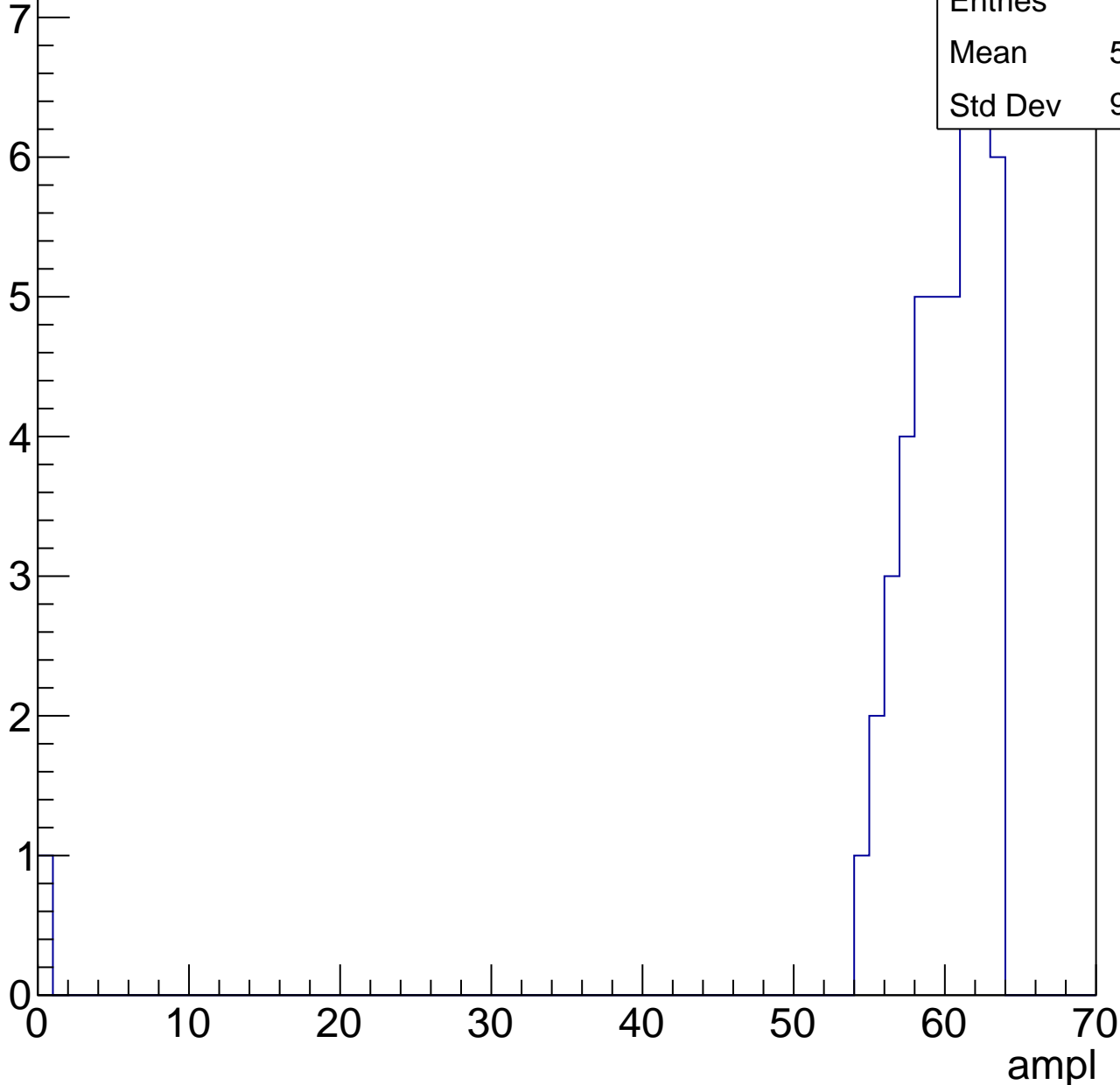


B1L101S, U14-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

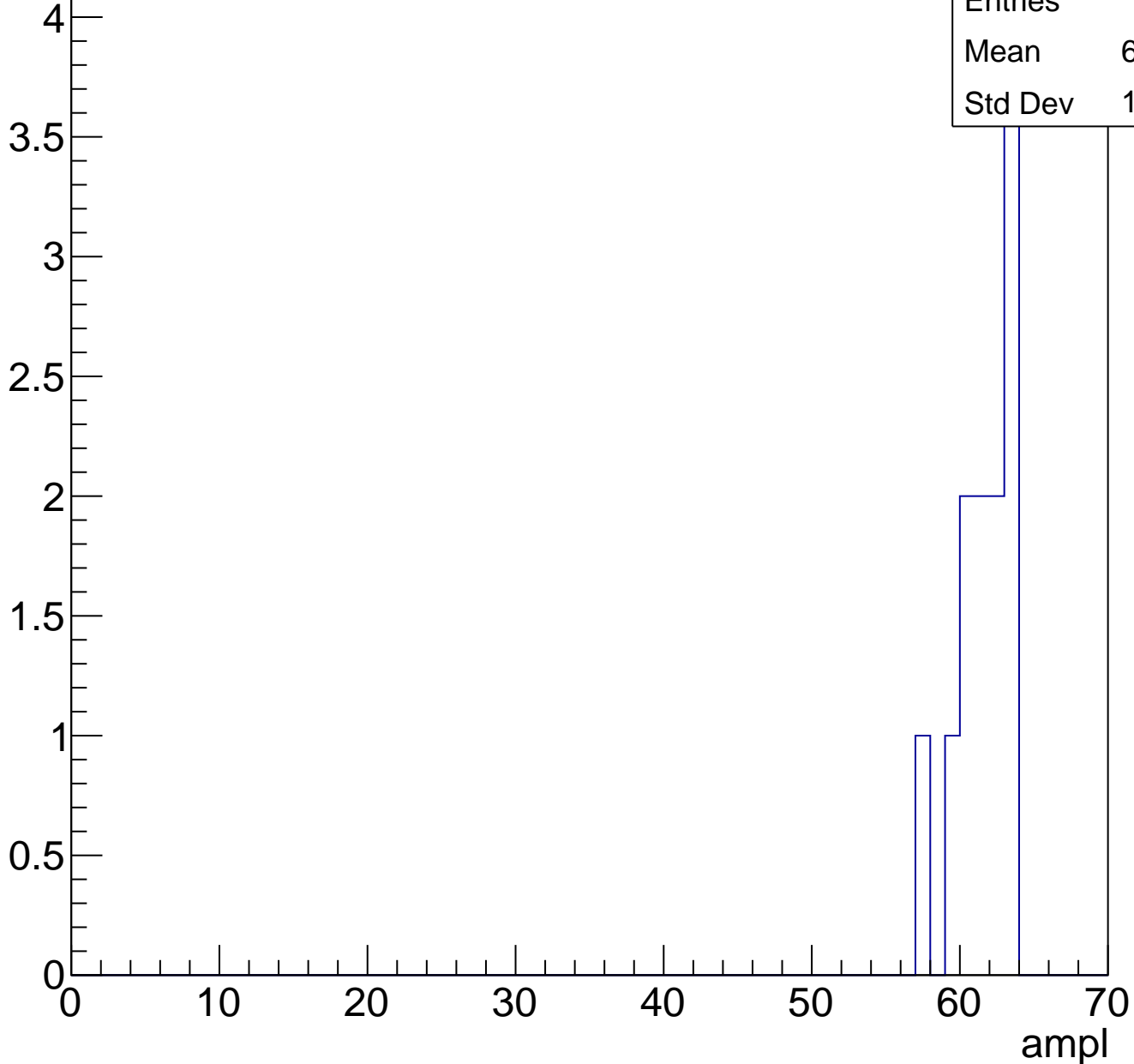
Entries	46
Mean	58.35
Std Dev	9.039



B1L101S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch13, adc0

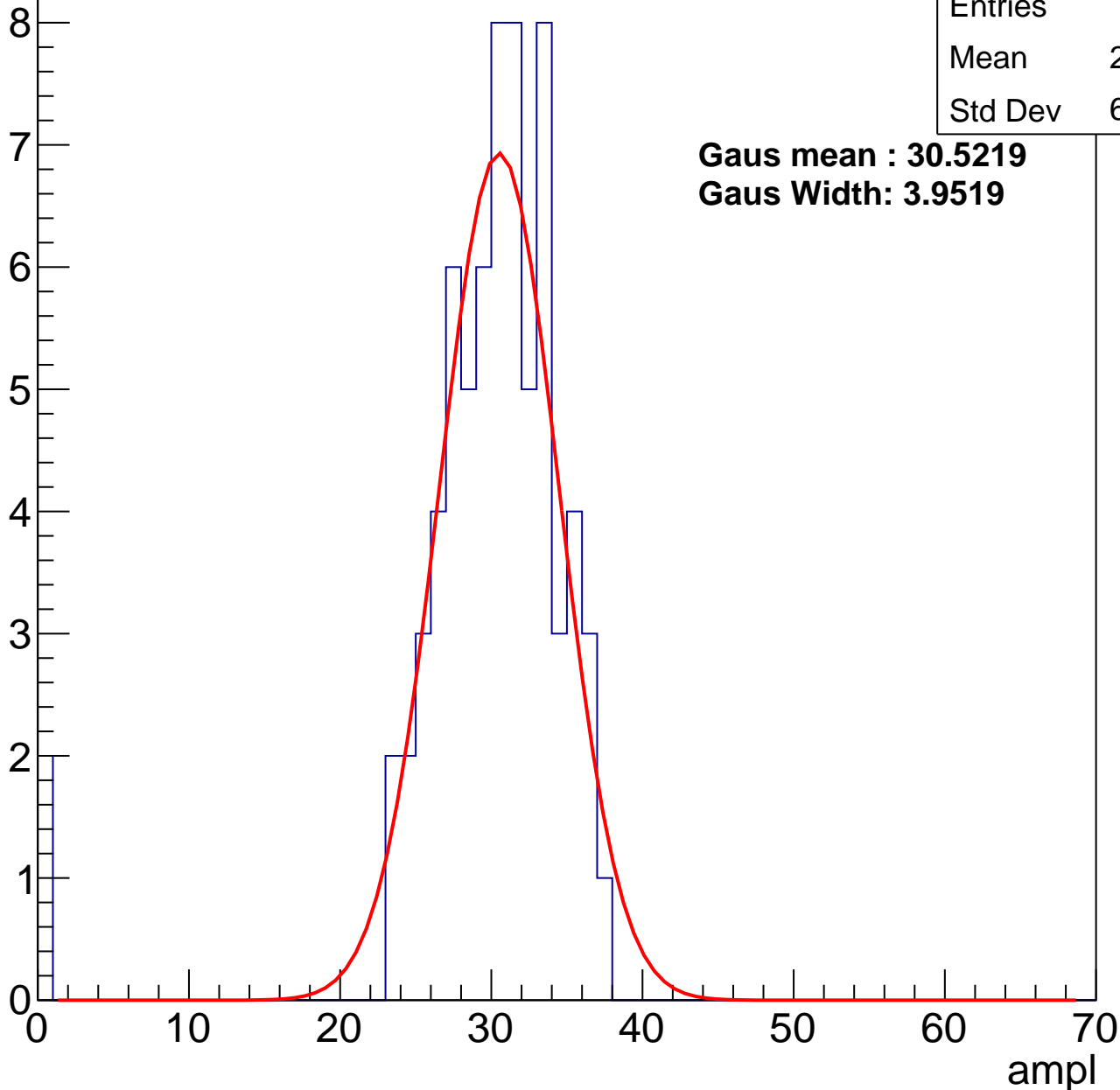
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.26
Std Dev	6.042

Gaus mean : 30.5219

Gaus Width: 3.9519



B1L101S, U14-ch13, adc1

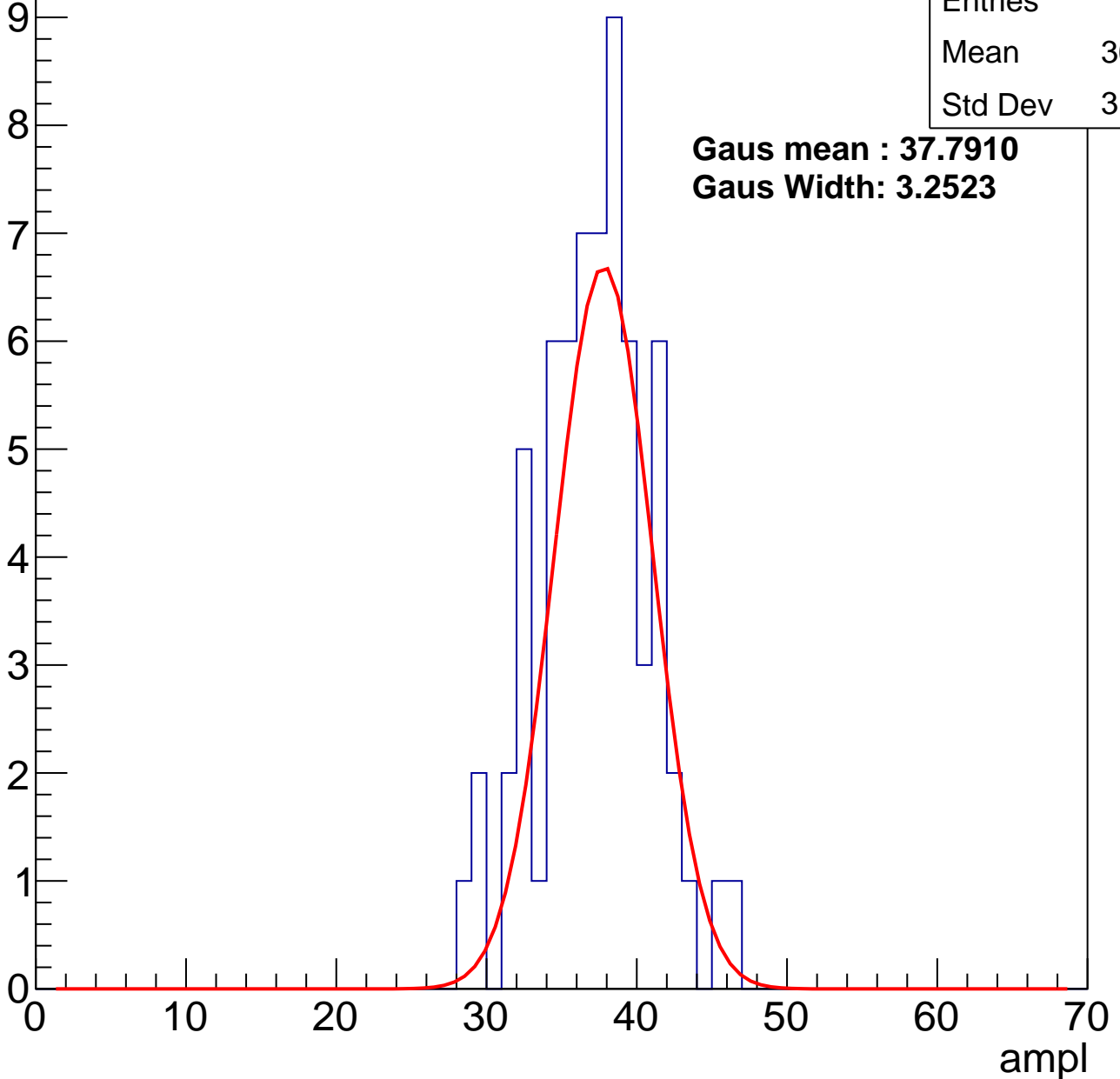
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.76
Std Dev	3.672

Gaus mean : 37.7910

Gaus Width: 3.2523



B1L101S, U14-ch13, adc2

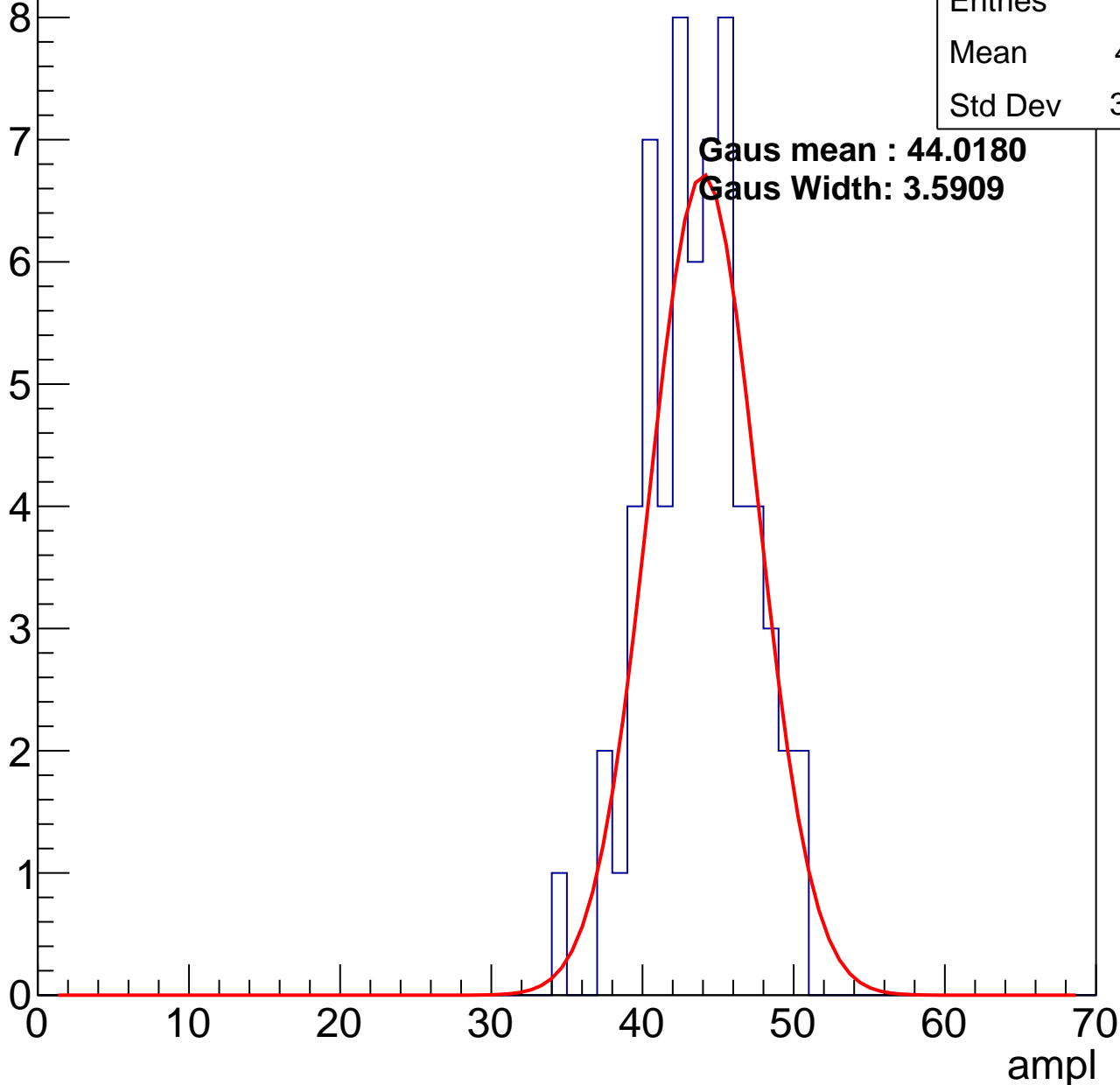
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.21
Std Dev	3.358

Gaus mean : 44.0180

Gaus Width: 3.5909

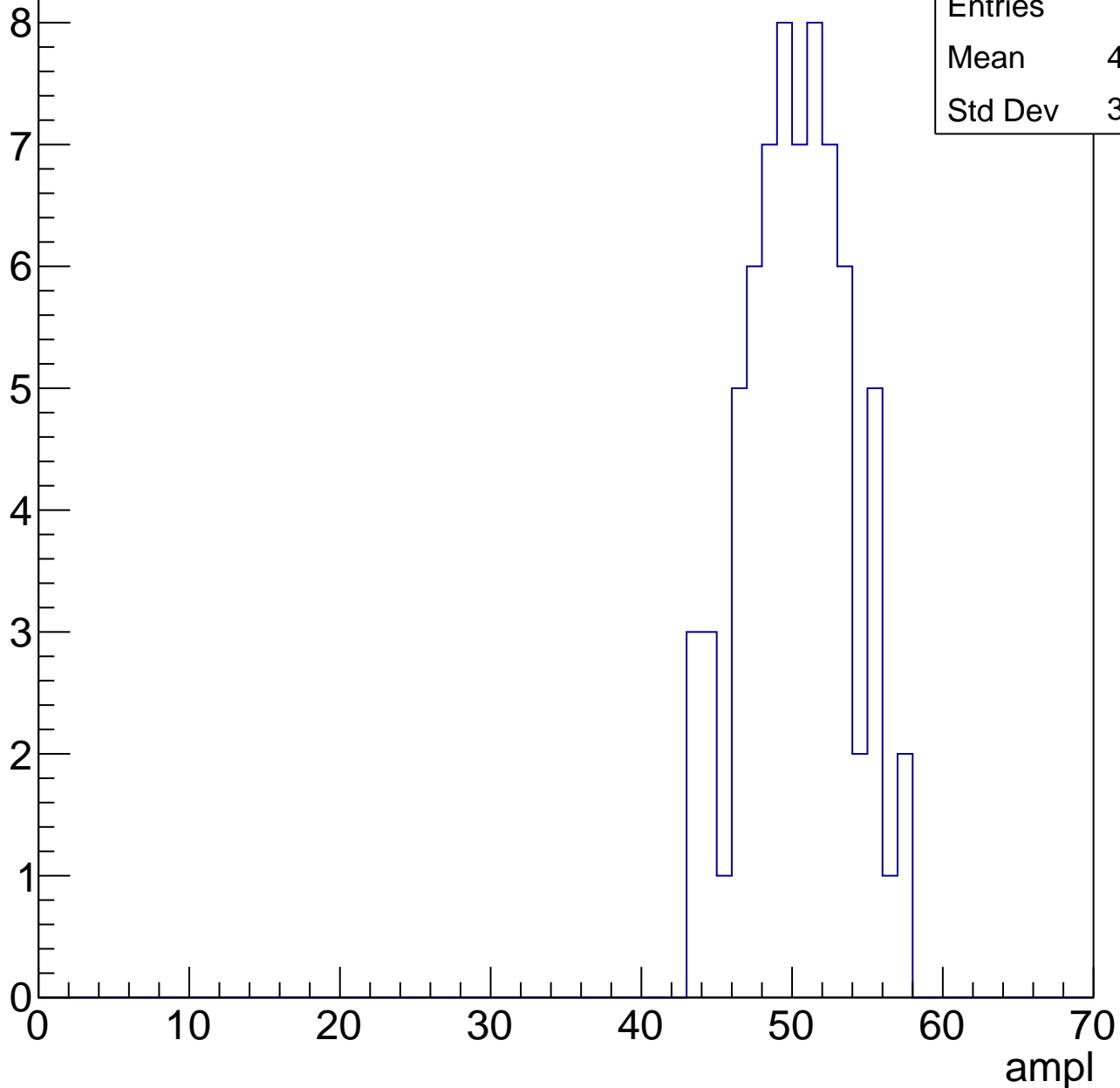


B1L101S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.85
Std Dev	3.418

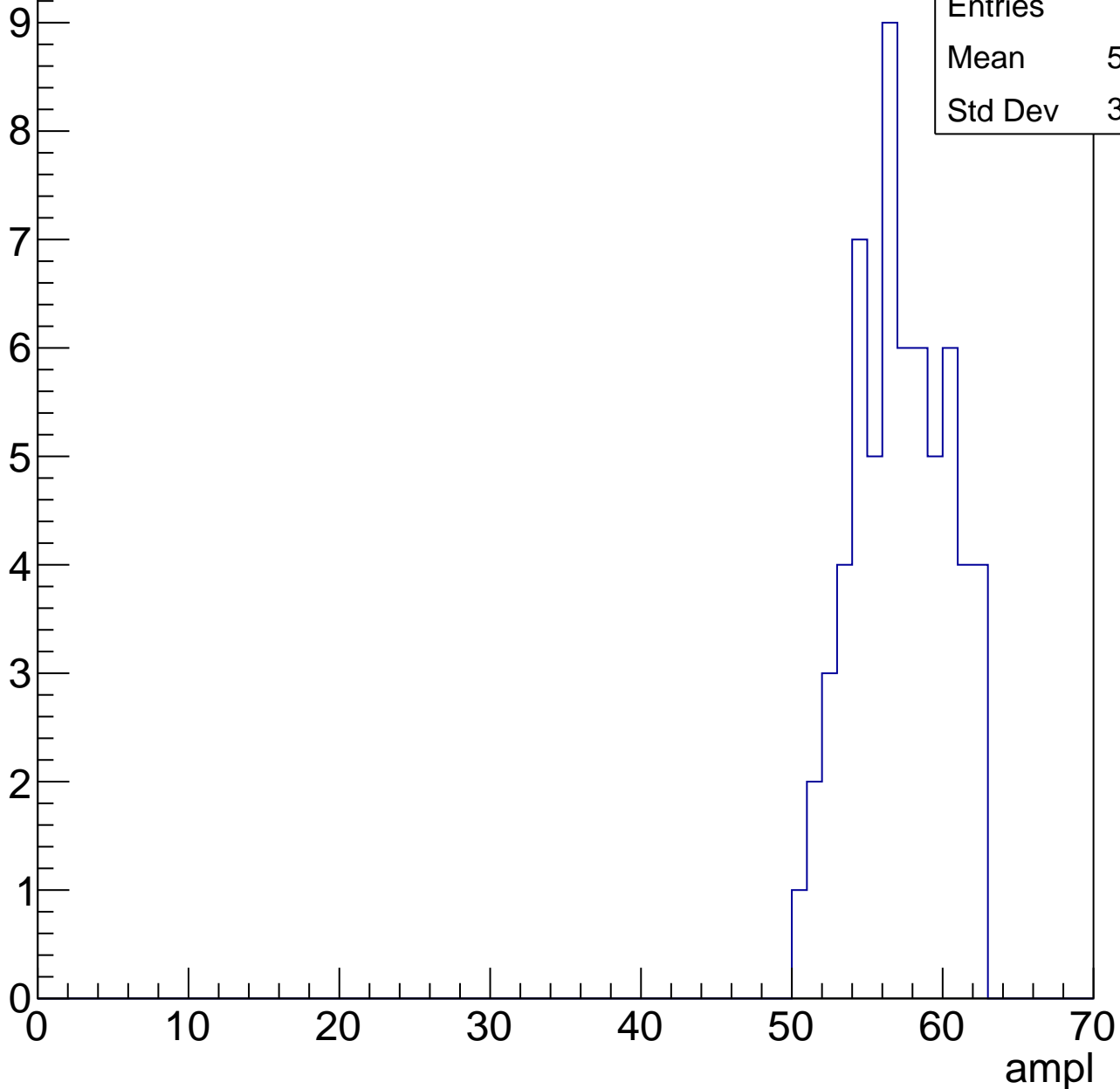


B1L101S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.68
Std Dev	3.084

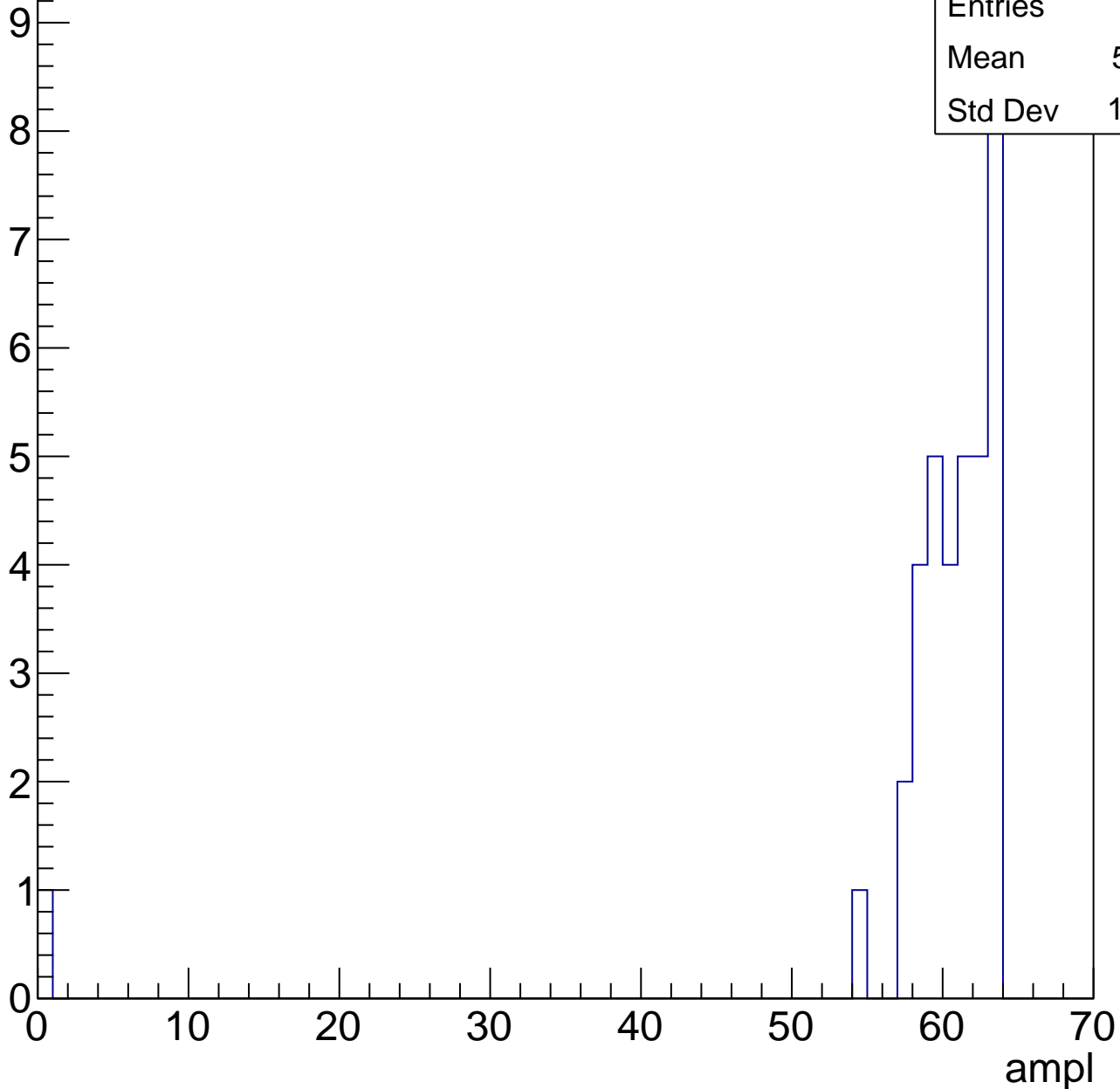


B1L101S, U14-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.81
Std Dev	10.18



B1L101S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch14, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	29.97
Std Dev	5.126

Gaus mean : 30.1186

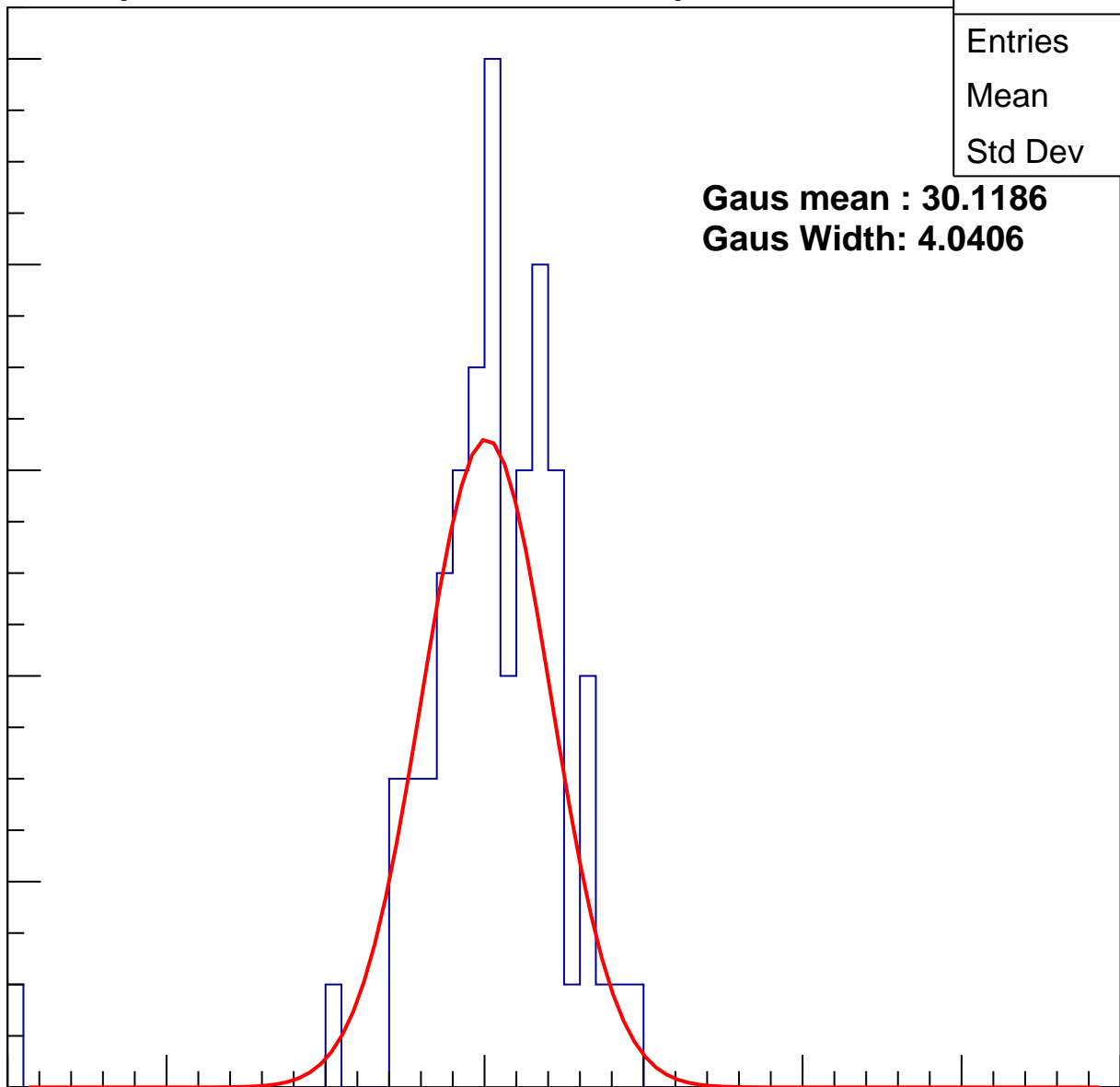
Gaus Width: 4.0406

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch14, adc1

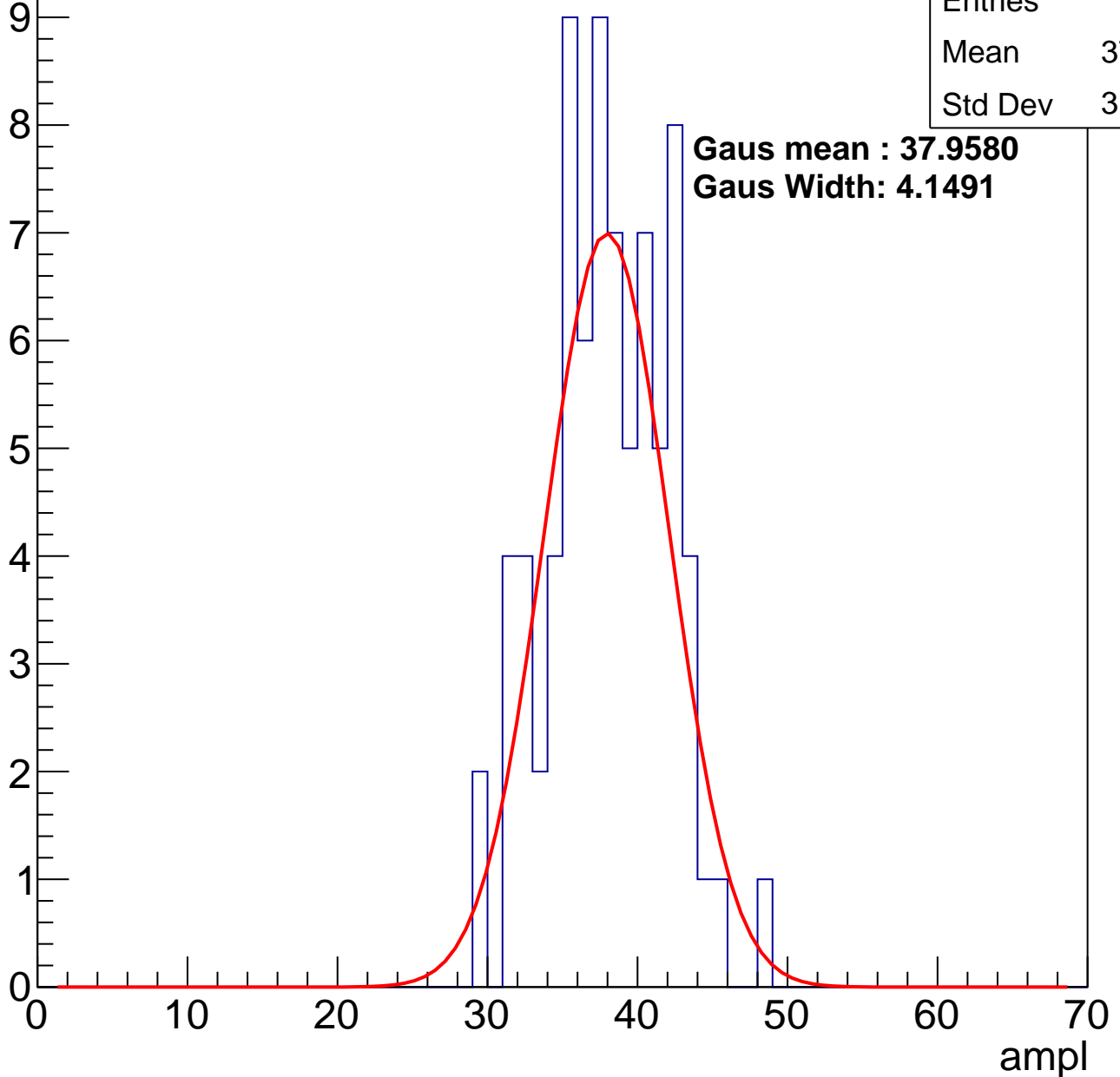
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	37.56
Std Dev	3.906

Gaus mean : 37.9580

Gaus Width: 4.1491



B1L101S, U14-ch14, adc2

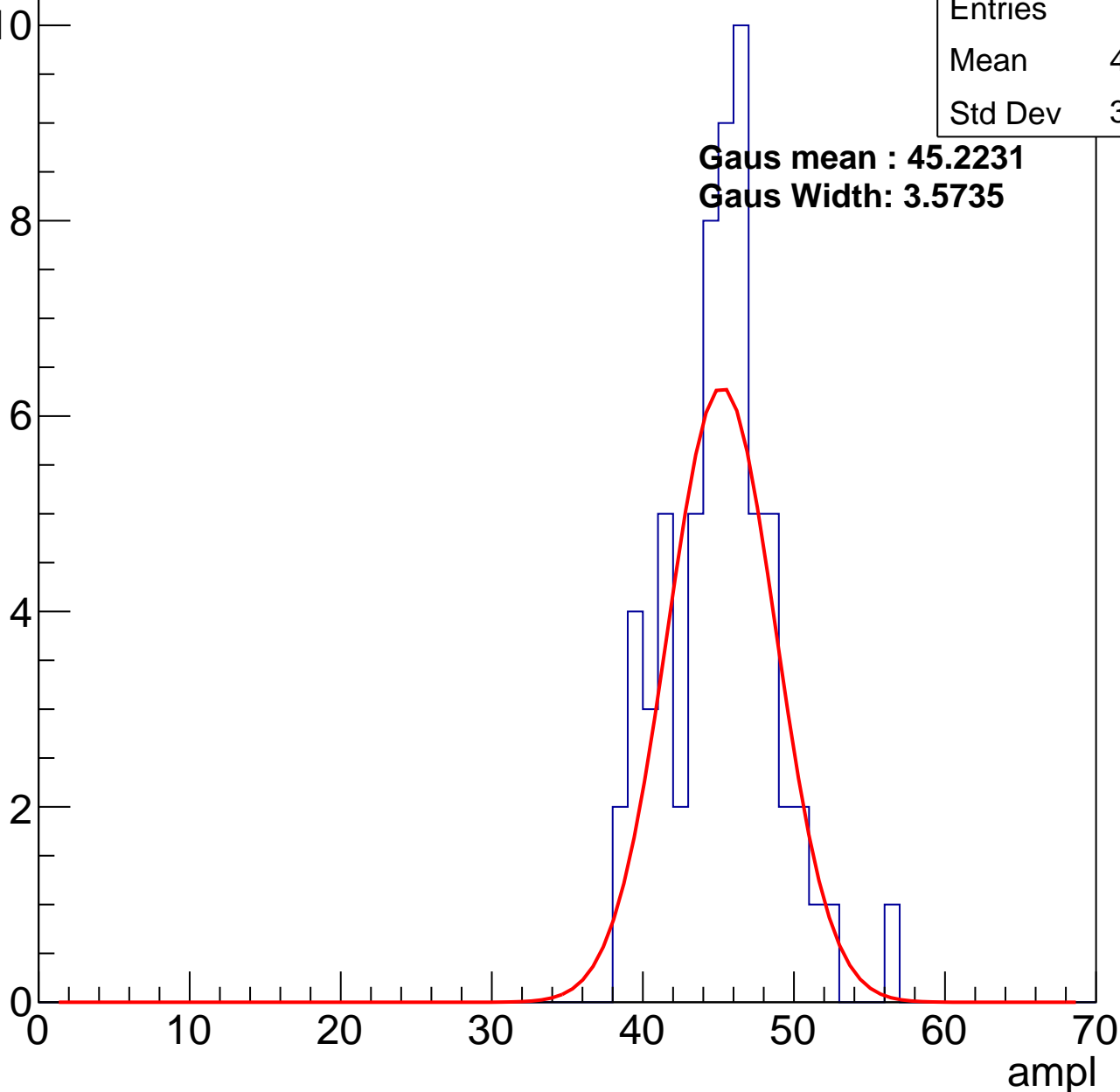
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.69
Std Dev	3.508

Gaus mean : 45.2231

Gaus Width: 3.5735

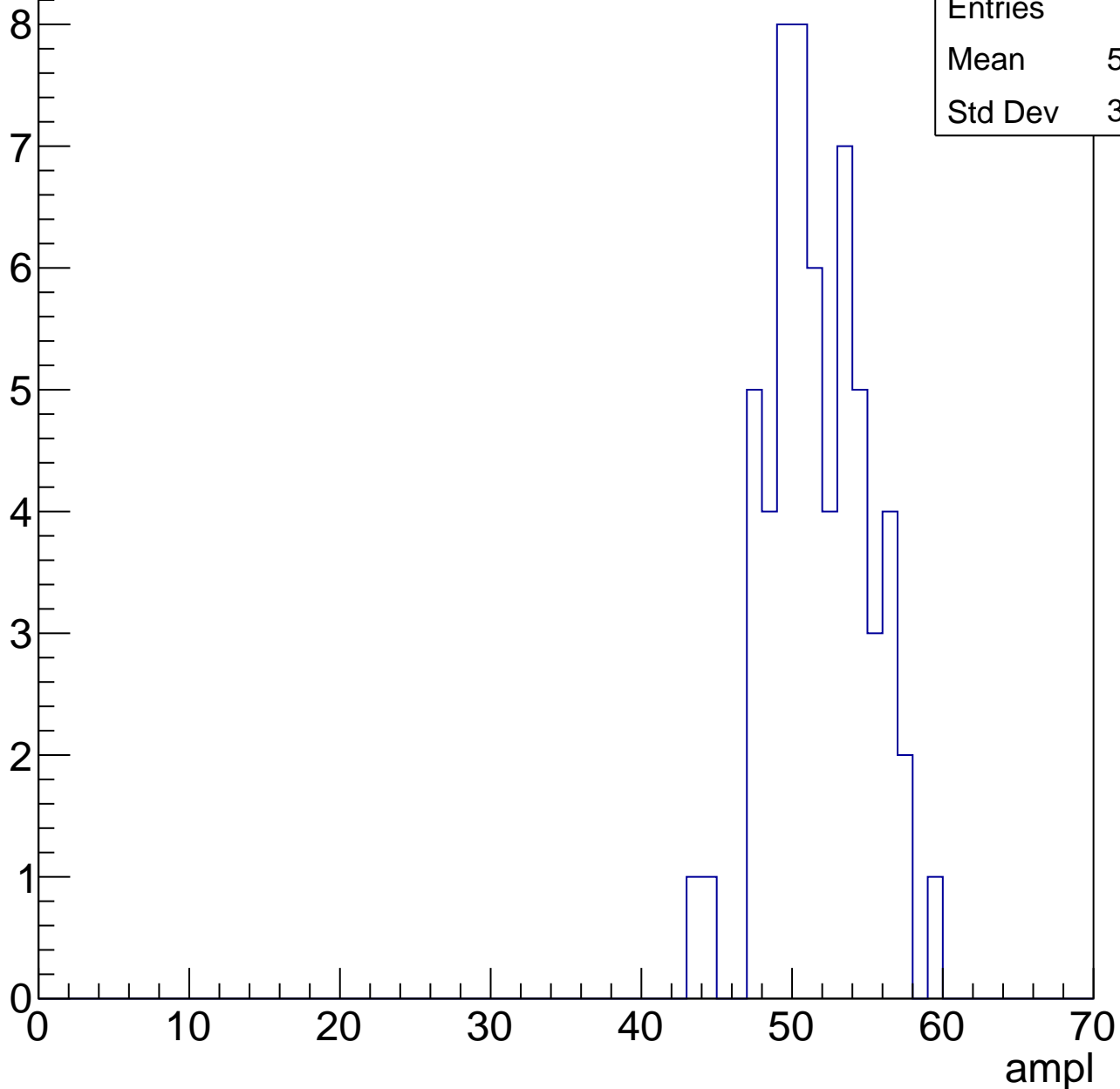


B1L101S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

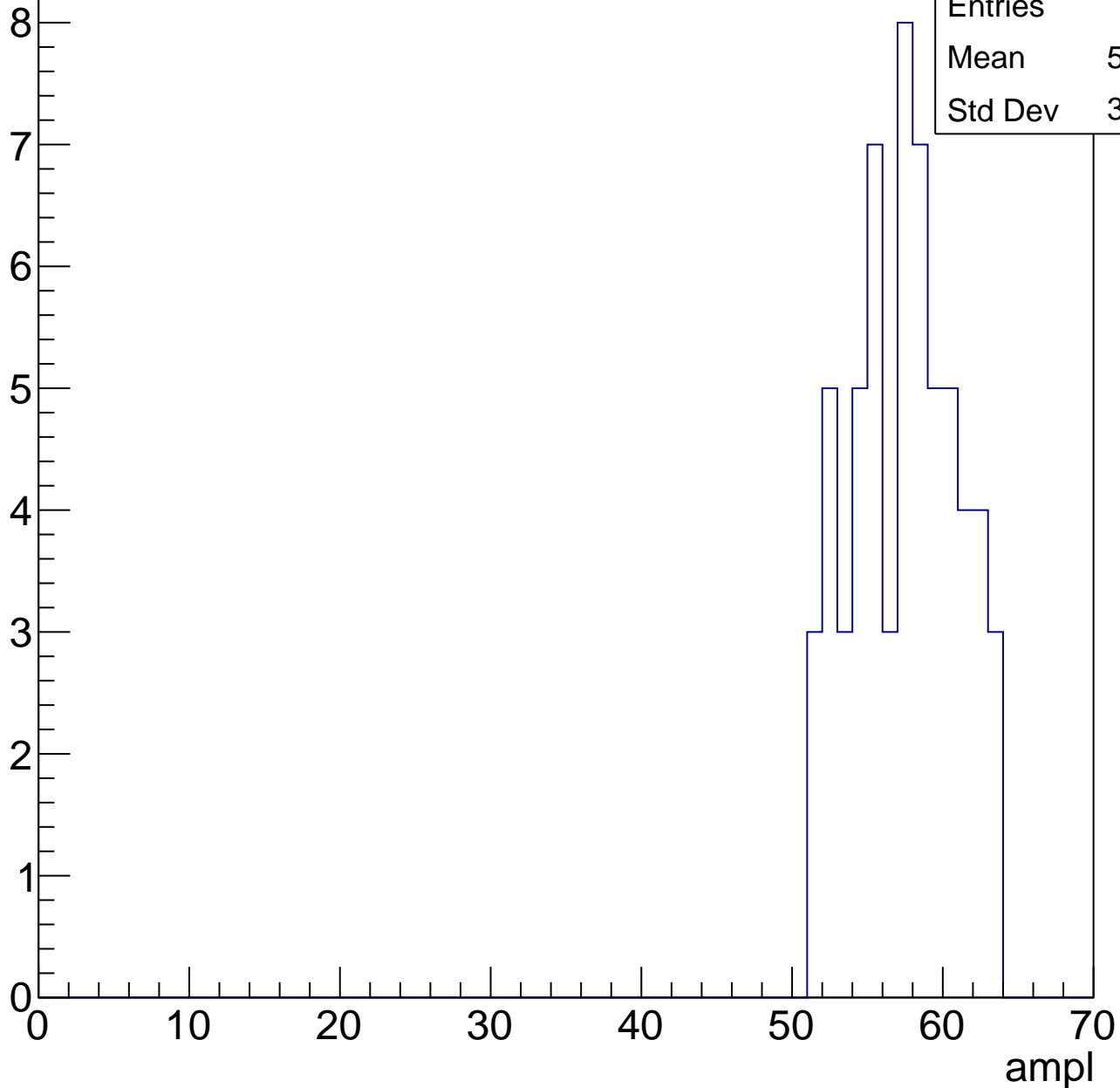
Entries	59
Mean	51.24
Std Dev	3.259



B1L101S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

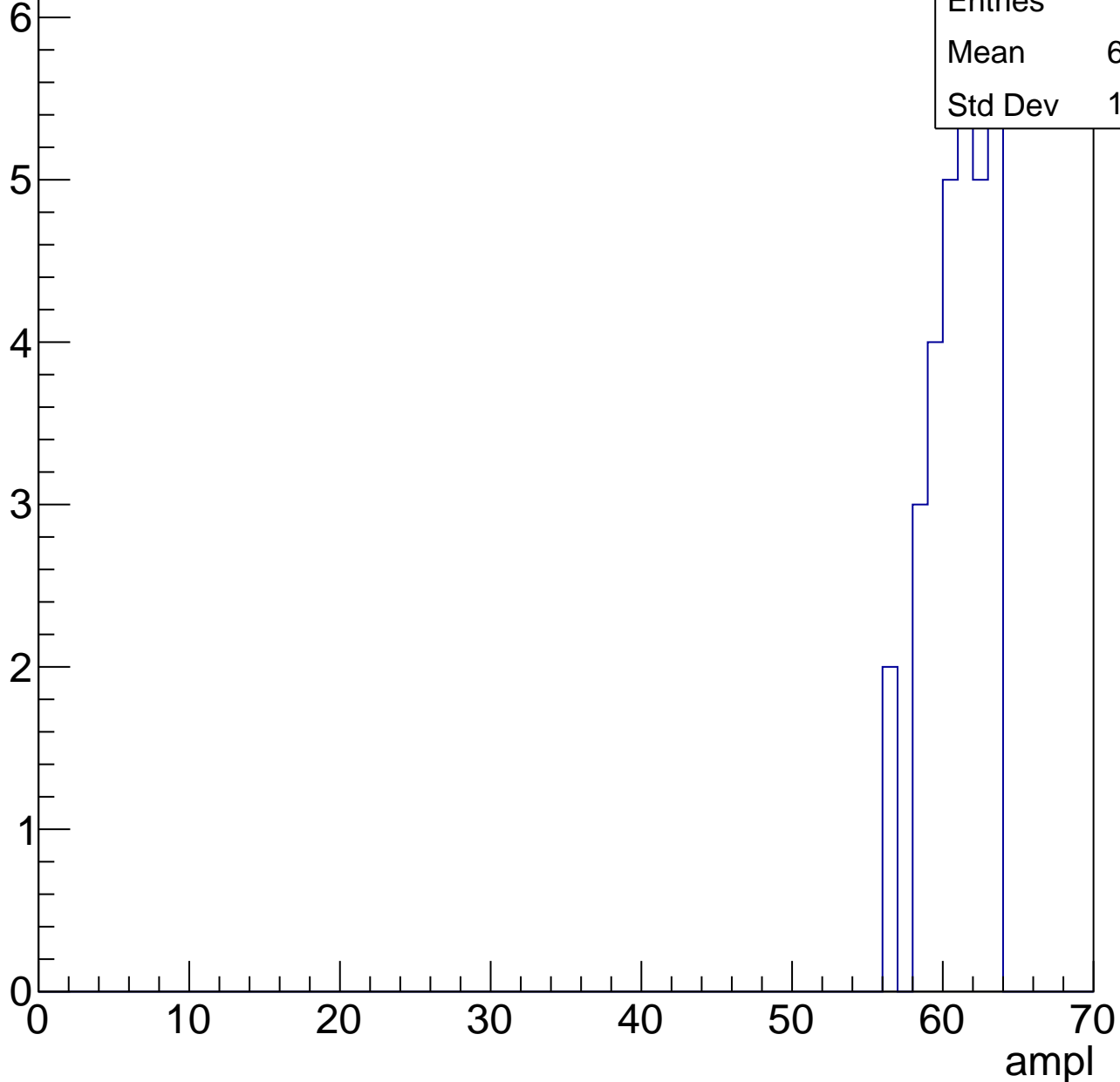
Entry



B1L101S, U14-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch15, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	28.62
Std Dev	7.084

Gaus mean : 29.2715

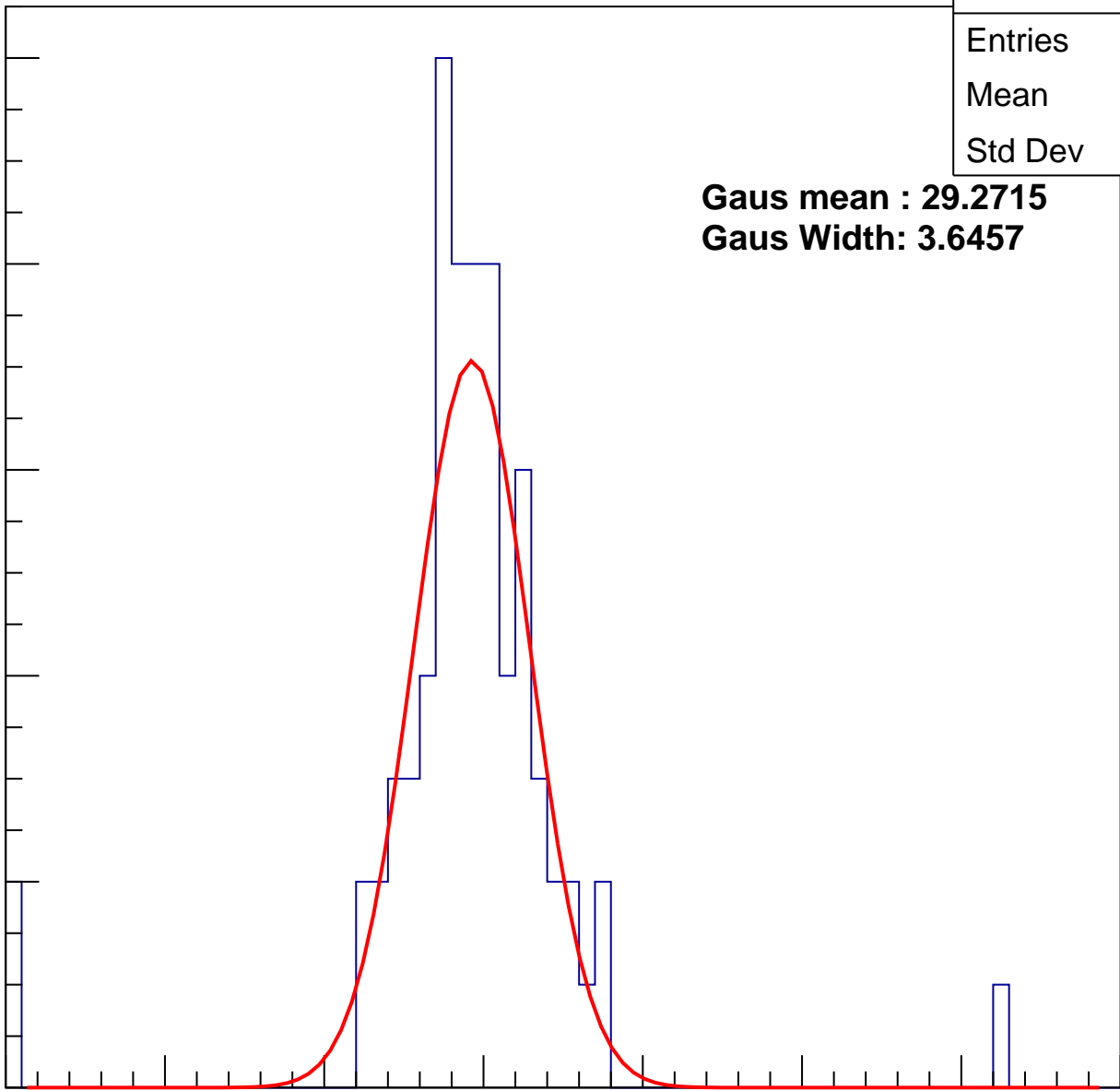
Gaus Width: 3.6457

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch15, adc1

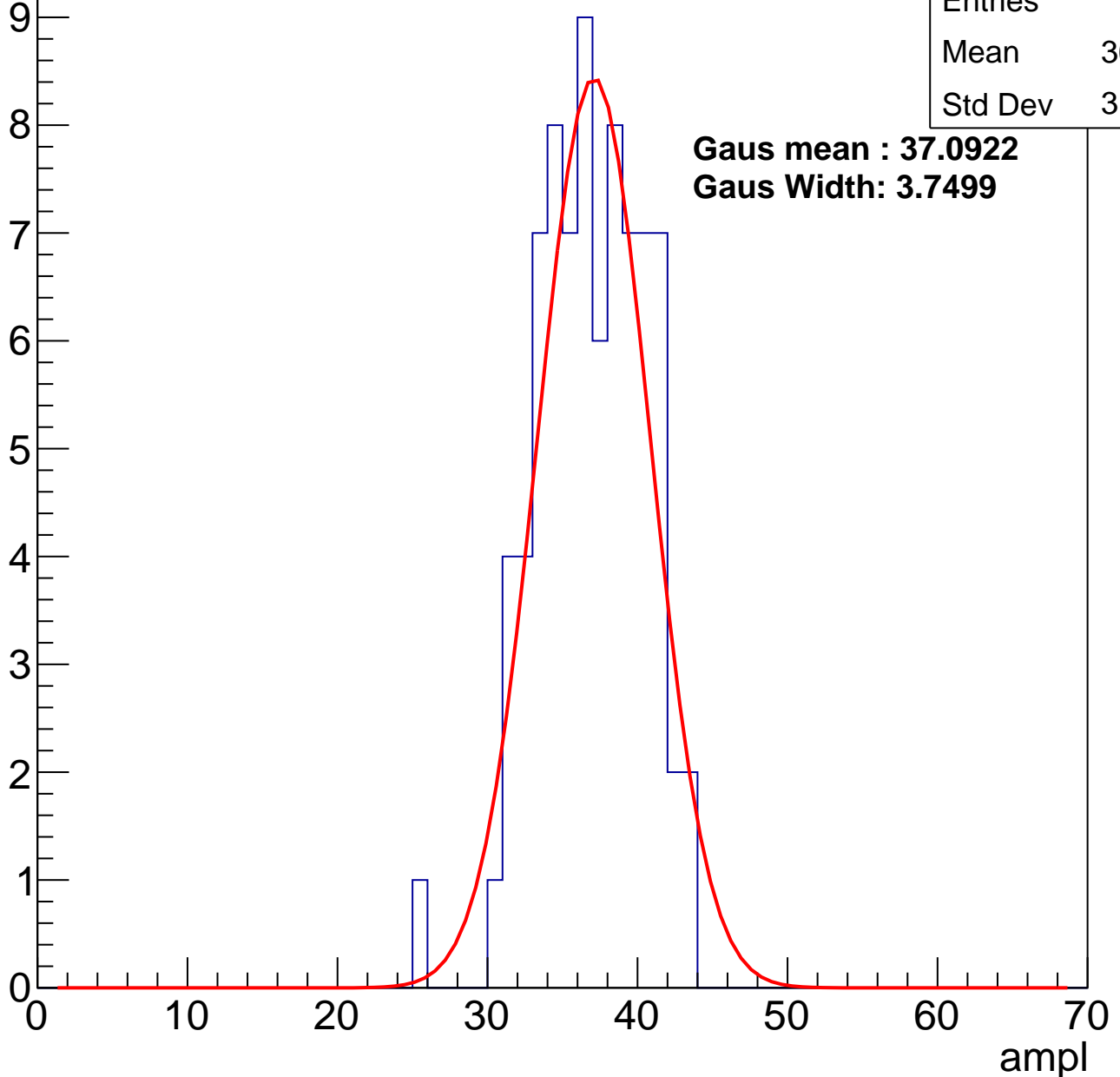
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.44
Std Dev	3.474

Gaus mean : 37.0922

Gaus Width: 3.7499



B1L101S, U14-ch15, adc2

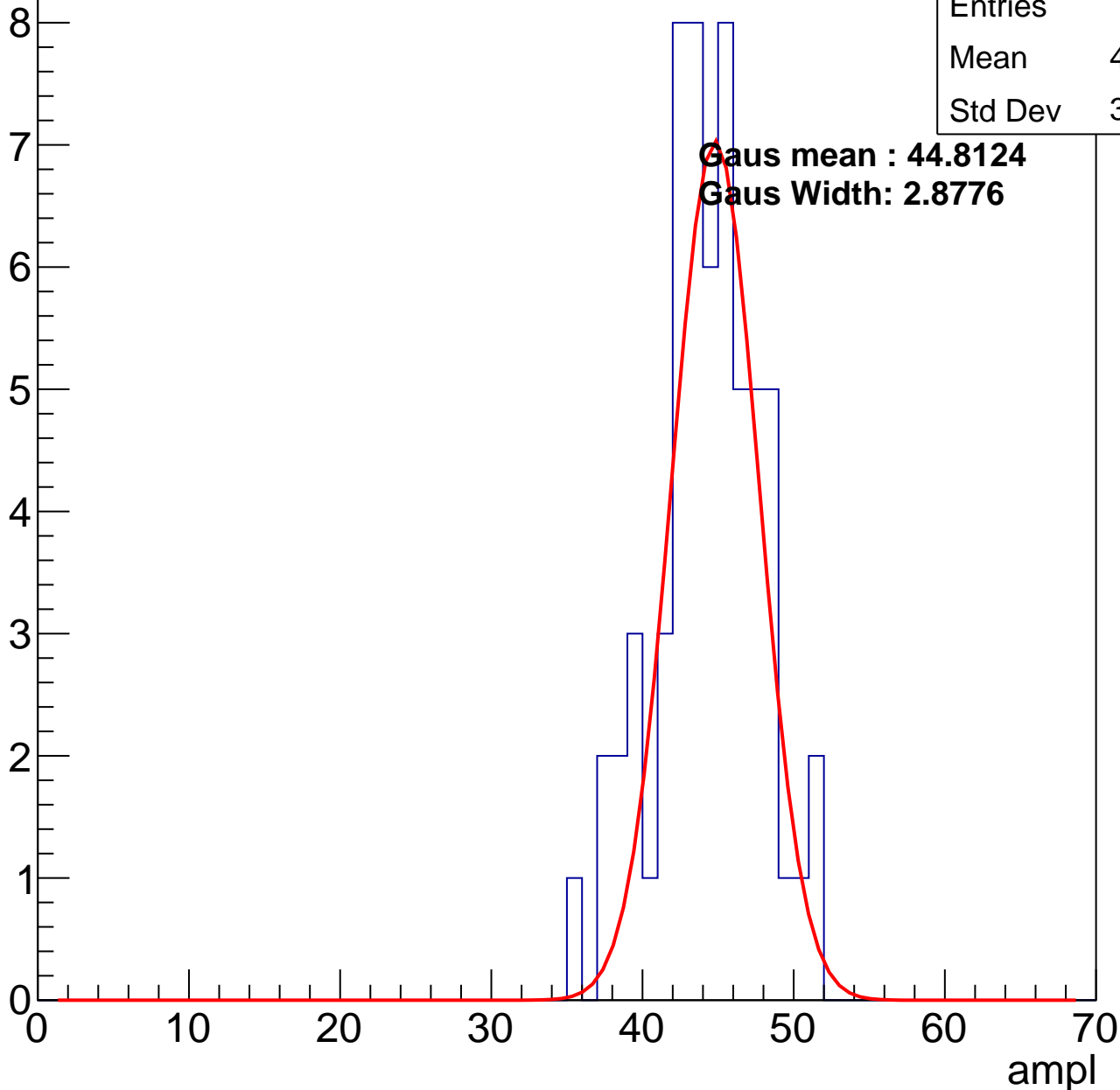
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.85
Std Dev	3.435

Gaus mean : 44.8124

Gaus Width: 2.8776

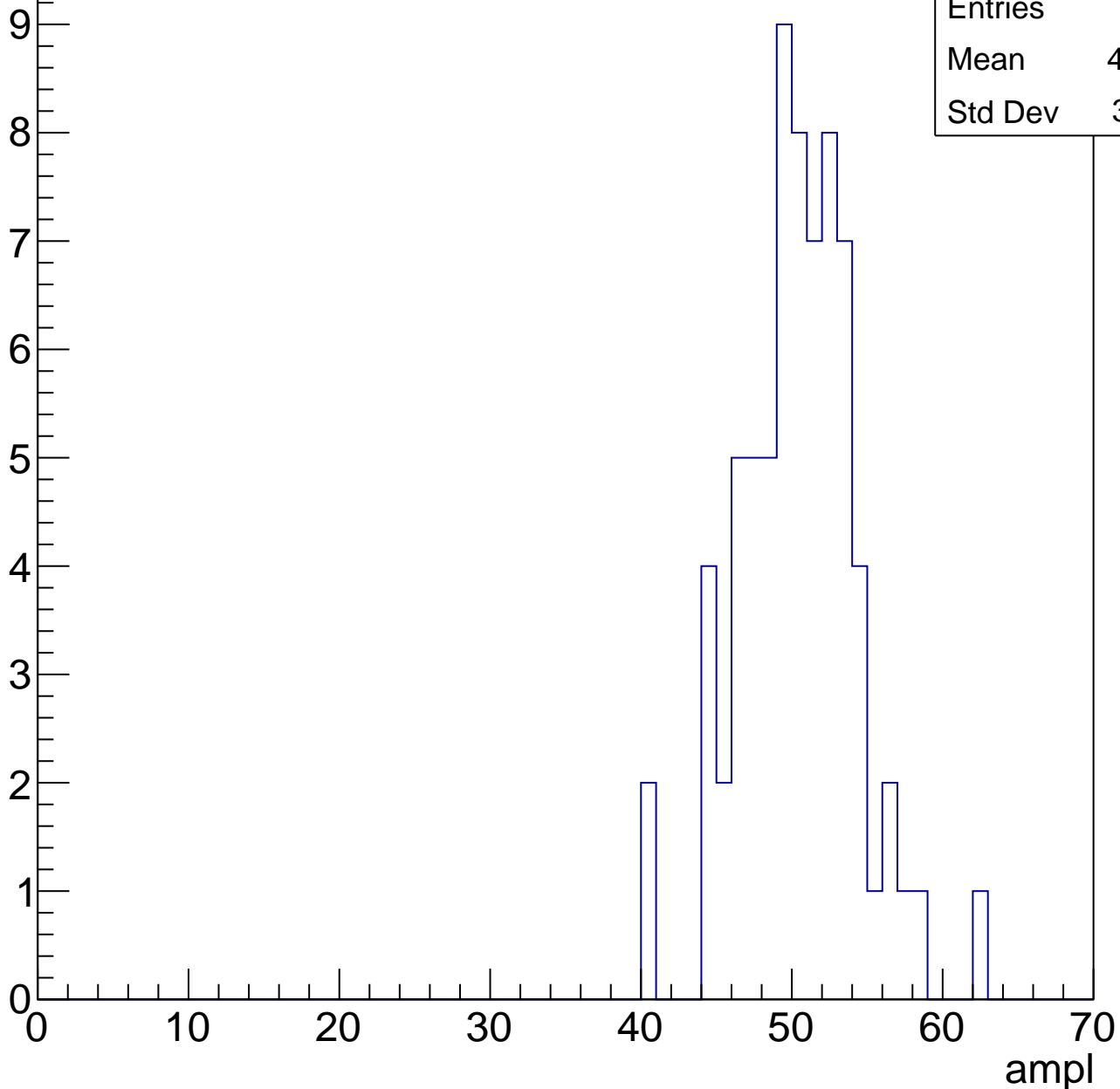


B1L101S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	49.94
Std Dev	3.851

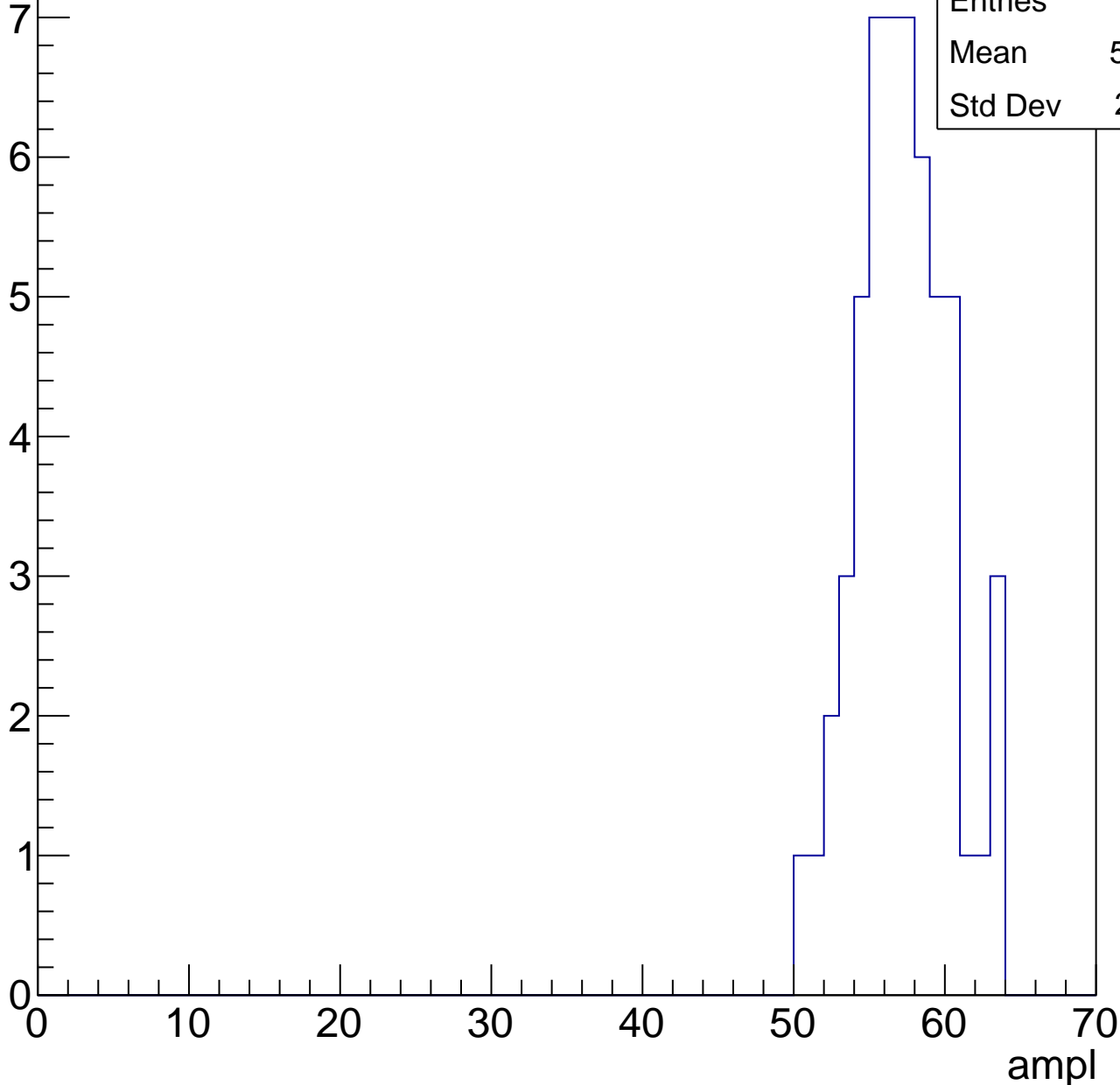


B1L101S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.76
Std Dev	2.981

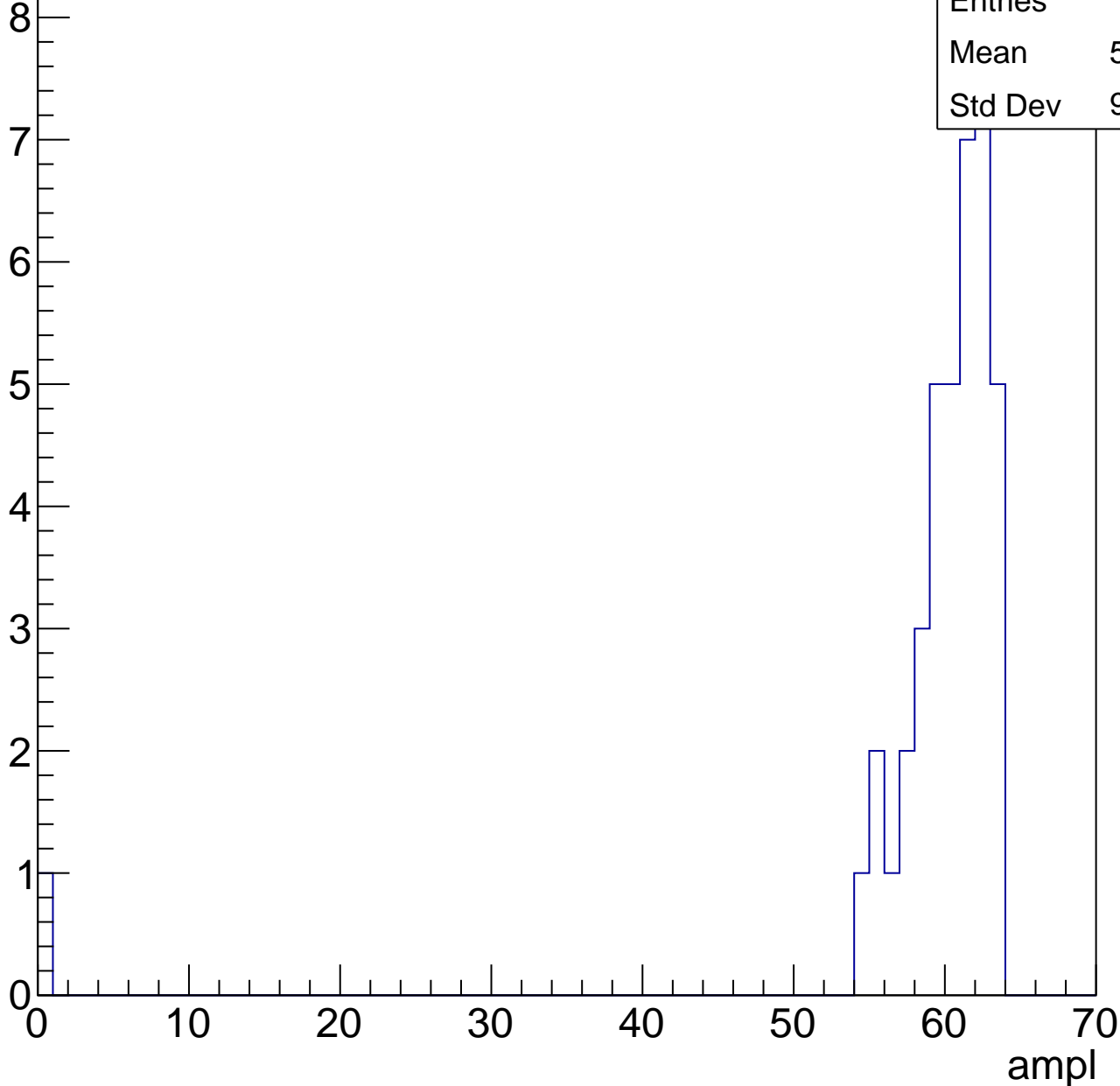


B1L101S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.52
Std Dev	9.662



B1L101S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch16, adc0

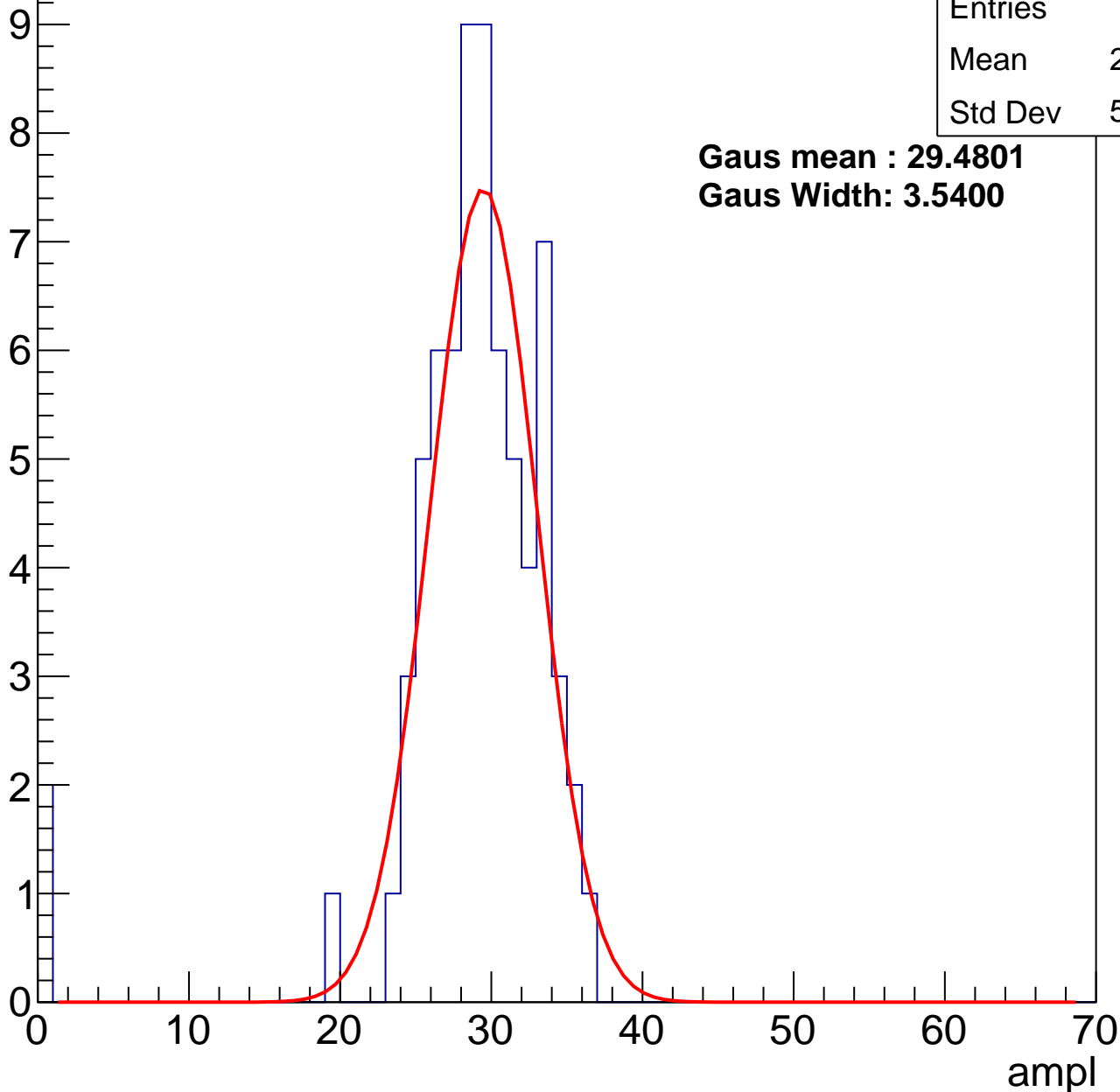
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.17
Std Dev	5.833

Gaus mean : 29.4801

Gaus Width: 3.5400



B1L101S, U14-ch16, adc1

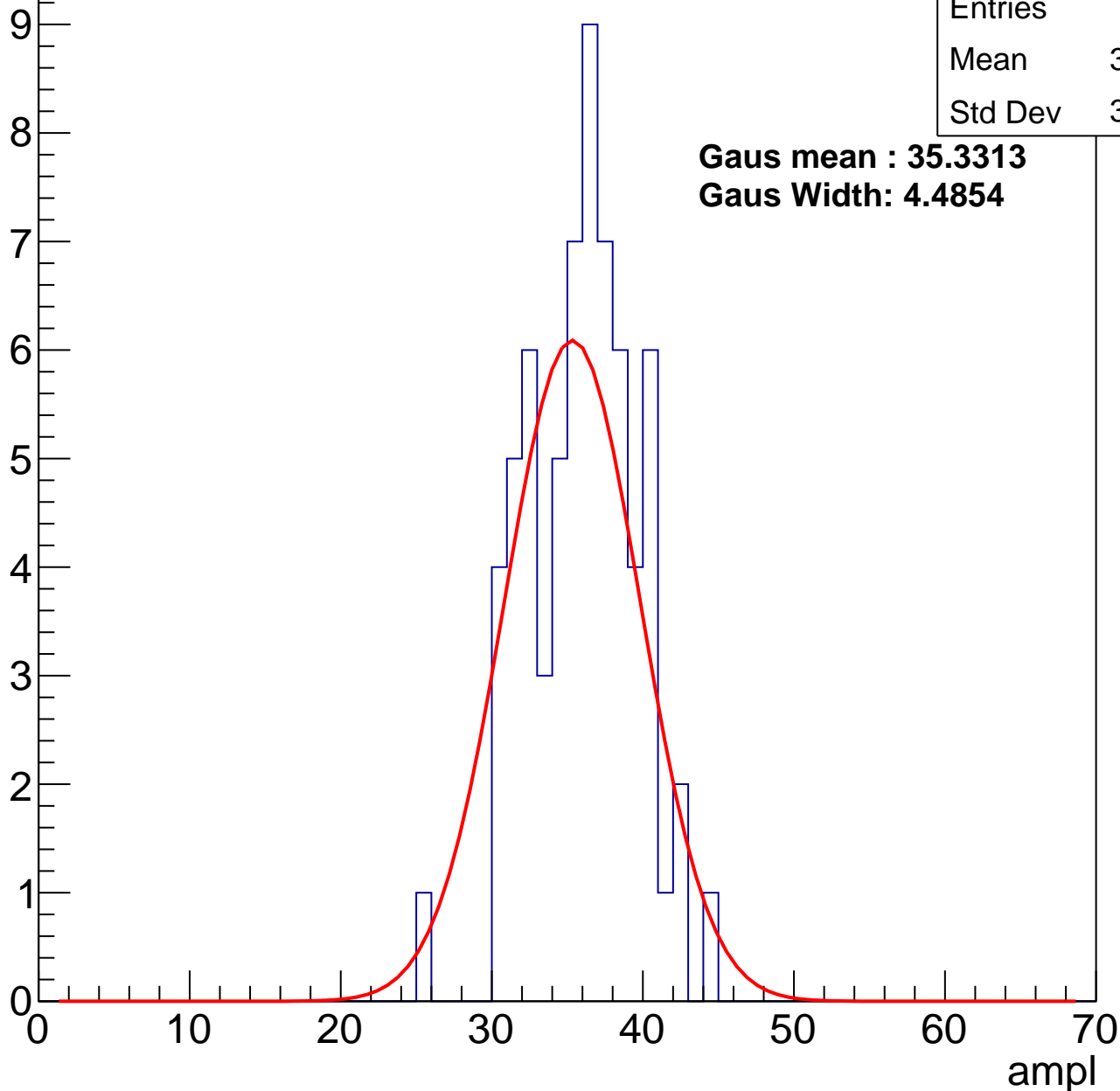
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.55
Std Dev	3.563

Gaus mean : 35.3313

Gaus Width: 4.4854



B1L101S, U14-ch16, adc2

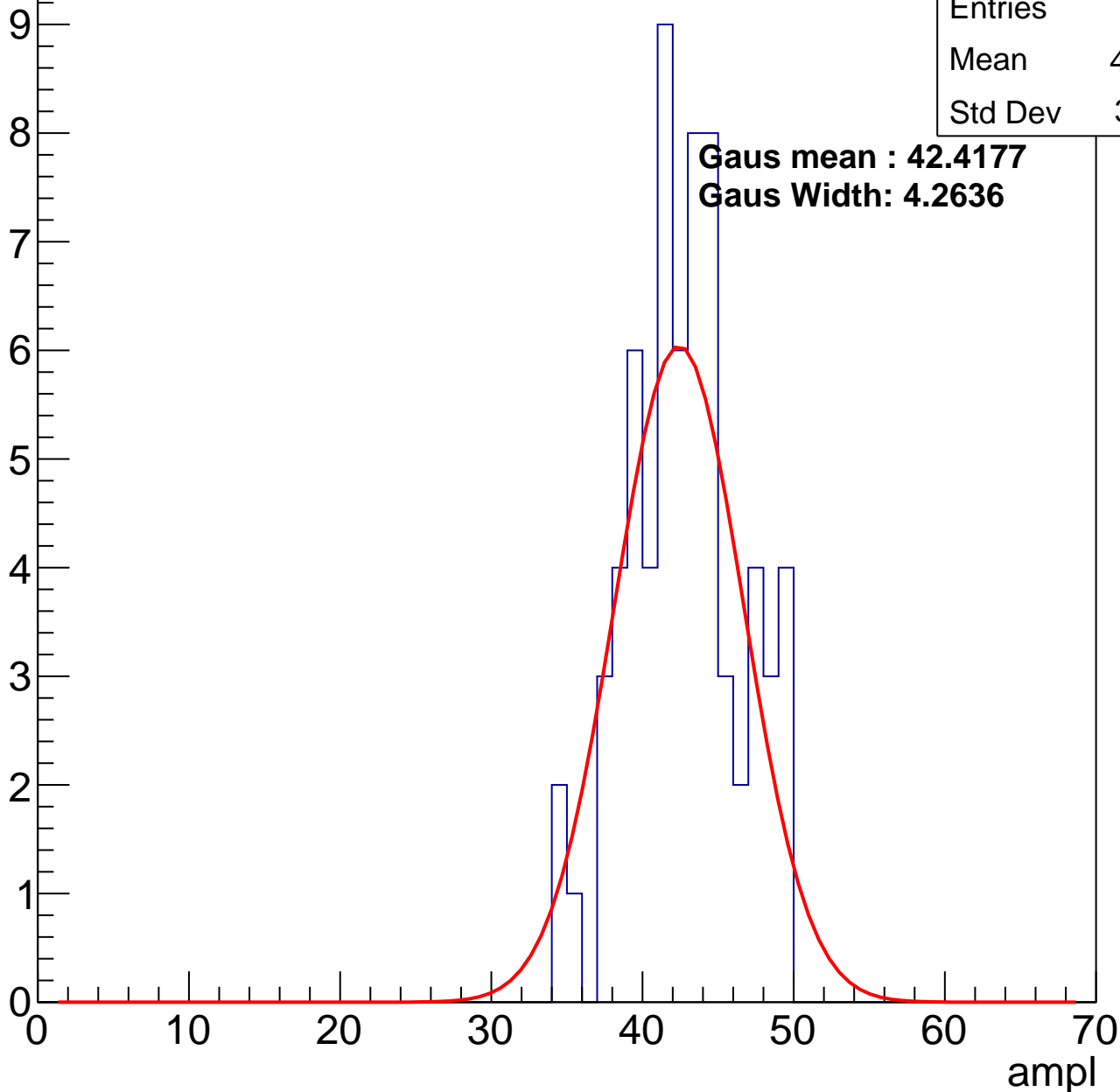
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.27
Std Dev	3.651

Gaus mean : 42.4177

Gaus Width: 4.2636

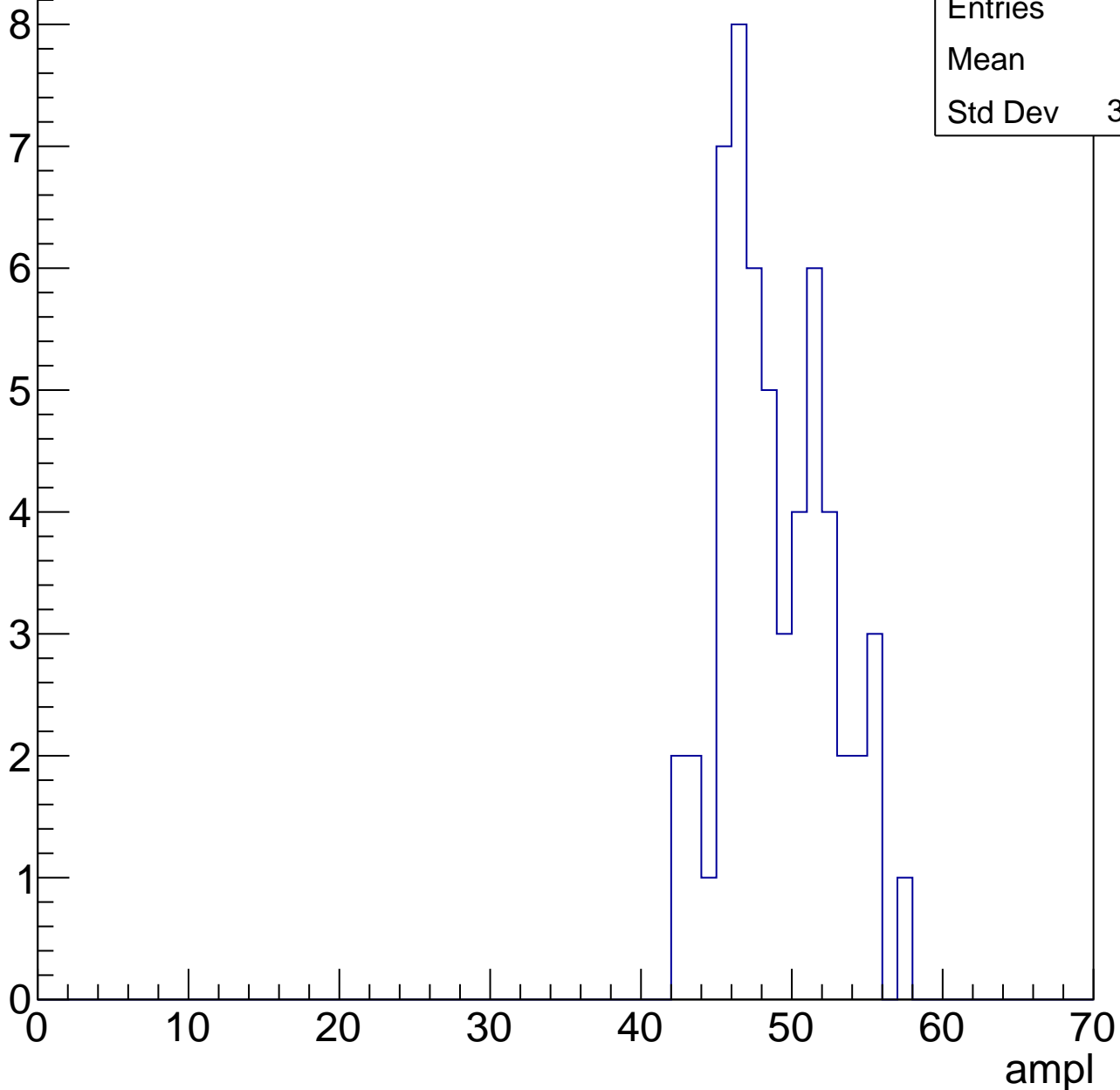


B1L101S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

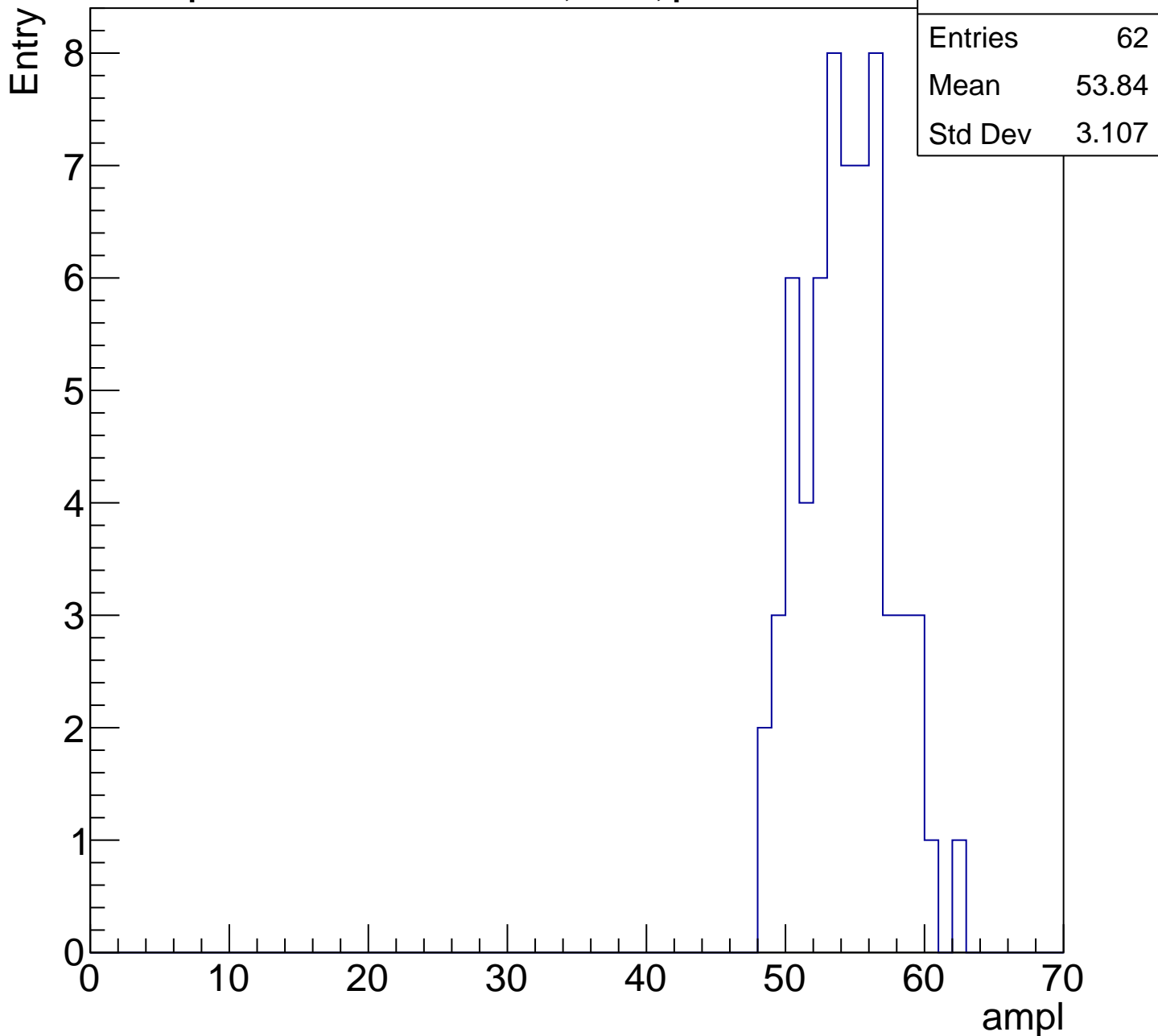
Entry

Entries	56
Mean	48.5
Std Dev	3.566



B1L101S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

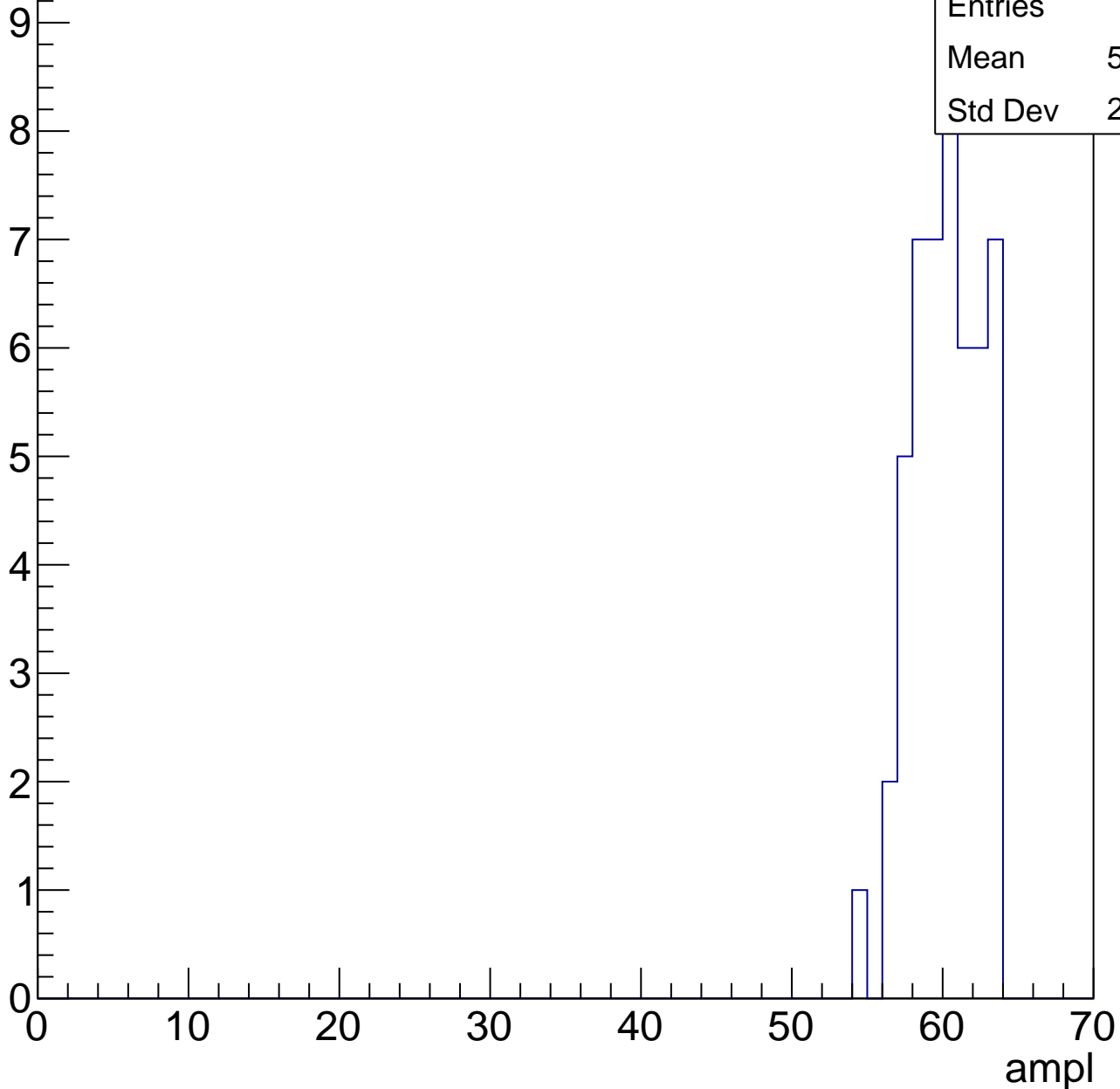


B1L101S, U14-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

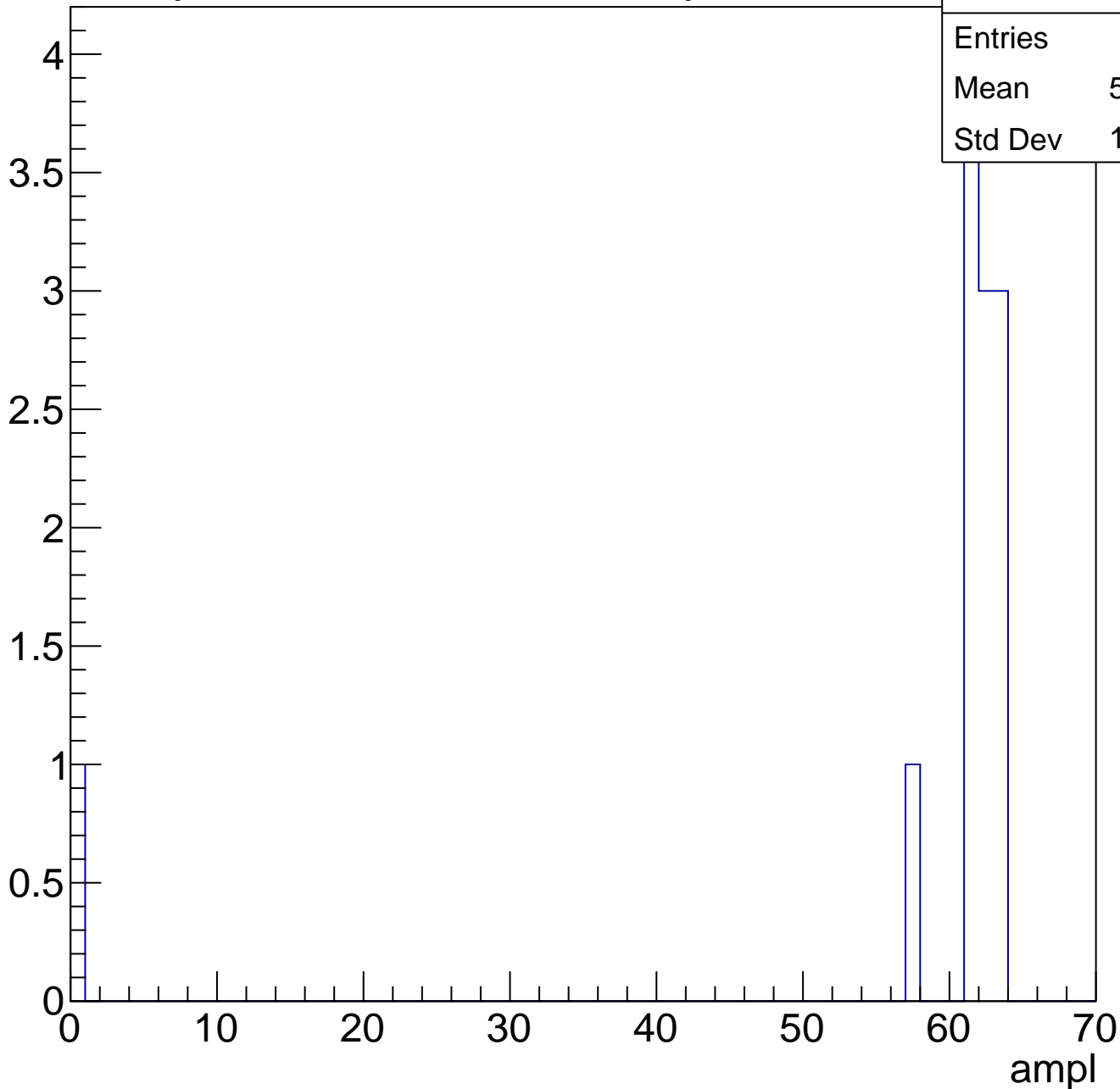
Entries	50
Mean	59.78
Std Dev	2.184



B1L101S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch17, adc0

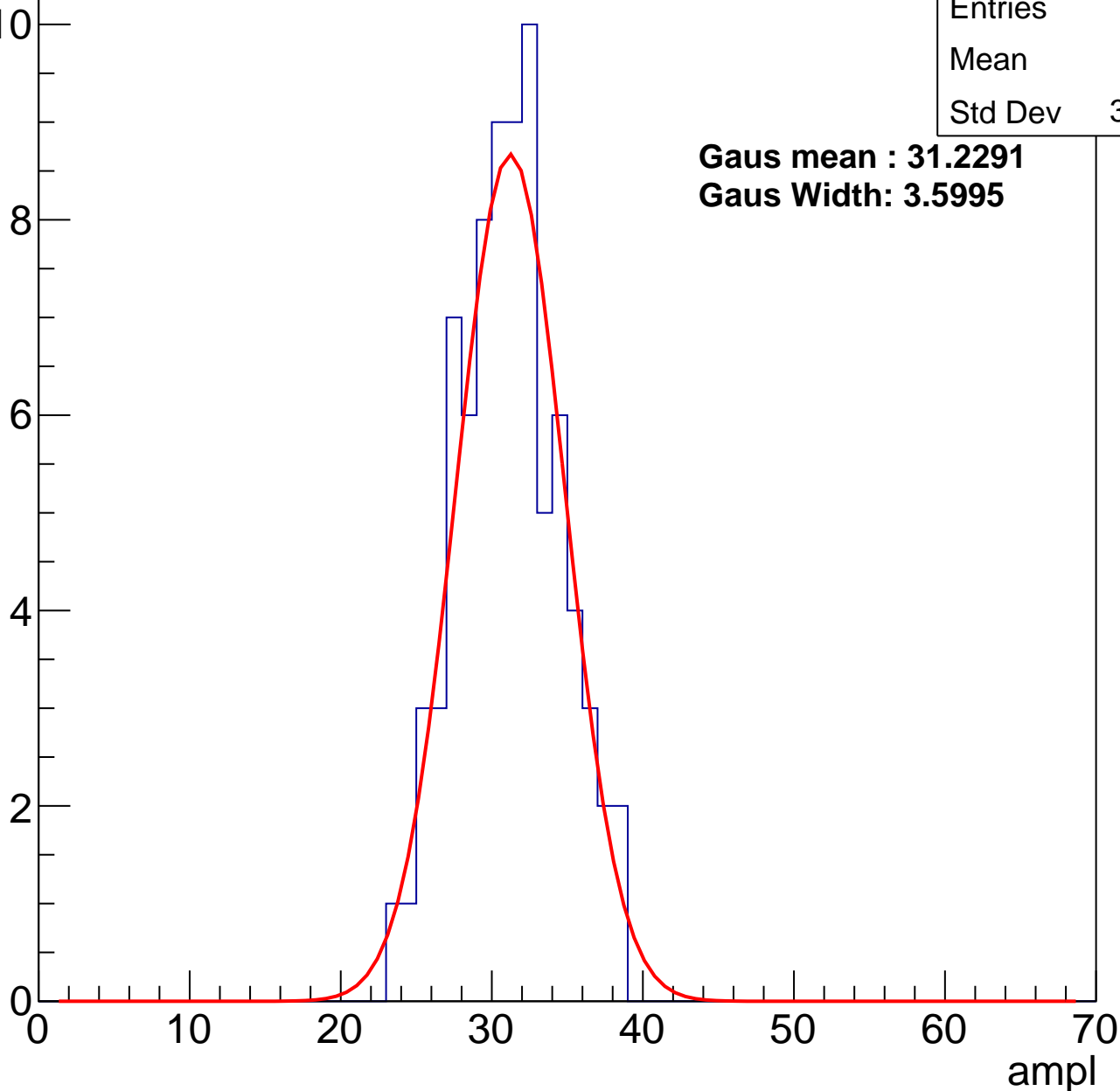
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	30.7
Std Dev	3.354

Gaus mean : 31.2291

Gaus Width: 3.5995



B1L101S, U14-ch17, adc1

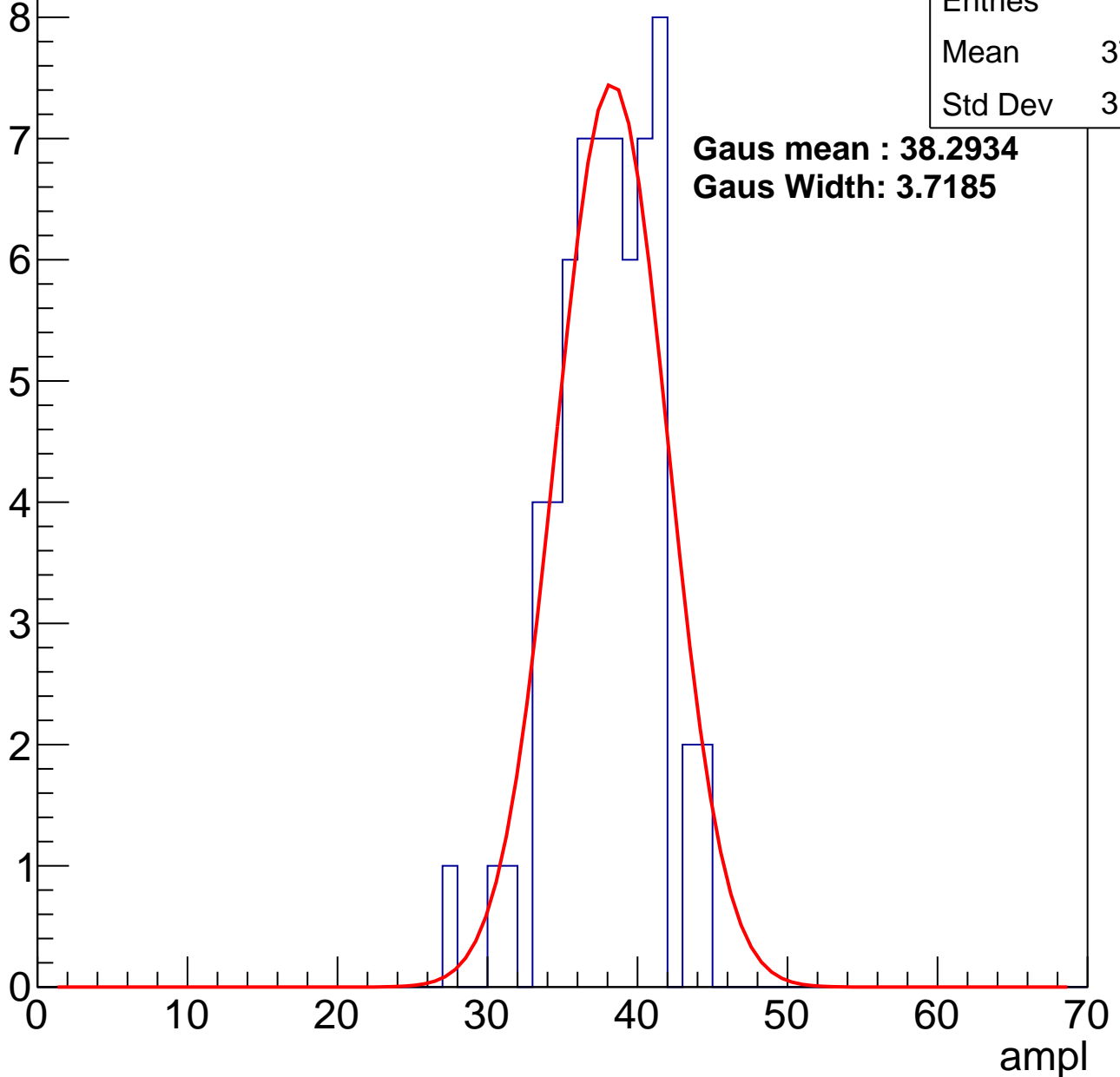
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.44
Std Dev	3.323

Gaus mean : 38.2934

Gaus Width: 3.7185



B1L101S, U14-ch17, adc2

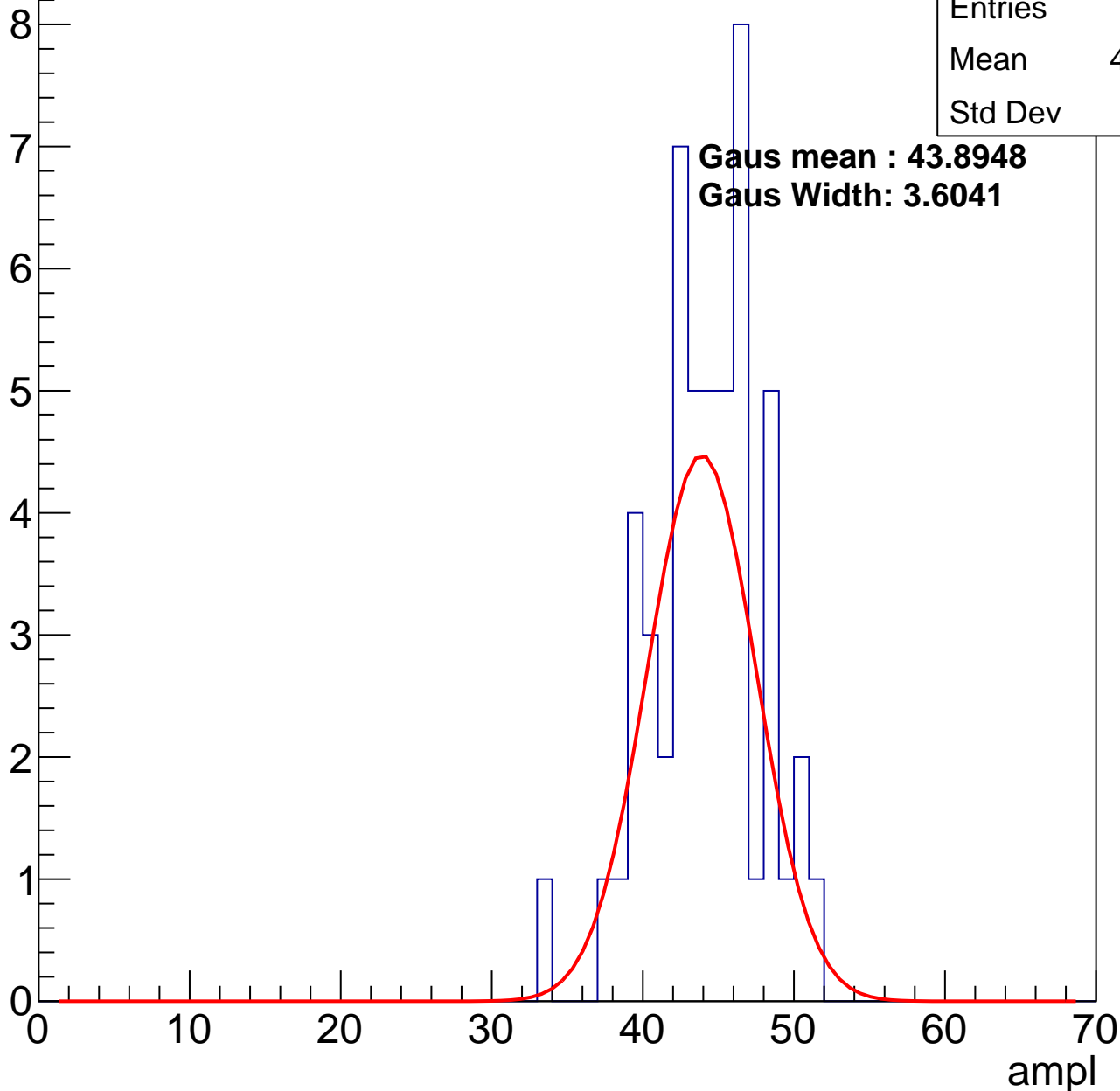
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.75
Std Dev	3.6

Gaus mean : 43.8948

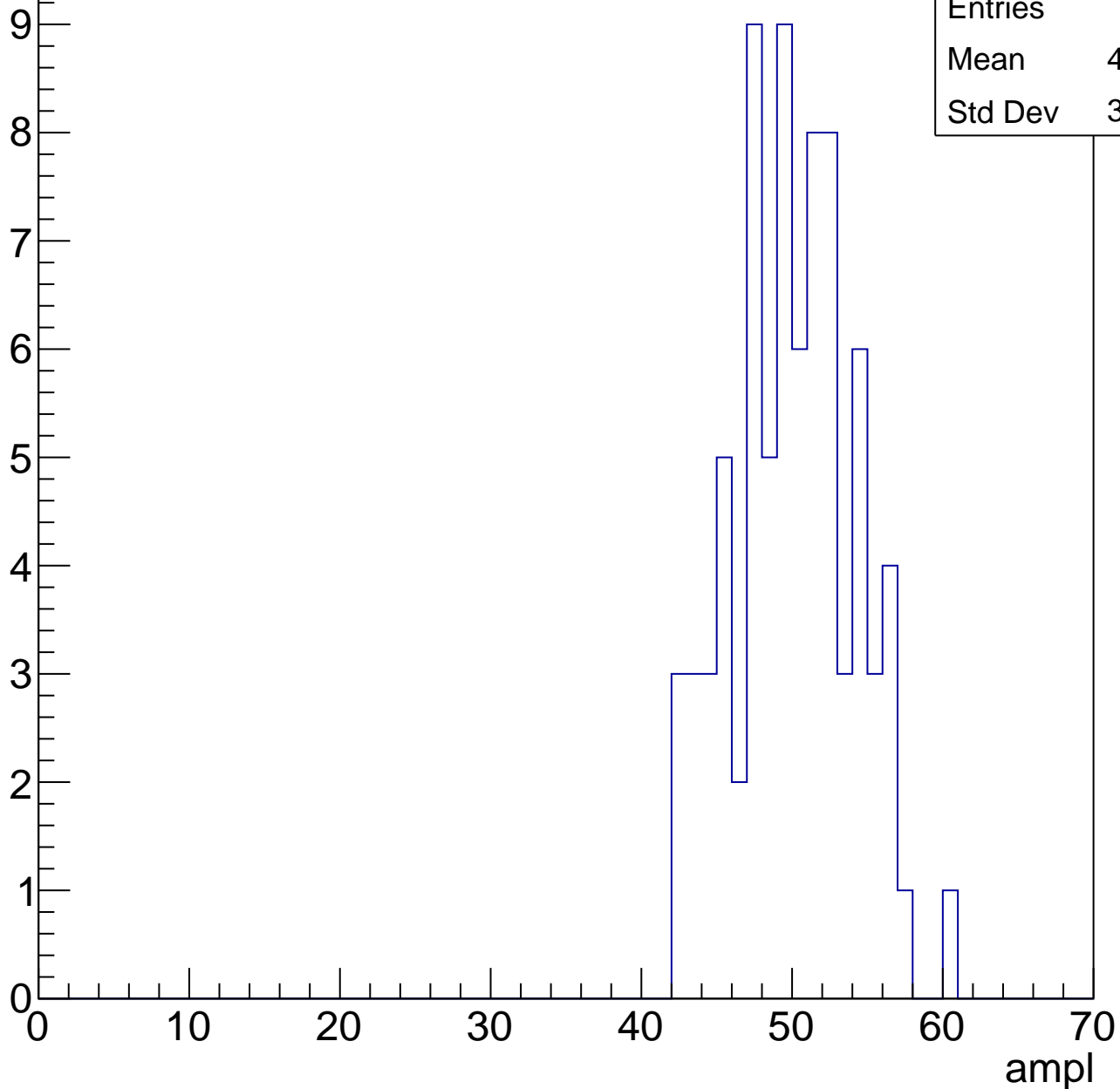
Gaus Width: 3.6041



B1L101S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

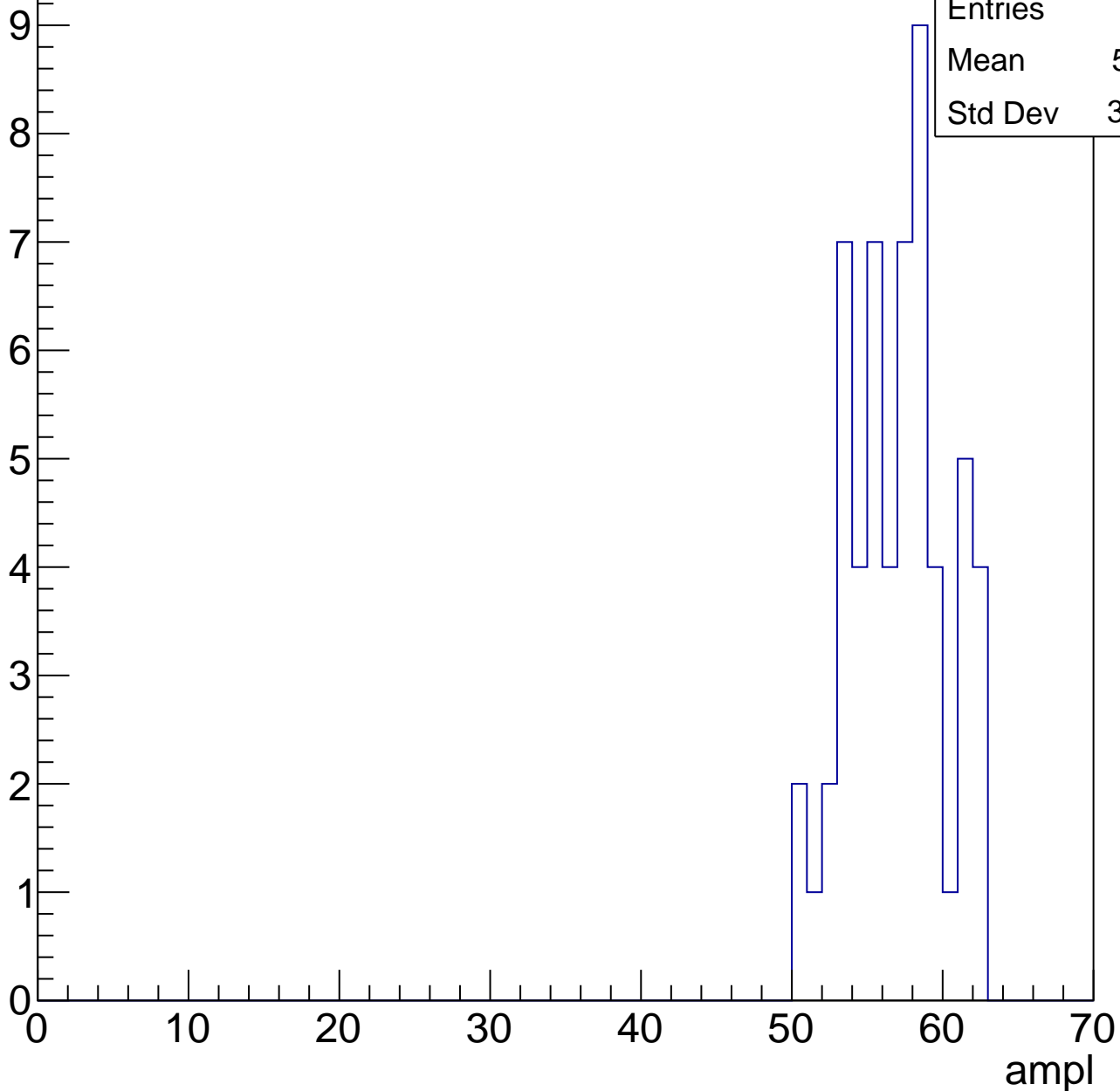


Entries	79
Mean	49.63
Std Dev	3.953

B1L101S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

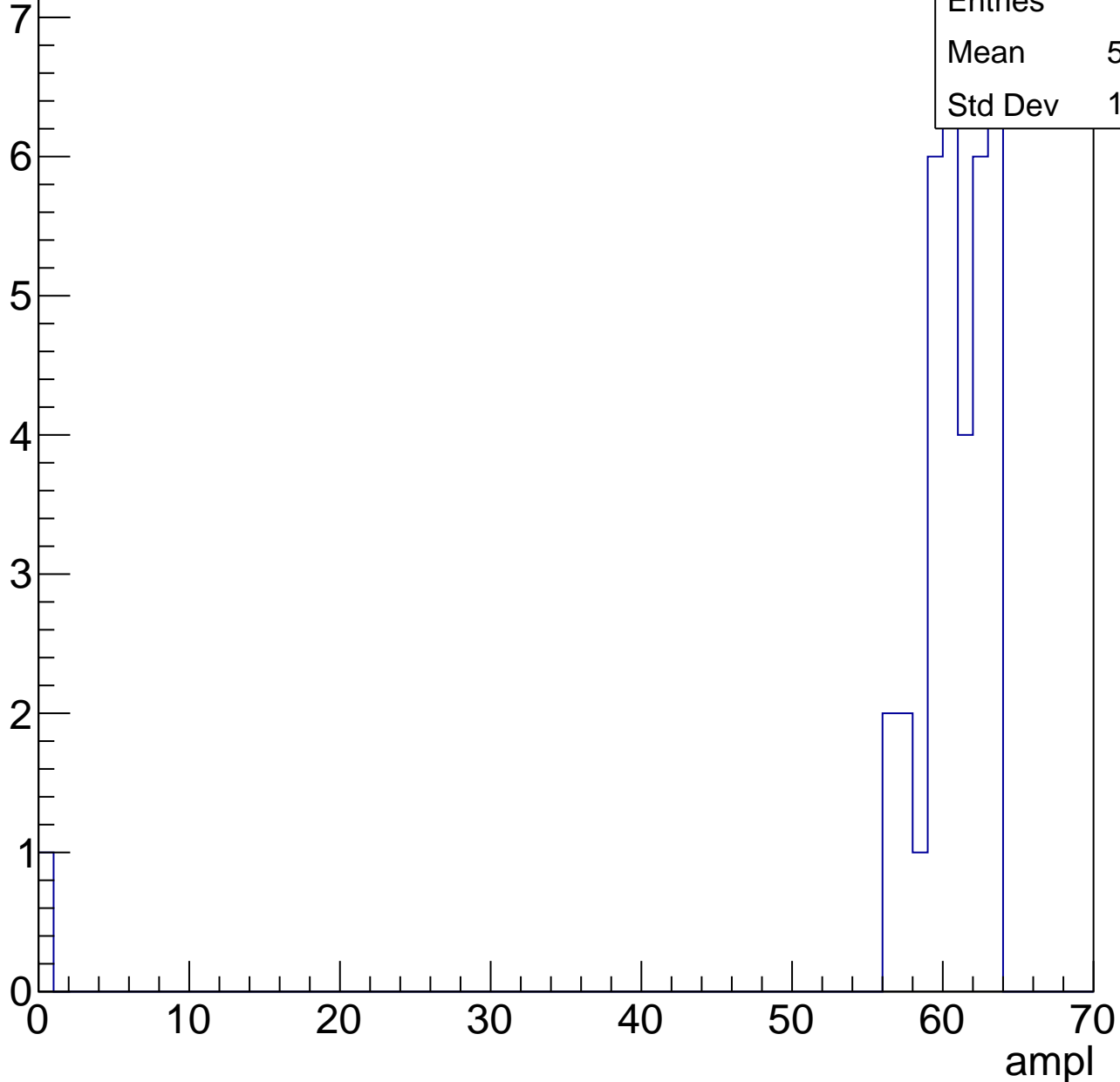


Entries	57
Mean	56.51
Std Dev	3.146

B1L101S, U14-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

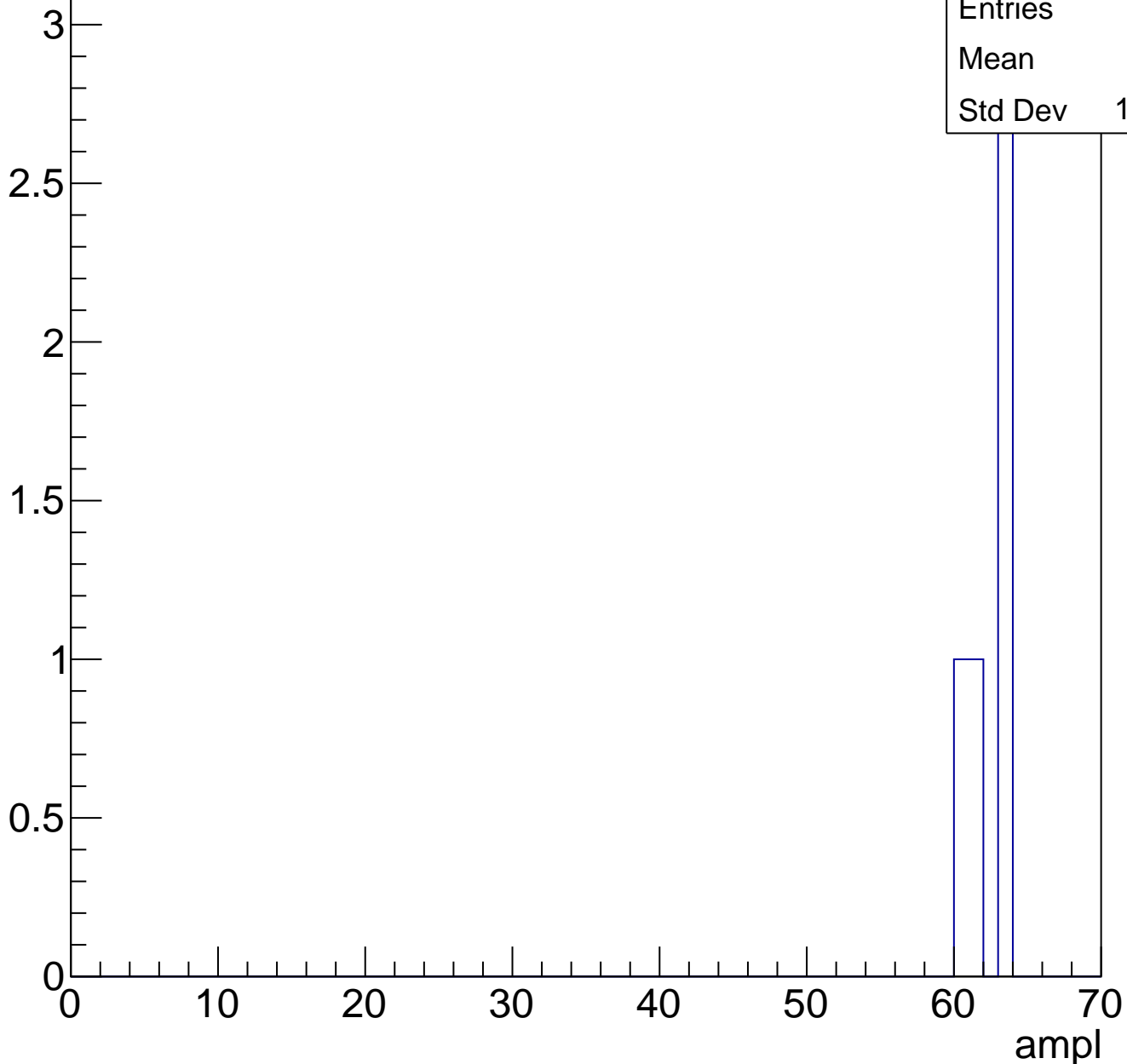
Entry



B1L101S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch18, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	29.35
Std Dev	5.016

Gaus mean : 29.8114

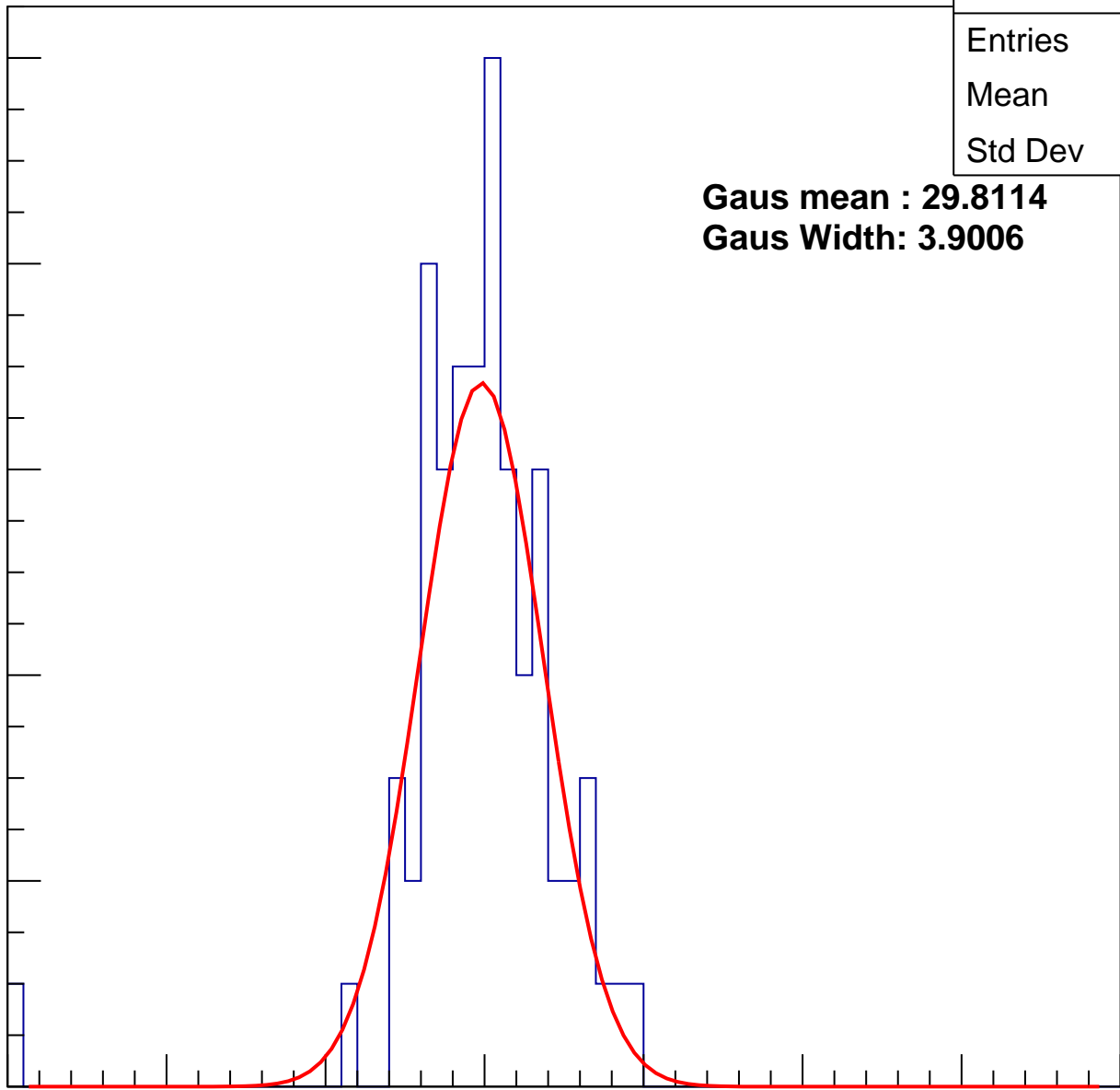
Gaus Width: 3.9006

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch18, adc1

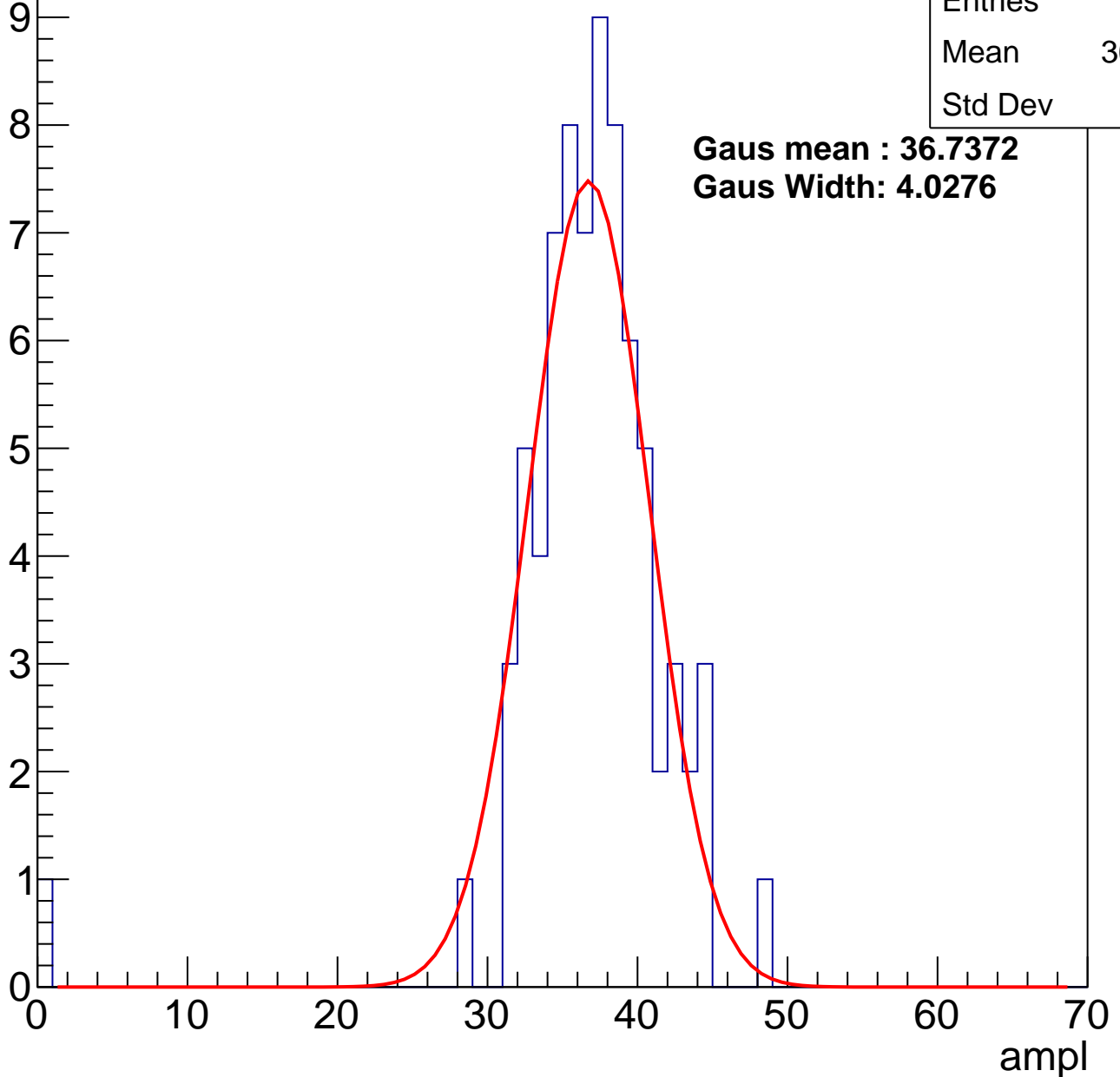
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.37
Std Dev	5.59

Gaus mean : 36.7372

Gaus Width: 4.0276



B1L101S, U14-ch18, adc2

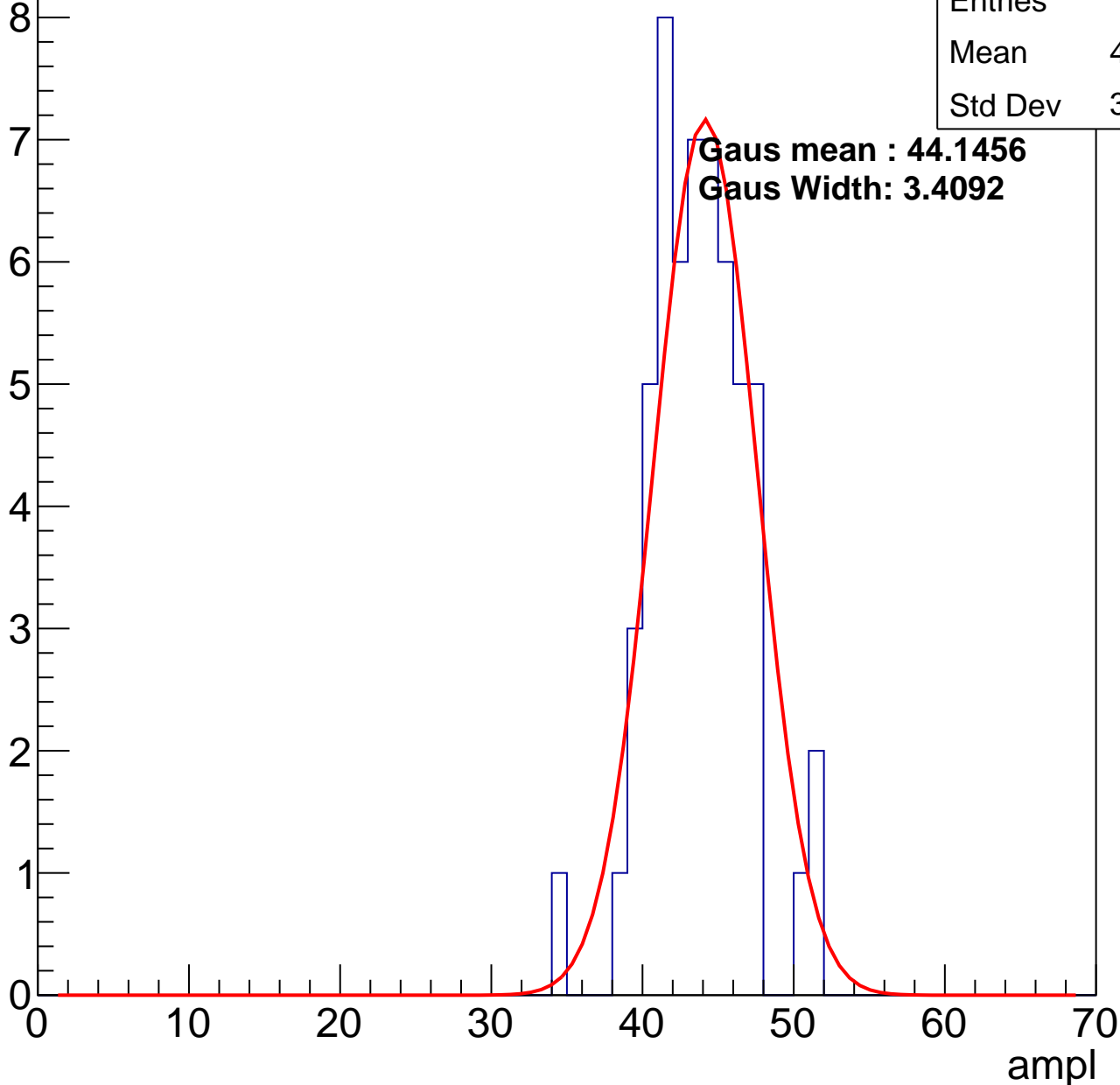
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.25
Std Dev	3.153

Gaus mean : 44.1456

Gaus Width: 3.4092



B1L101S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

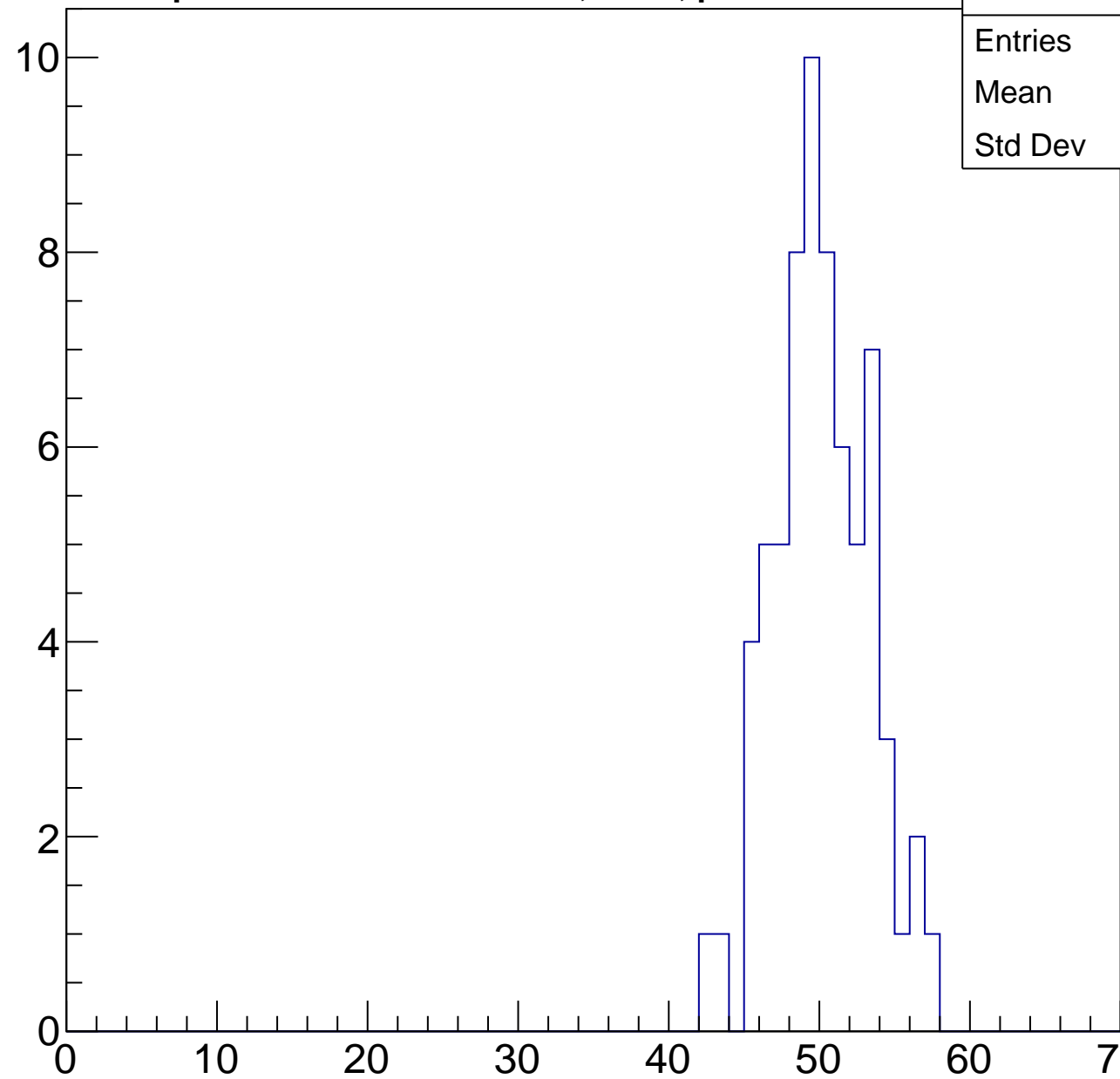
Entries	67
Mean	49.66
Std Dev	3.127

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

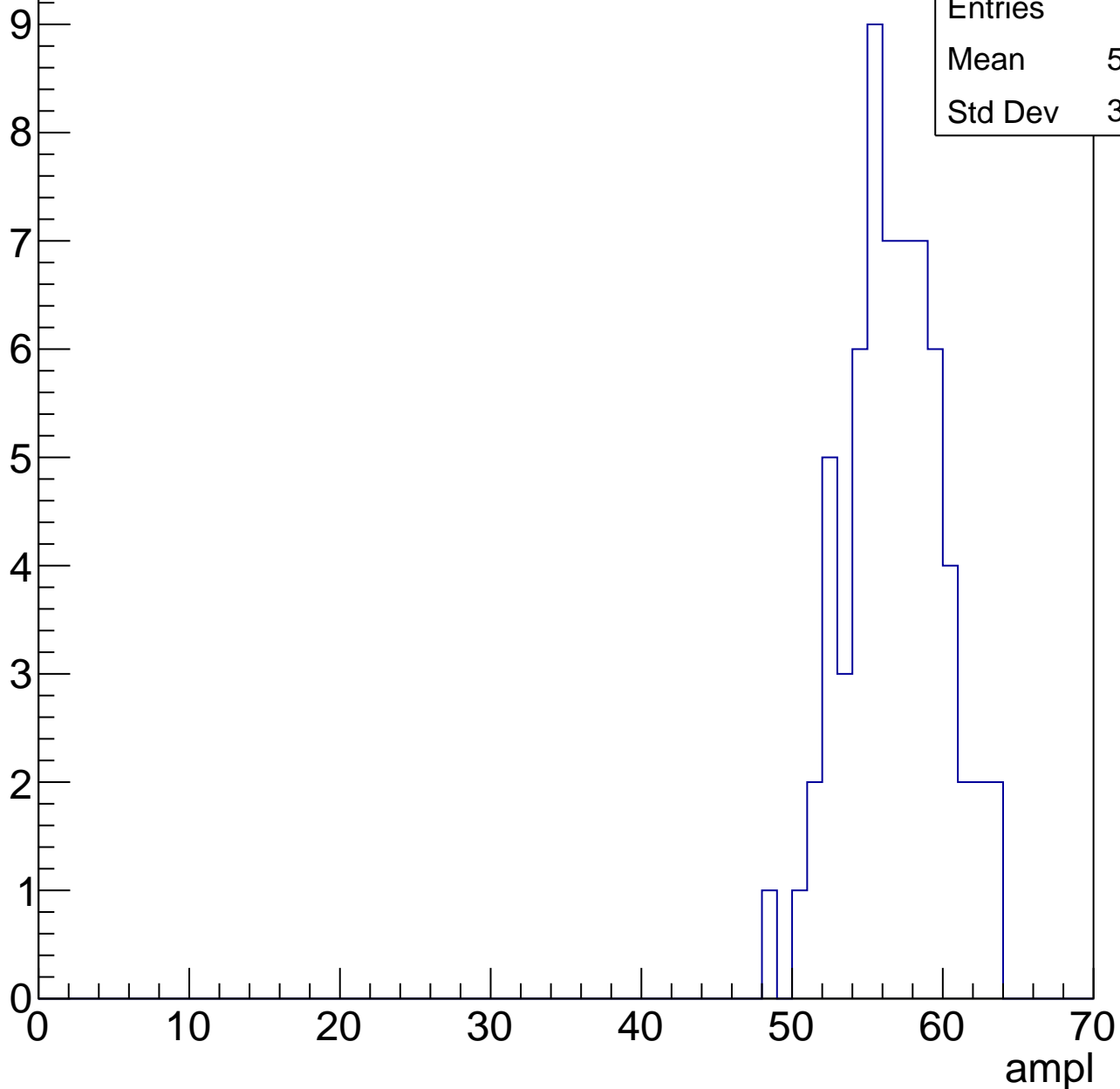
ampl



B1L101S, U14-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

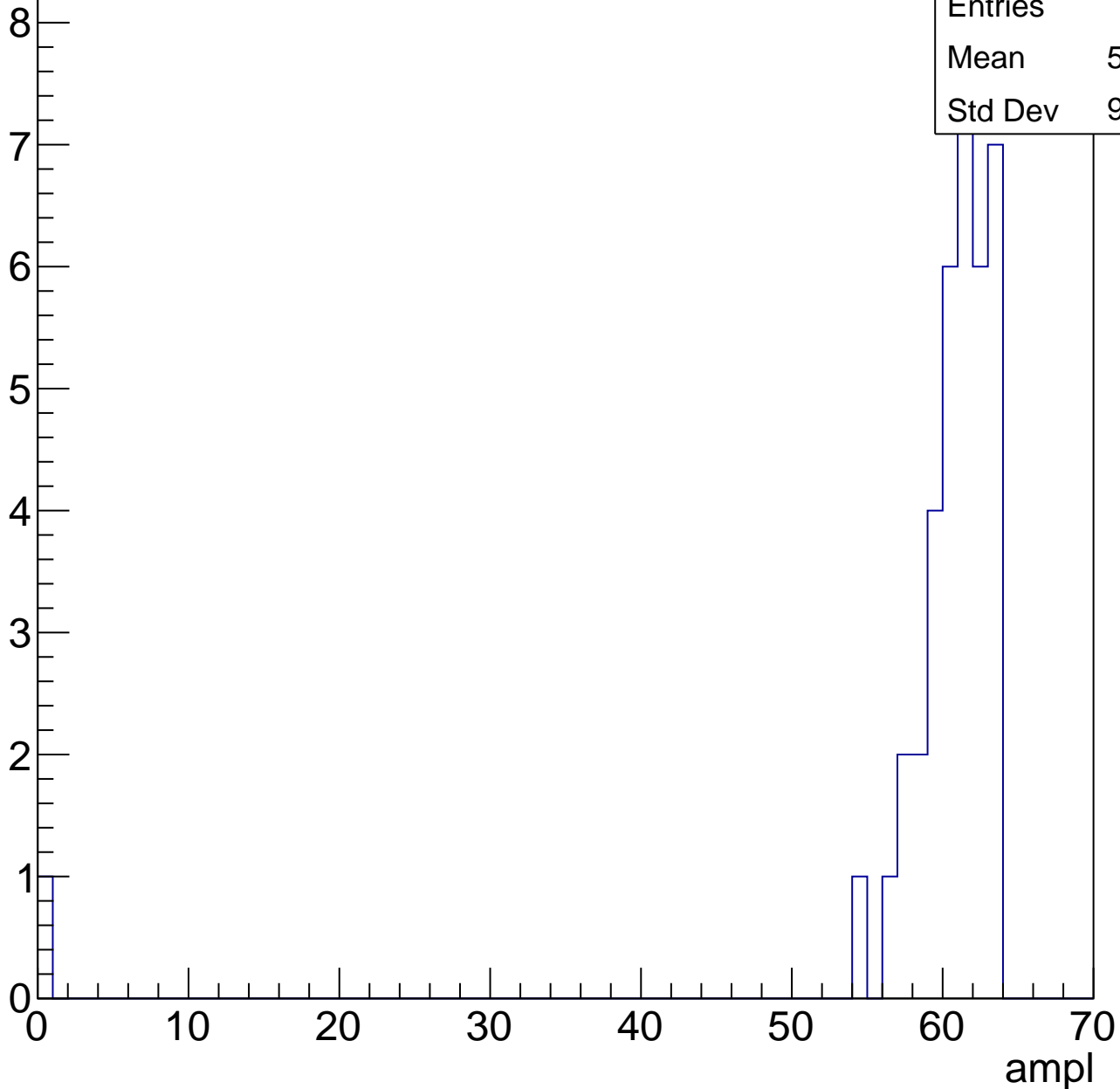


B1L101S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

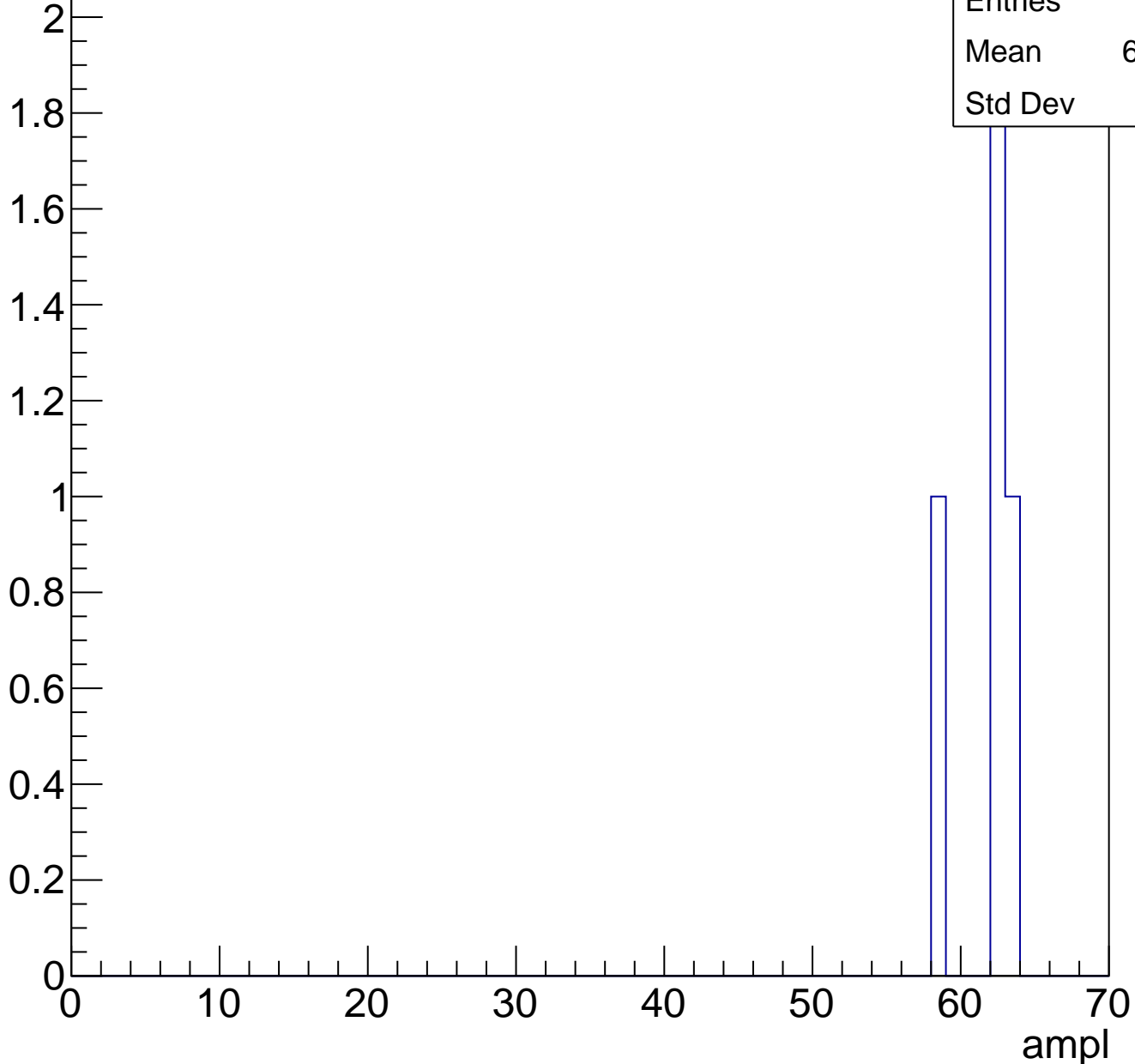
Entries	38
Mean	58.87
Std Dev	9.905



B1L101S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch19, adc0

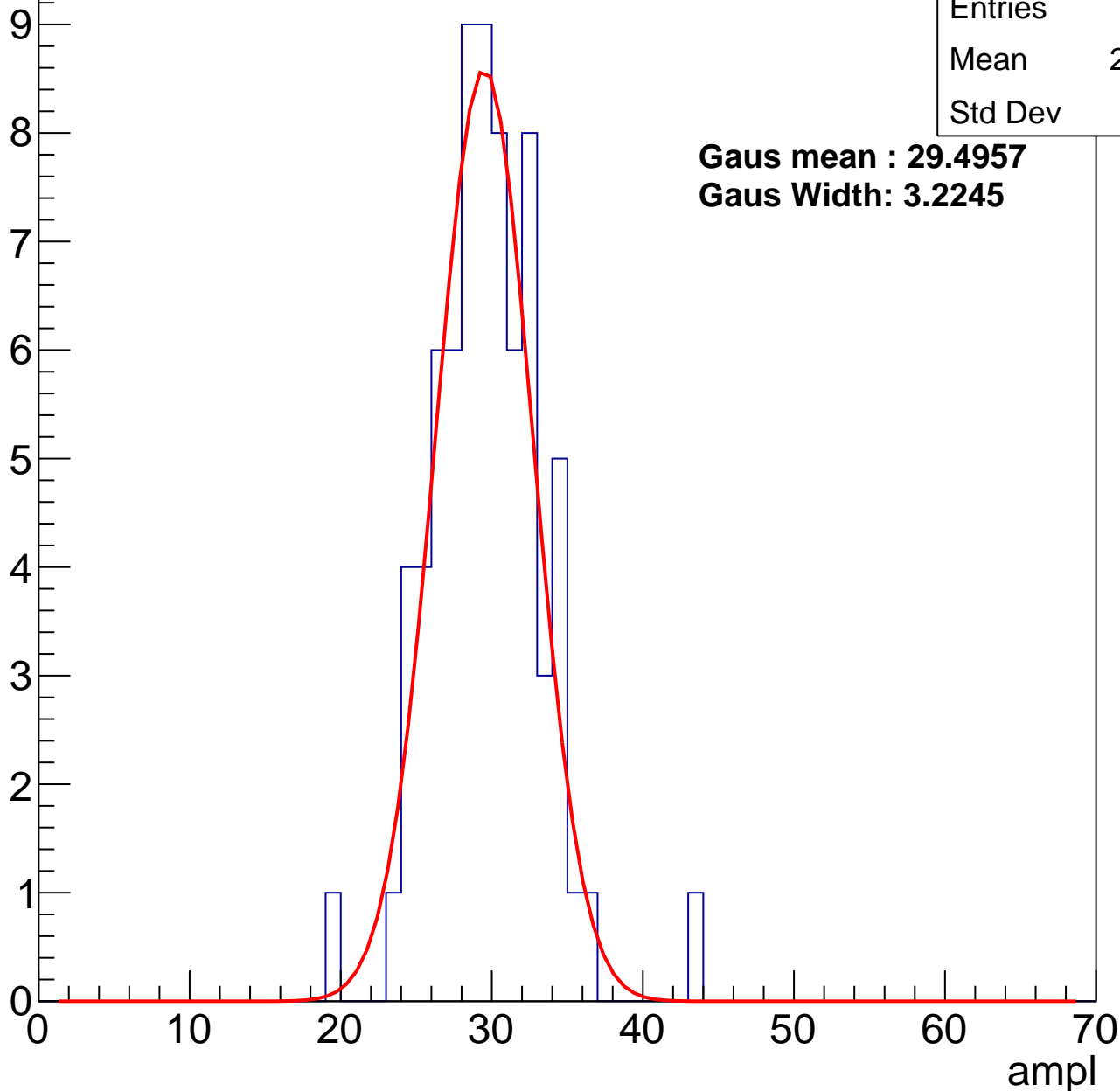
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.23
Std Dev	3.59

Gaus mean : 29.4957

Gaus Width: 3.2245



B1L101S, U14-ch19, adc1

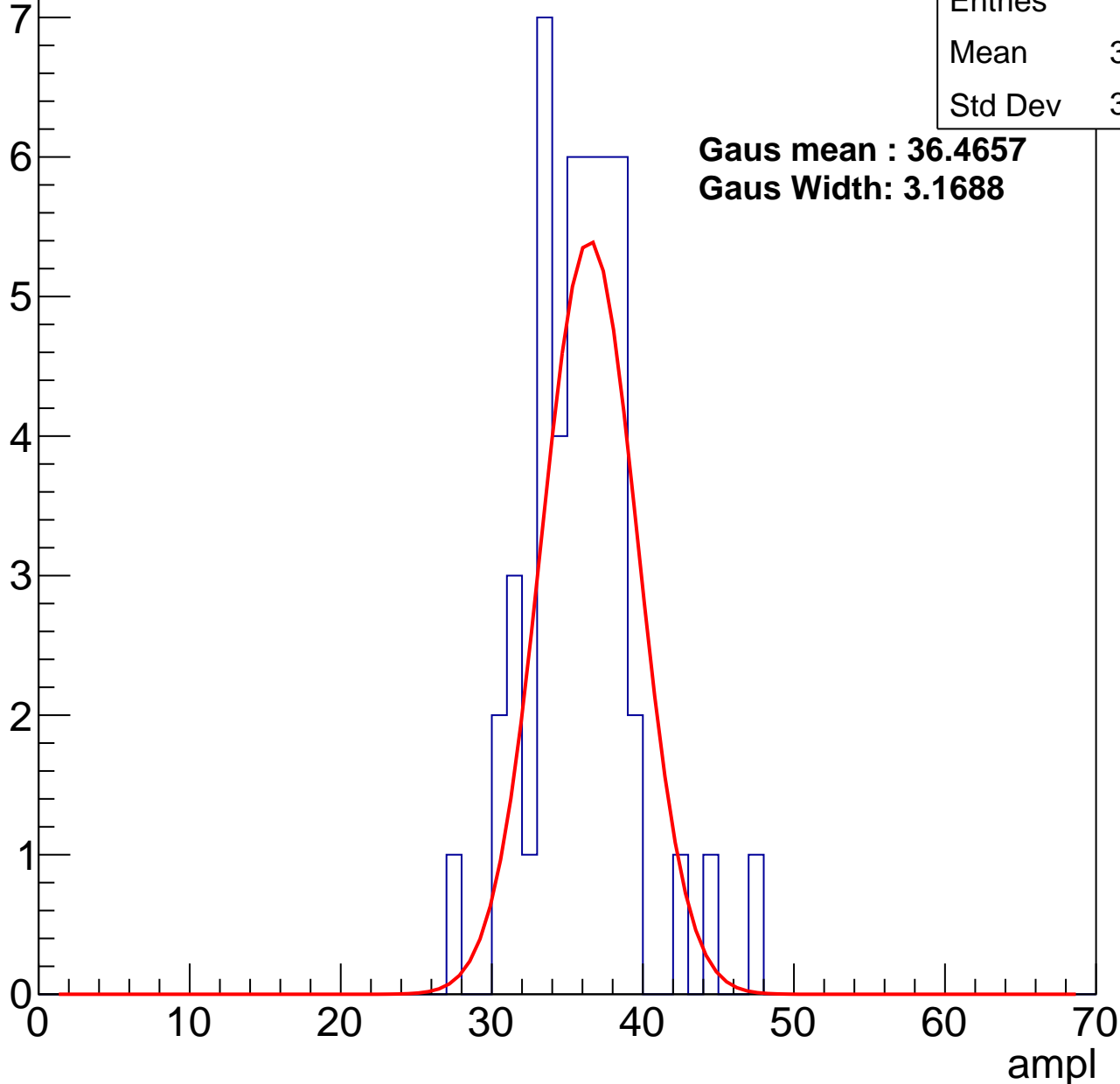
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	35.45
Std Dev	3.524

Gaus mean : 36.4657

Gaus Width: 3.1688



B1L101S, U14-ch19, adc2

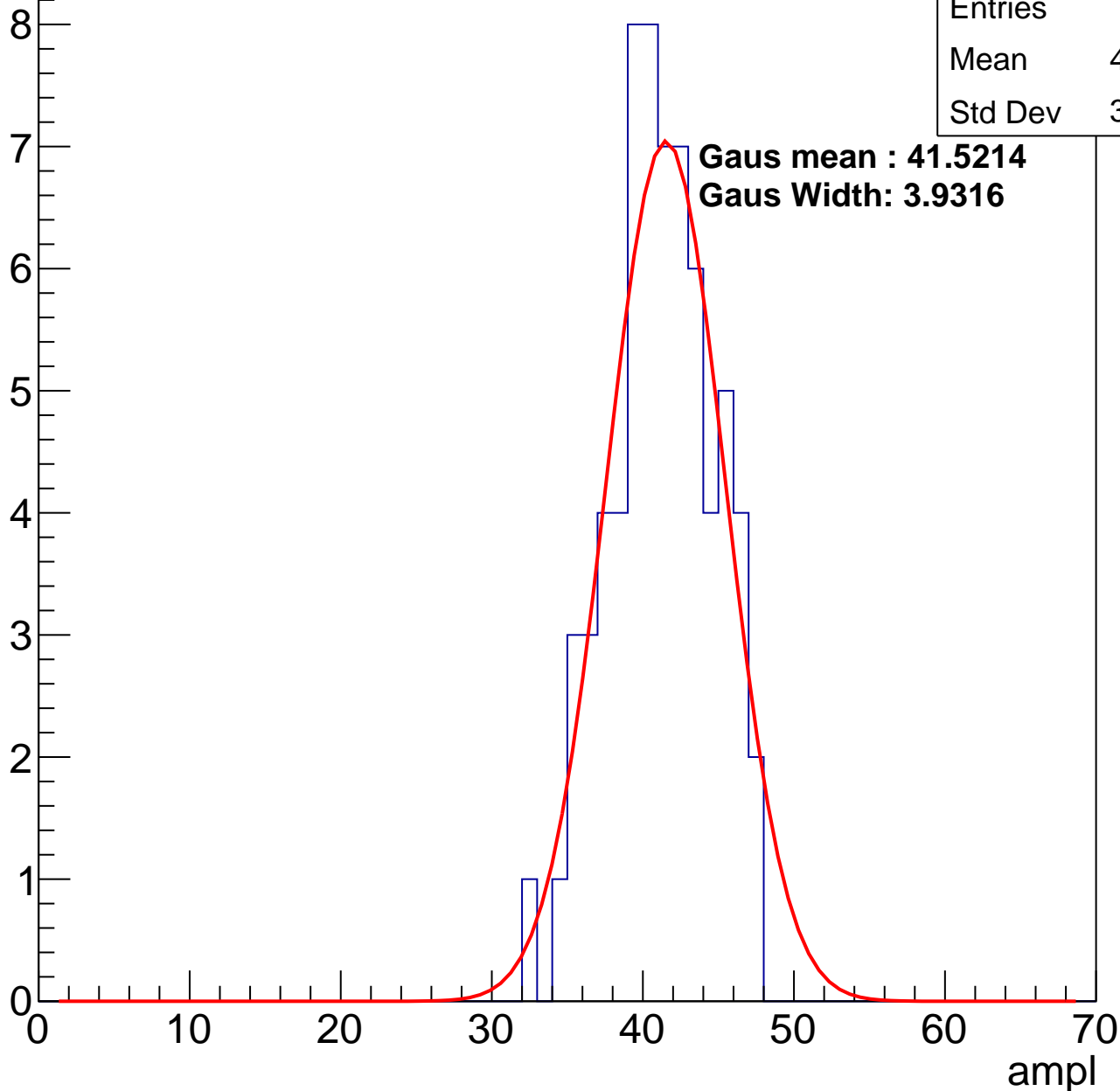
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	40.73
Std Dev	3.384

Gaus mean : 41.5214

Gaus Width: 3.9316



B1L101S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	47.3
Std Dev	3.461

Entry

10

8

6

4

2

0

0

10

20

30

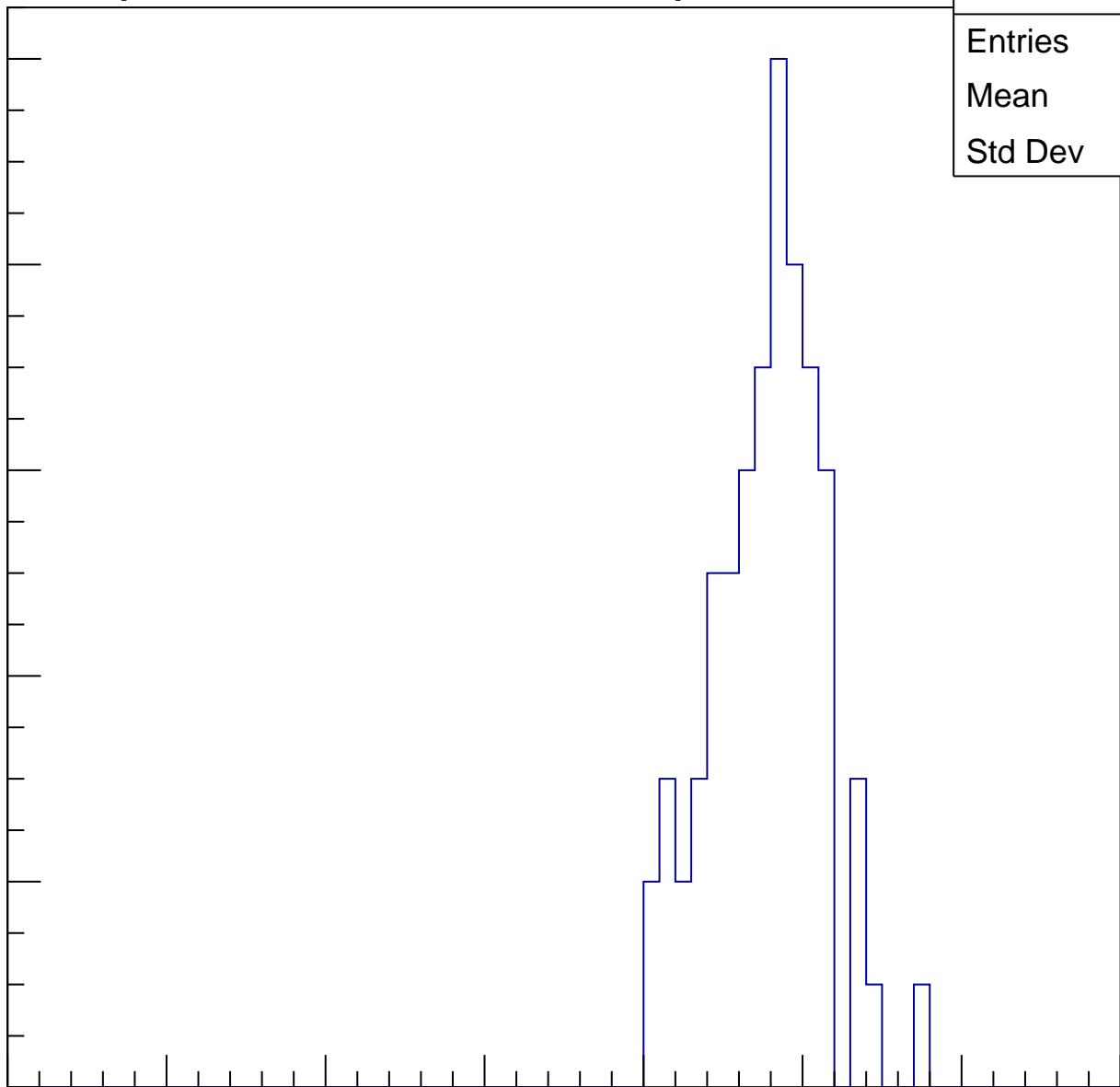
40

50

60

70

ampl

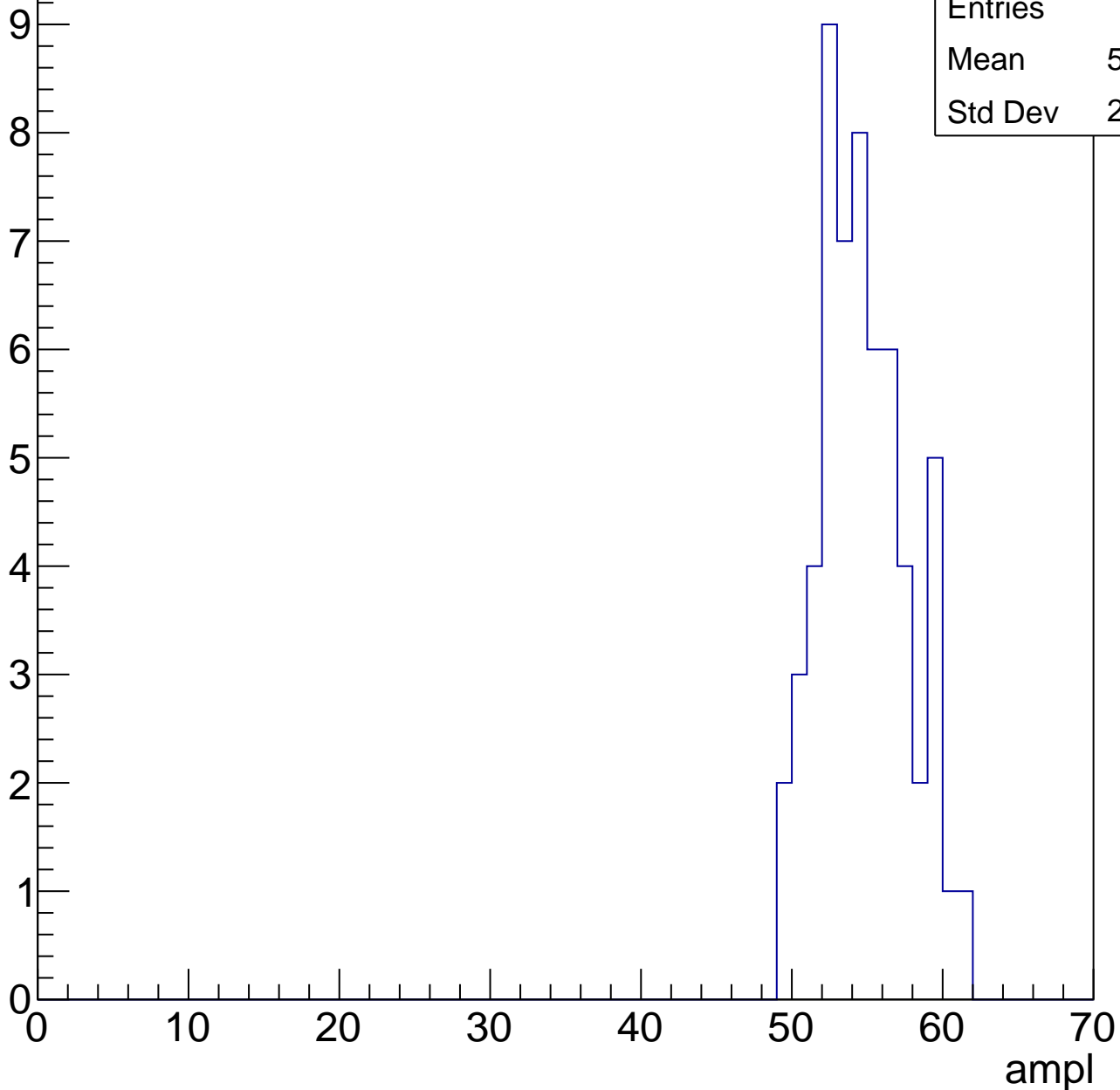


B1L101S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

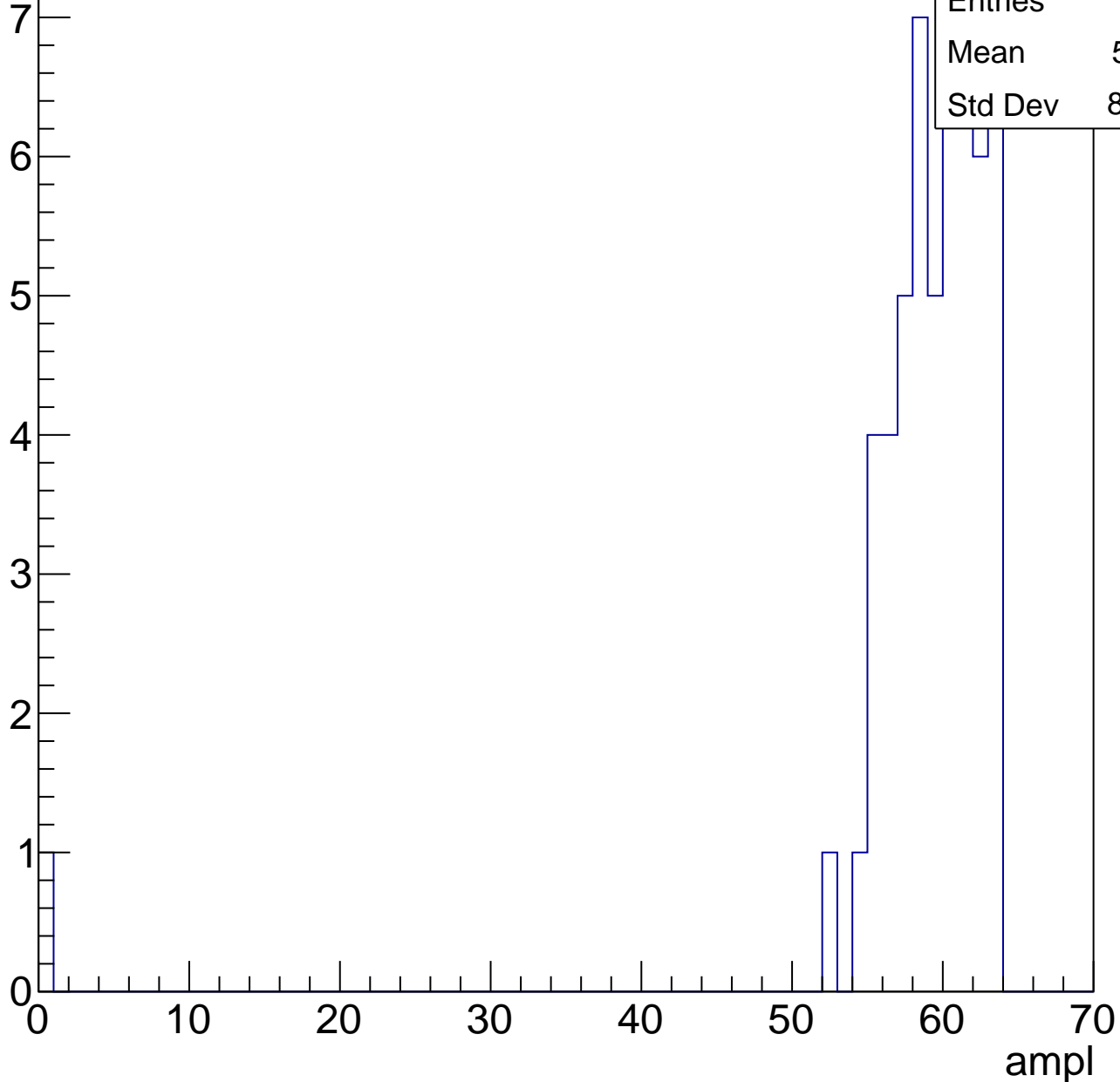
Entries	58
Mean	54.29
Std Dev	2.877



B1L101S, U14-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

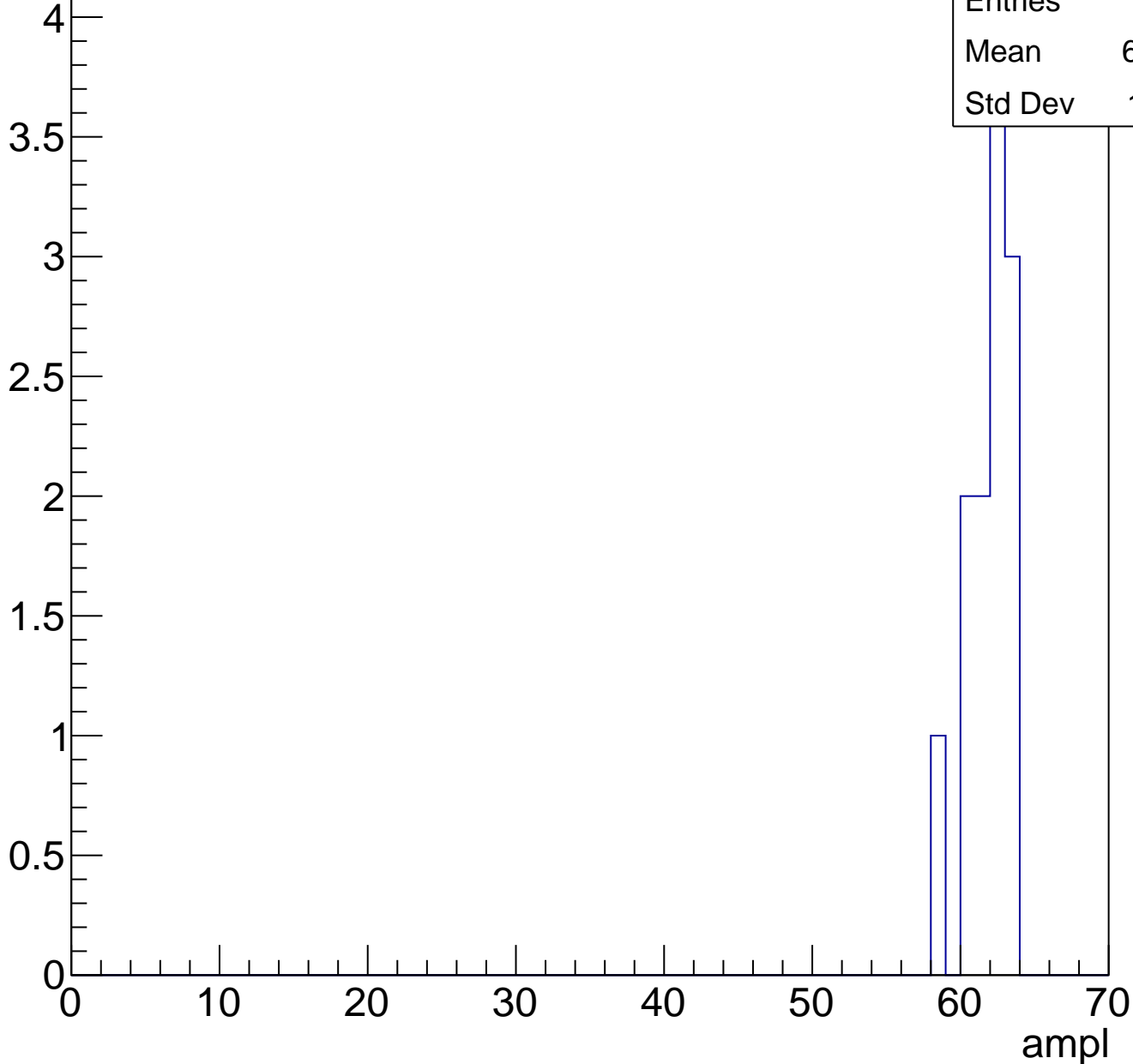
Entry



B1L101S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch20, adc0

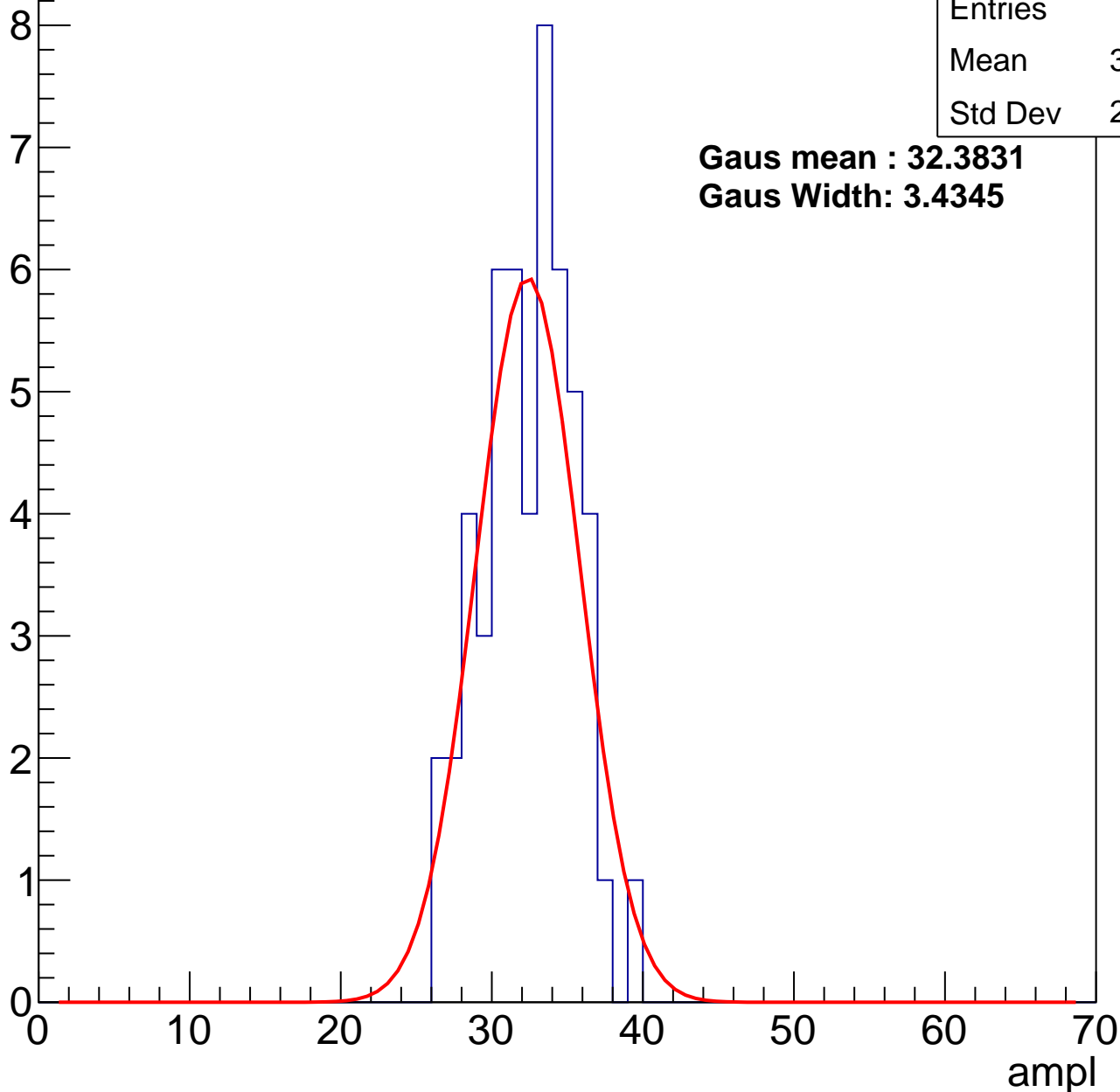
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	31.96
Std Dev	2.968

Gaus mean : 32.3831

Gaus Width: 3.4345

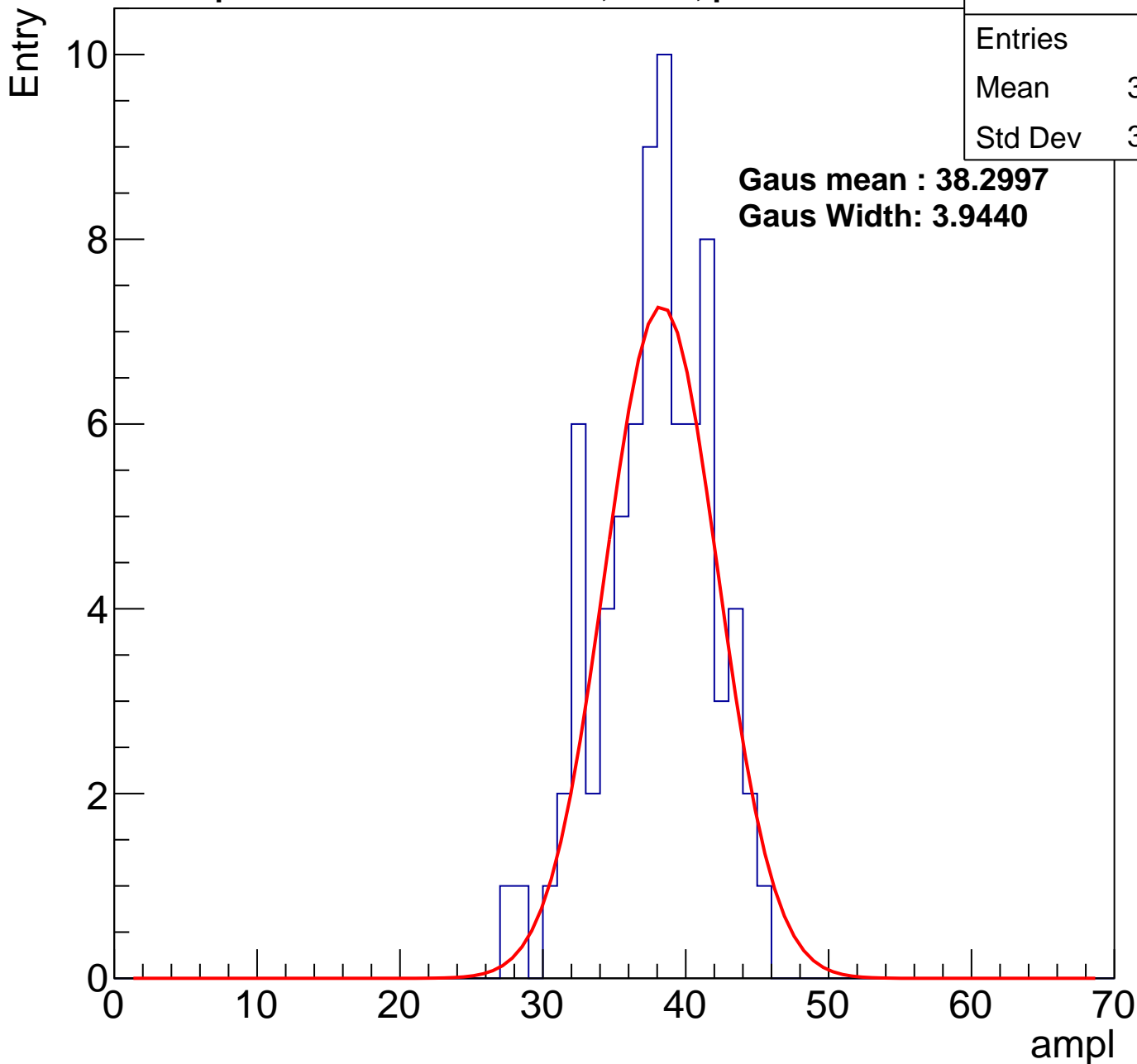


B1L101S, U14-ch20, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.38
Std Dev	3.834

Gaus mean : 38.2997
Gaus Width: 3.9440



B1L101S, U14-ch20, adc2

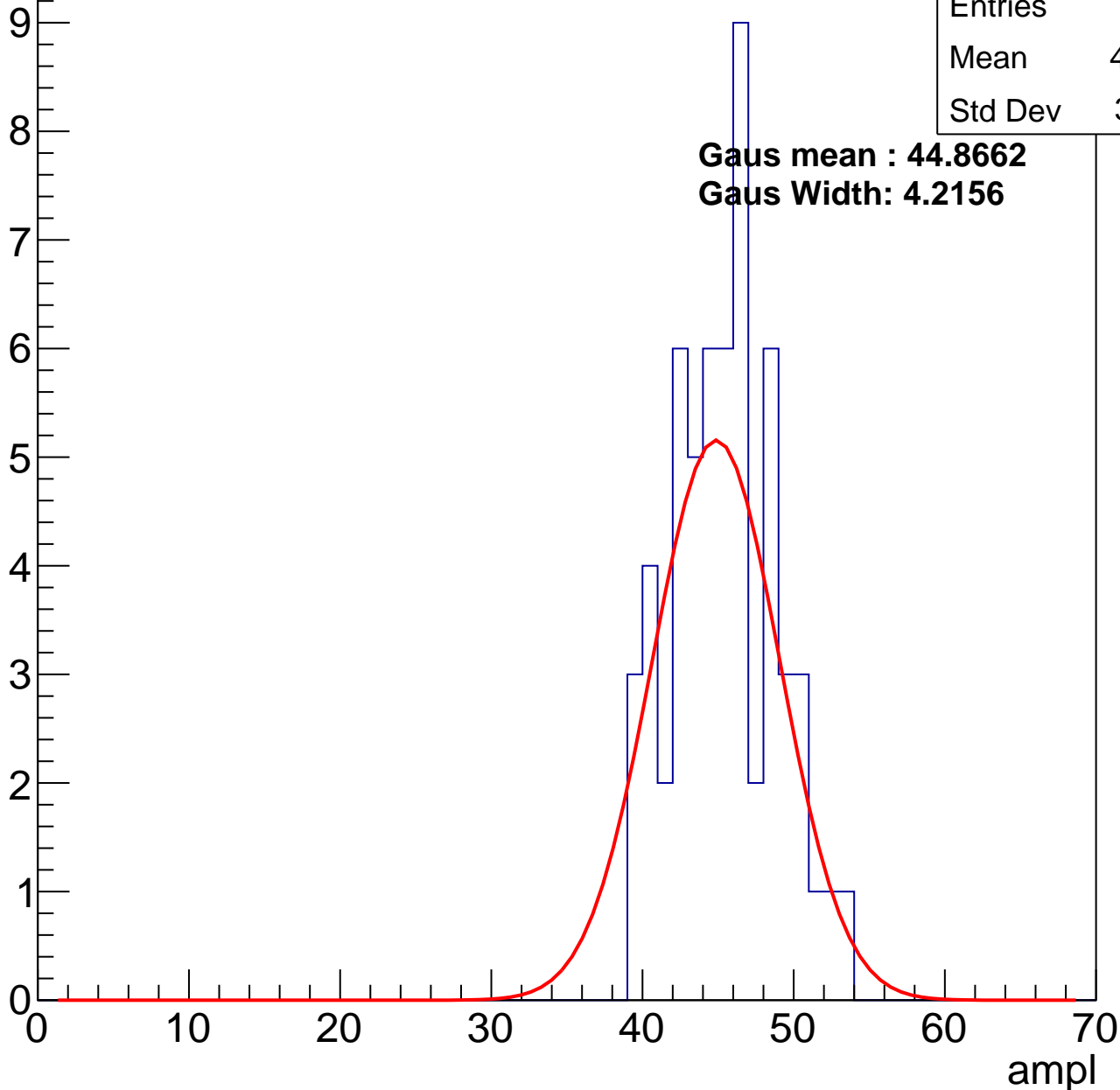
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.98
Std Dev	3.381

Gaus mean : 44.8662

Gaus Width: 4.2156

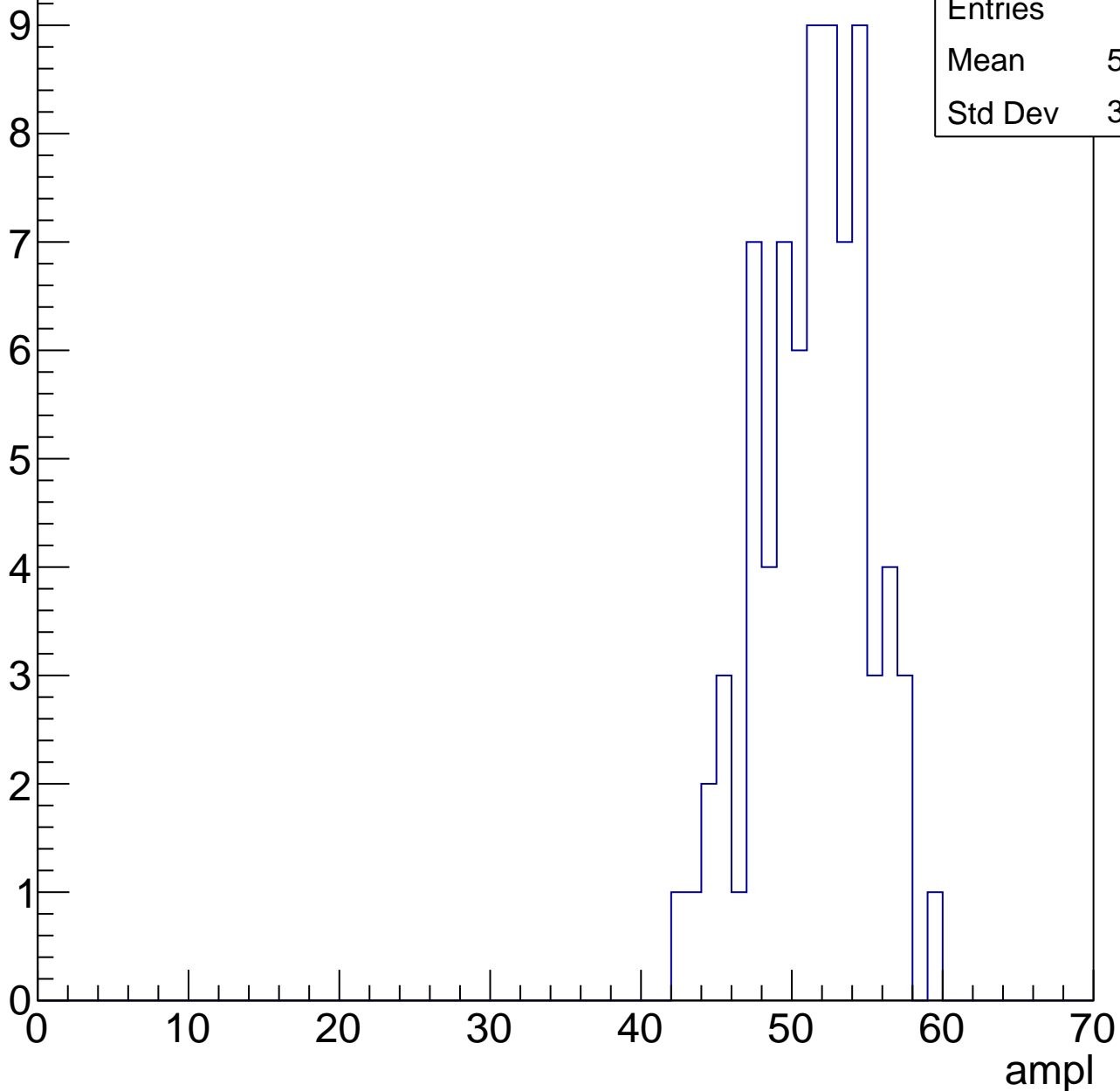


B1L101S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50.92
Std Dev	3.596

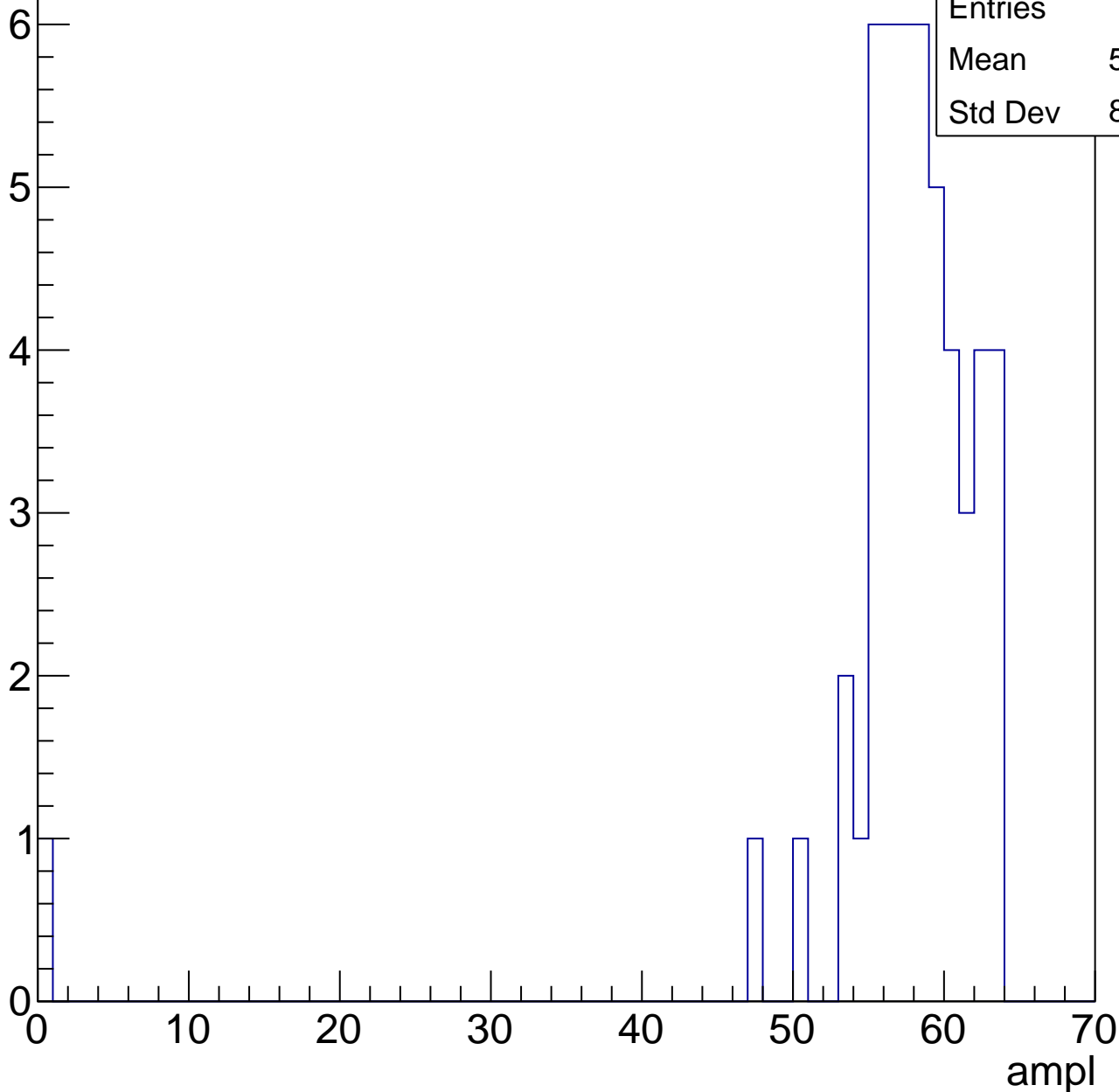


B1L101S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	56.62
Std Dev	8.736

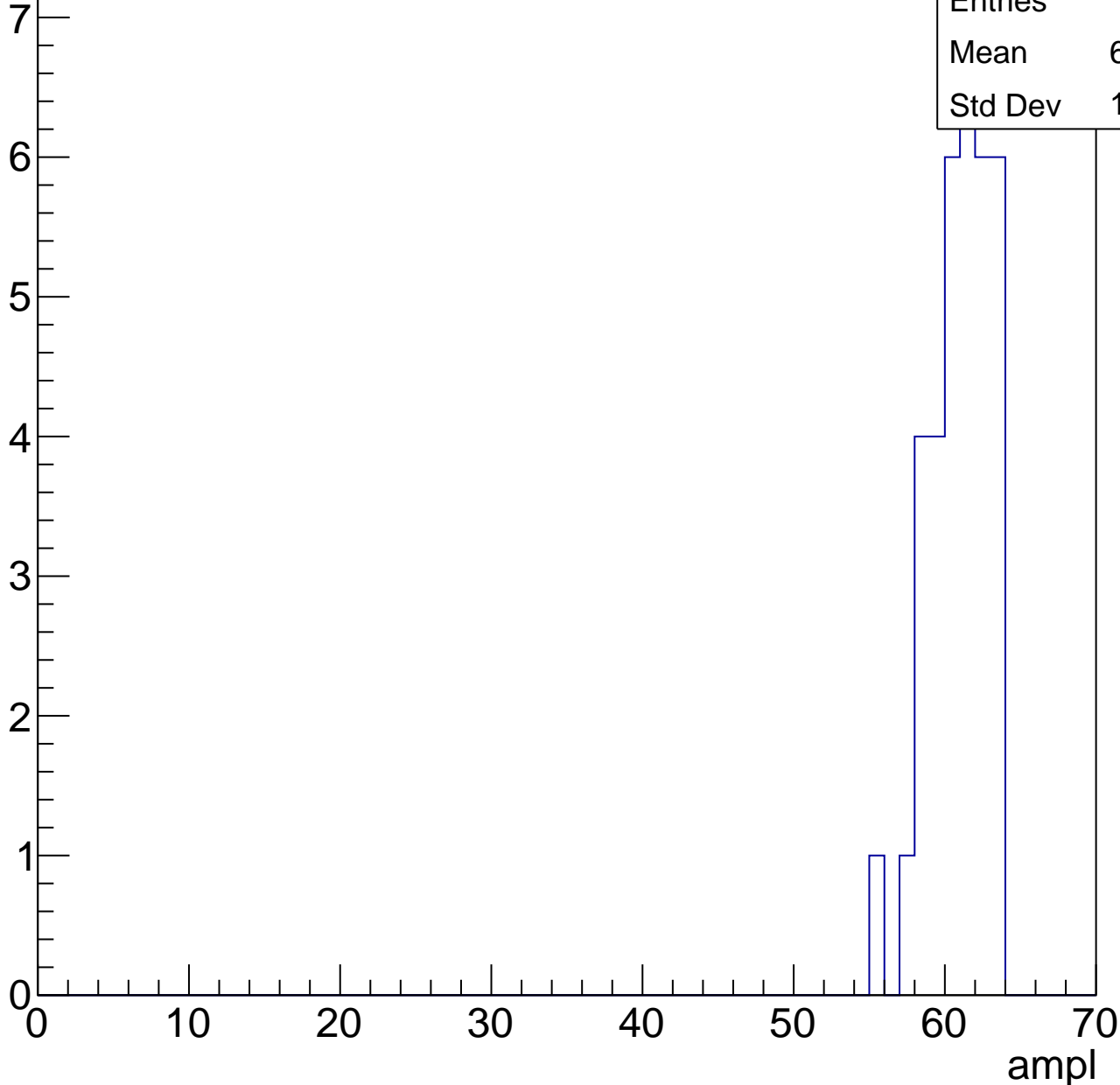


B1L101S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.49
Std Dev	1.933



B1L101S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch21, adc0

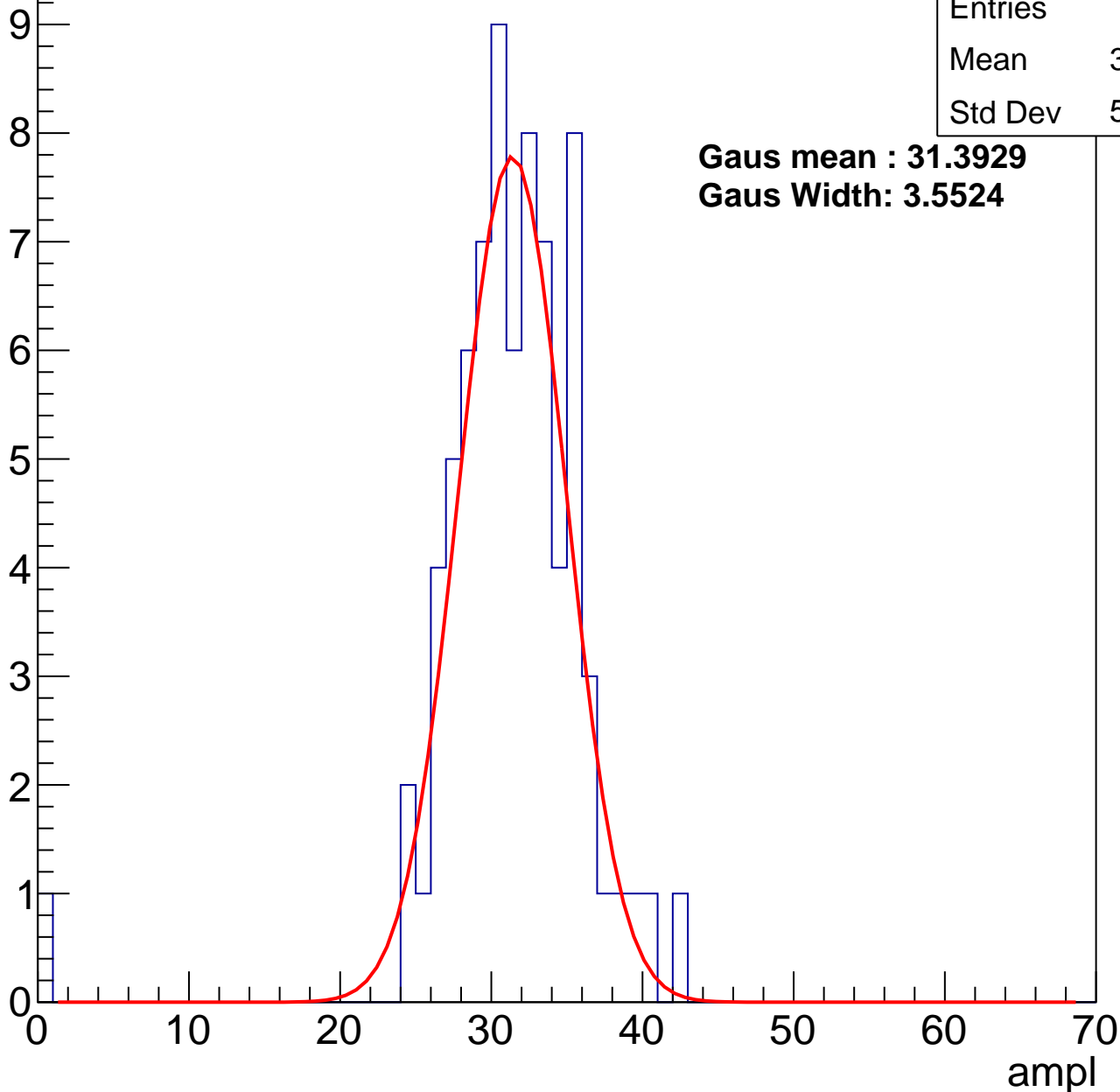
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.87
Std Dev	5.118

Gaus mean : 31.3929

Gaus Width: 3.5524



B1L101S, U14-ch21, adc1

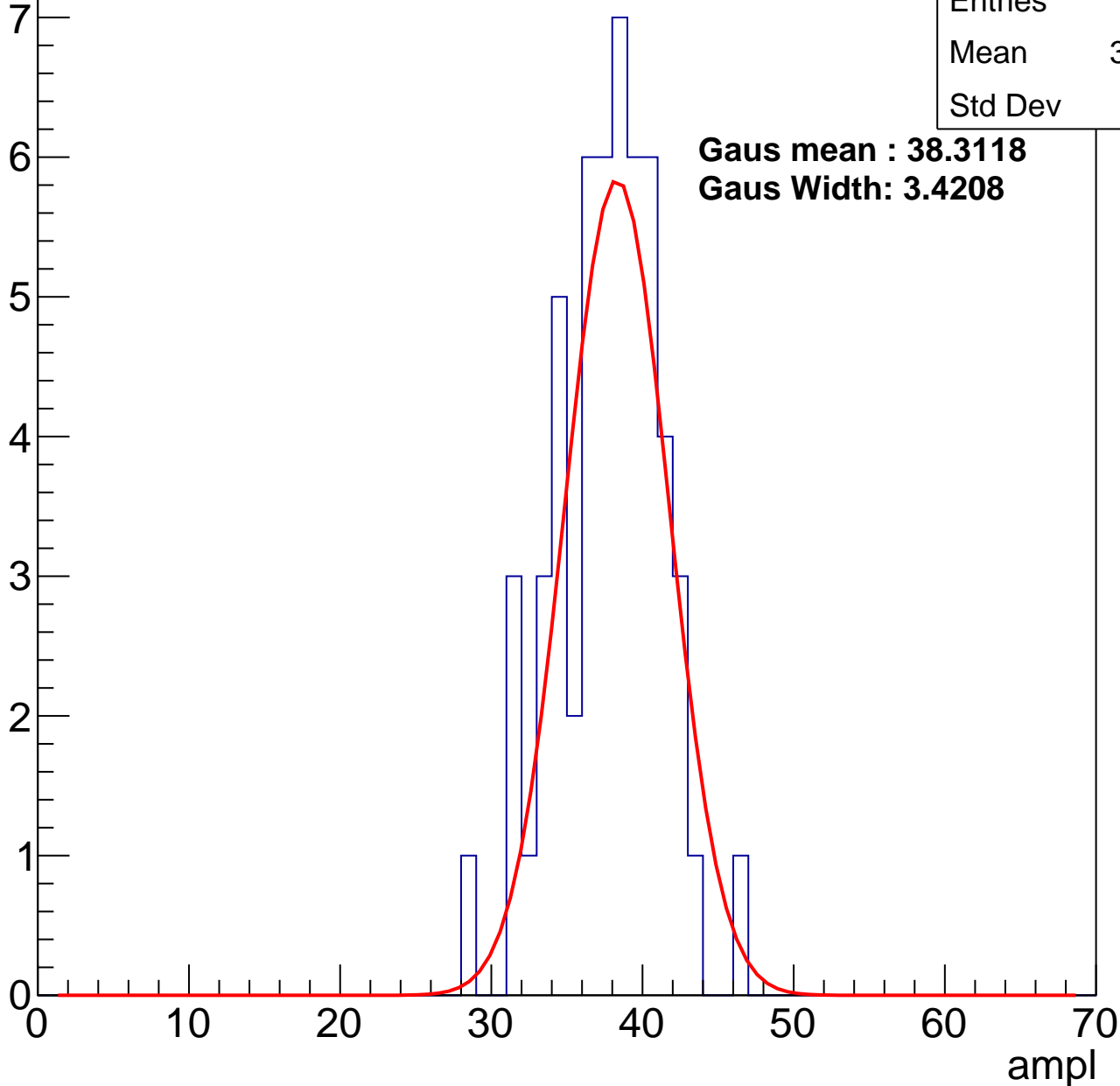
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.25
Std Dev	3.46

Gaus mean : 38.3118

Gaus Width: 3.4208



B1L101S, U14-ch21, adc2

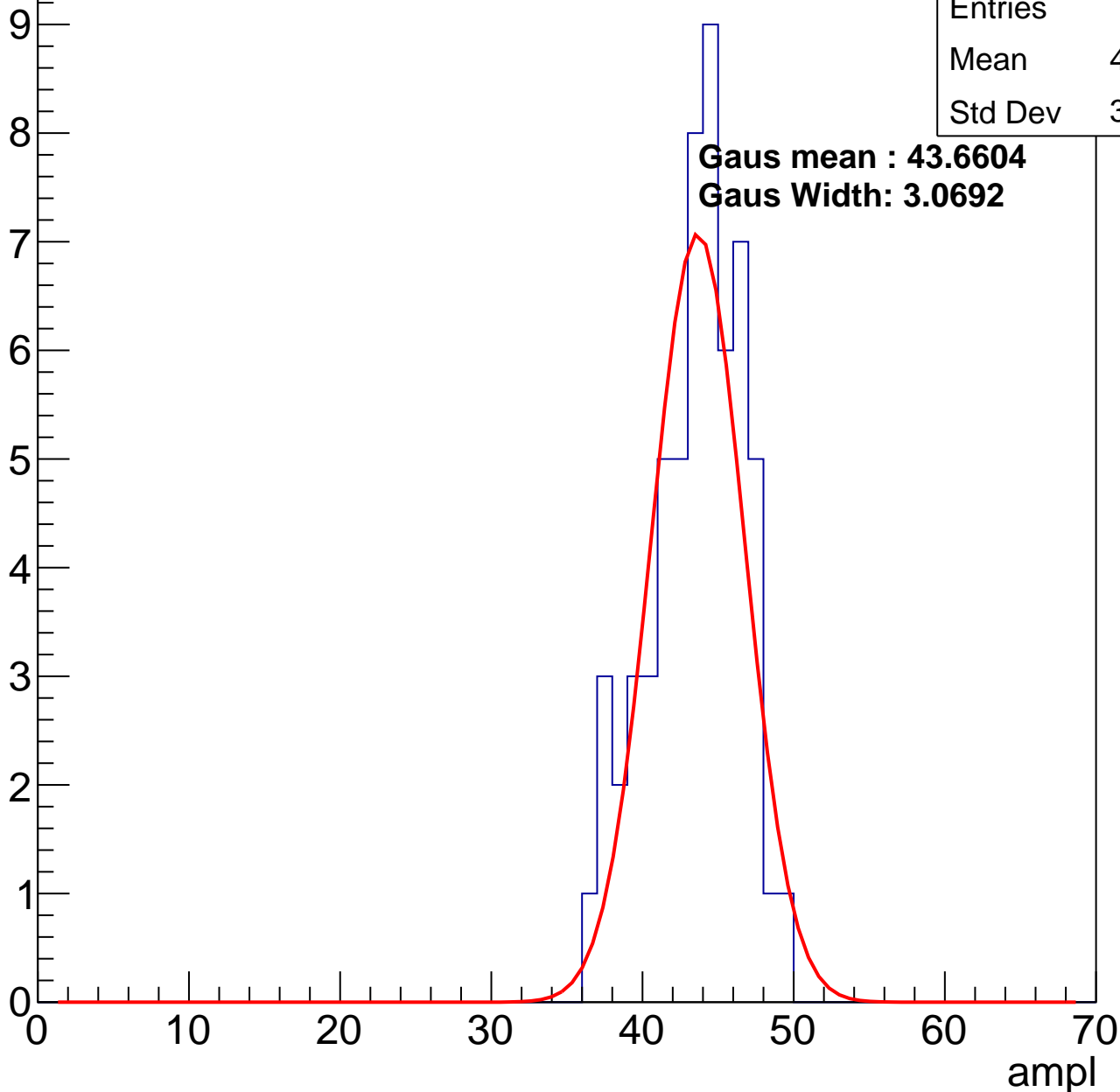
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.03
Std Dev	3.036

Gaus mean : 43.6604

Gaus Width: 3.0692



B1L101S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

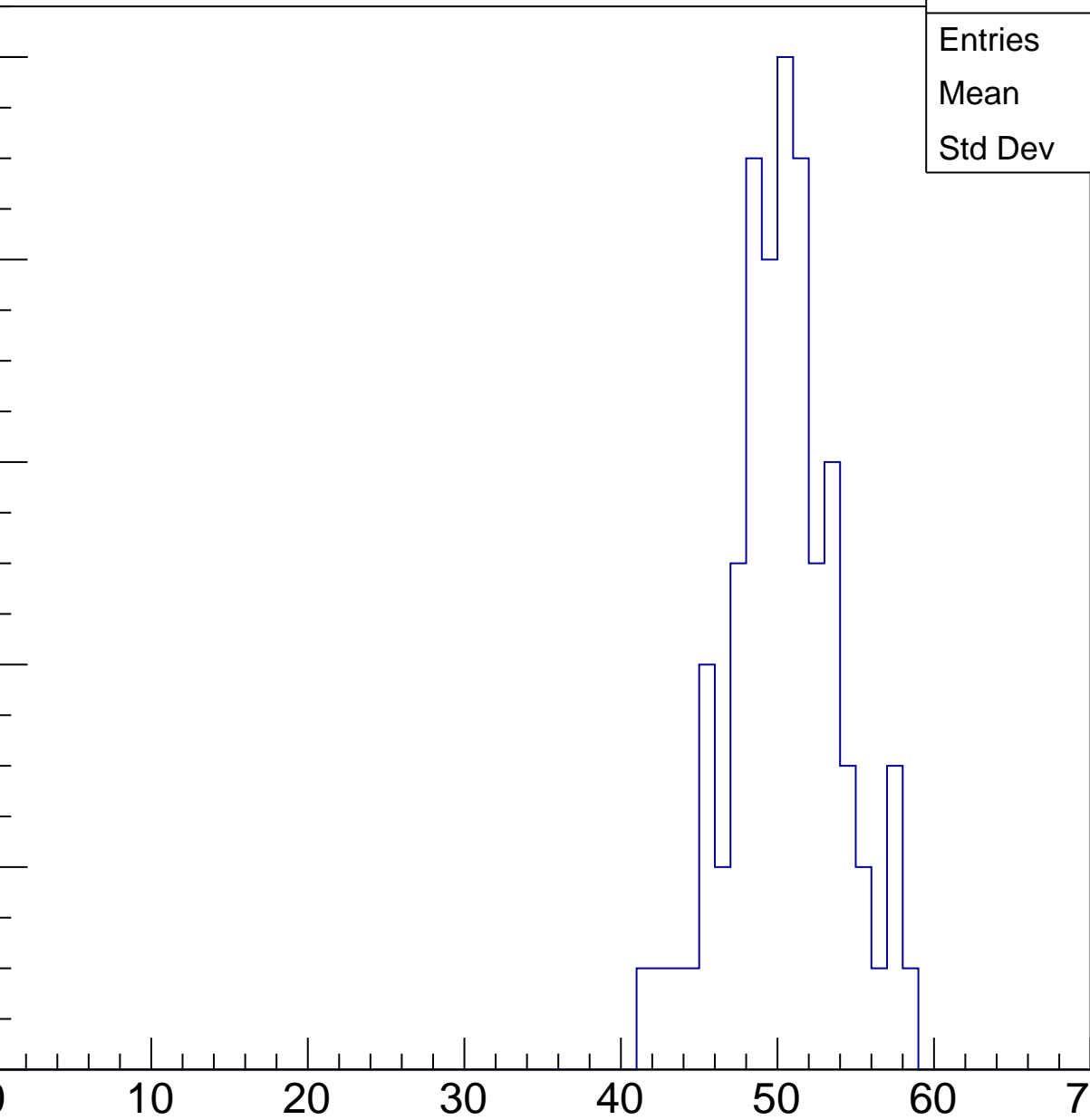
Entries	72
Mean	49.93
Std Dev	3.493

Entry

10
8
6
4
2
0

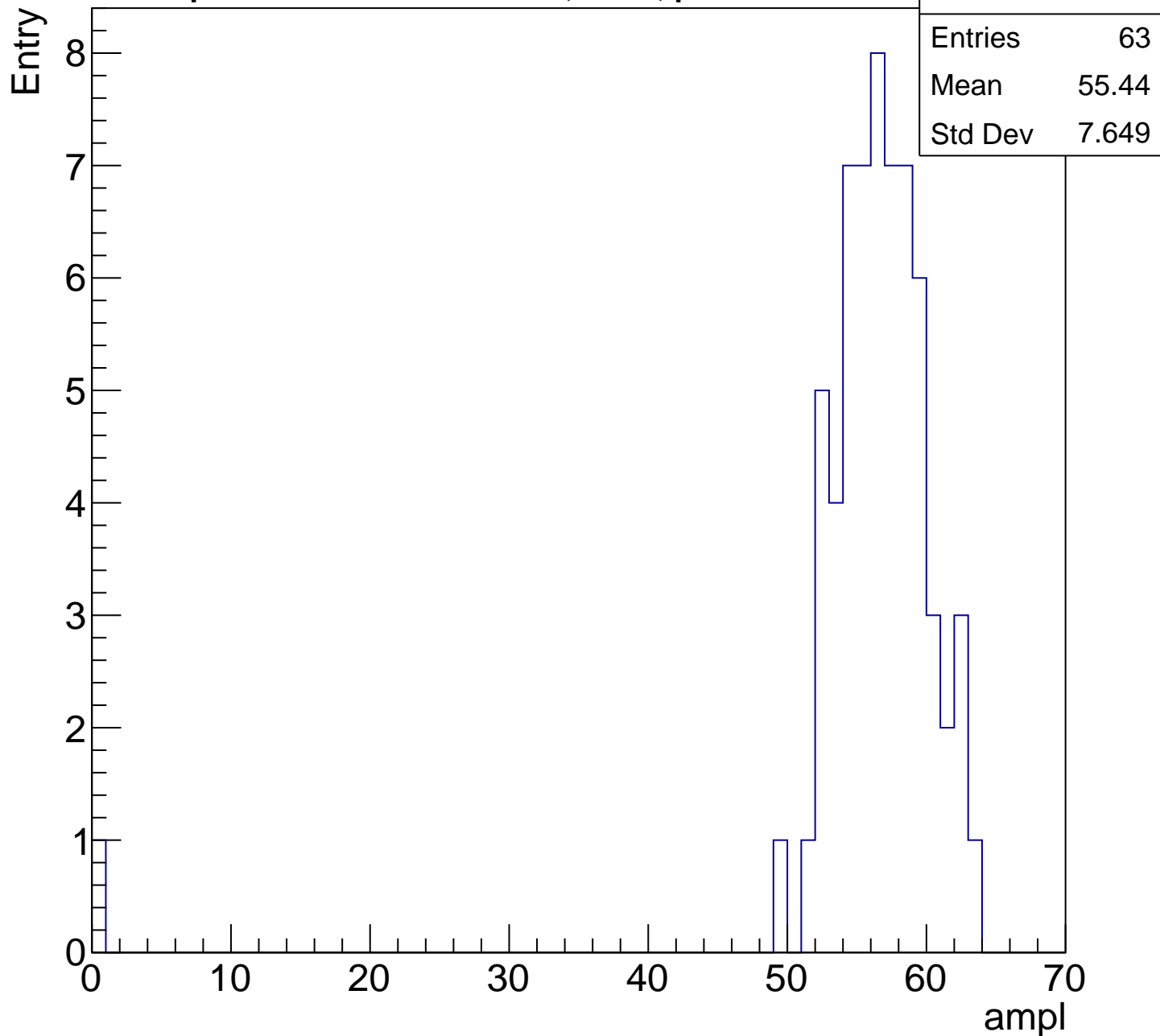
0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.67
Std Dev	1.915

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

B1L101S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch22, adc0

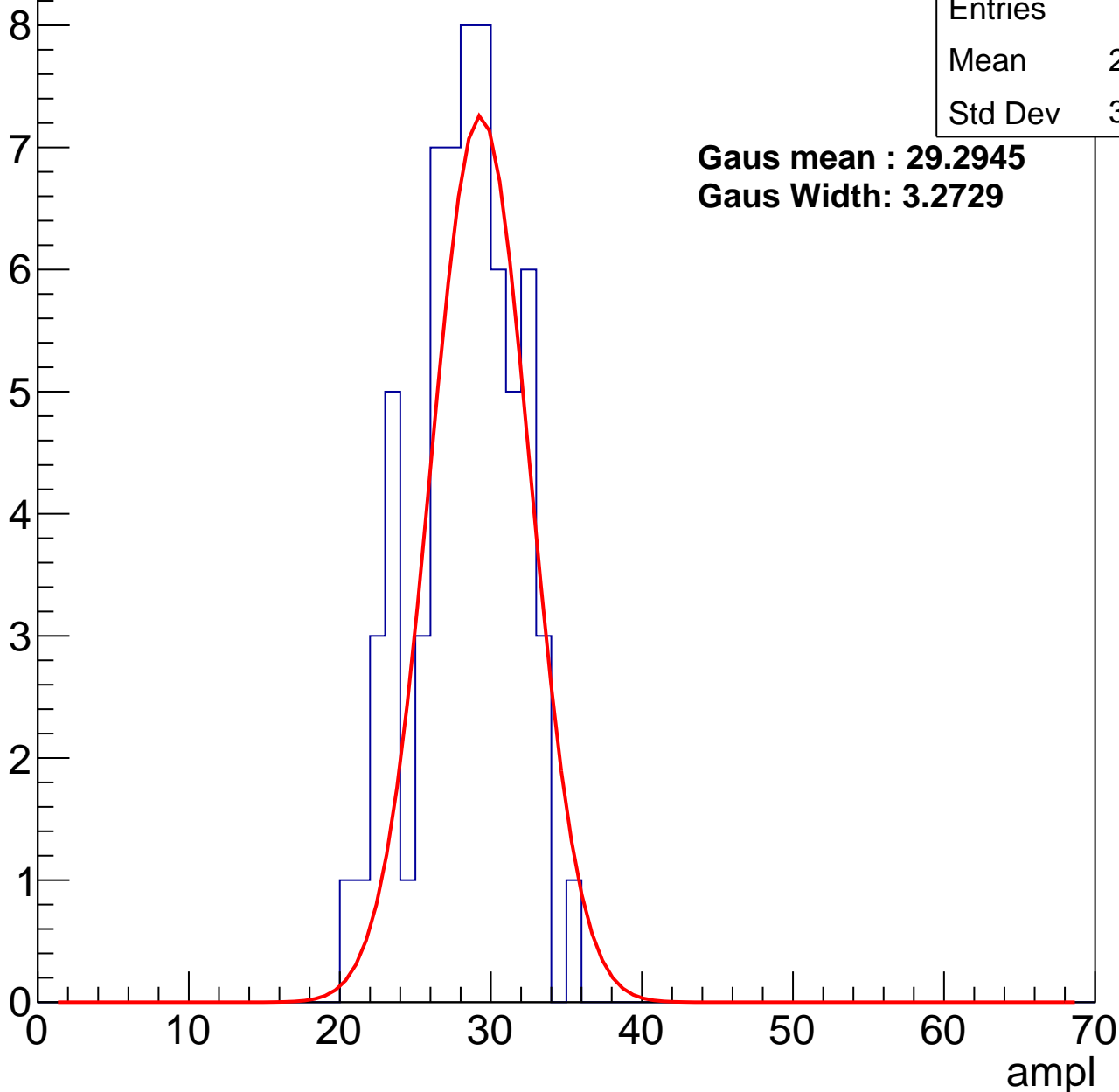
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.83
Std Dev	3.326

Gaus mean : 29.2945

Gaus Width: 3.2729



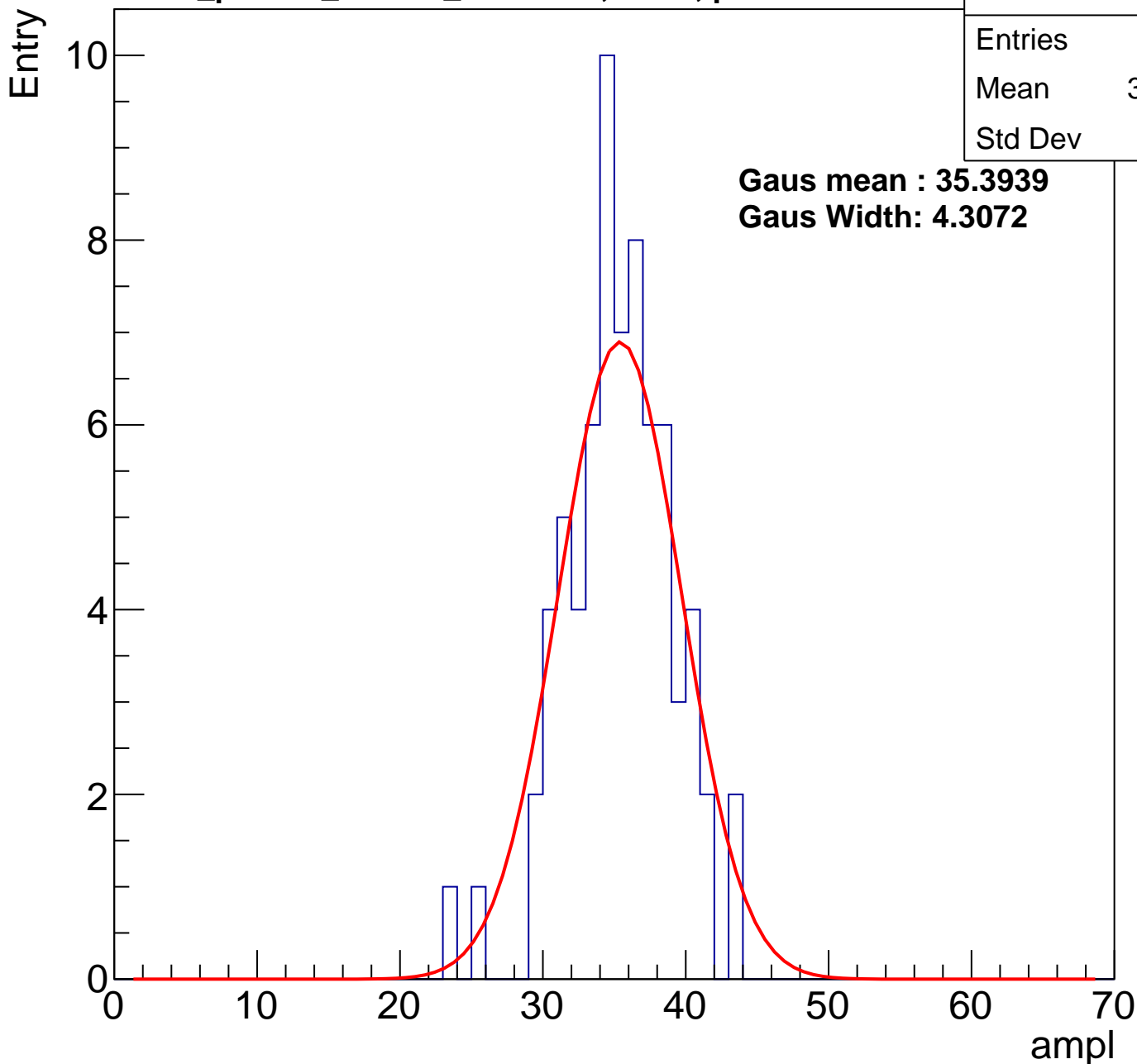
B1L101S, U14-ch22, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	34.86
Std Dev	3.75

Gaus mean : 35.3939

Gaus Width: 4.3072



B1L101S, U14-ch22, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

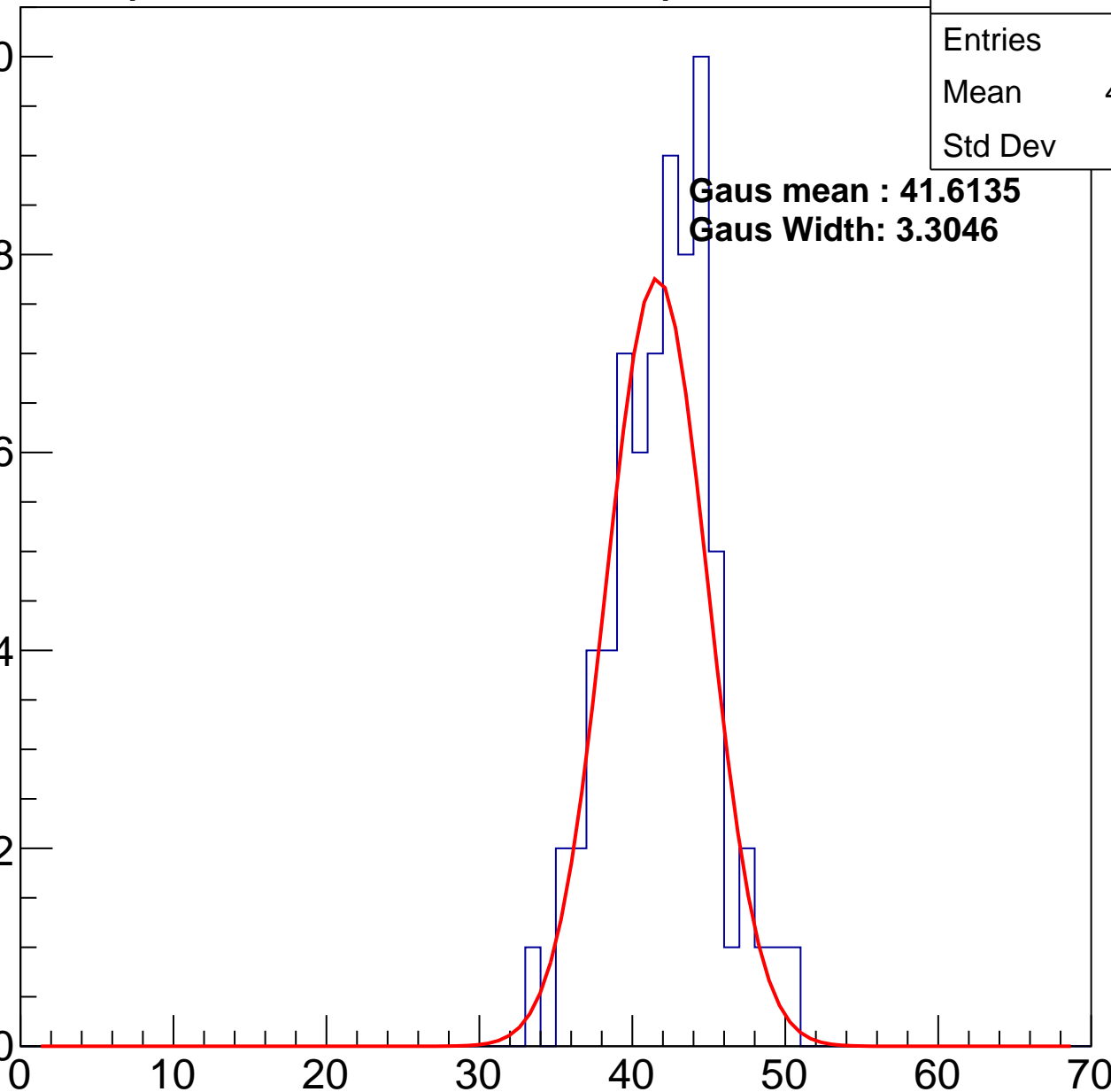
Entries	71
Mean	41.54
Std Dev	3.36

Gaus mean : 41.6135

Gaus Width: 3.3046

10
8
6
4
2
0

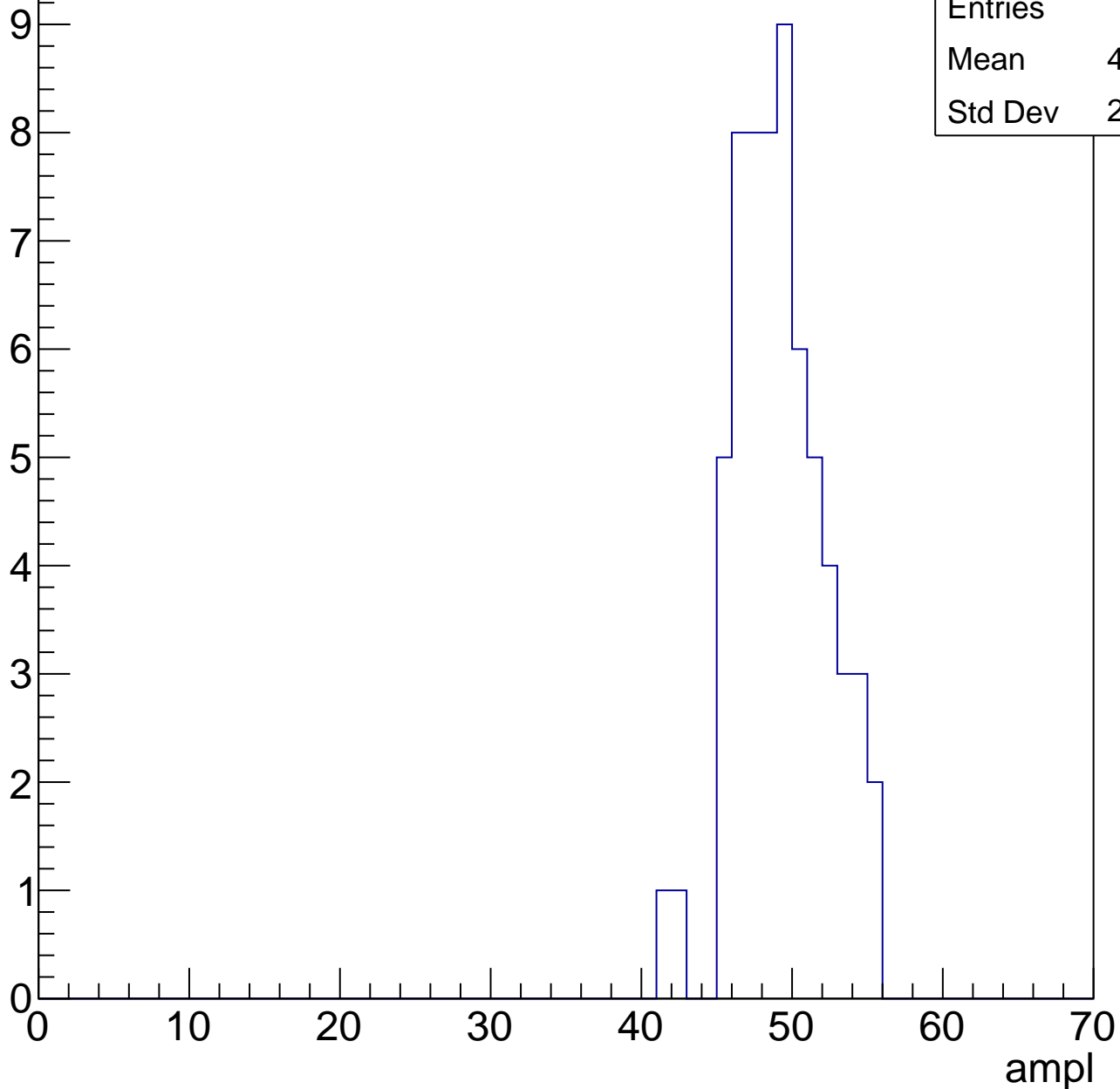
ampl



B1L101S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



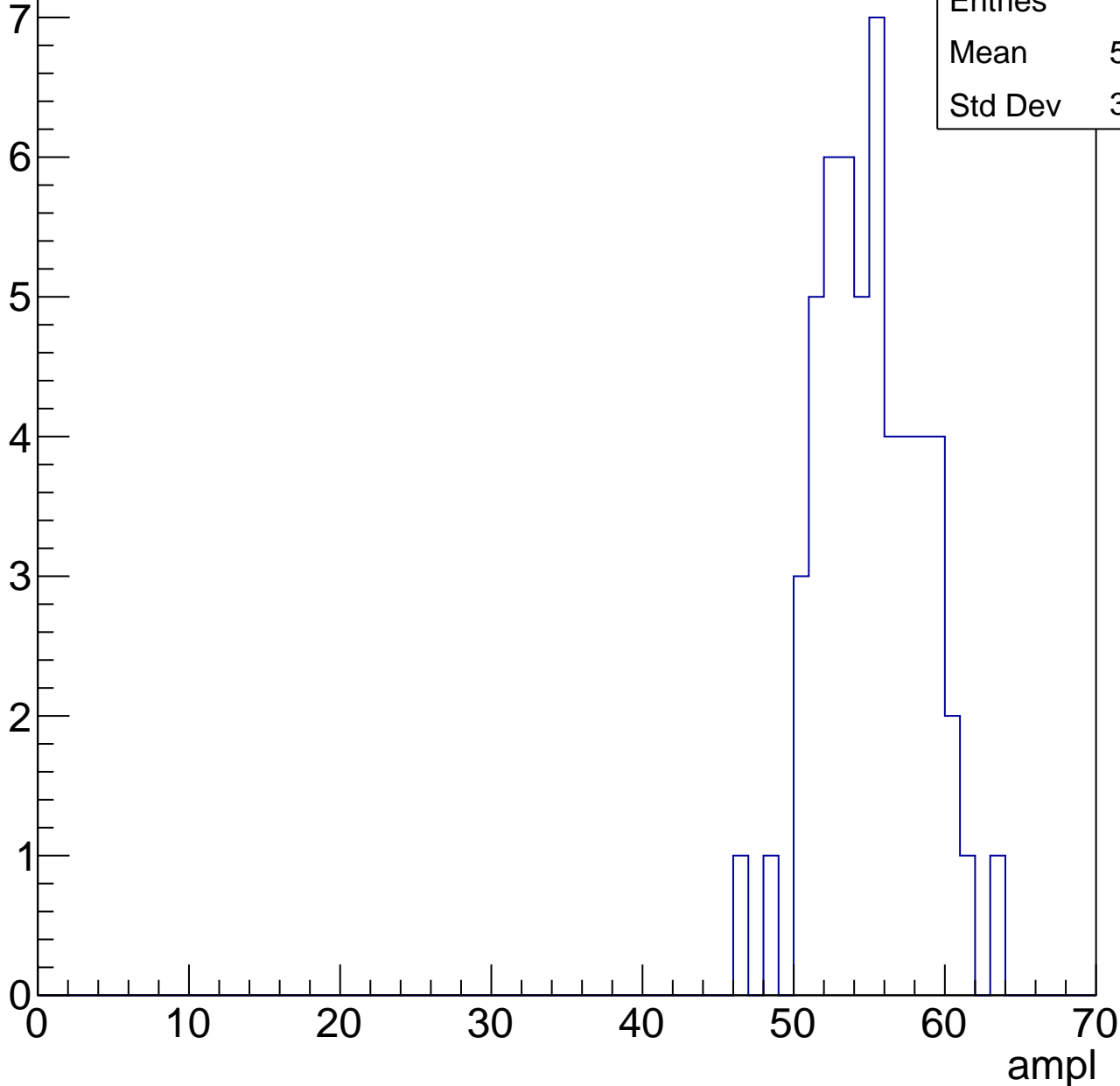
Entries	63
Mean	48.75
Std Dev	2.976

B1L101S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.59
Std Dev	3.413

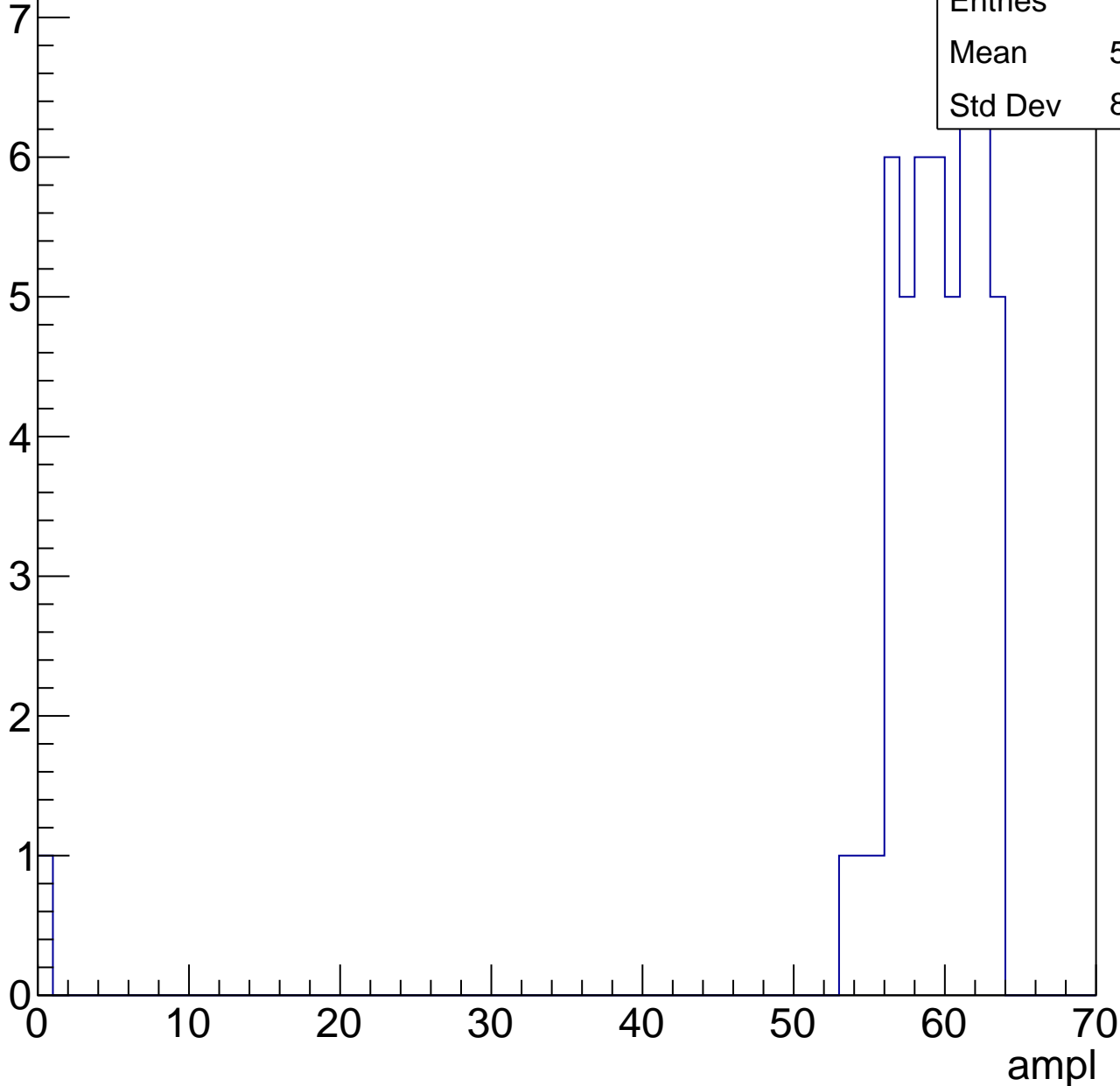


B1L101S, U14-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

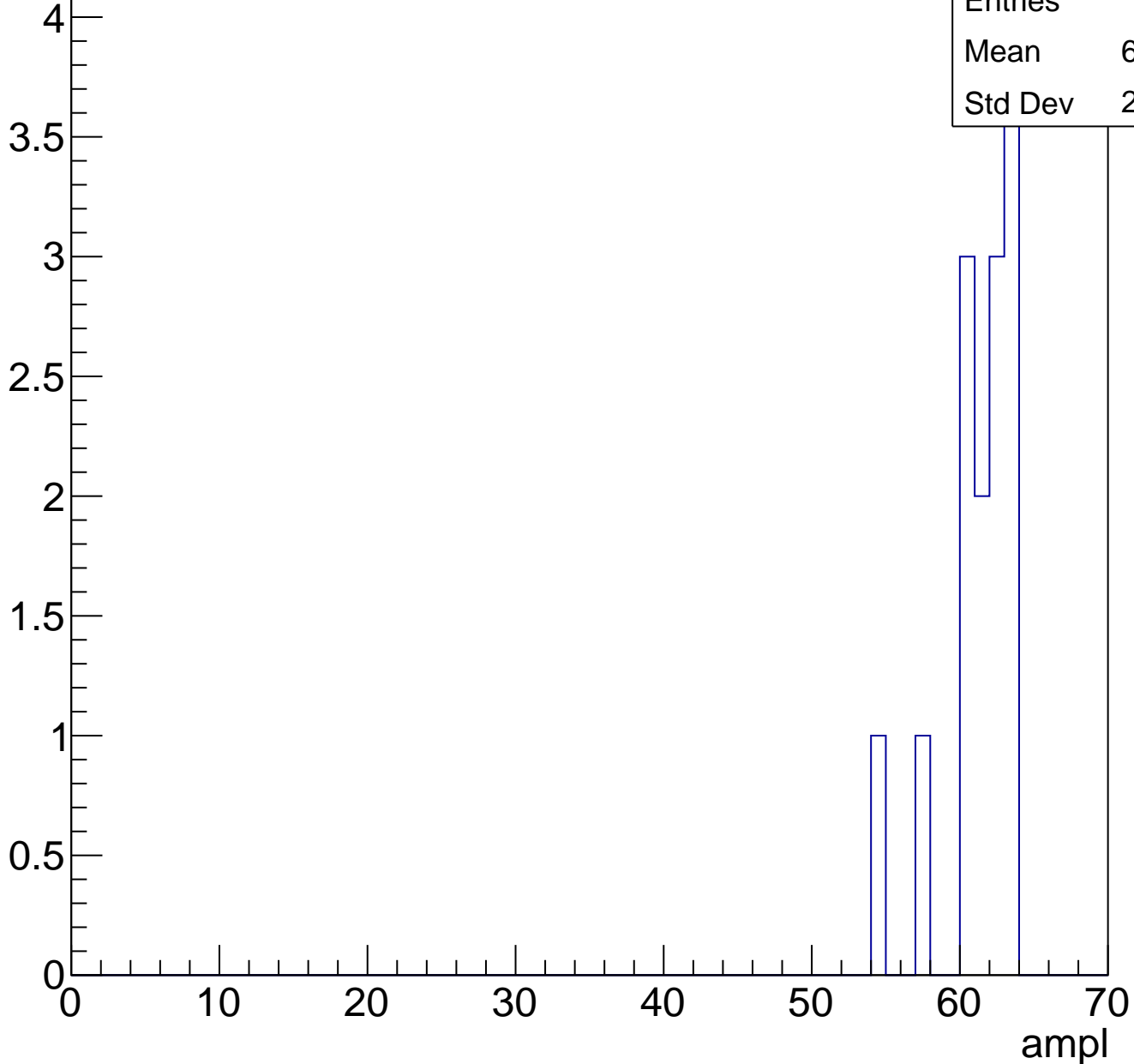
Entries	51
Mean	58.06
Std Dev	8.596



B1L101S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

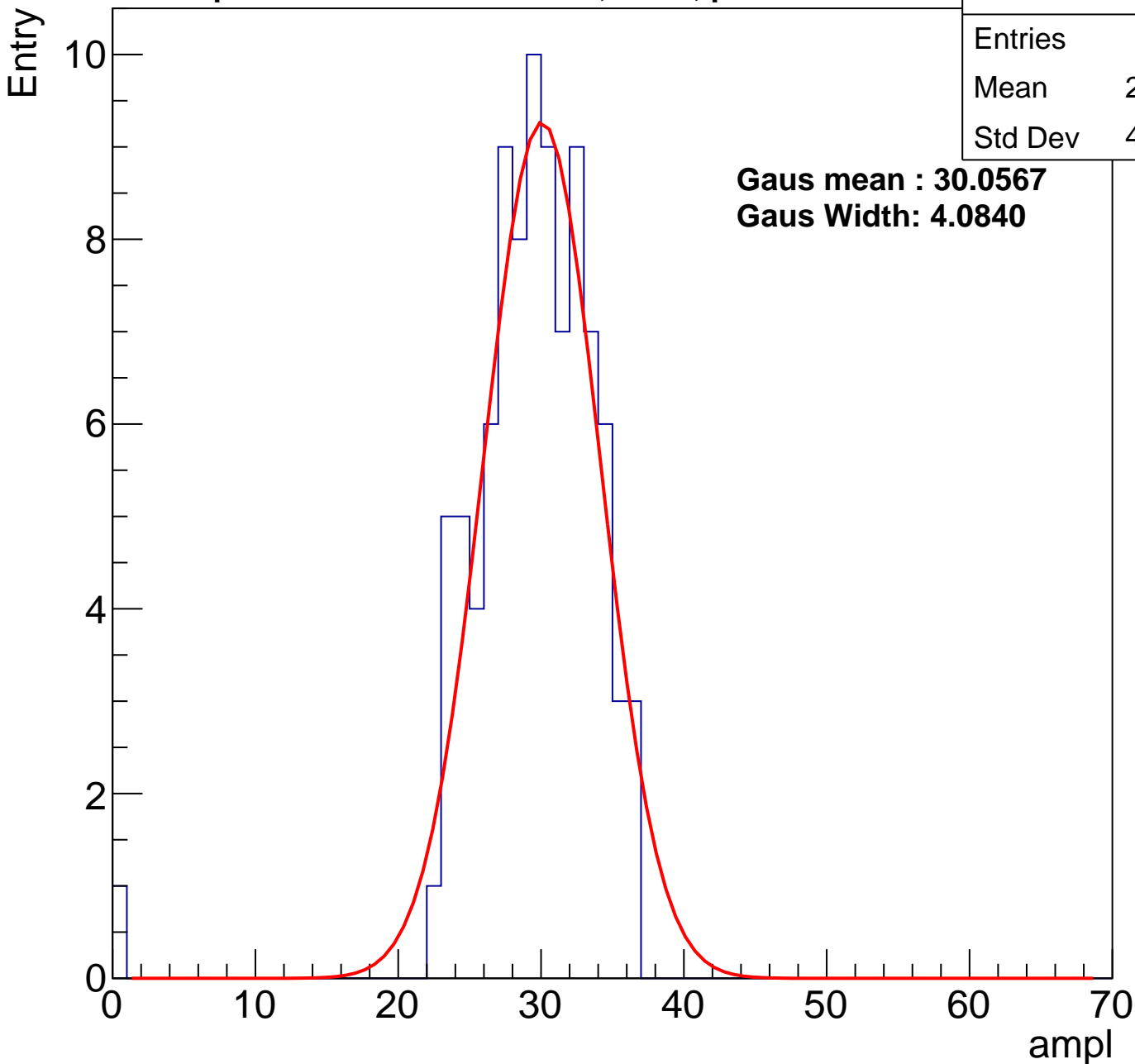


B1L101S, U14-ch23, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	28.96
Std Dev	4.628

Gaus mean : 30.0567
Gaus Width: 4.0840



B1L101S, U14-ch23, adc1

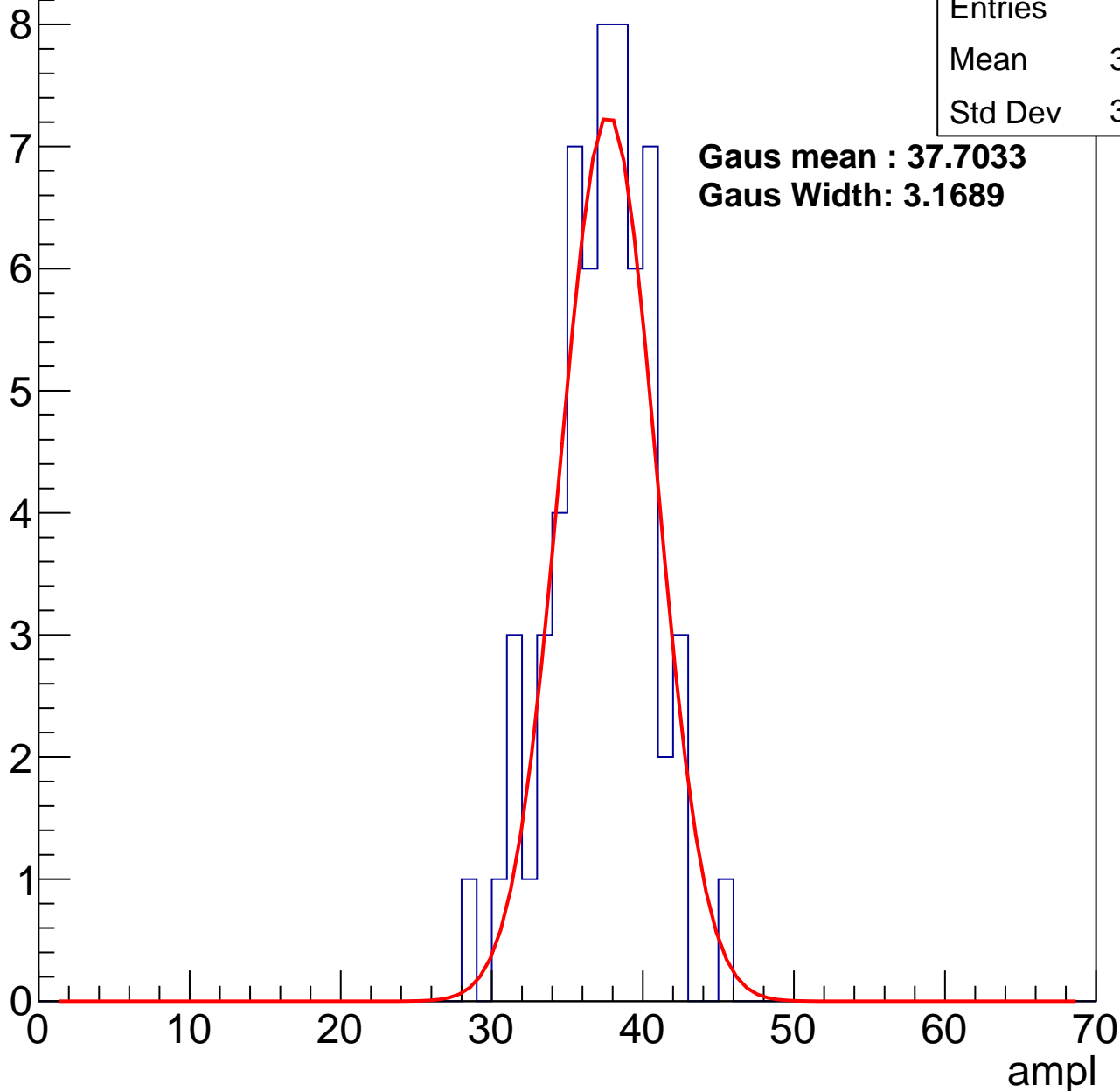
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.82
Std Dev	3.252

Gaus mean : 37.7033

Gaus Width: 3.1689



B1L101S, U14-ch23, adc2

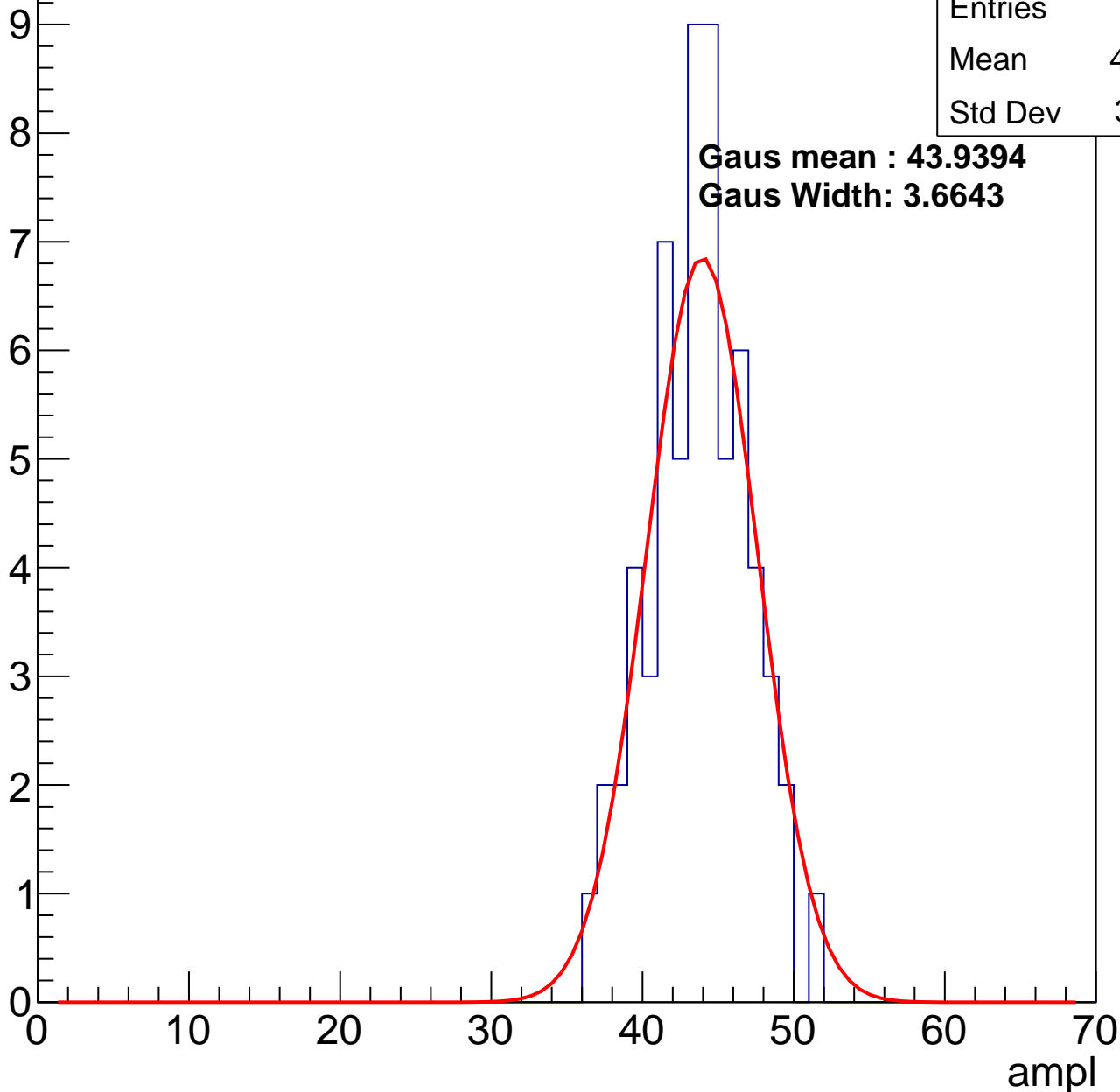
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.24
Std Dev	3.211

Gaus mean : 43.9394

Gaus Width: 3.6643

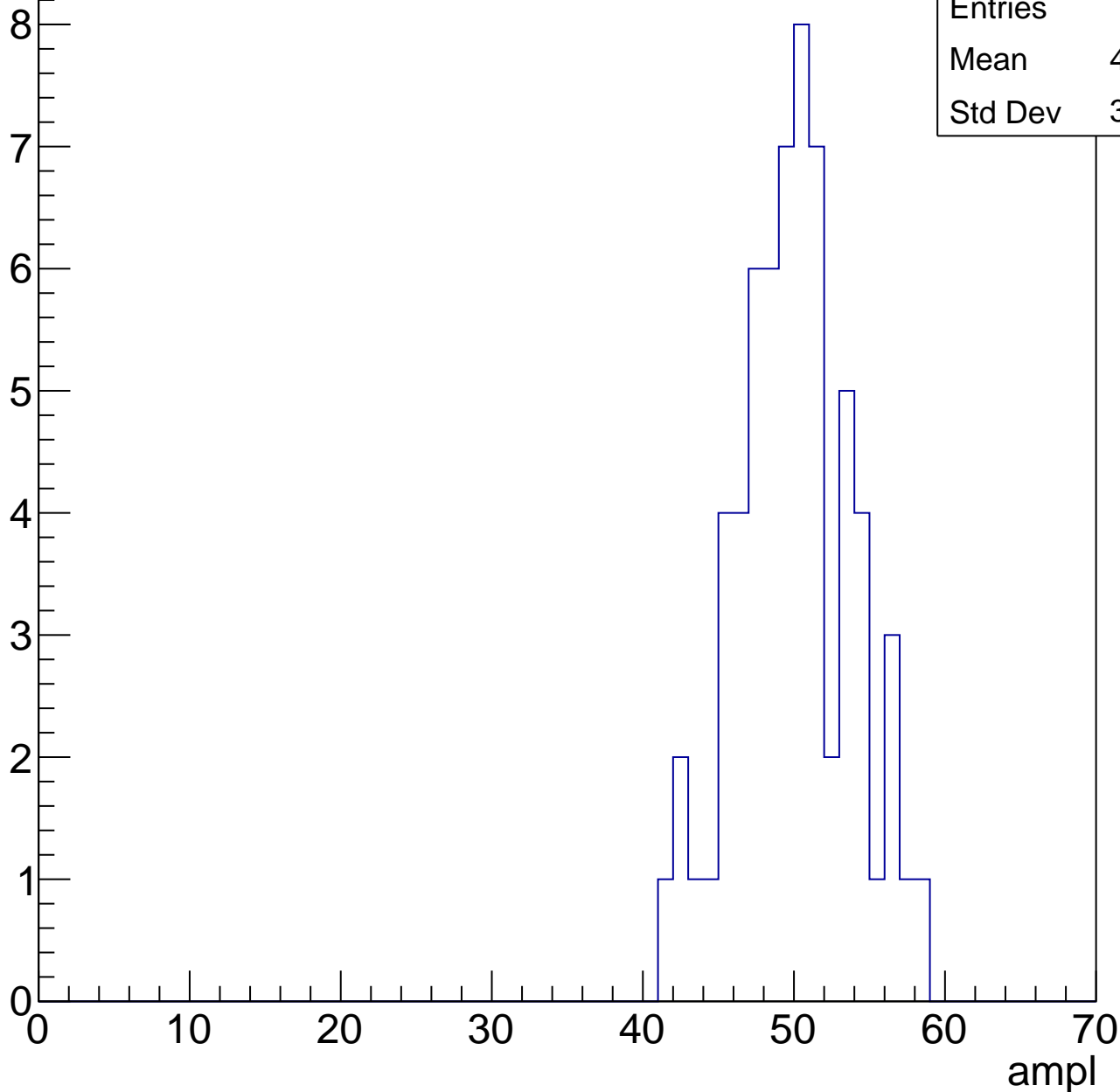


B1L101S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.52
Std Dev	3.746

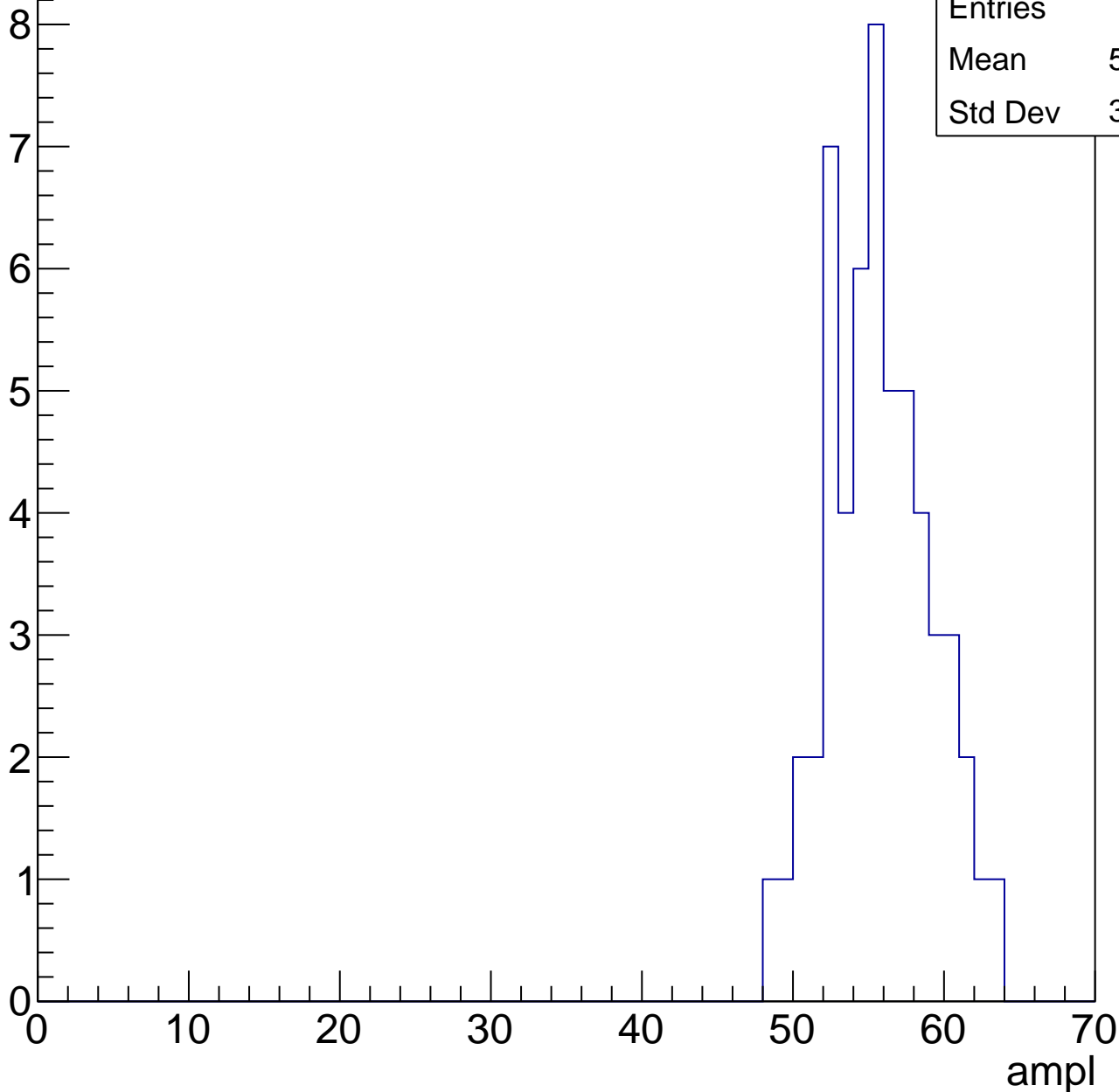


B1L101S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

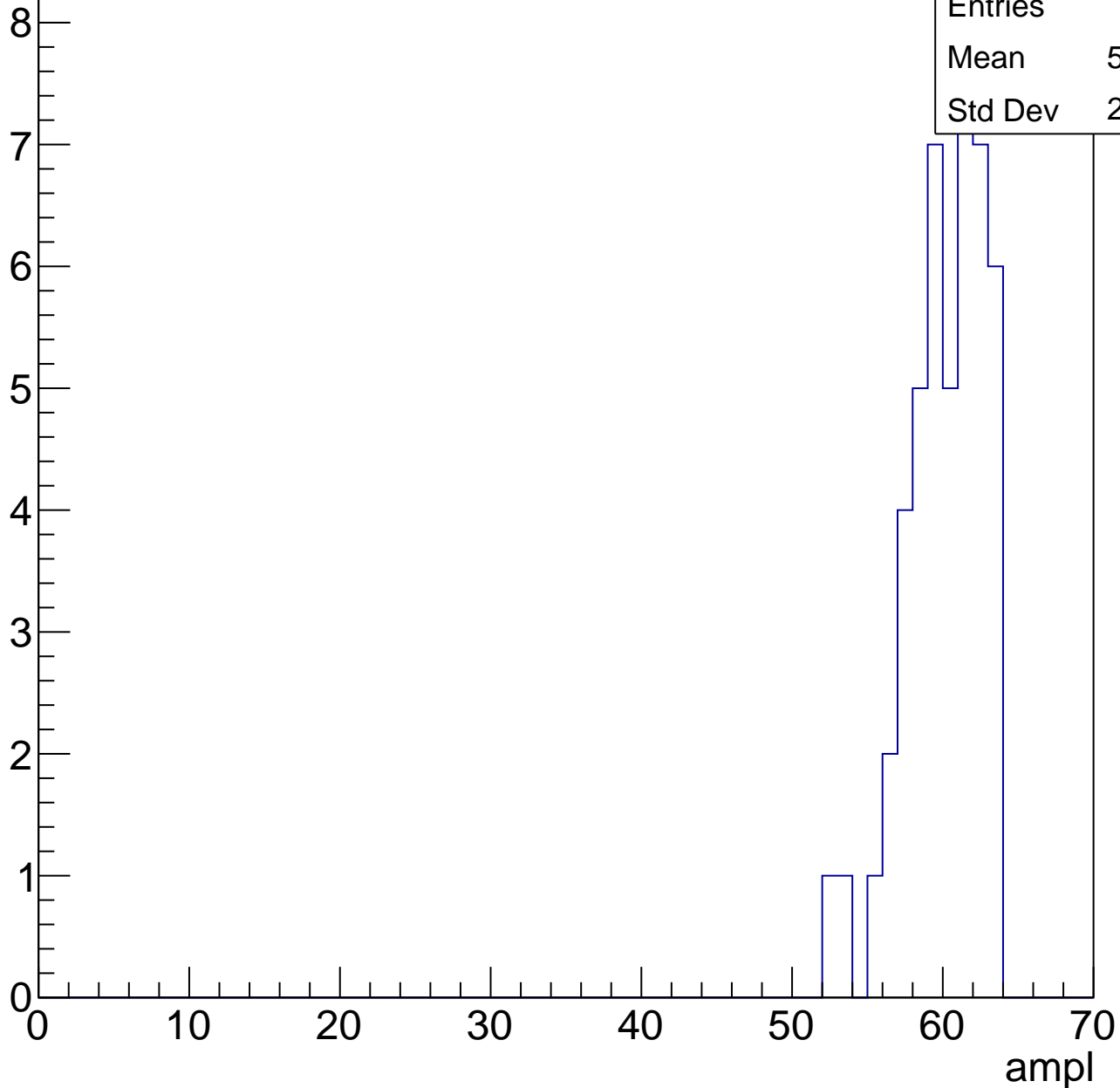
Entries	55
Mean	55.27
Std Dev	3.349



B1L101S, U14-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

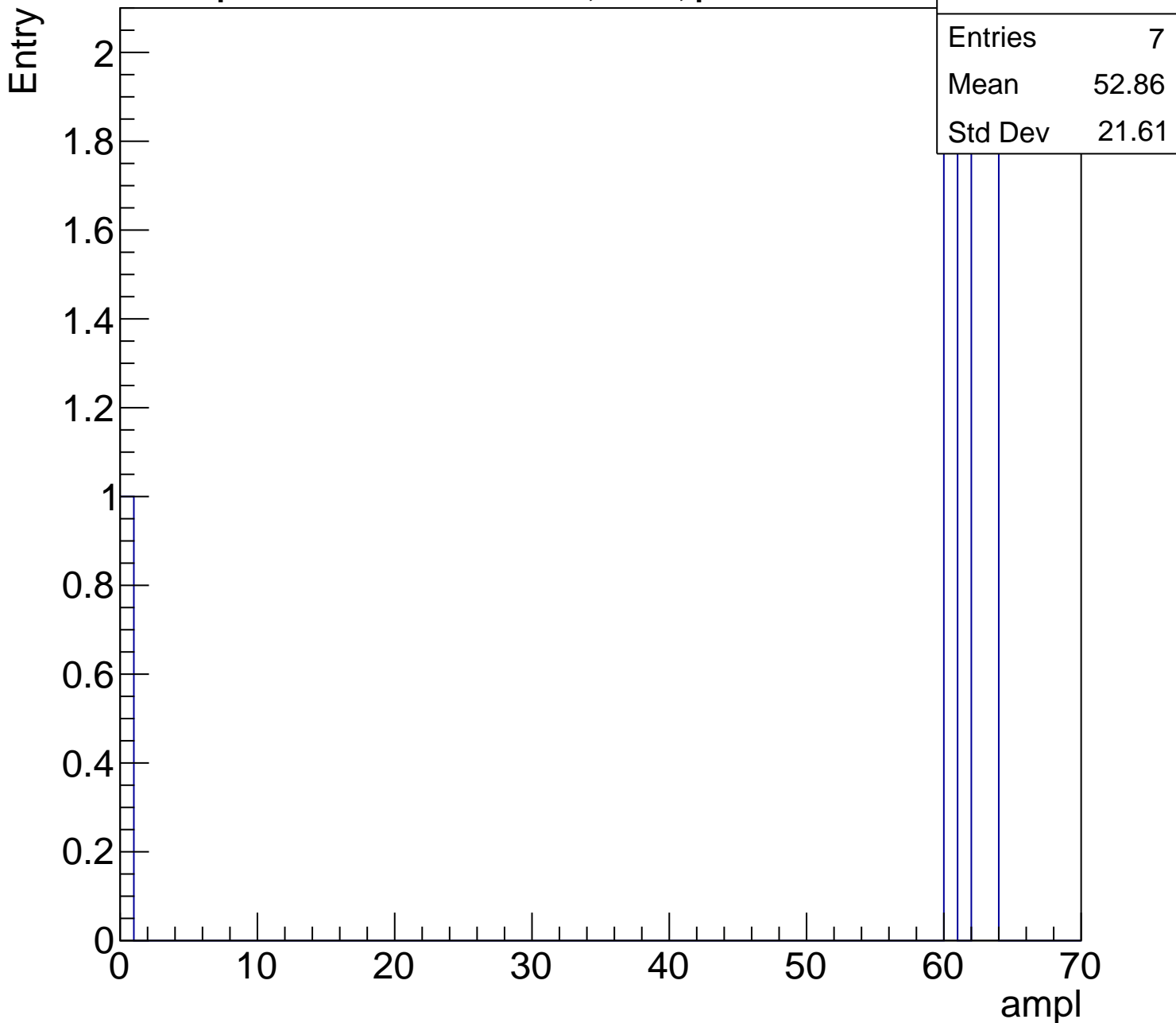
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.61

0 10 20 30 40 50 60 70

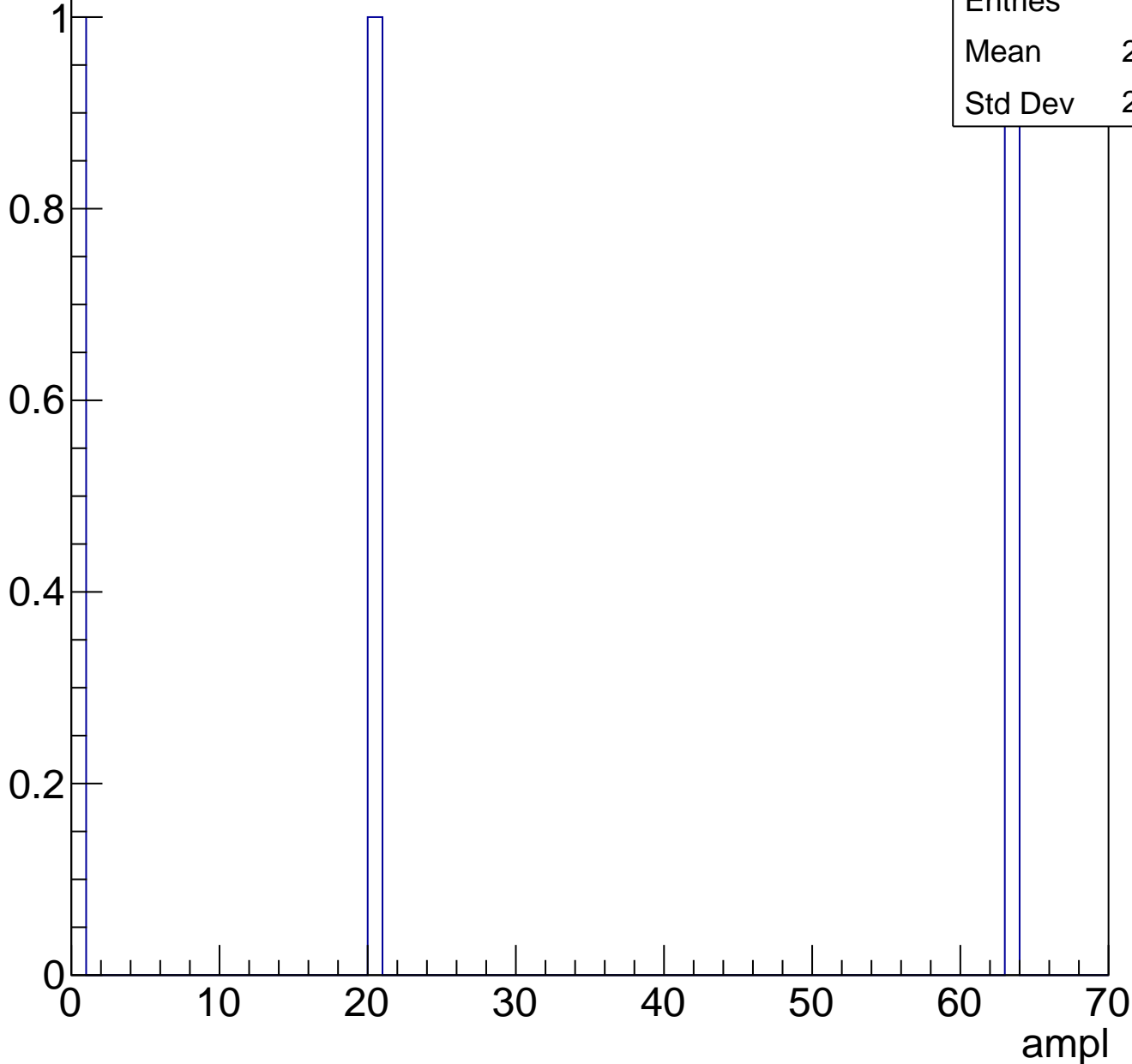
ampl



B1L101S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch24, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	29.3
Std Dev	4.85

Gaus mean : 30.0312

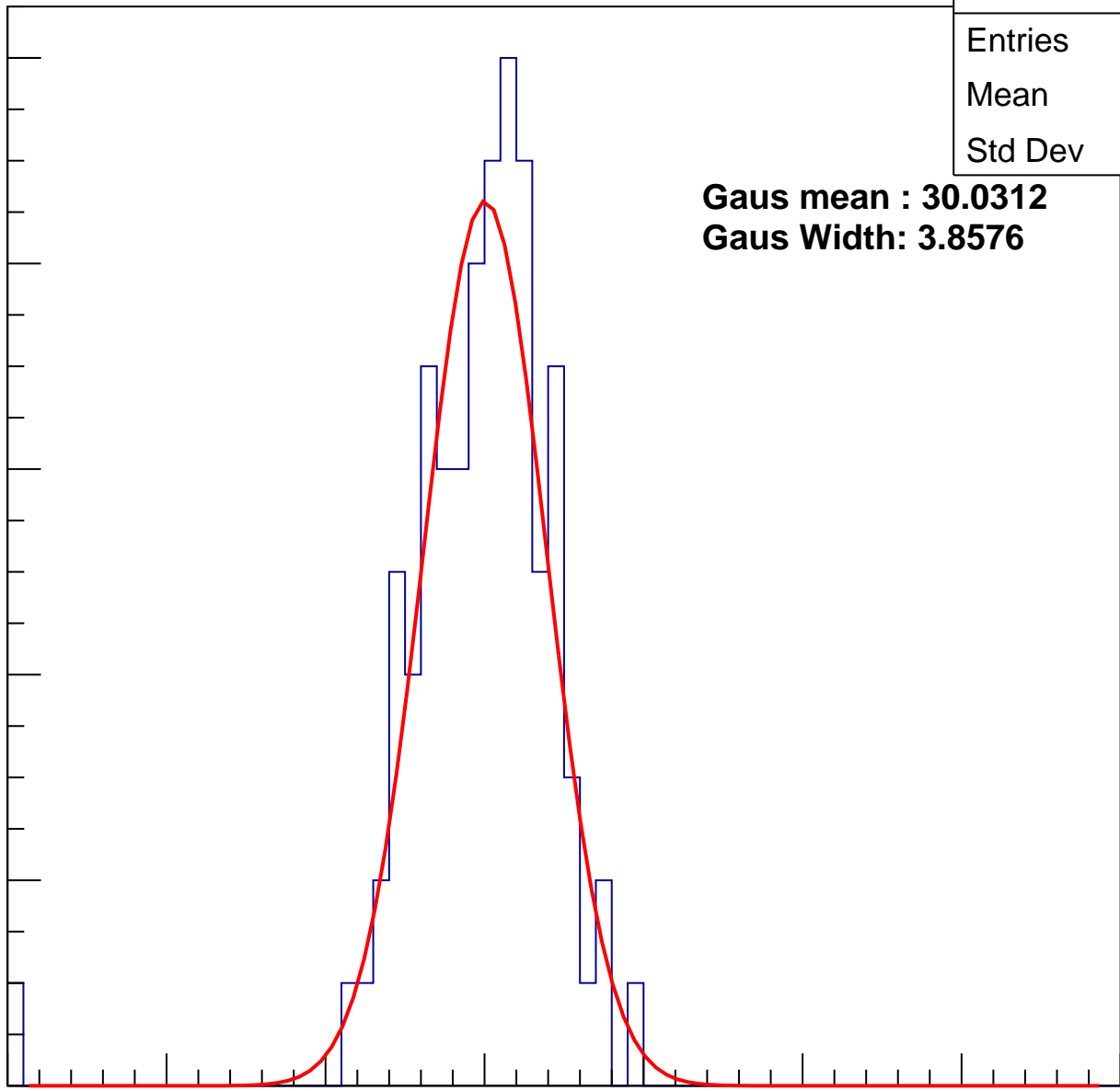
Gaus Width: 3.8576

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch24, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	37.1
Std Dev	2.809

Gaus mean : 37.5631

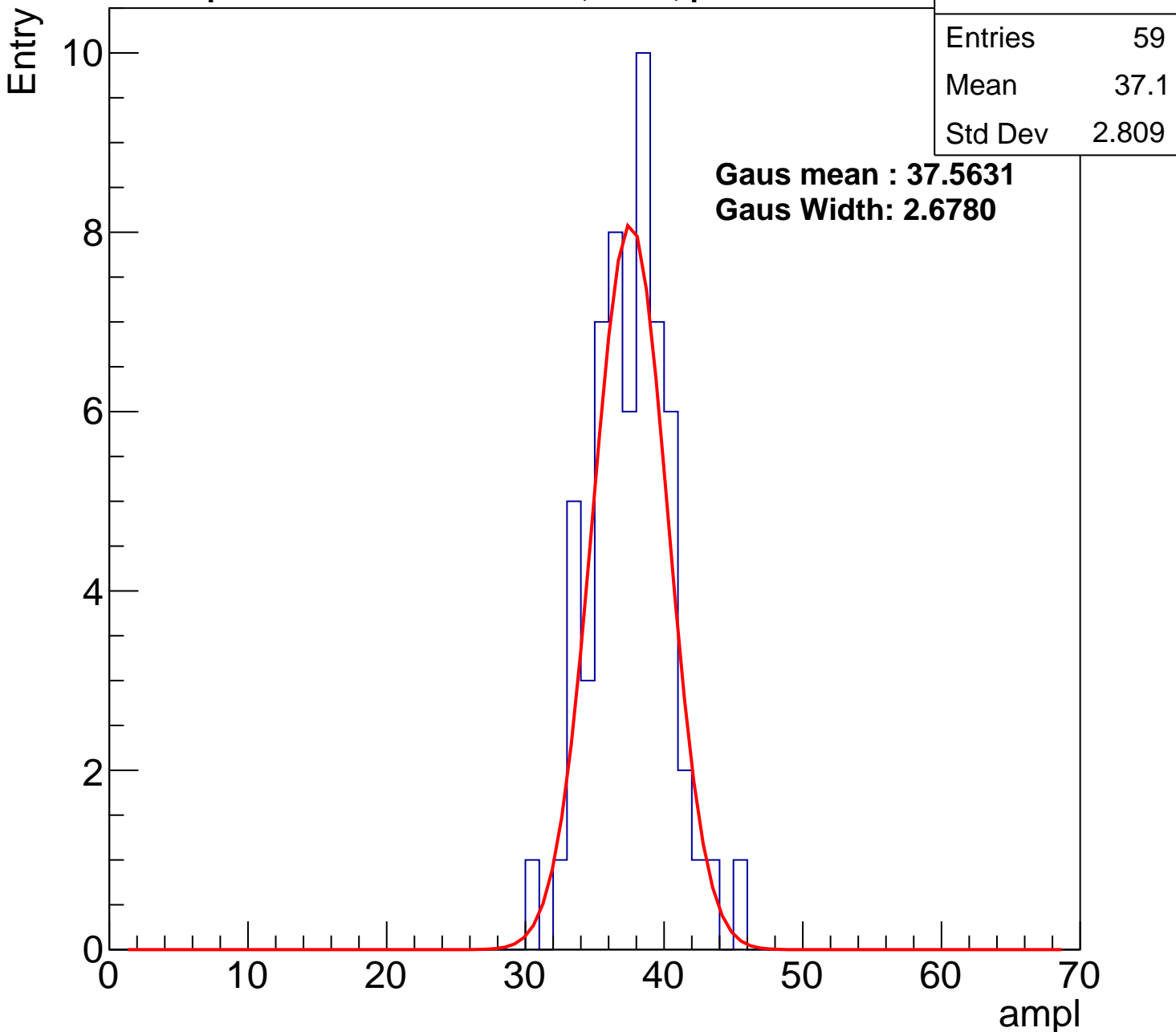
Gaus Width: 2.6780

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch24, adc2

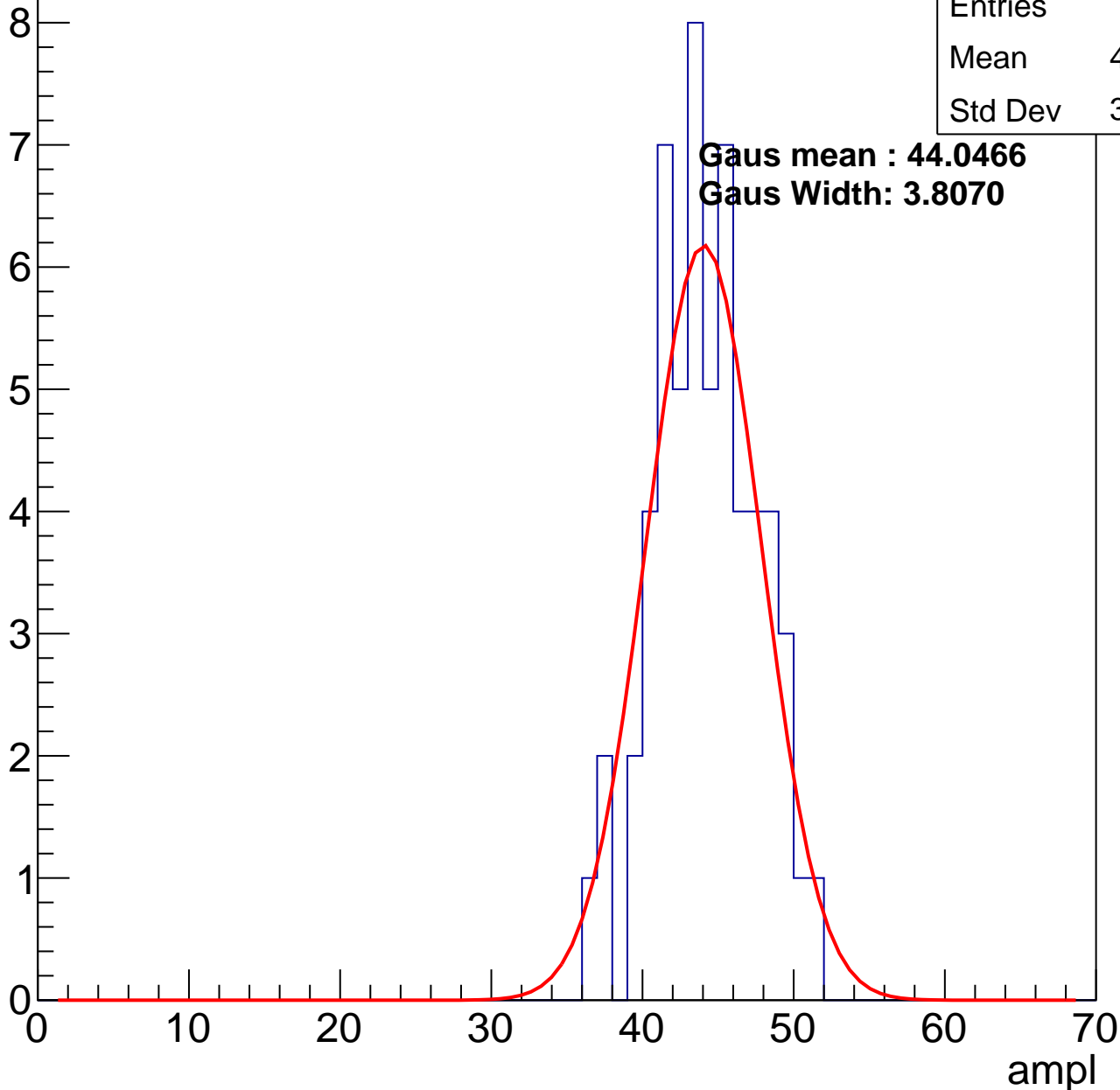
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.72
Std Dev	3.336

Gaus mean : 44.0466

Gaus Width: 3.8070

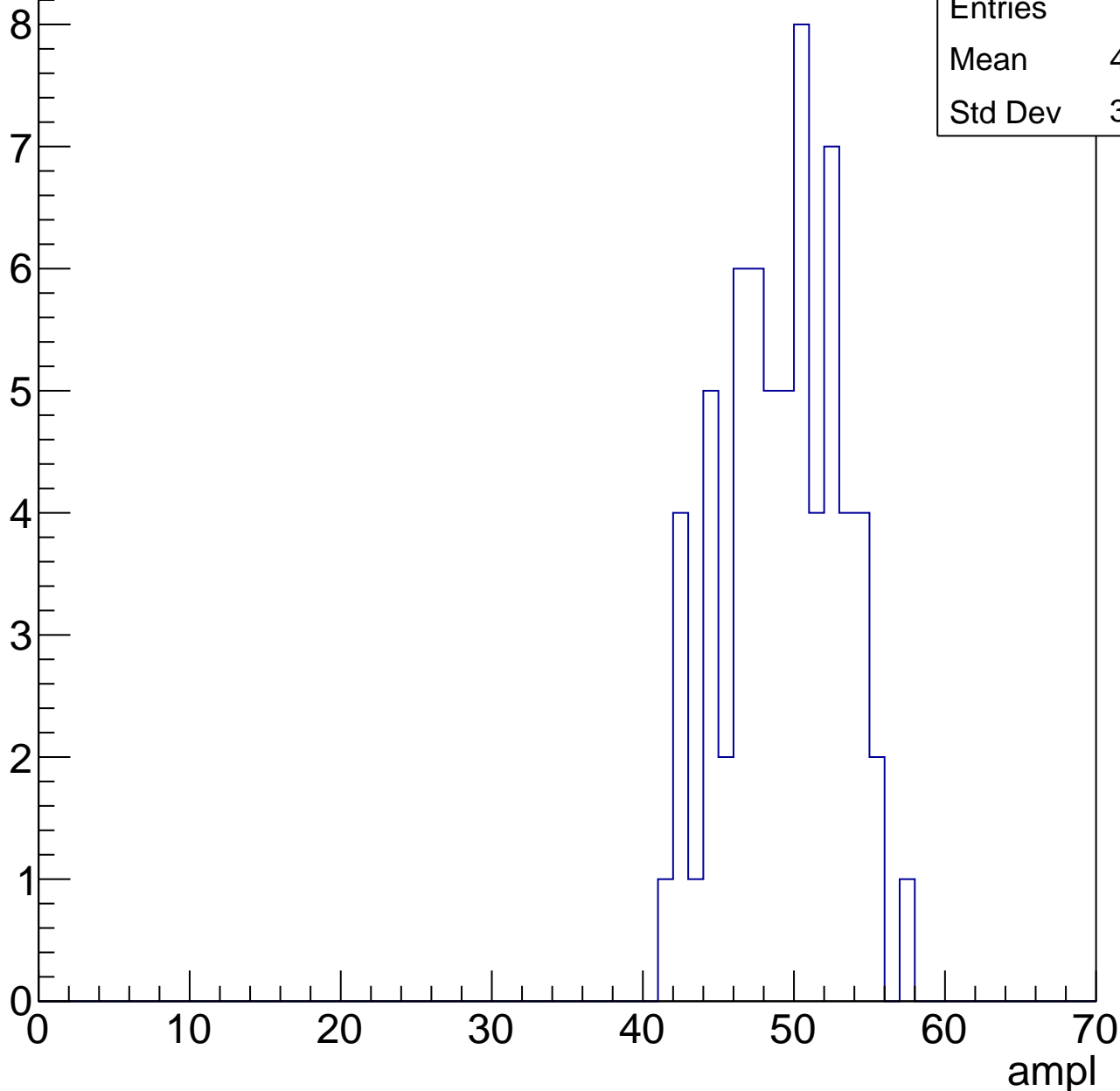


B1L101S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

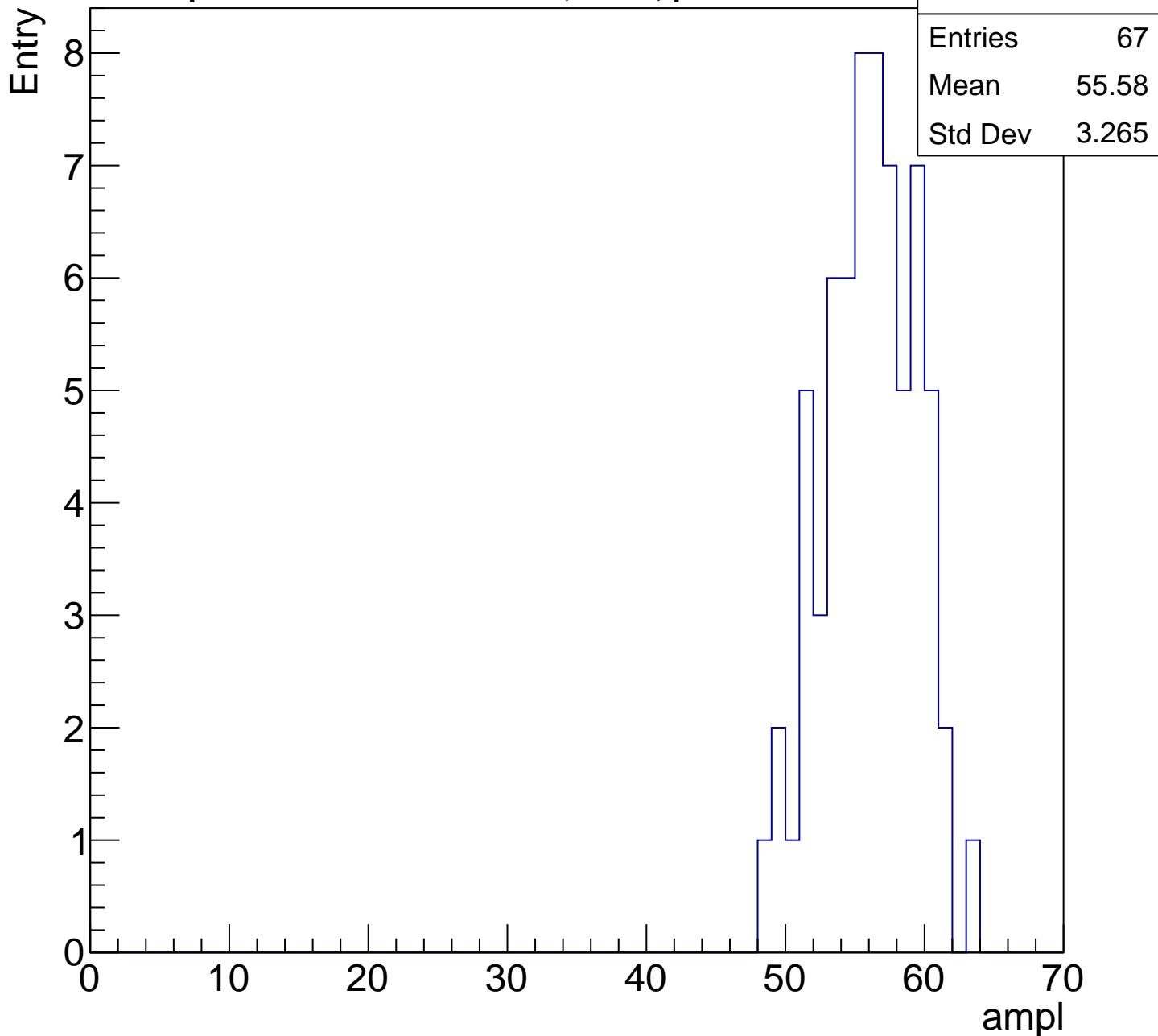
Entry

Entries	65
Mean	48.74
Std Dev	3.759



B1L101S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

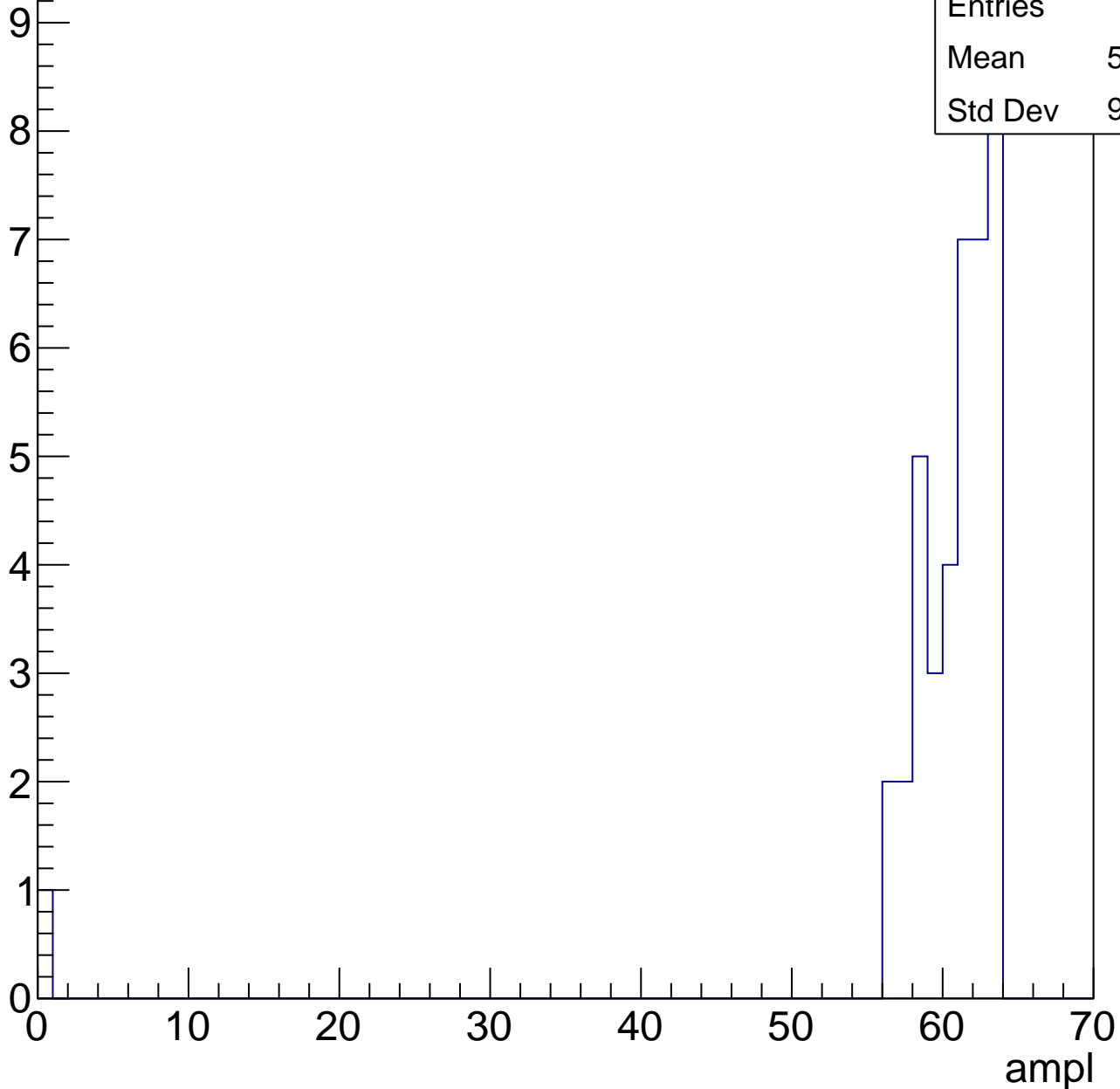


B1L101S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.02
Std Dev	9.684



B1L101S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	0.8292

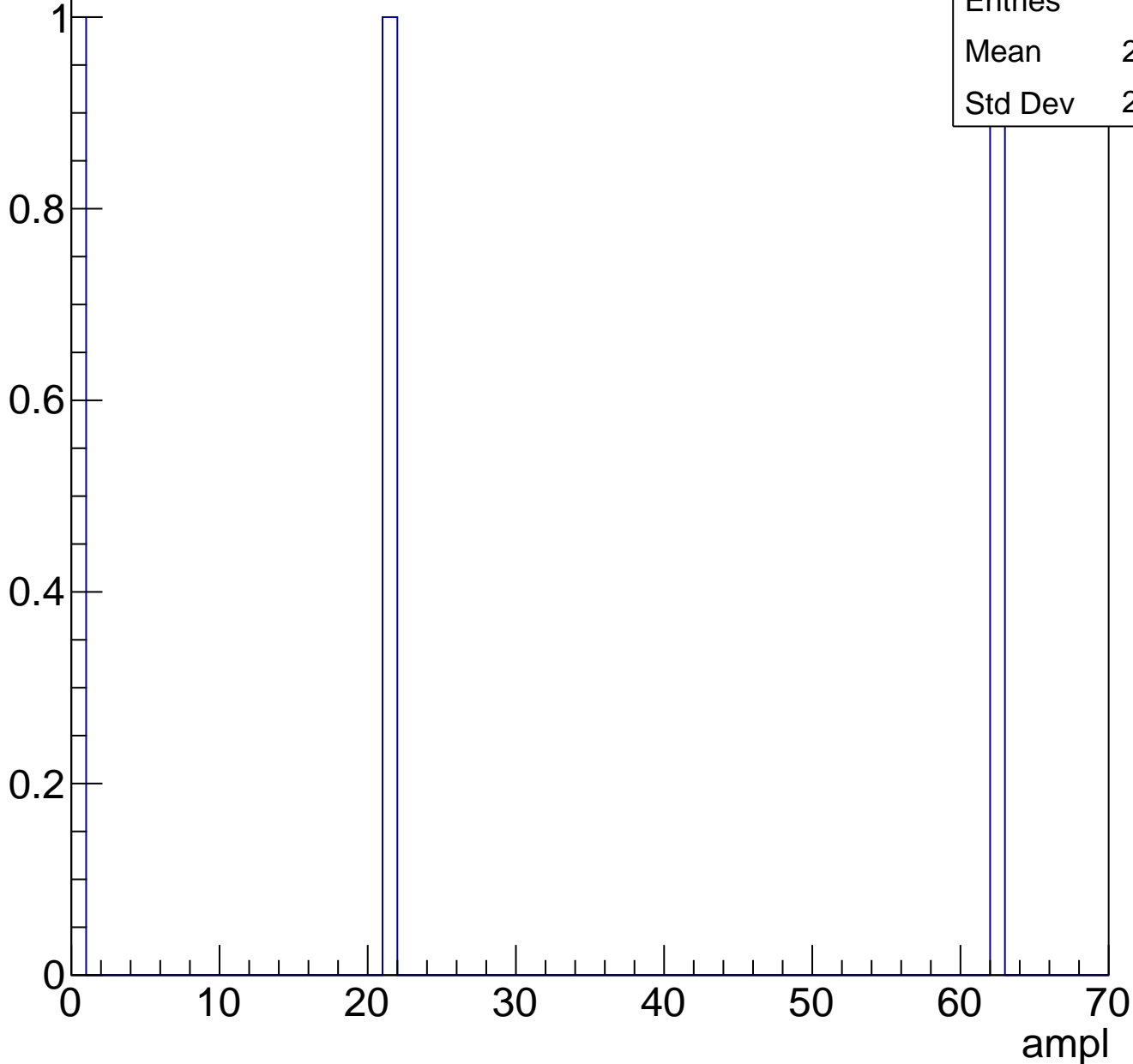
ampl

0 10 20 30 40 50 60 70

B1L101S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B1L101S, U14-ch25, adc0

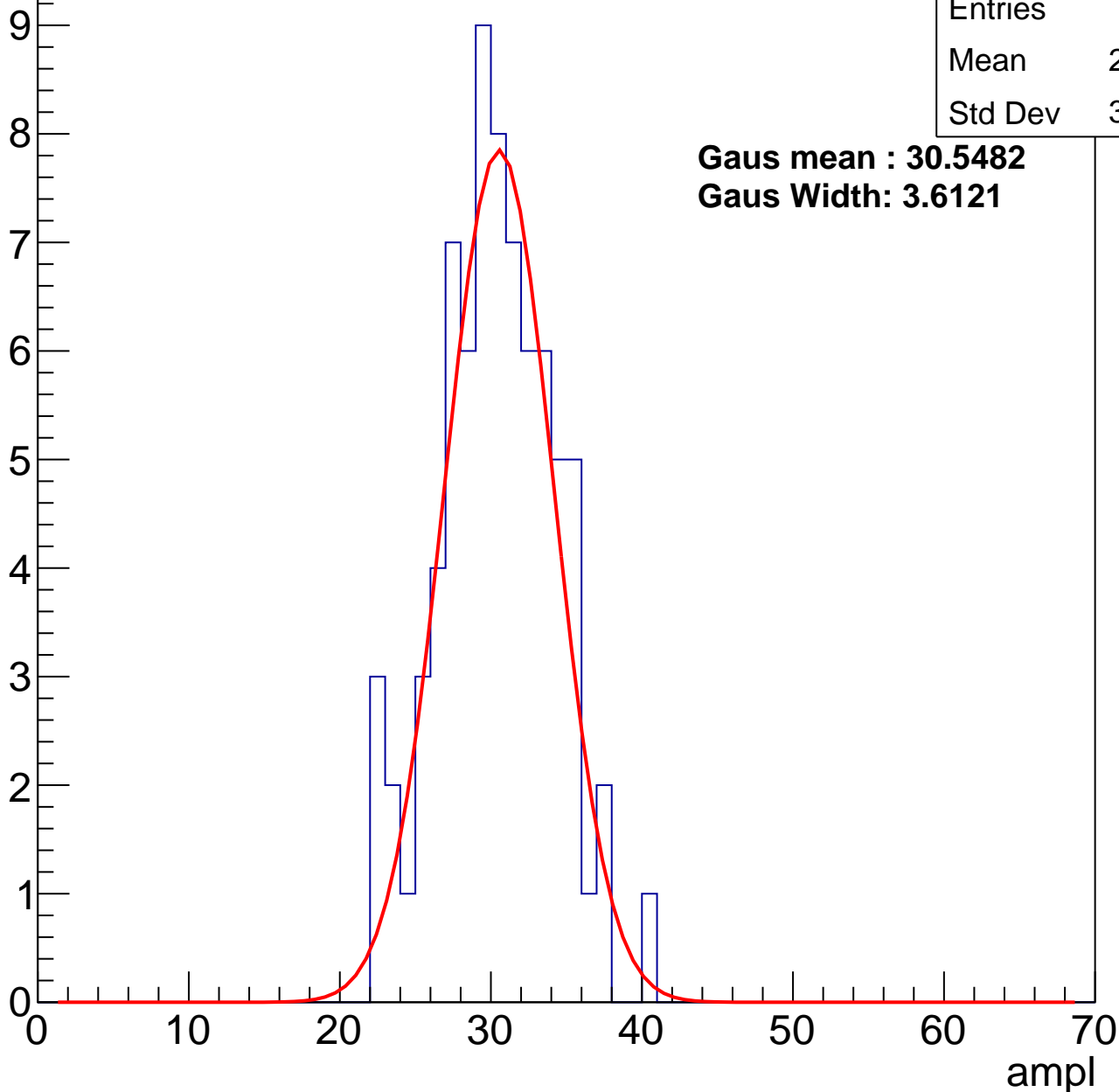
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.93
Std Dev	3.778

Gaus mean : 30.5482

Gaus Width: 3.6121



B1L101S, U14-ch25, adc1

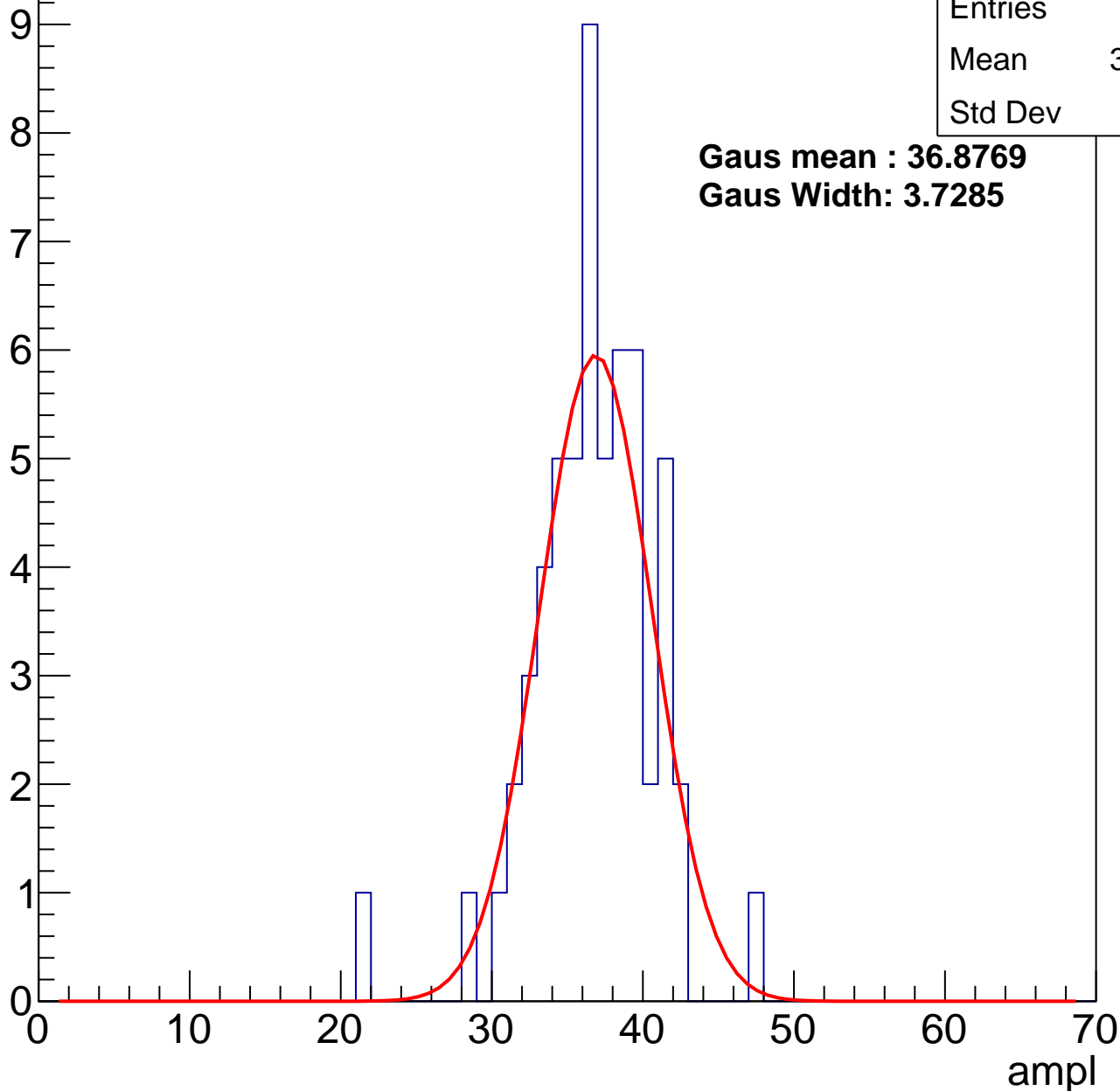
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.22
Std Dev	3.97

Gaus mean : 36.8769

Gaus Width: 3.7285



B1L101S, U14-ch25, adc2

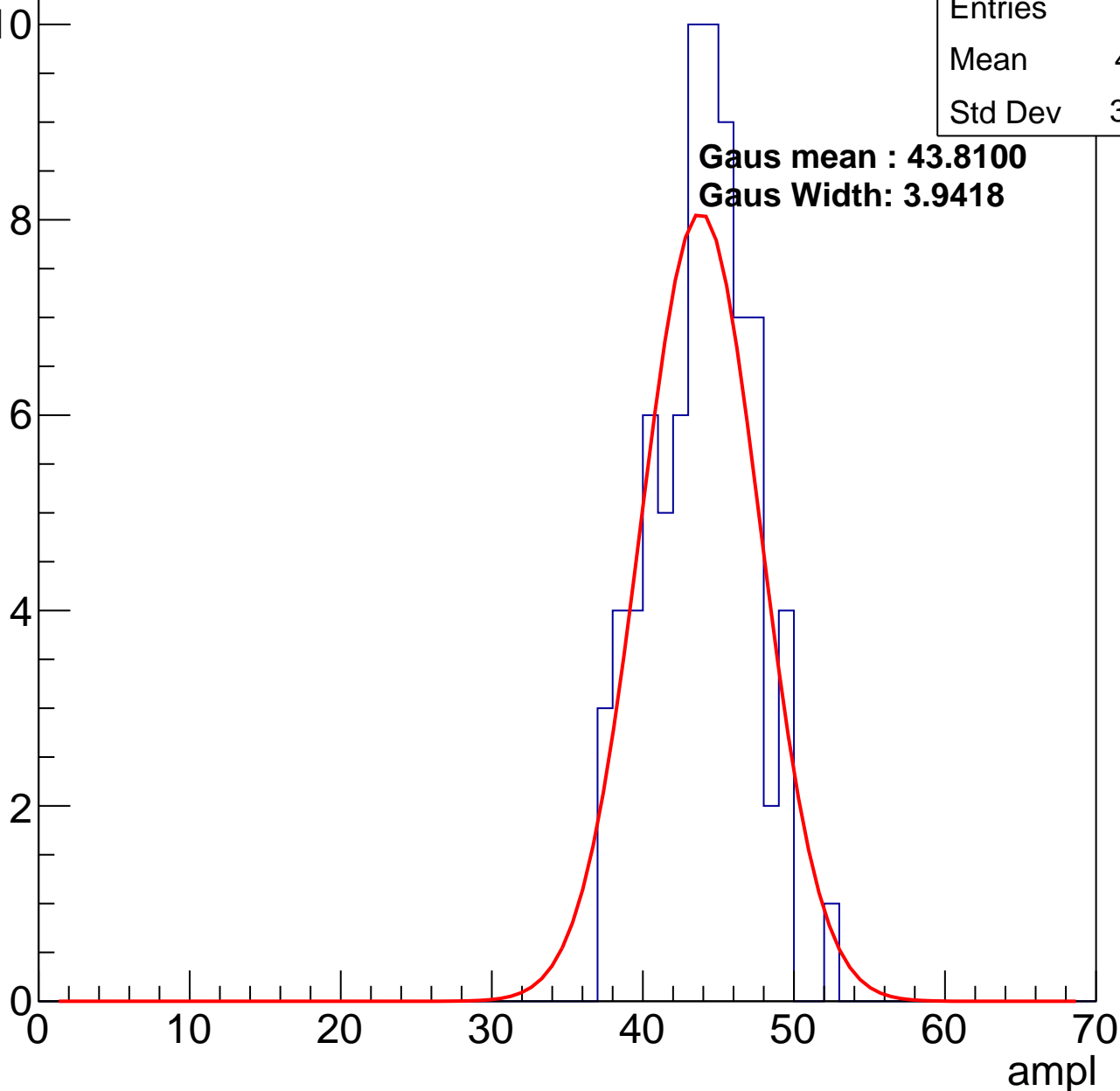
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.41
Std Dev	3.272

Gaus mean : 43.8100

Gaus Width: 3.9418

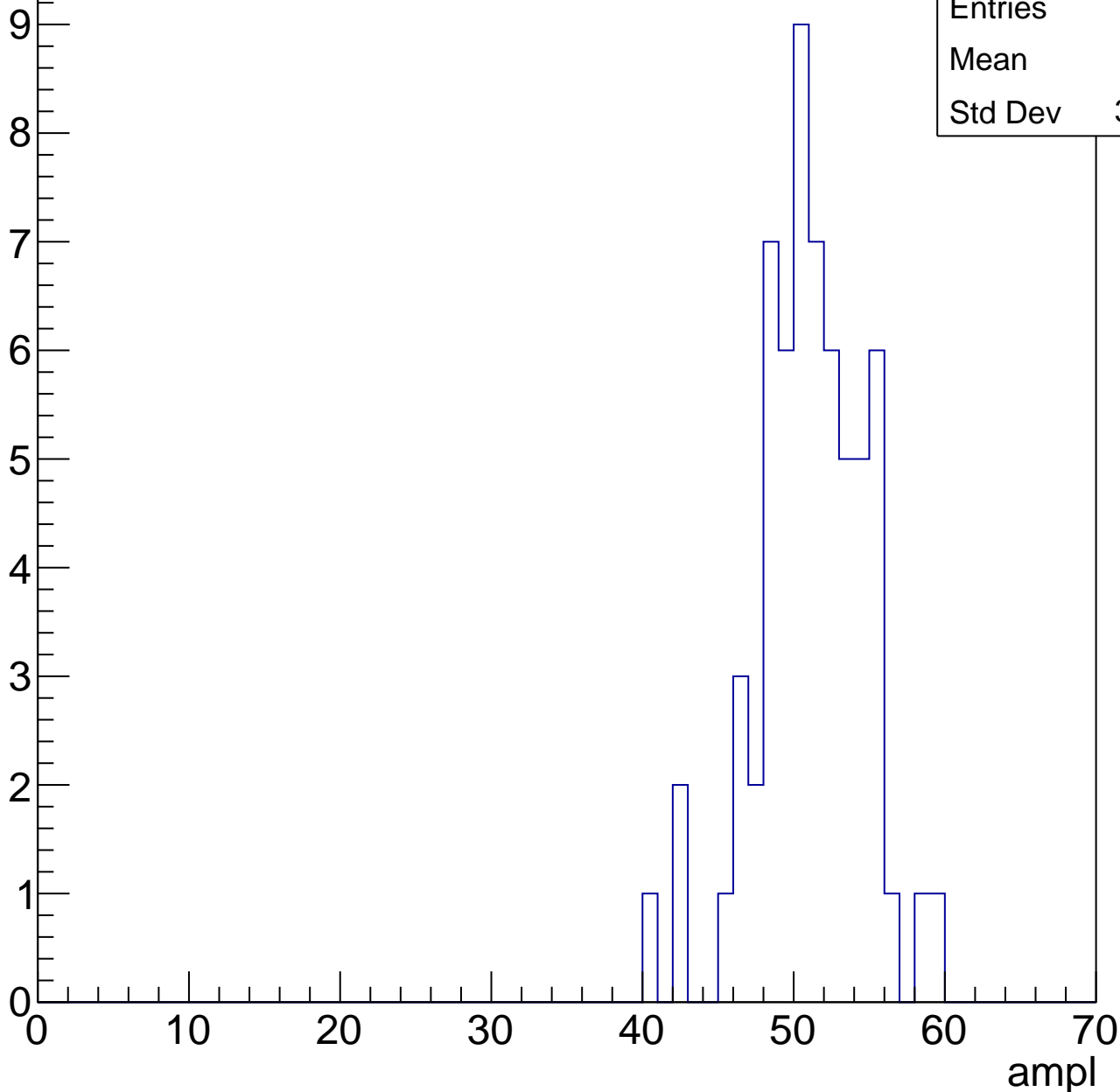


B1L101S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.6
Std Dev	3.601

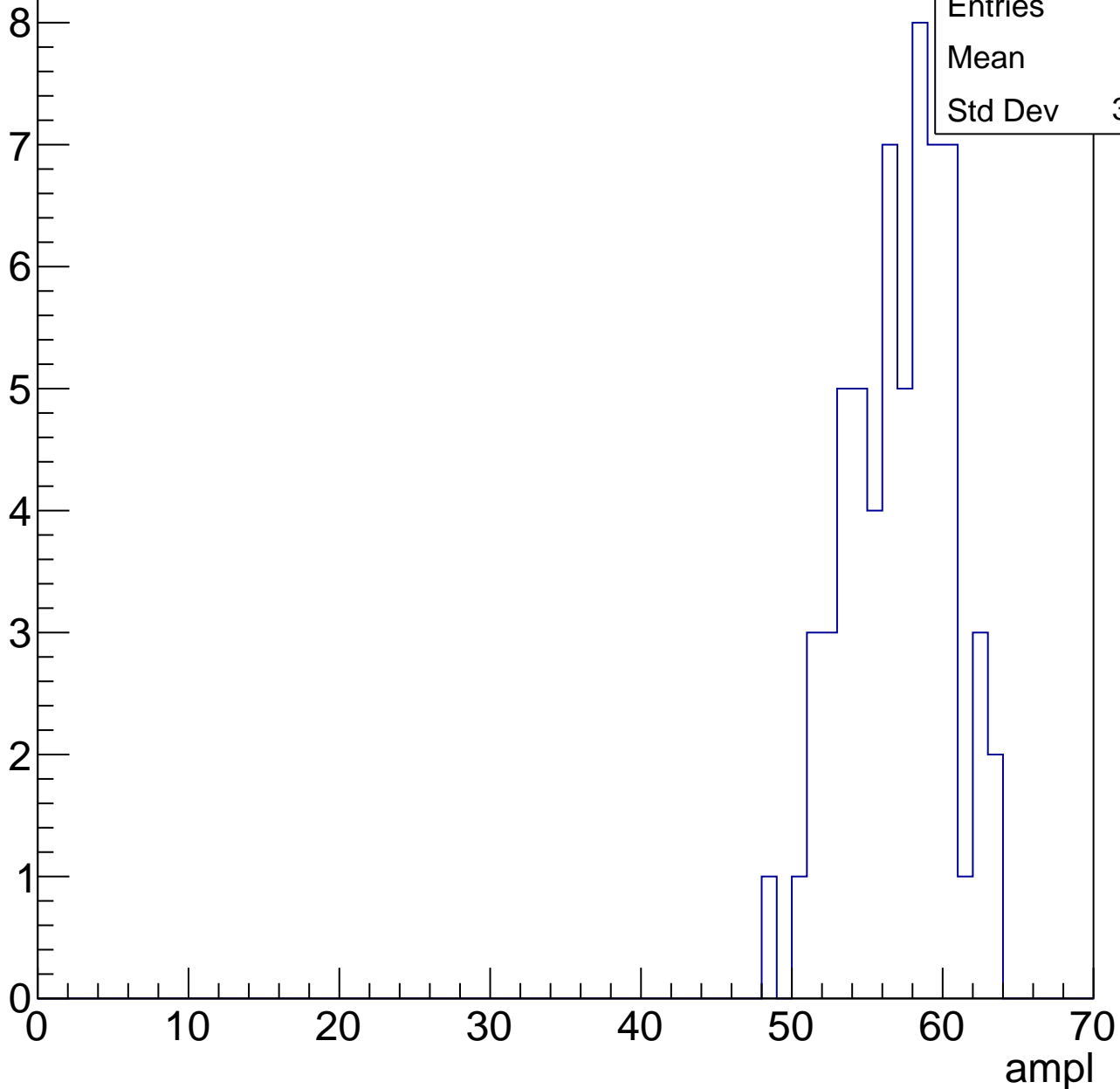


B1L101S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

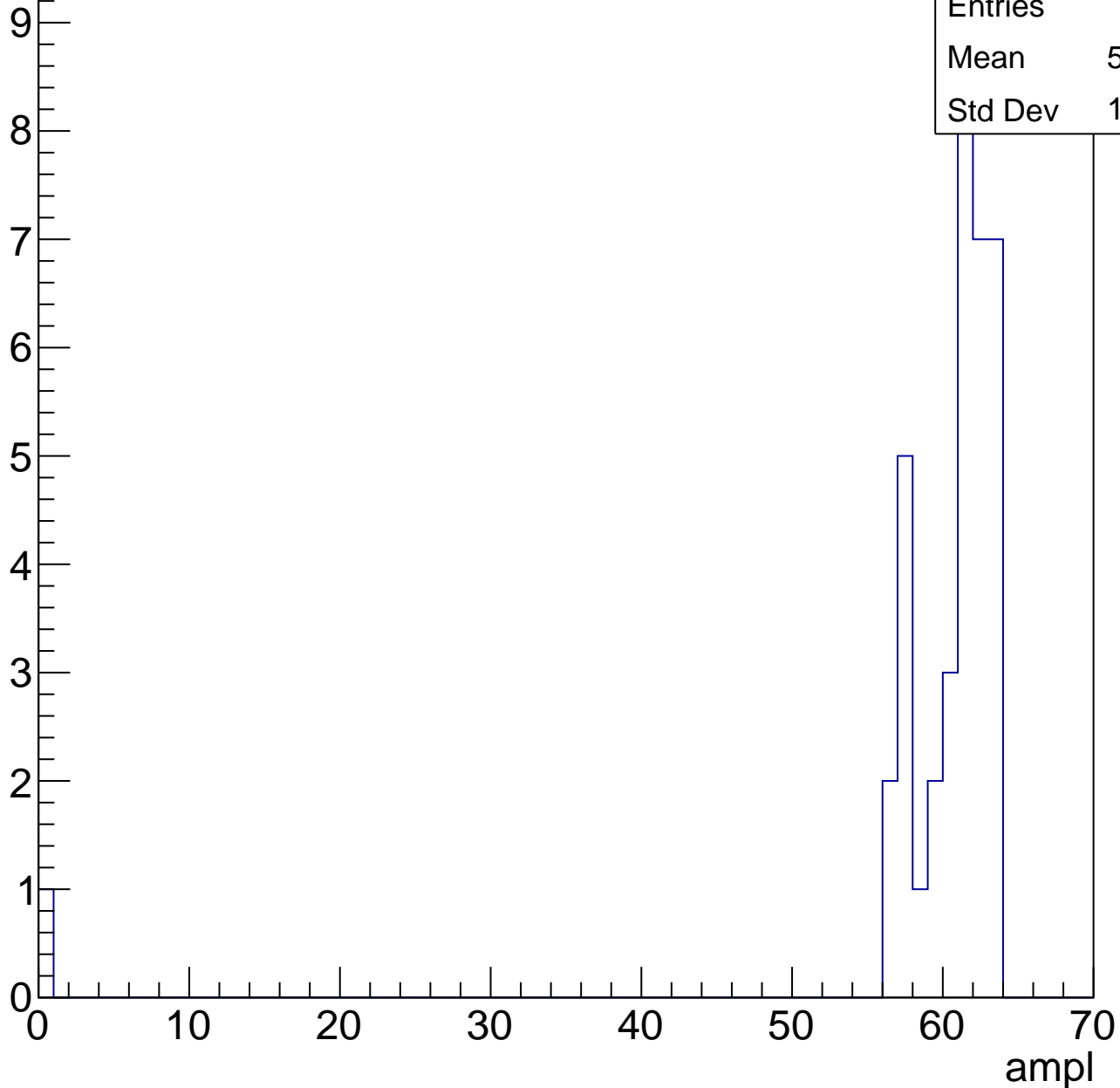
Entries	62
Mean	56.6
Std Dev	3.391



B1L101S, U14-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch26, adc0

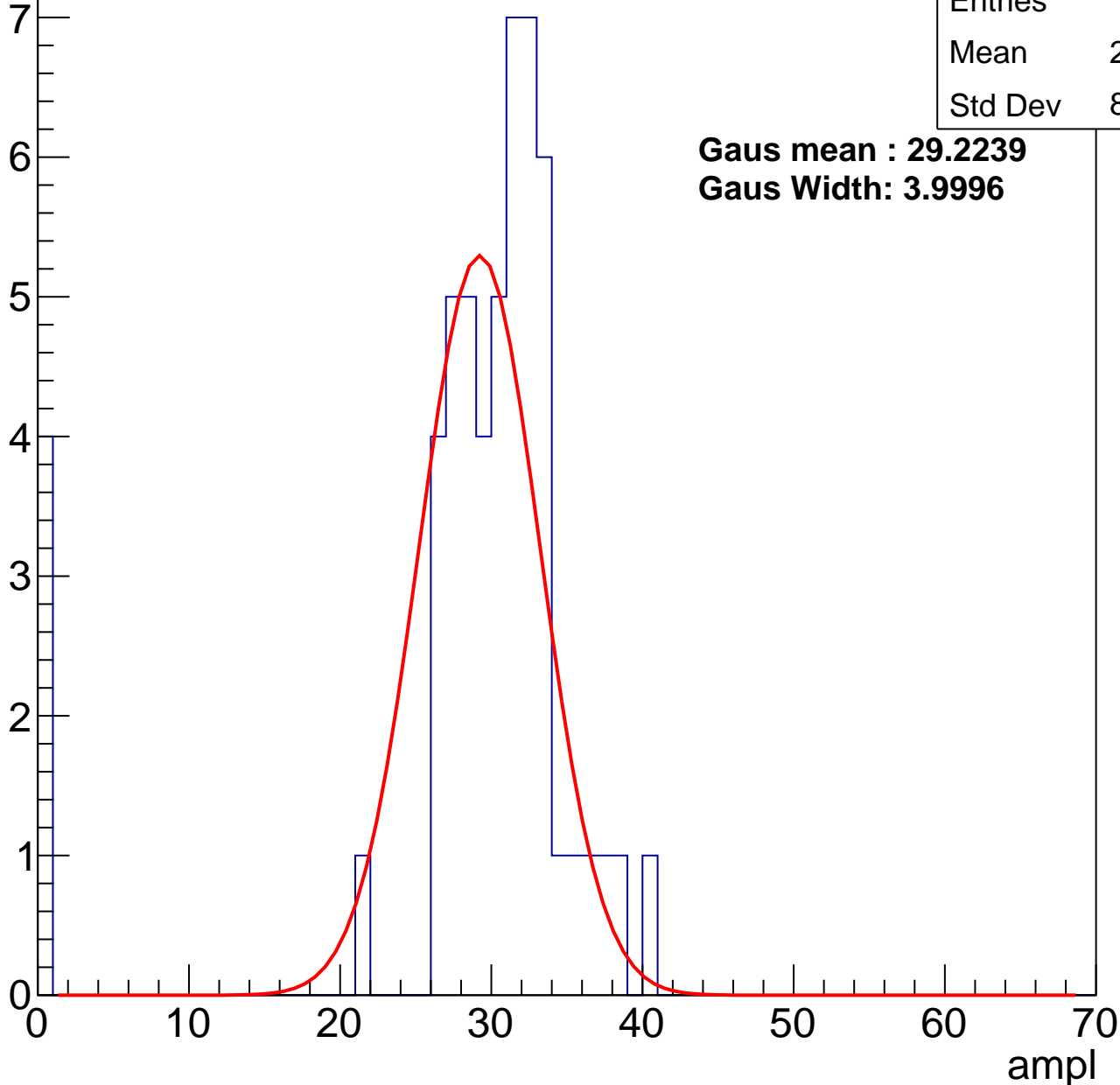
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	28.24
Std Dev	8.634

Gaus mean : 29.2239

Gaus Width: 3.9996



B1L101S, U14-ch26, adc1

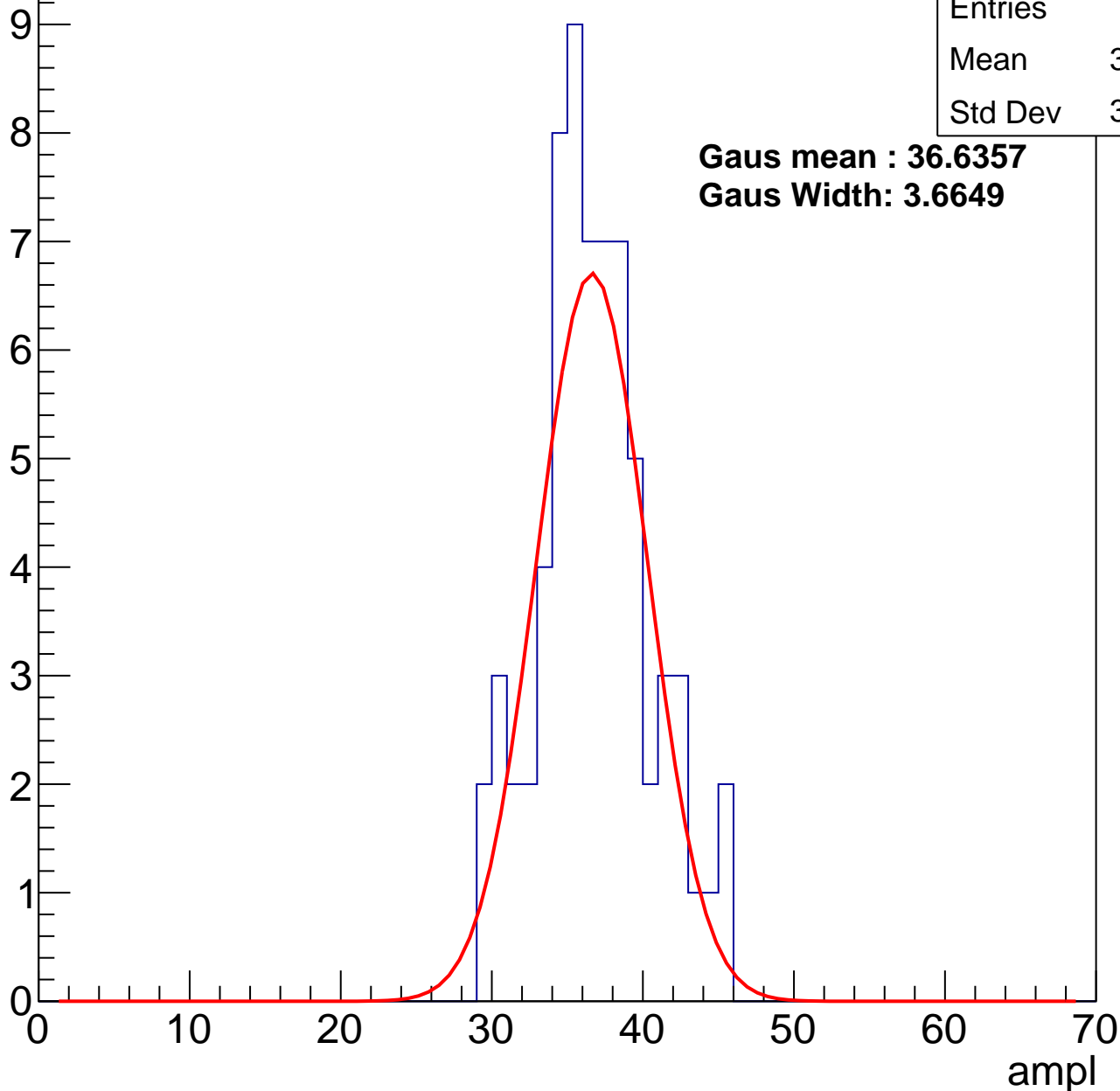
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.34
Std Dev	3.693

Gaus mean : 36.6357

Gaus Width: 3.6649



B1L101S, U14-ch26, adc2

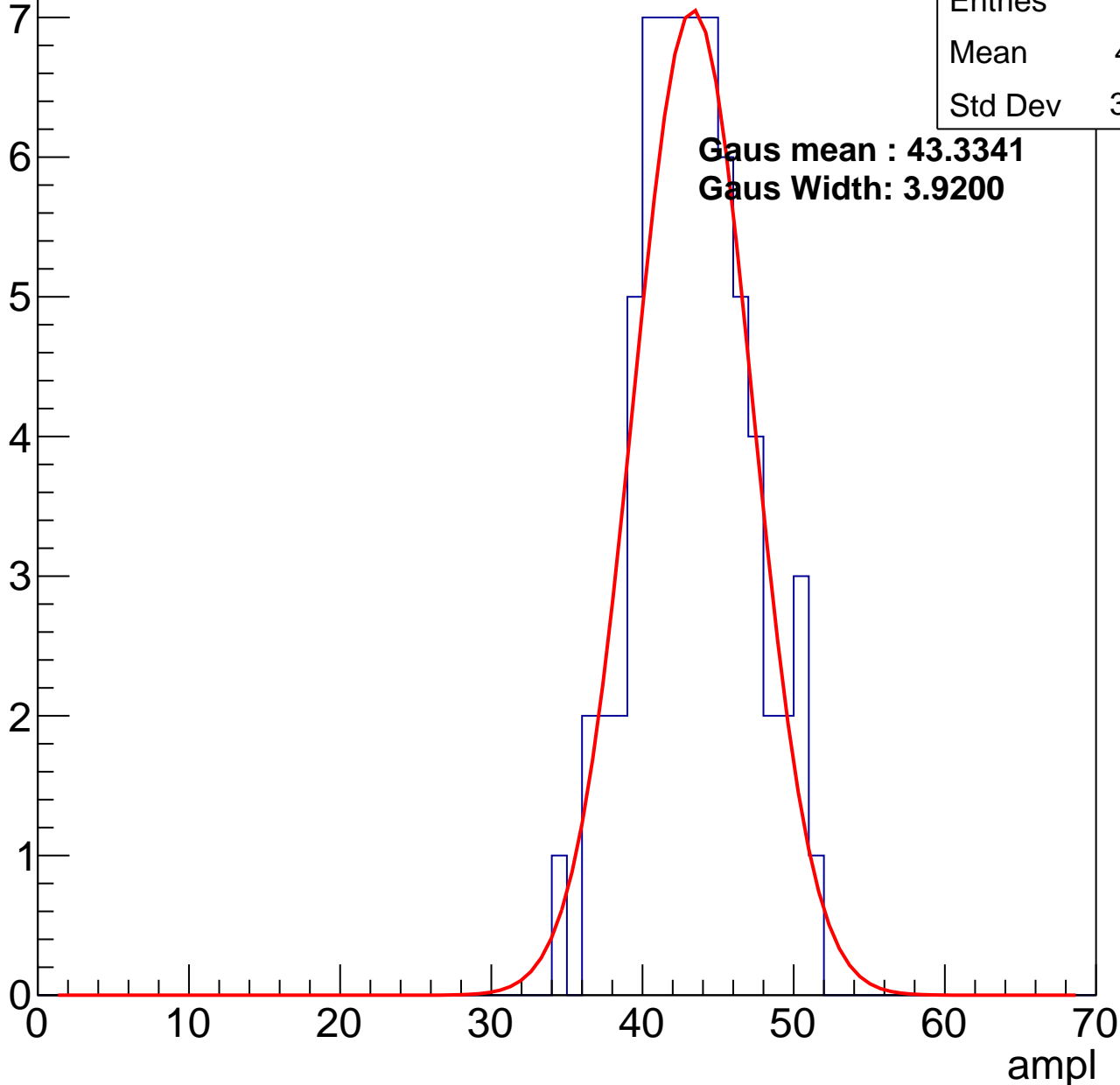
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.91
Std Dev	3.687

Gaus mean : 43.3341

Gaus Width: 3.9200

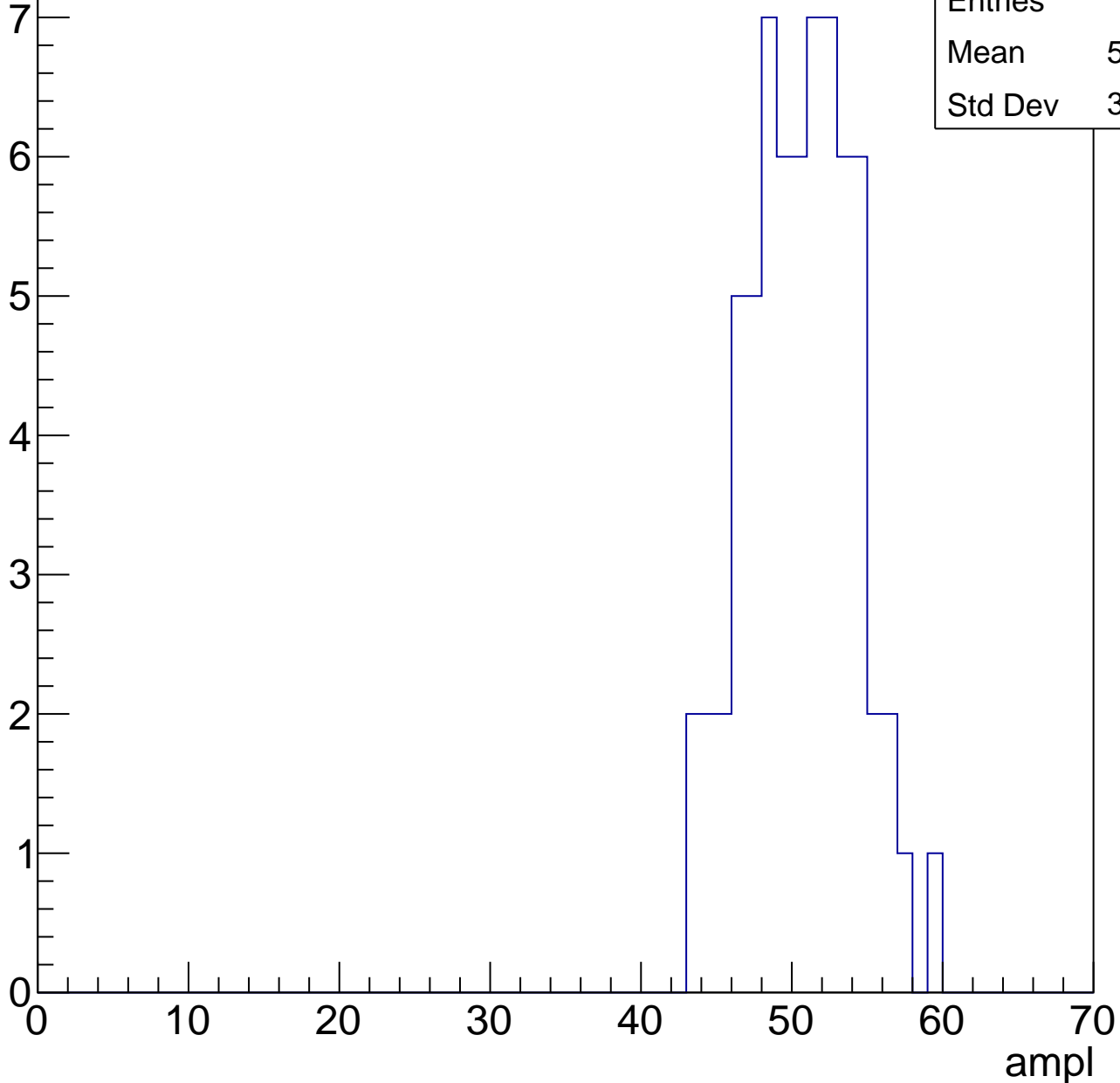


B1L101S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.15
Std Dev	3.487

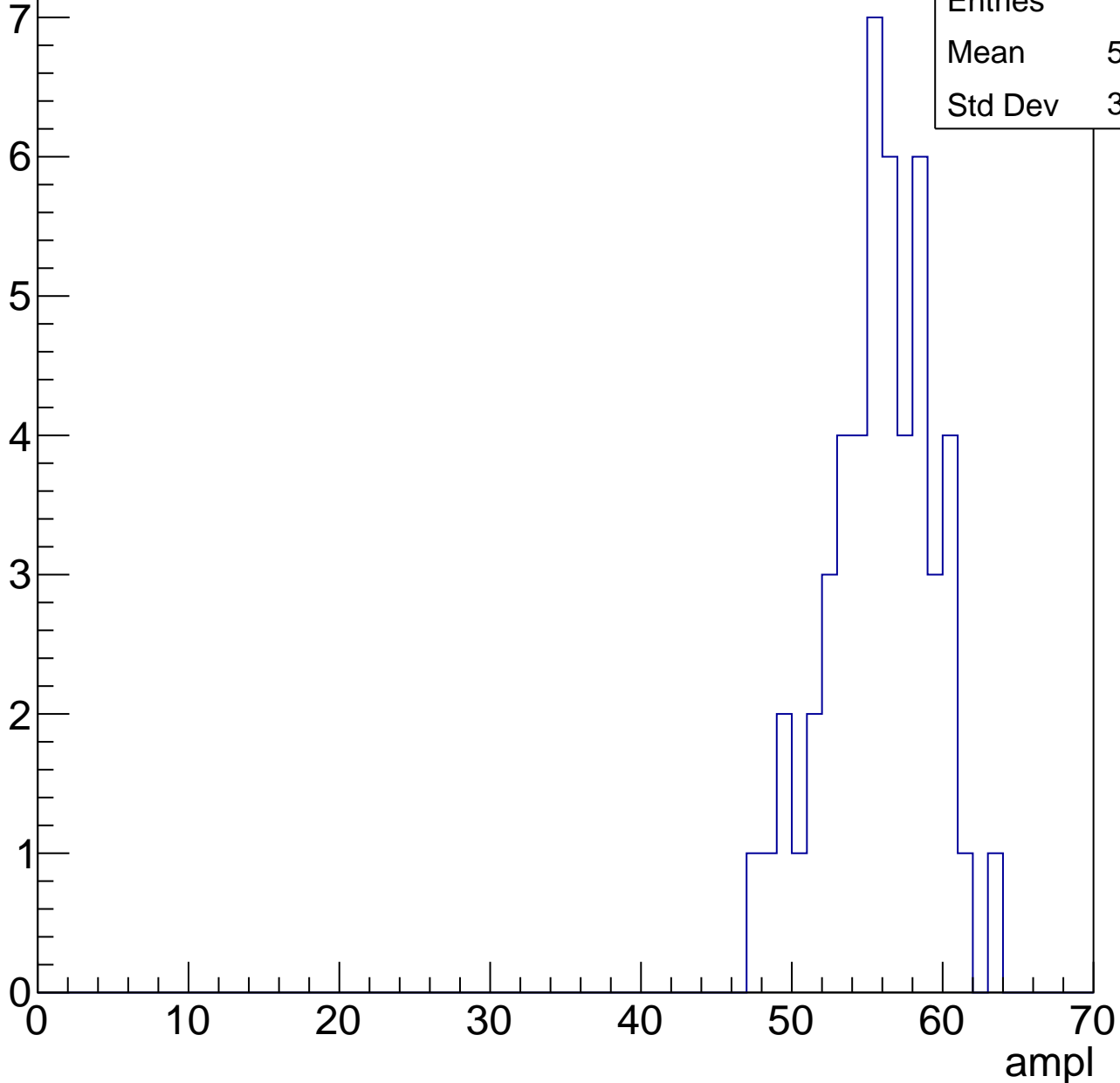


B1L101S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

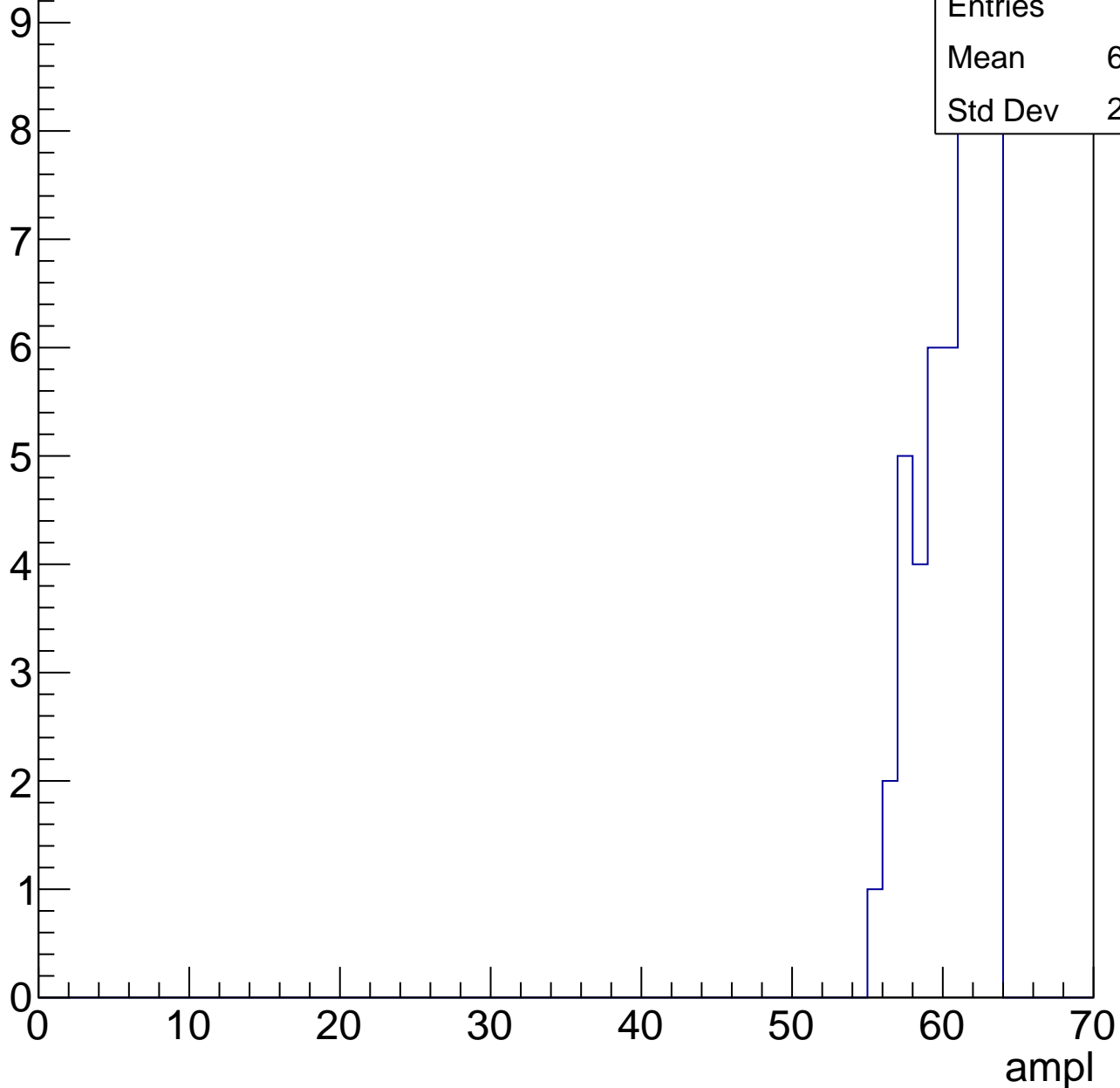
Entries	50
Mean	55.34
Std Dev	3.485



B1L101S, U14-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

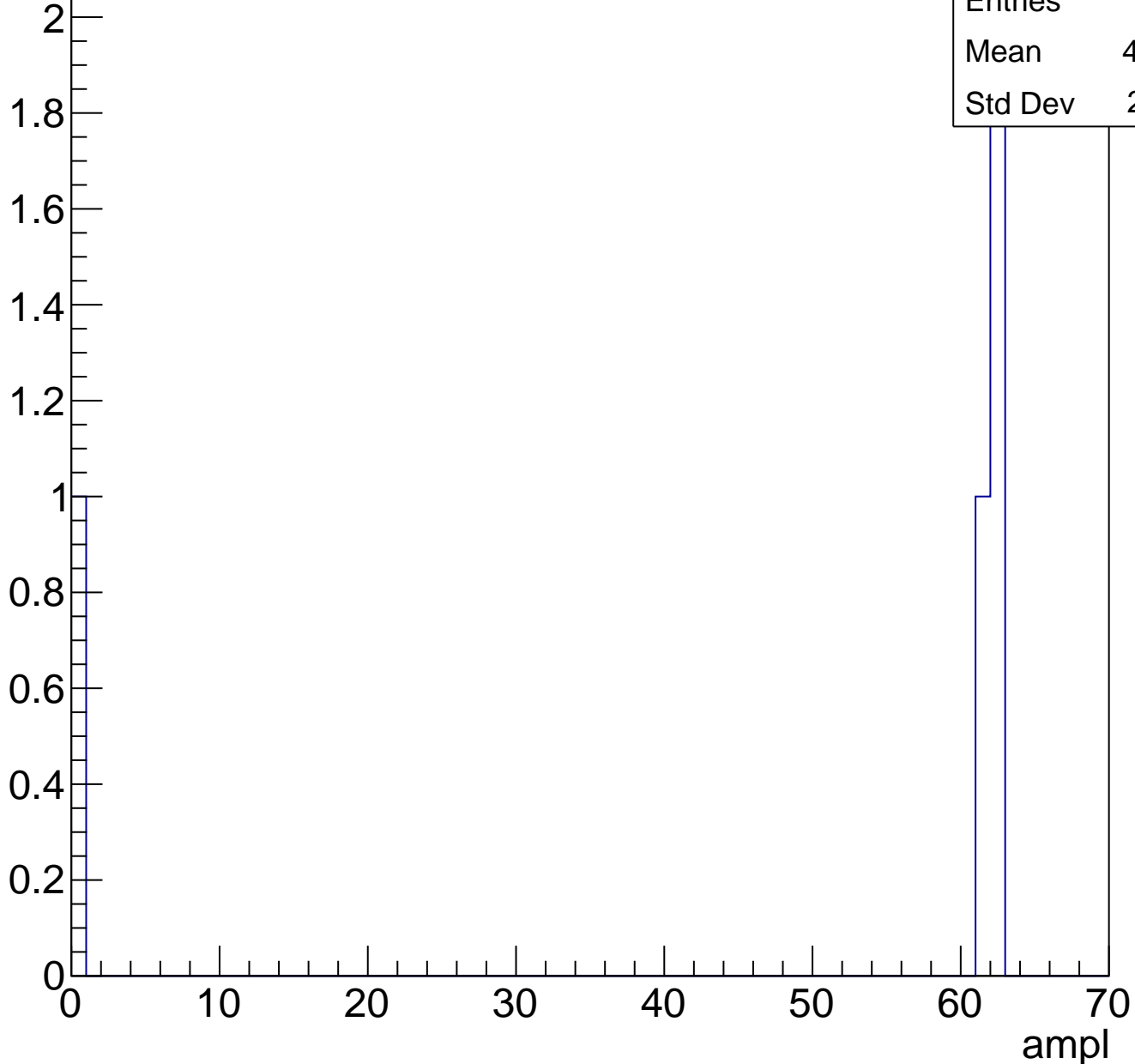
Entry



B1L101S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch27, adc0

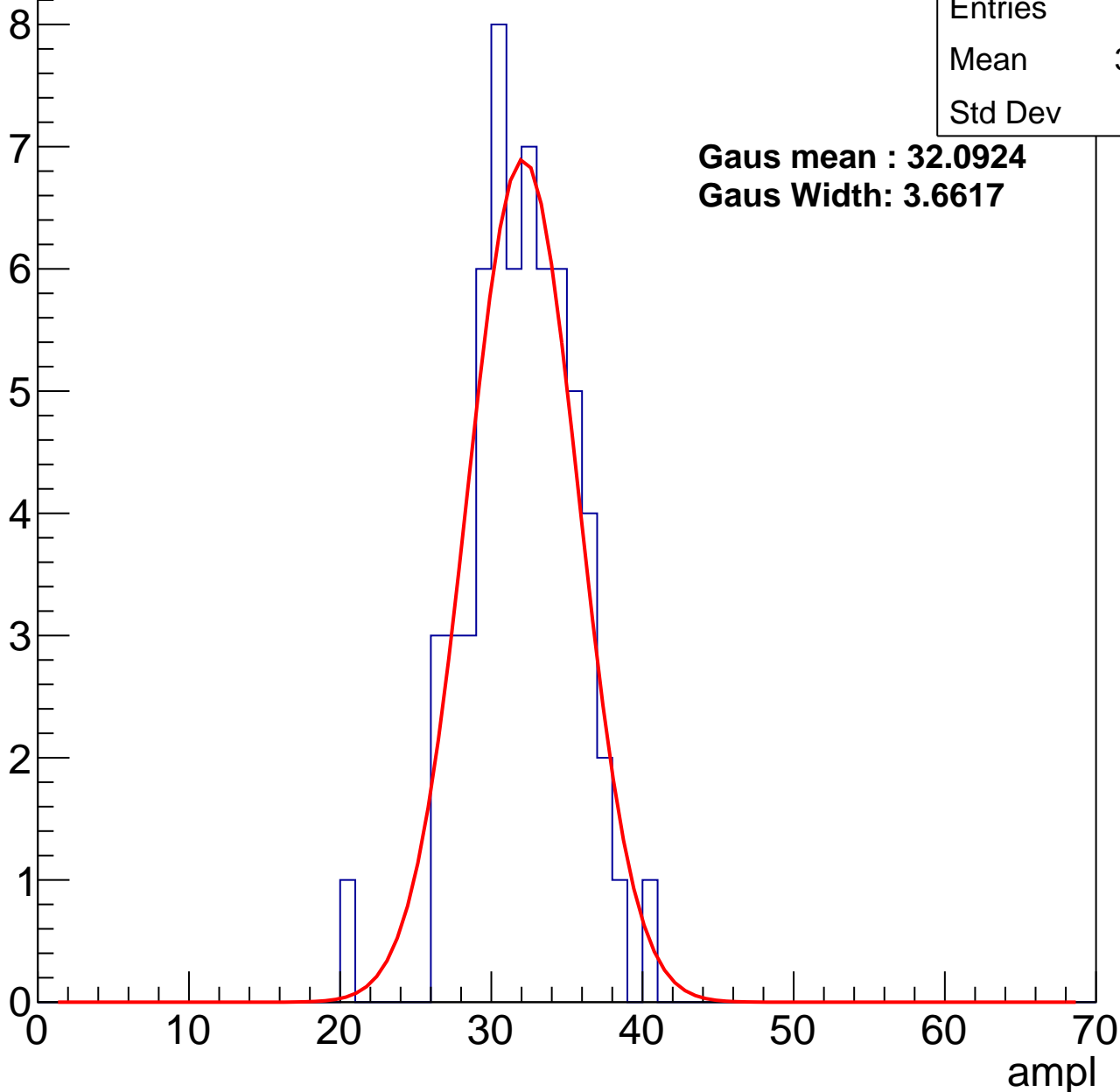
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.61
Std Dev	3.47

Gaus mean : 32.0924

Gaus Width: 3.6617



B1L101S, U14-ch27, adc1

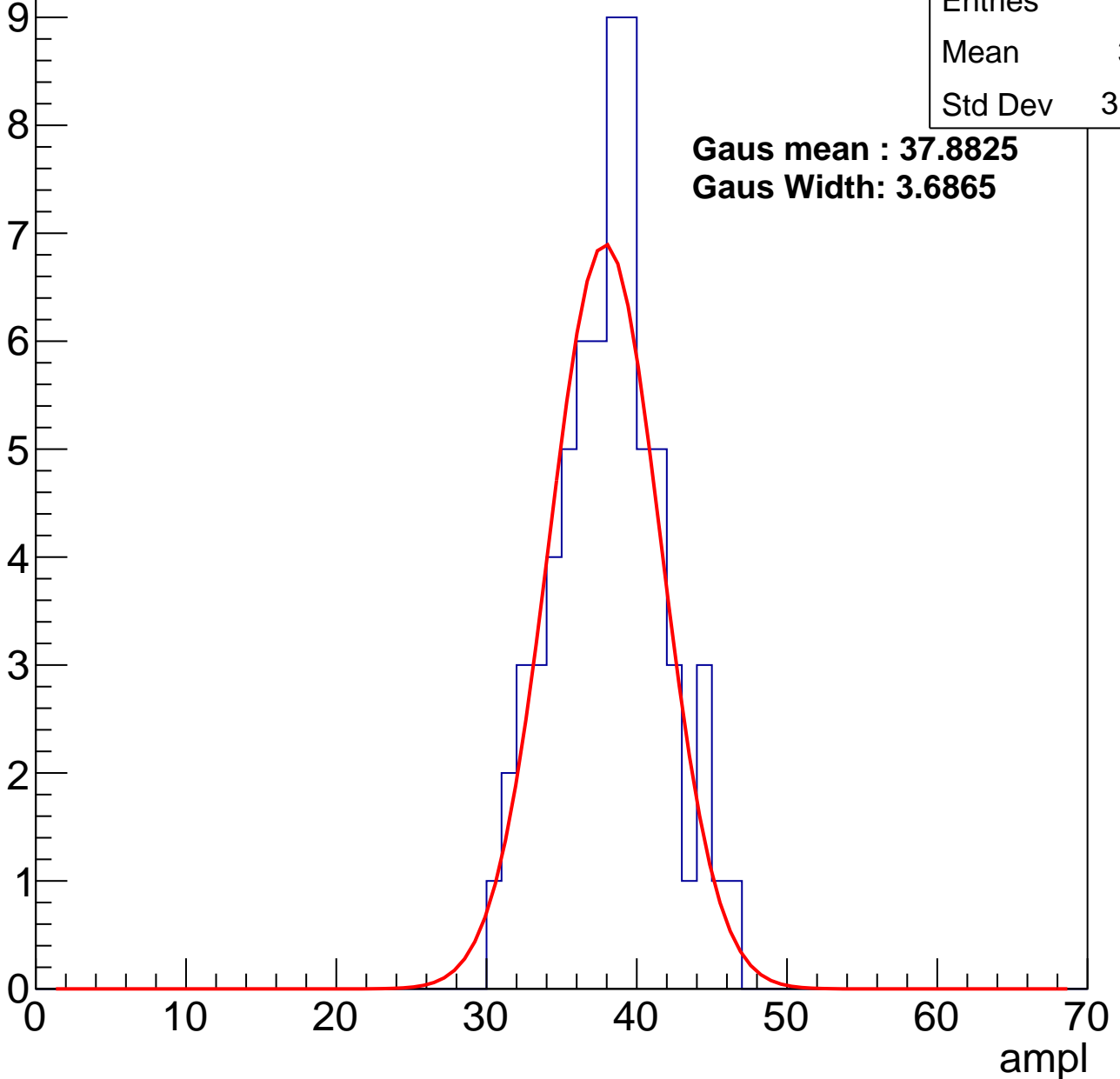
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.7
Std Dev	3.553

Gaus mean : 37.8825

Gaus Width: 3.6865



B1L101S, U14-ch27, adc2

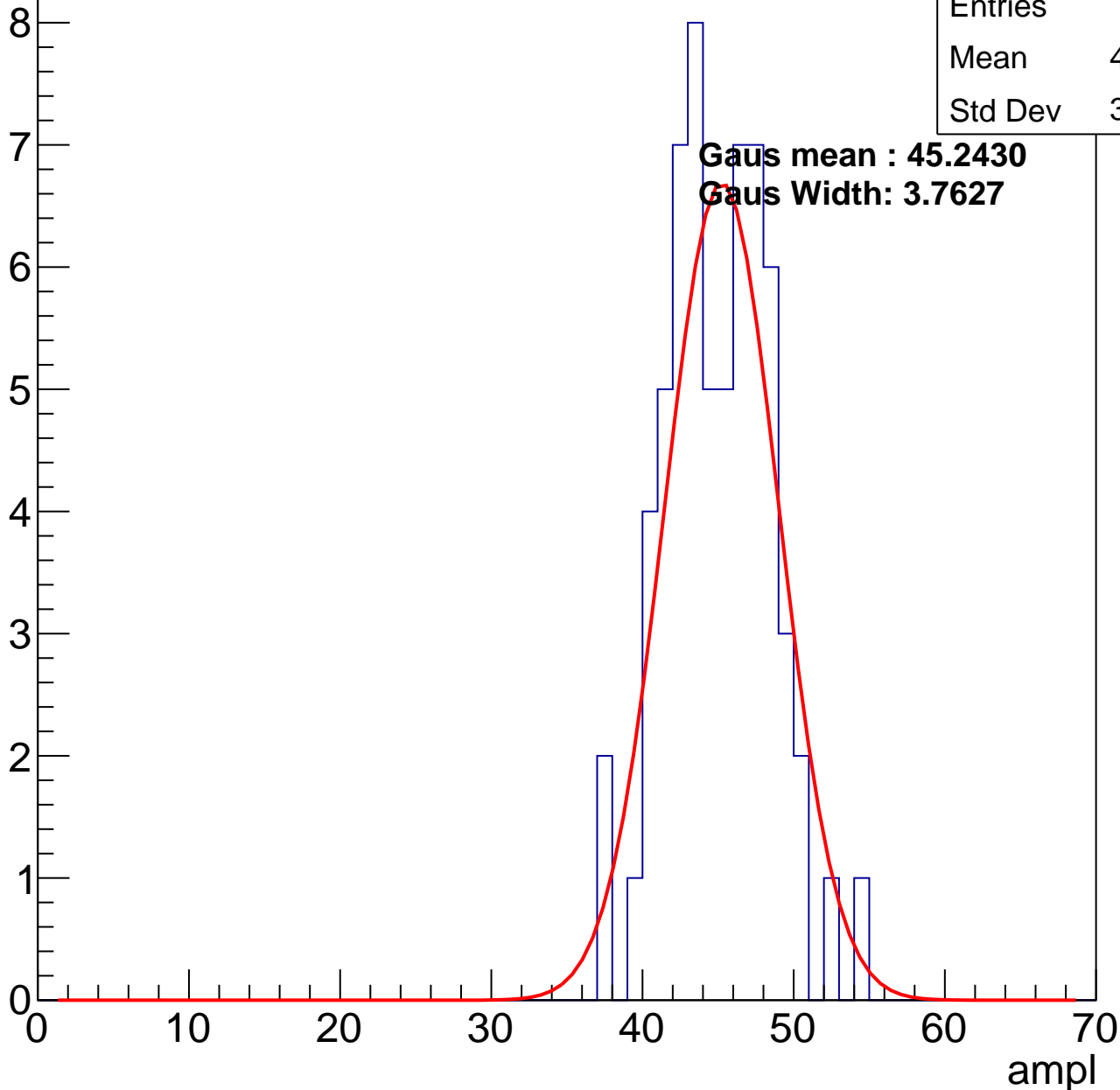
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.58
Std Dev	3.422

Gaus mean : 45.2430

Gaus Width: 3.7627

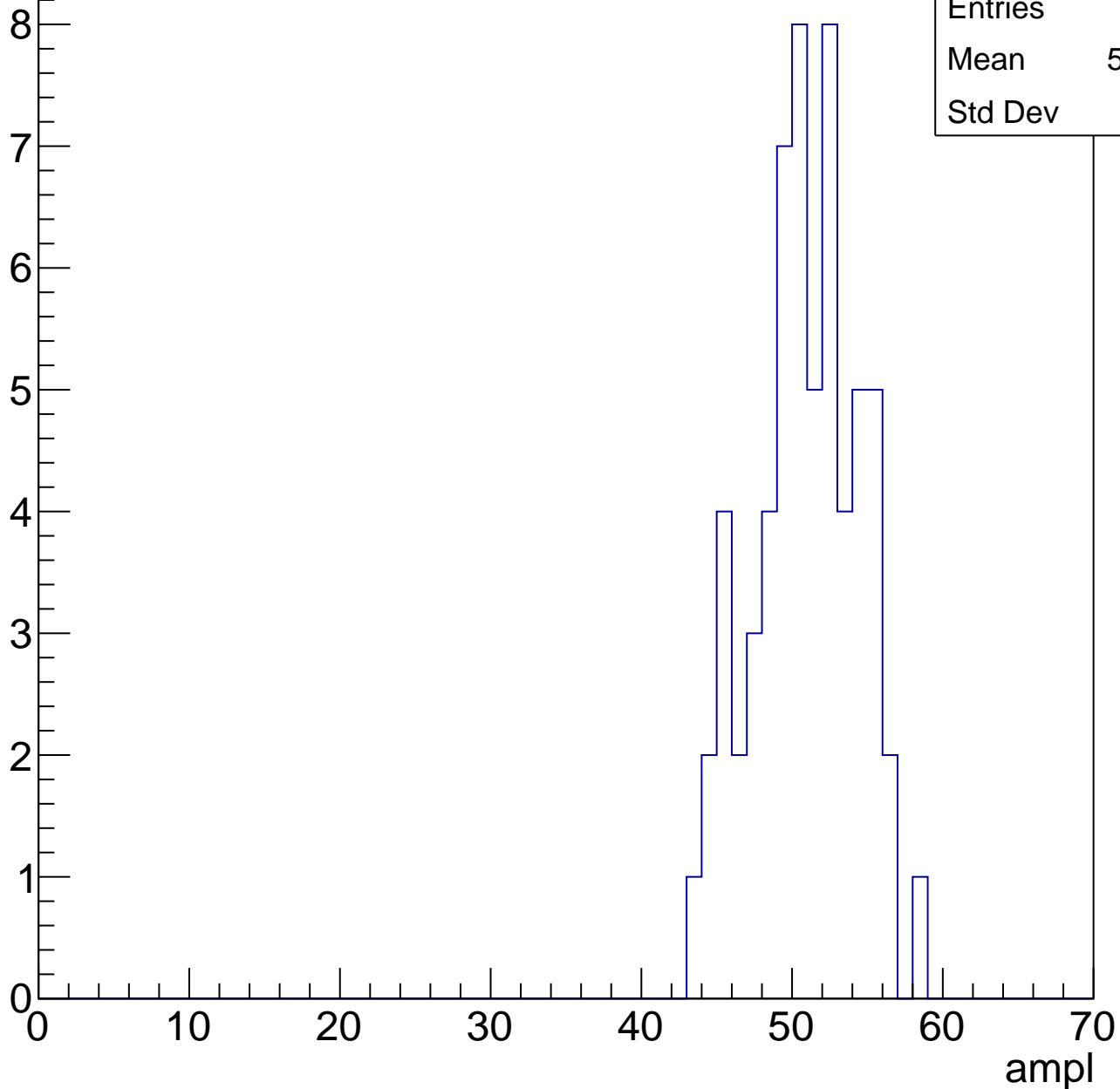


B1L101S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

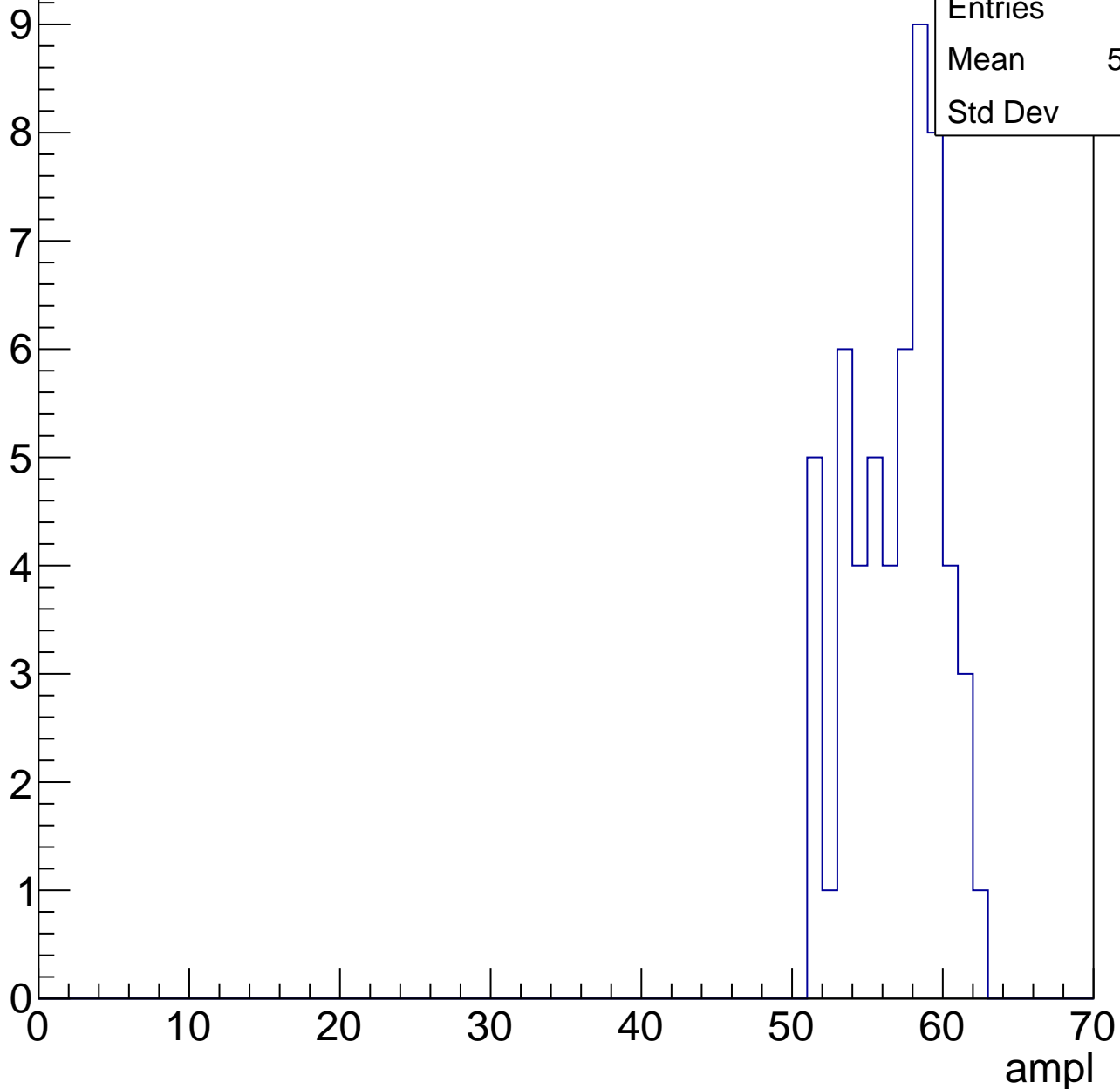
Entries	61
Mean	50.44
Std Dev	3.4



B1L101S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

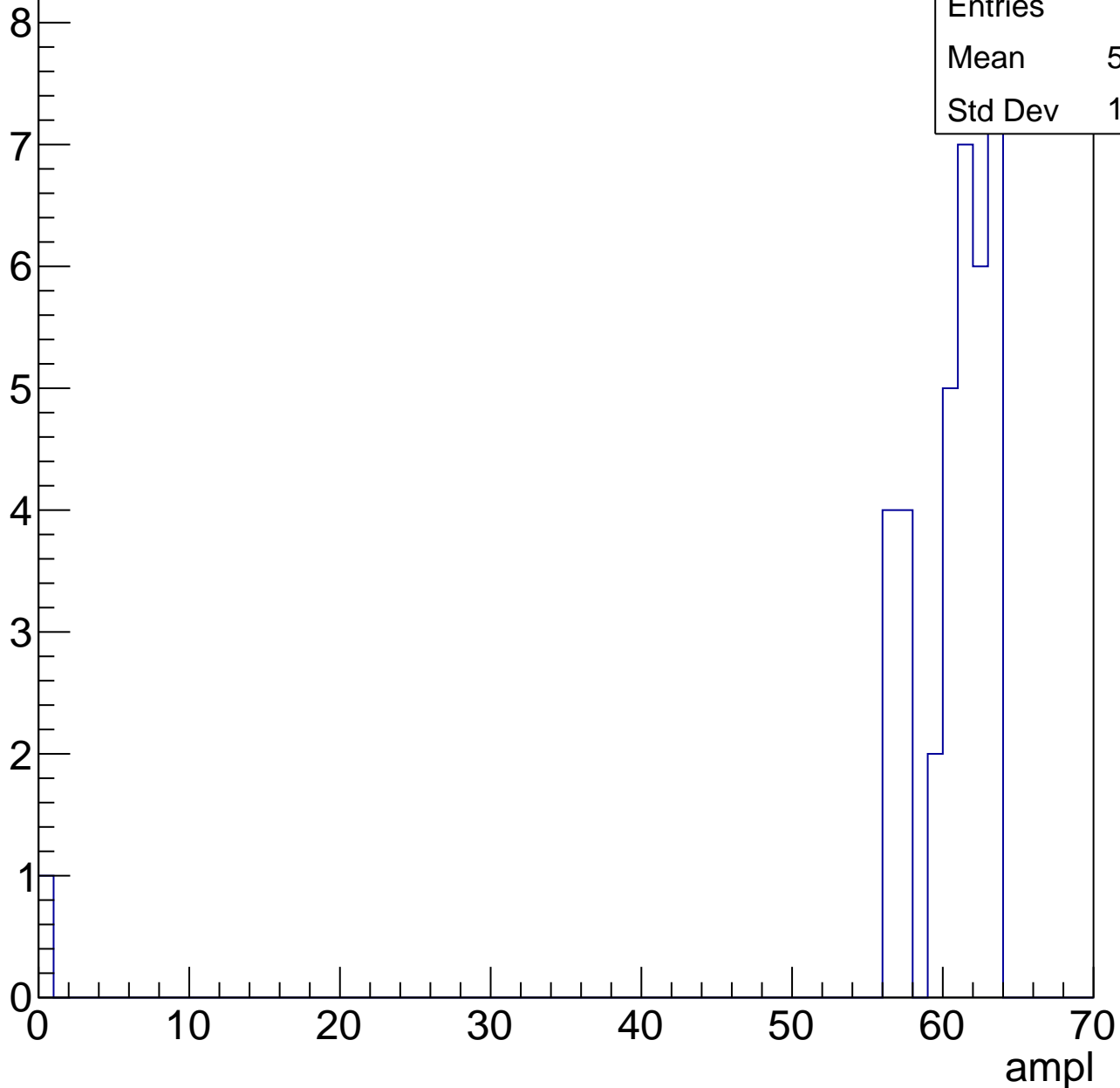
Entry



B1L101S, U14-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch27, adc6

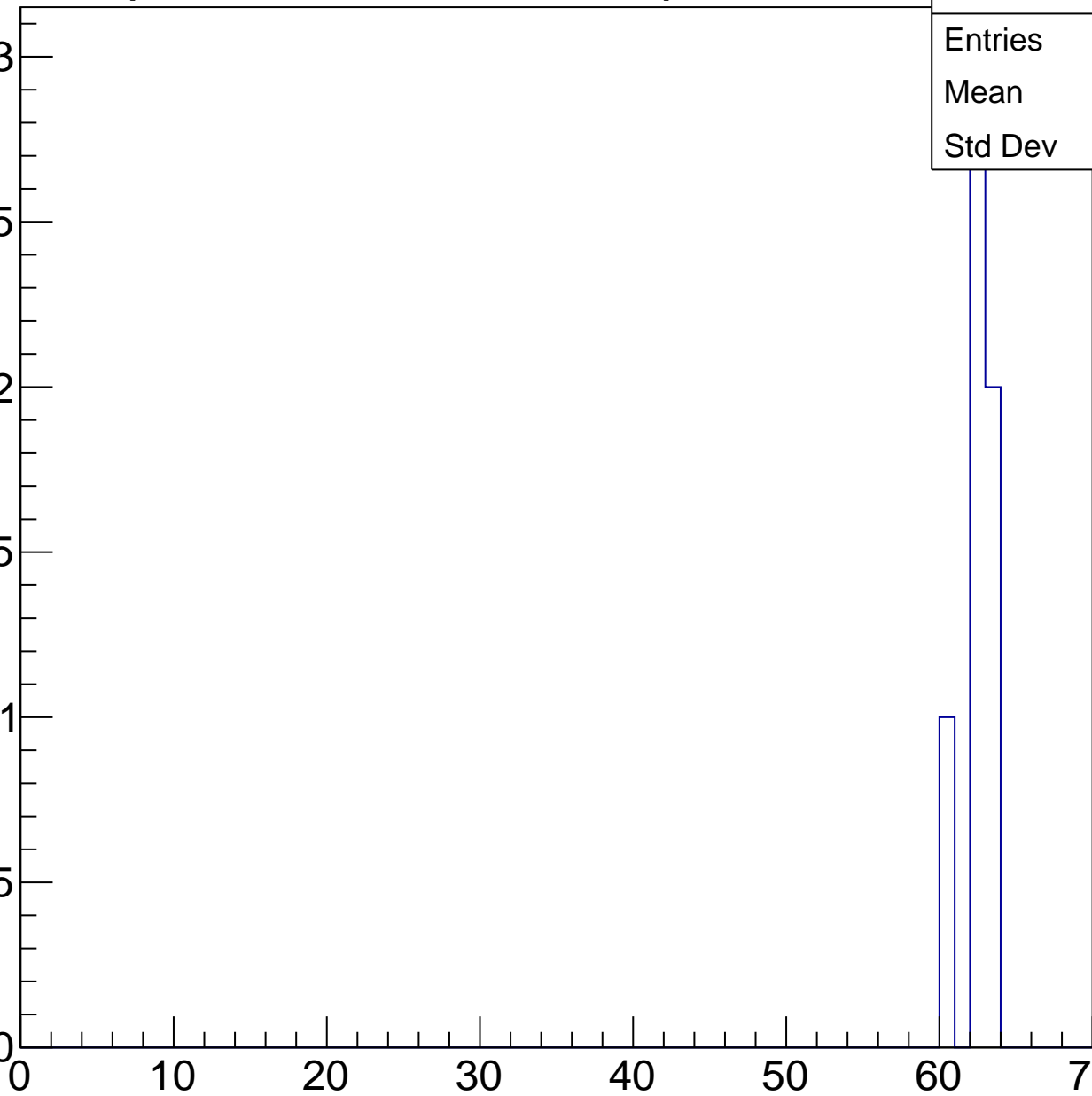
calib_packv5_042523_0143.root, FC#0, port D2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	6
Mean	62
Std Dev	1

ampl



B1L101S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch28, adc0

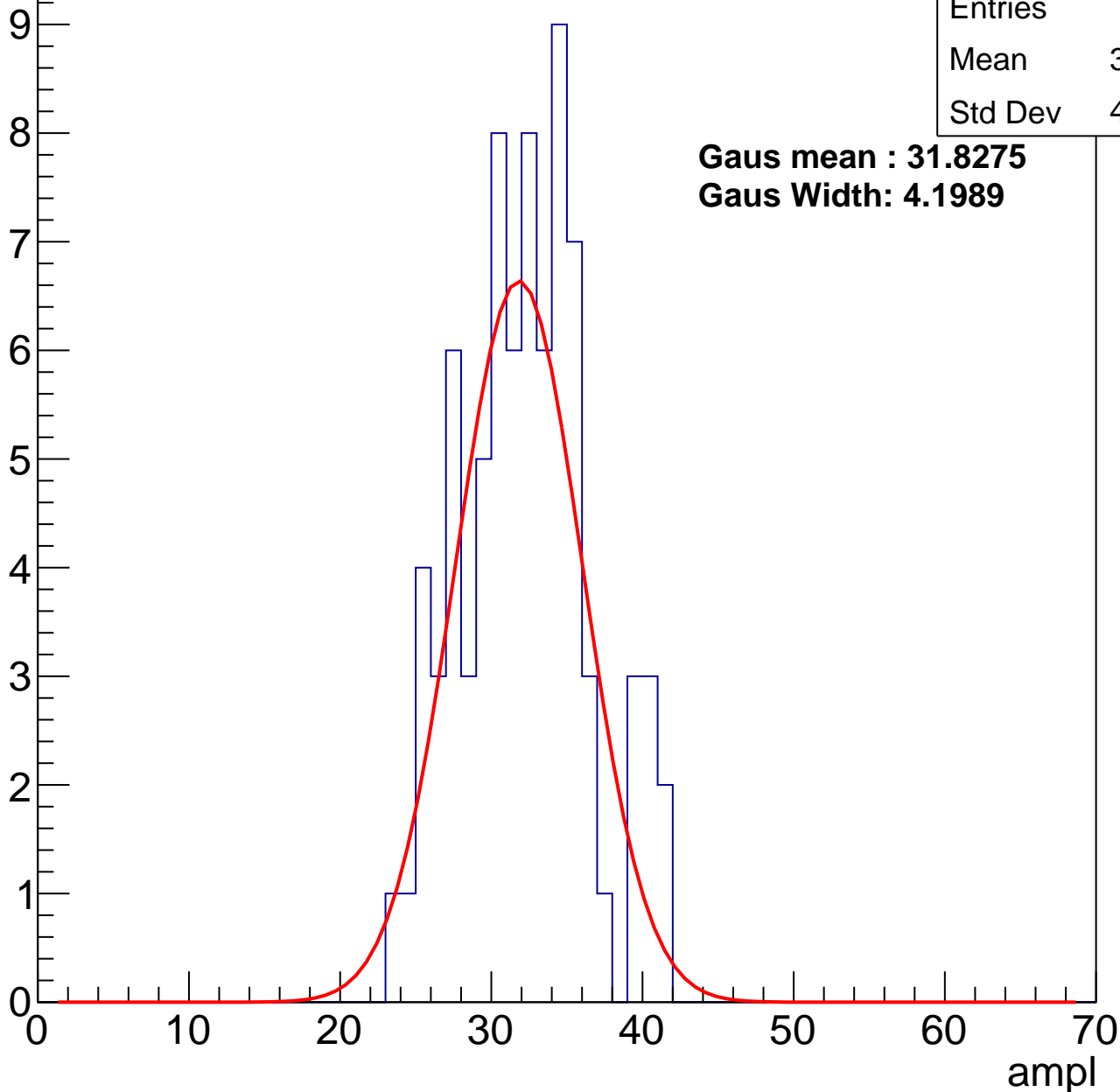
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	31.78
Std Dev	4.218

Gaus mean : 31.8275

Gaus Width: 4.1989



B1L101S, U14-ch28, adc1

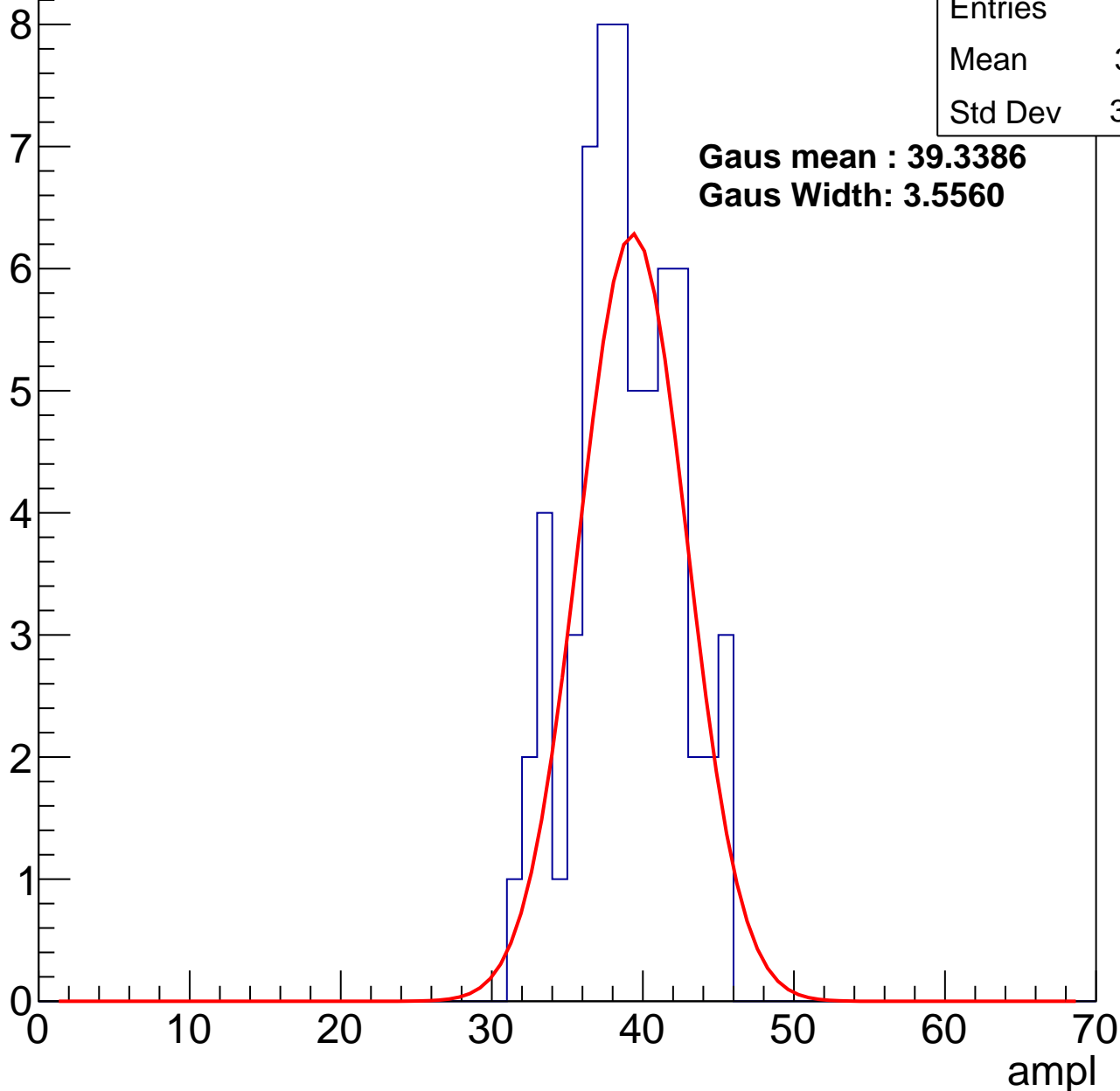
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.41
Std Dev	3.412

Gaus mean : 39.3386

Gaus Width: 3.5560



B1L101S, U14-ch28, adc2

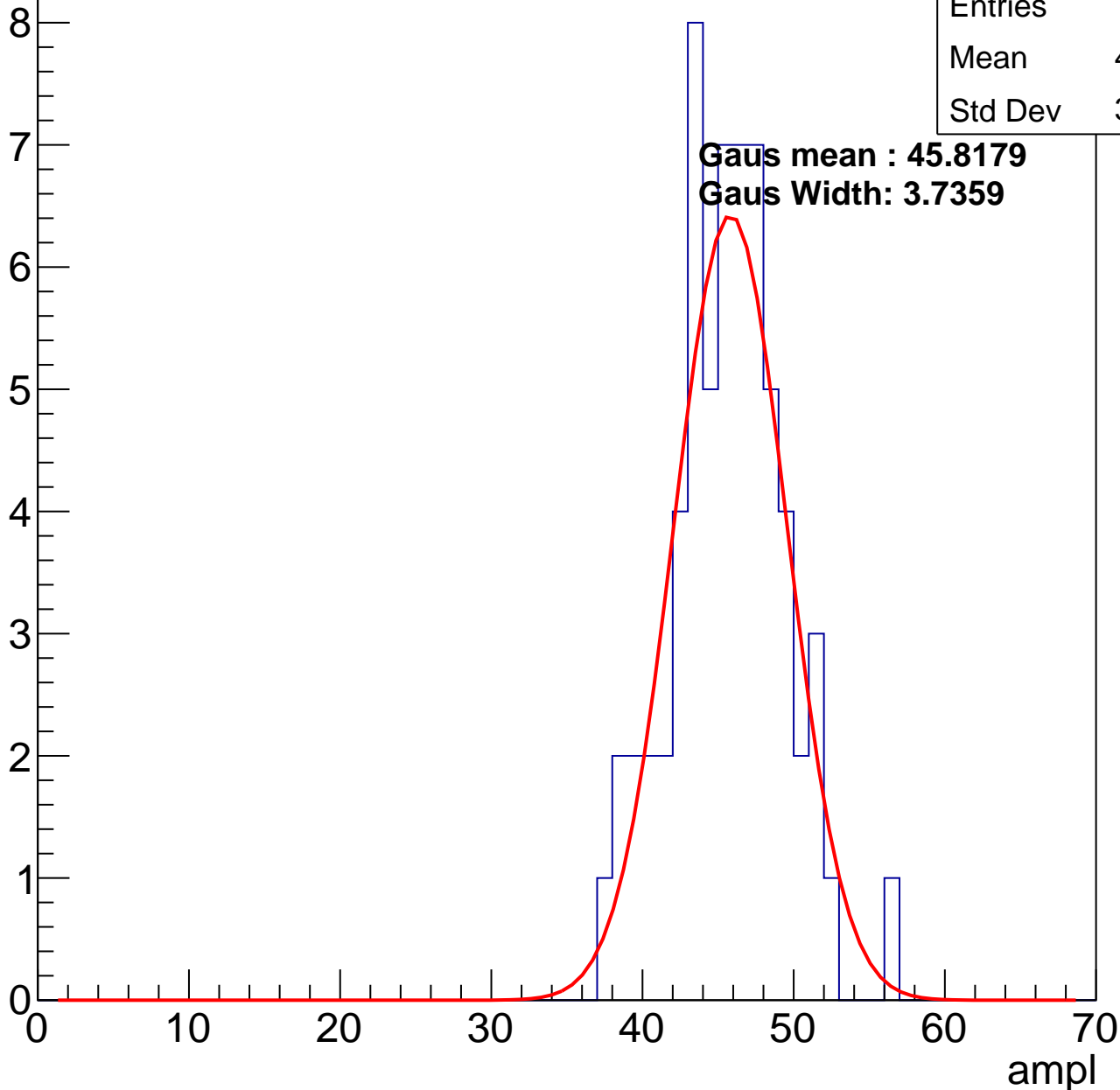
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	45.21
Std Dev	3.691

Gaus mean : 45.8179

Gaus Width: 3.7359

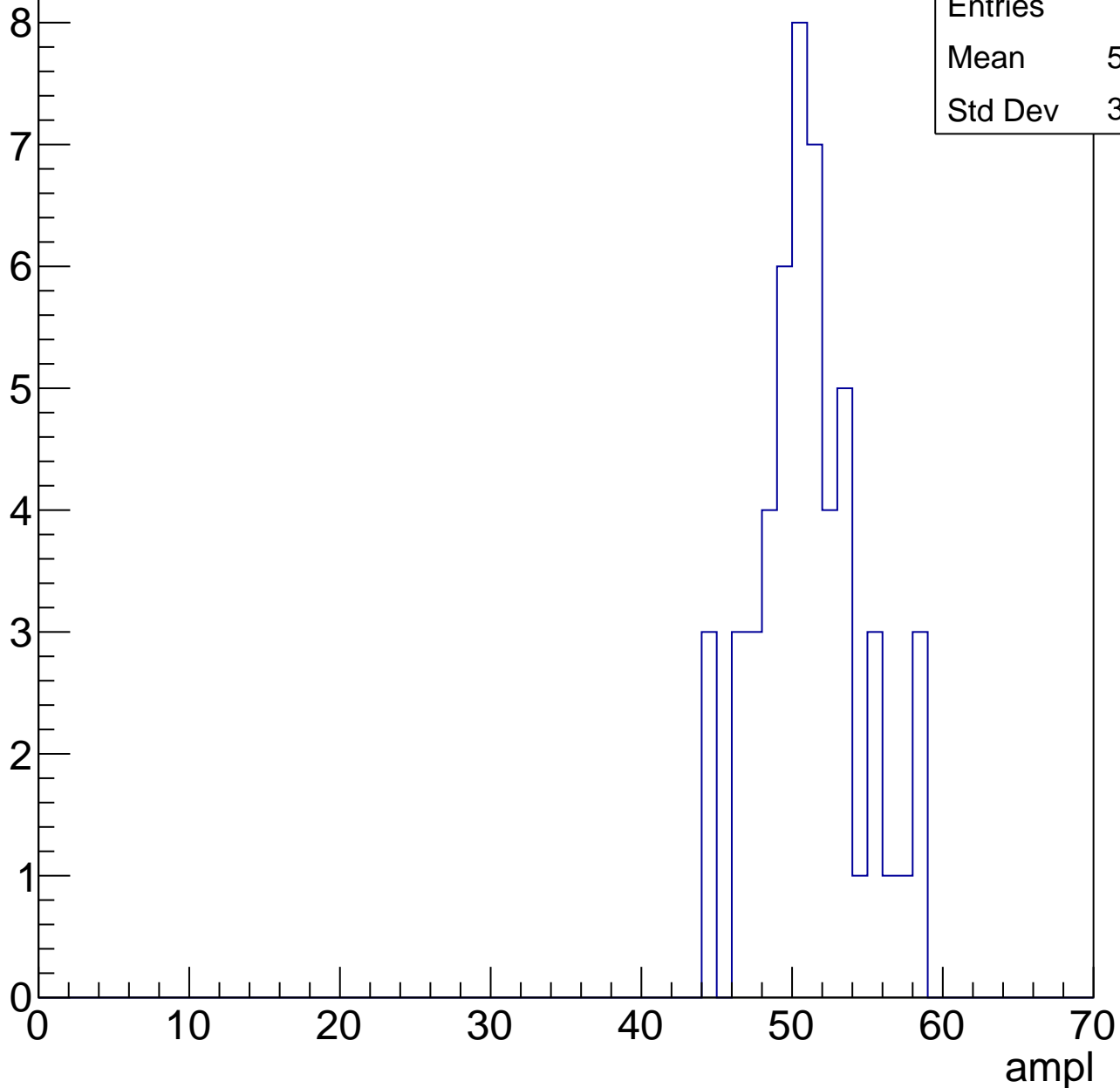


B1L101S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	50.63
Std Dev	3.453

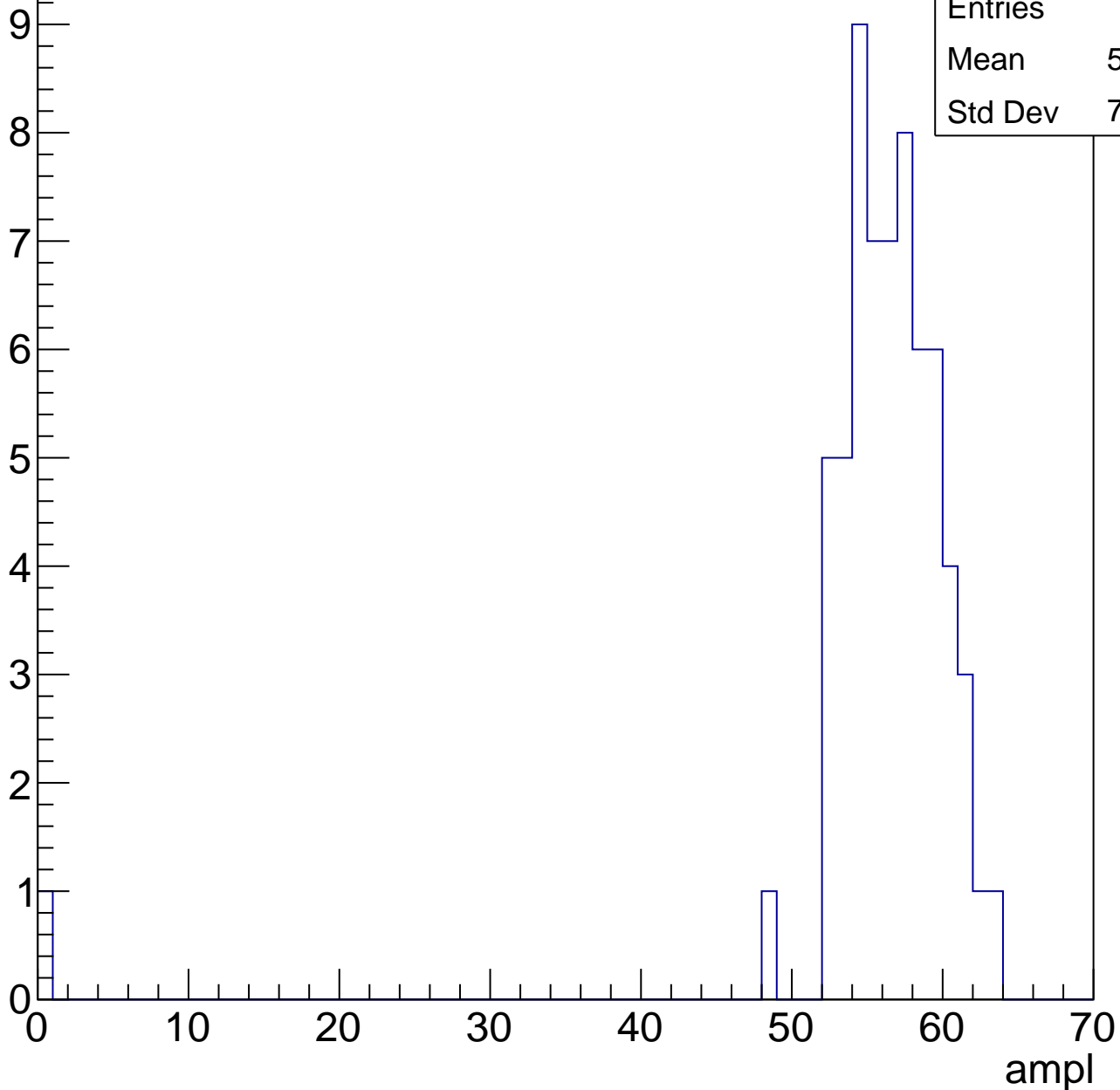


B1L101S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

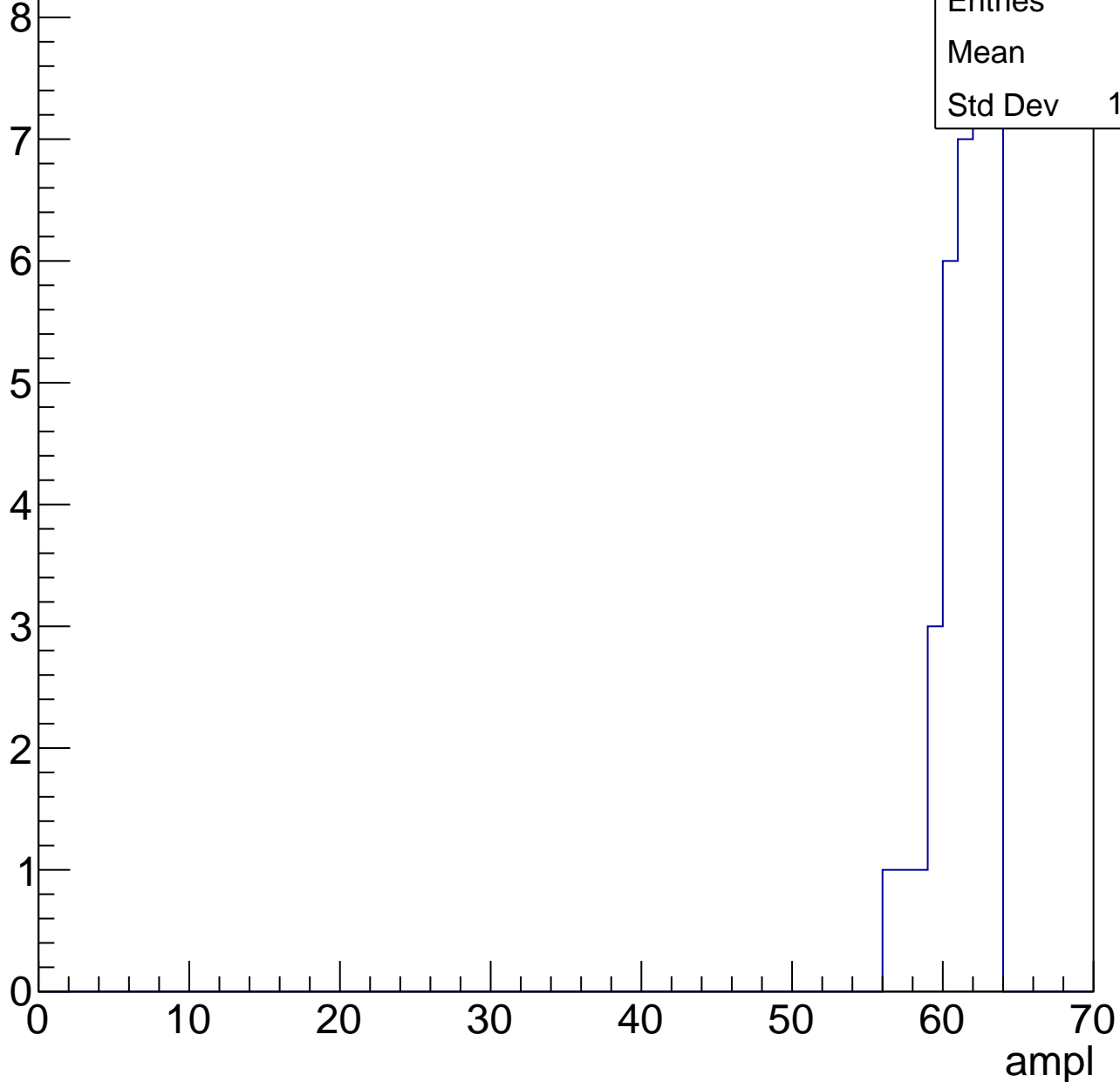
Entries	64
Mean	55.34
Std Dev	7.552



B1L101S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

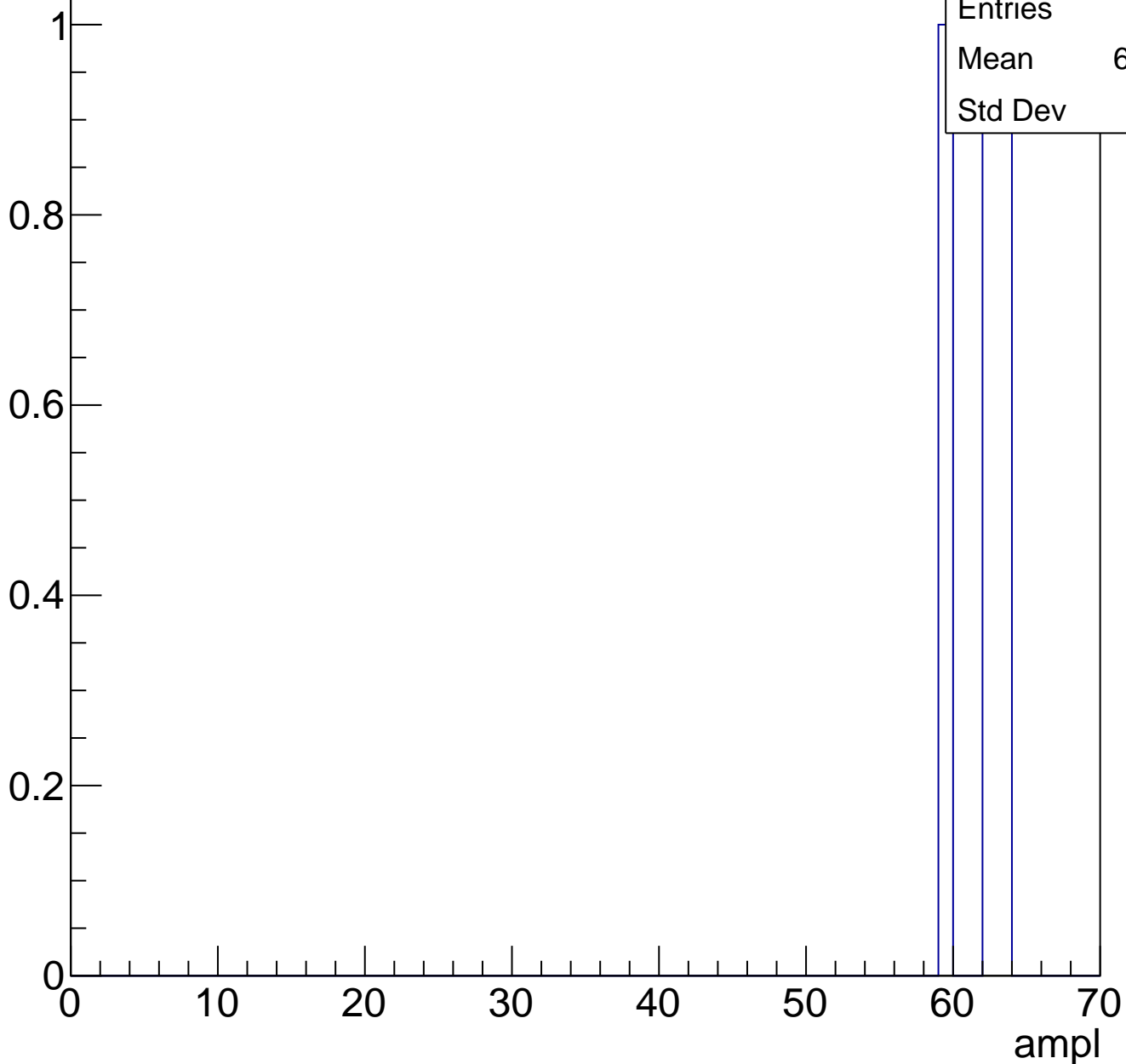
Entry



B1L101S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch29, adc0

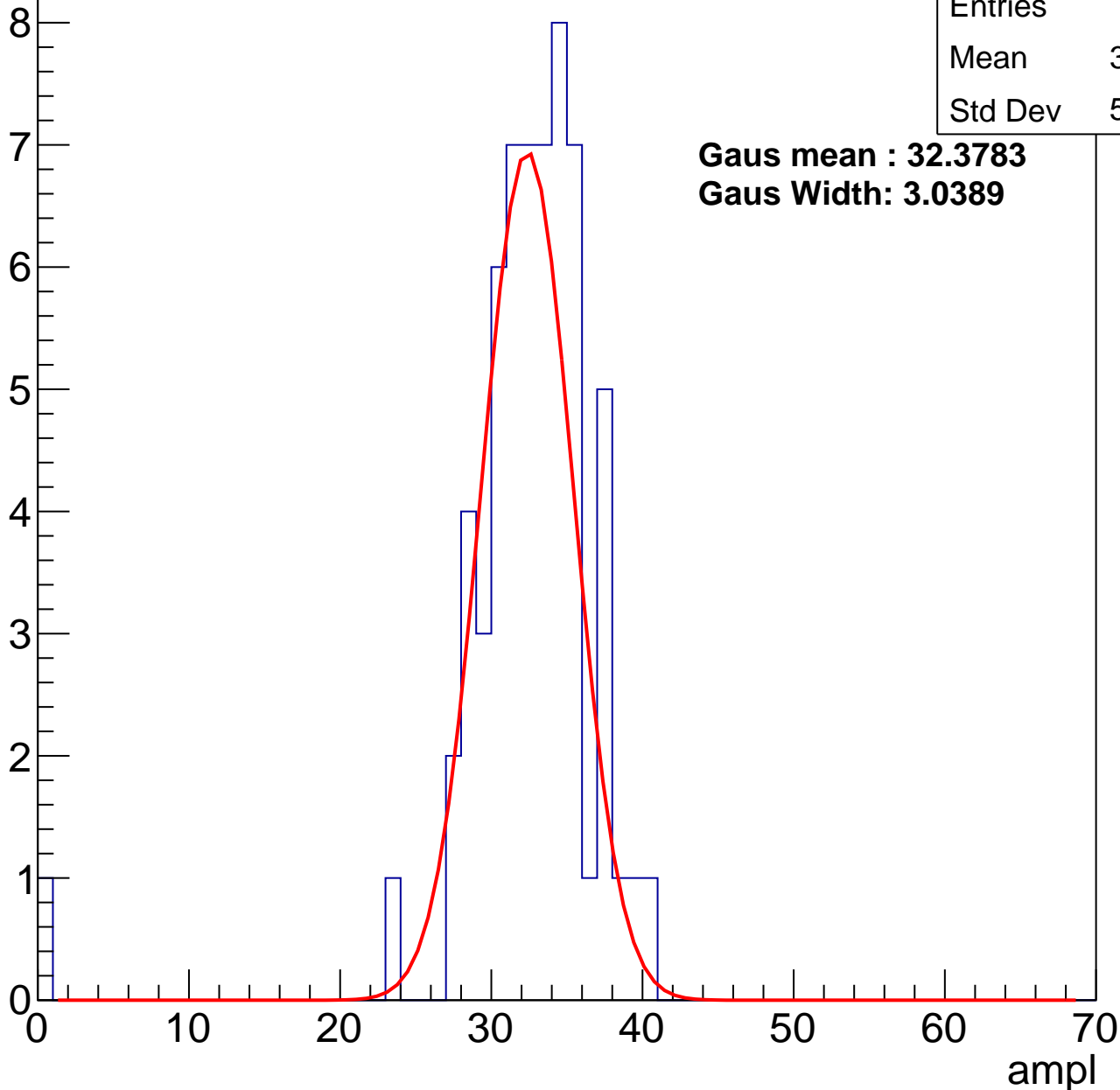
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.98
Std Dev	5.188

Gaus mean : 32.3783

Gaus Width: 3.0389



B1L101S, U14-ch29, adc1

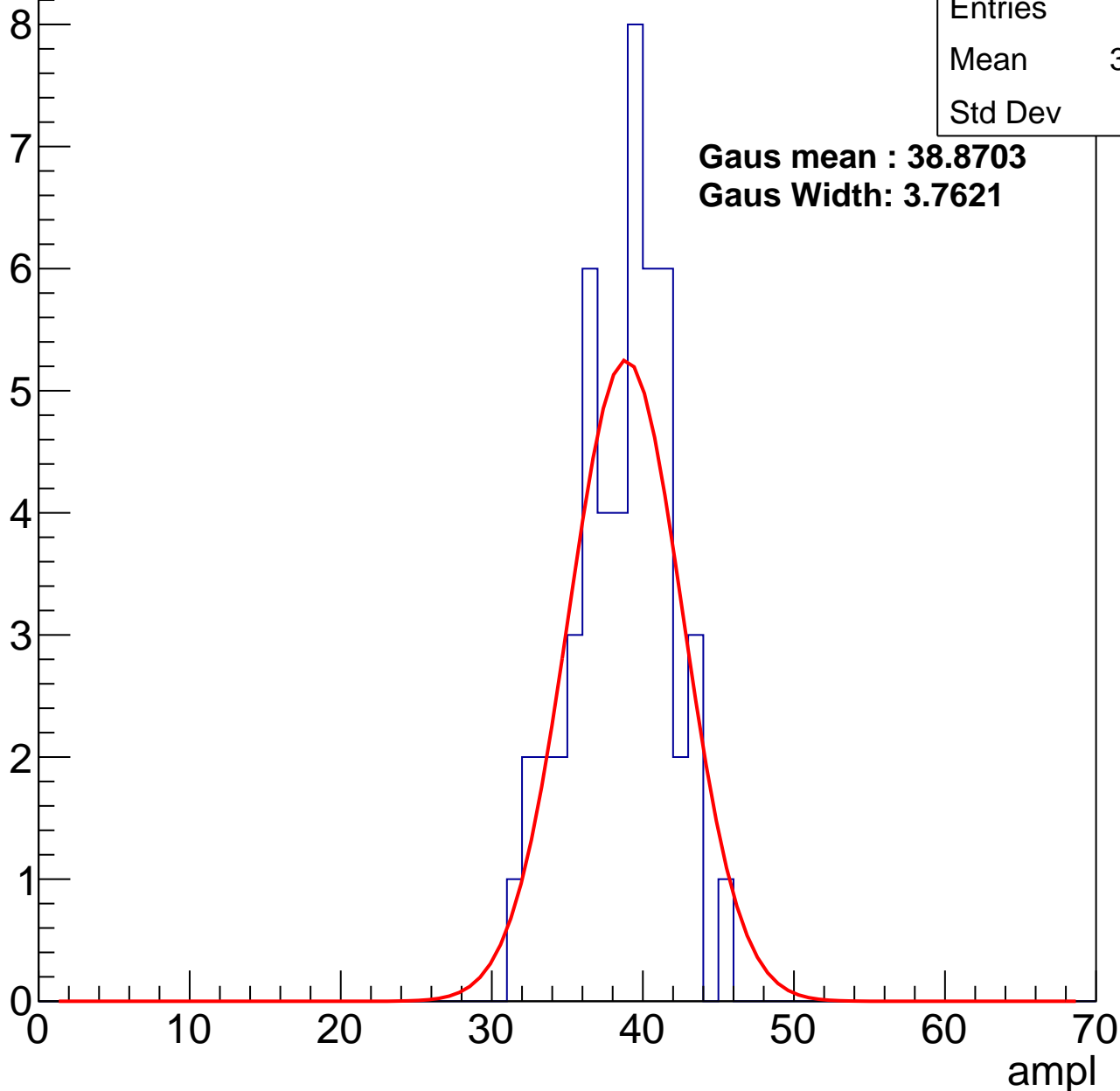
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	38.12
Std Dev	3.16

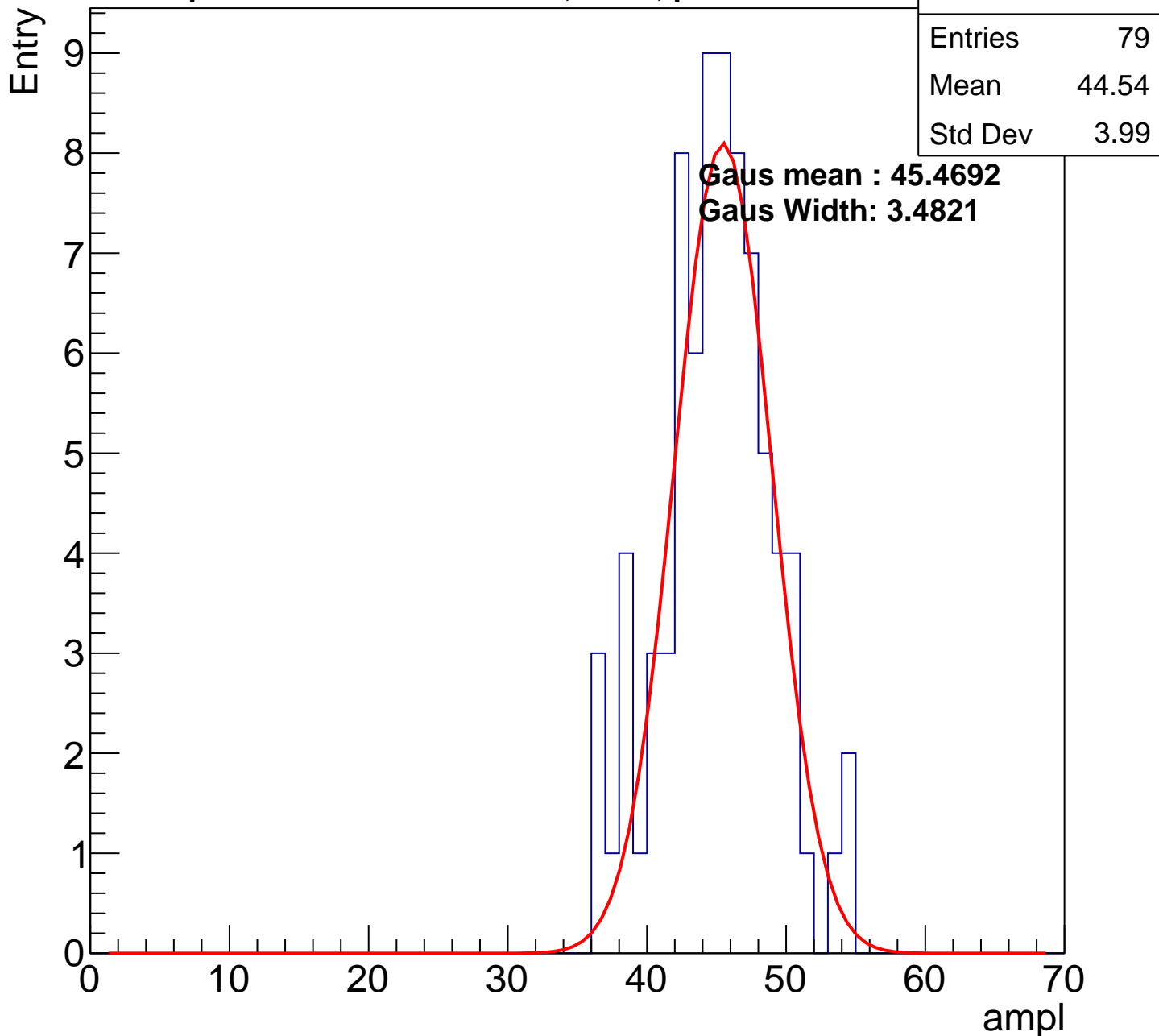
Gaus mean : 38.8703

Gaus Width: 3.7621



B1L101S, U14-ch29, adc2

calib_packv5_042523_0143.root, FC#0, port D2

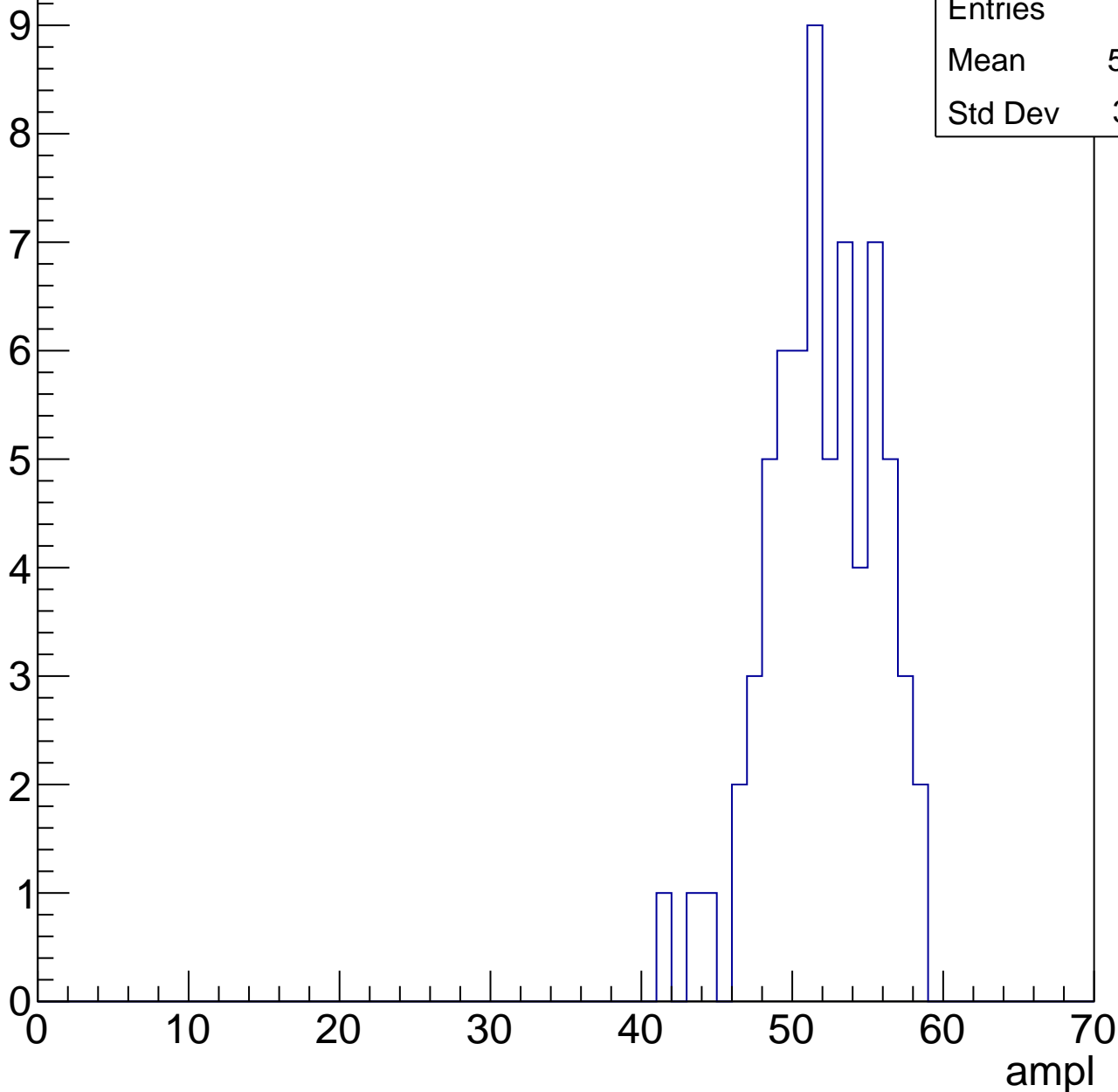


B1L101S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.54
Std Dev	3.621

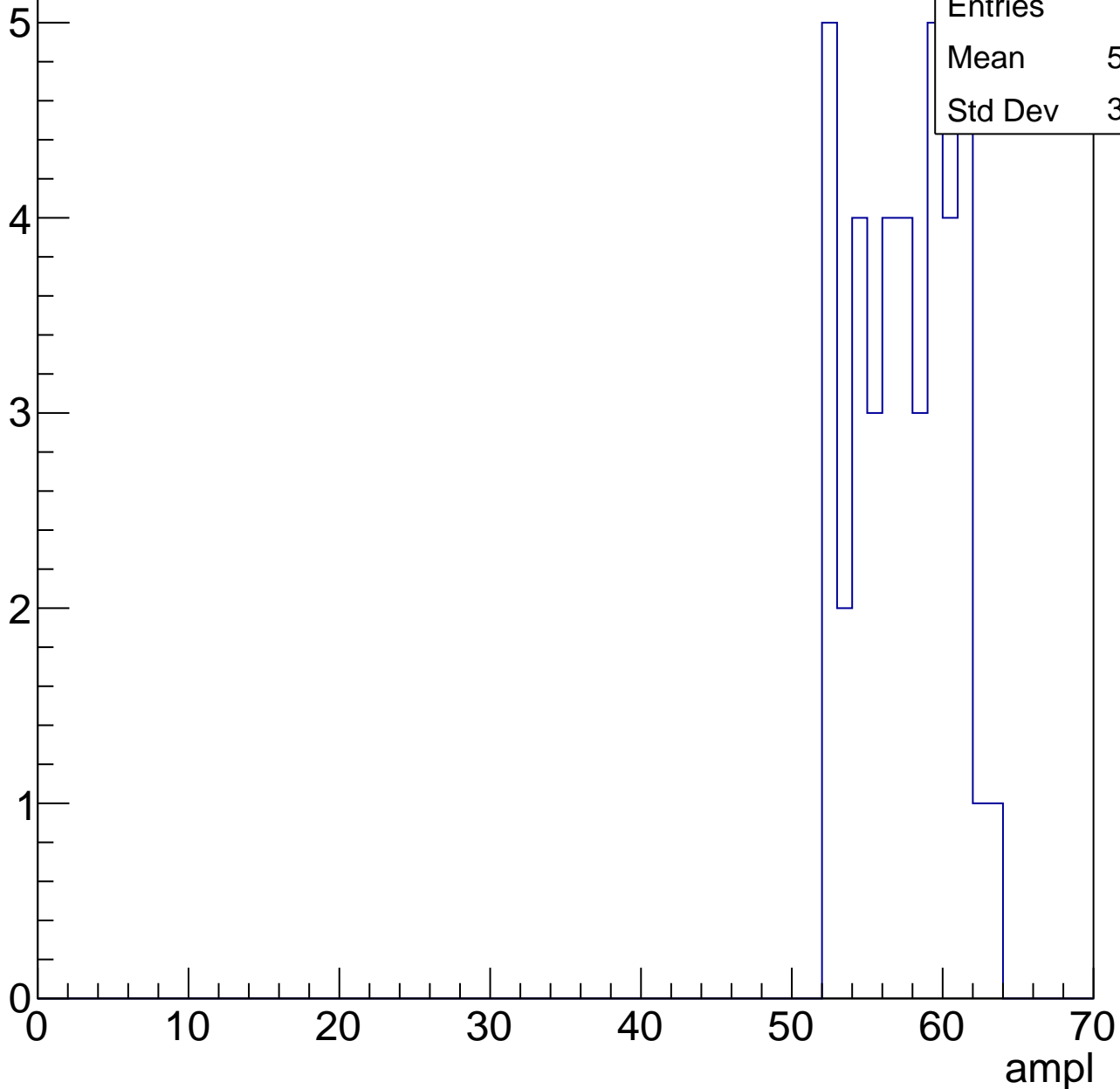


B1L101S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	57.02
Std Dev	3.158

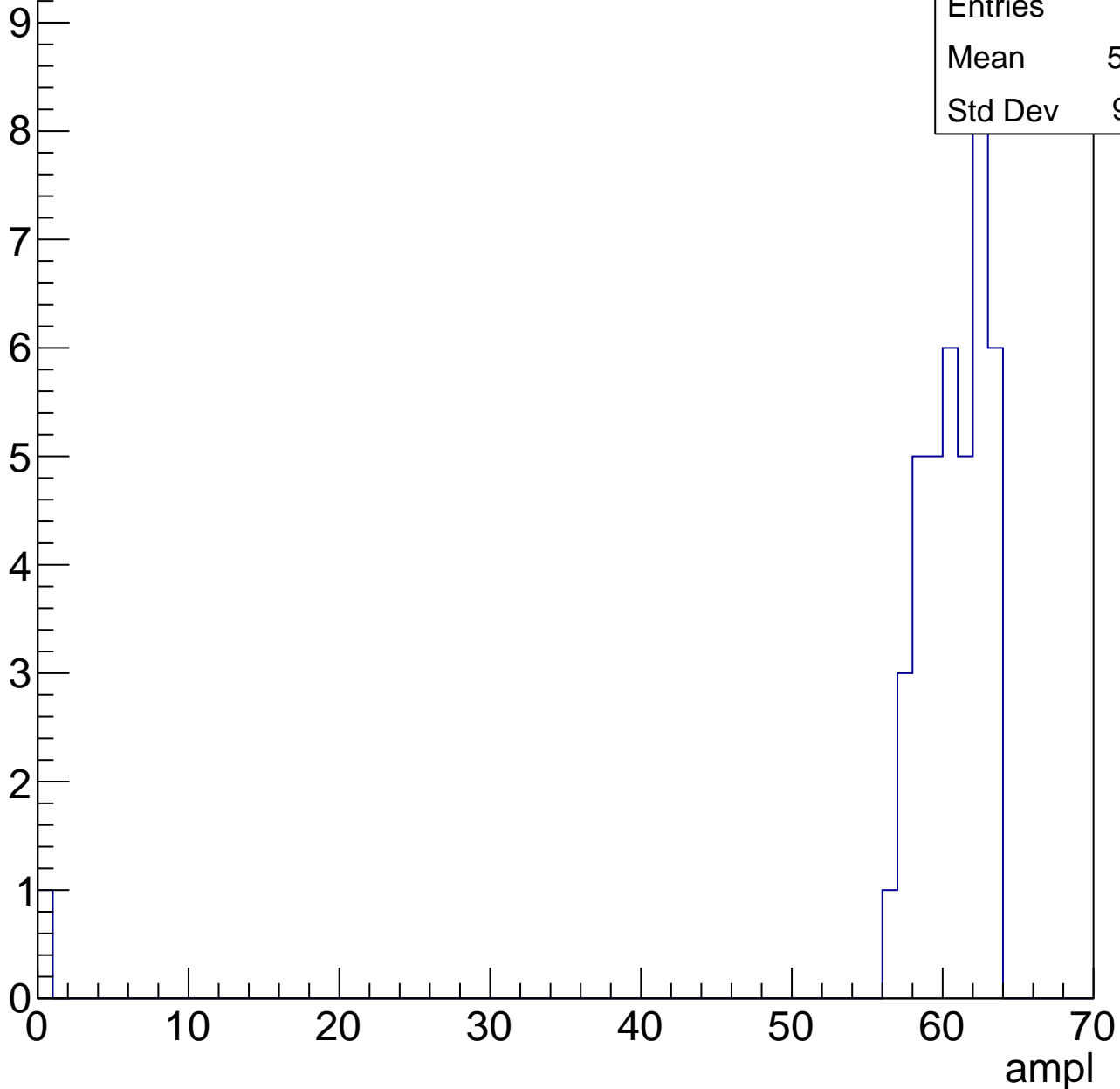


B1L101S, U14-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.85
Std Dev	9.511



B1L101S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

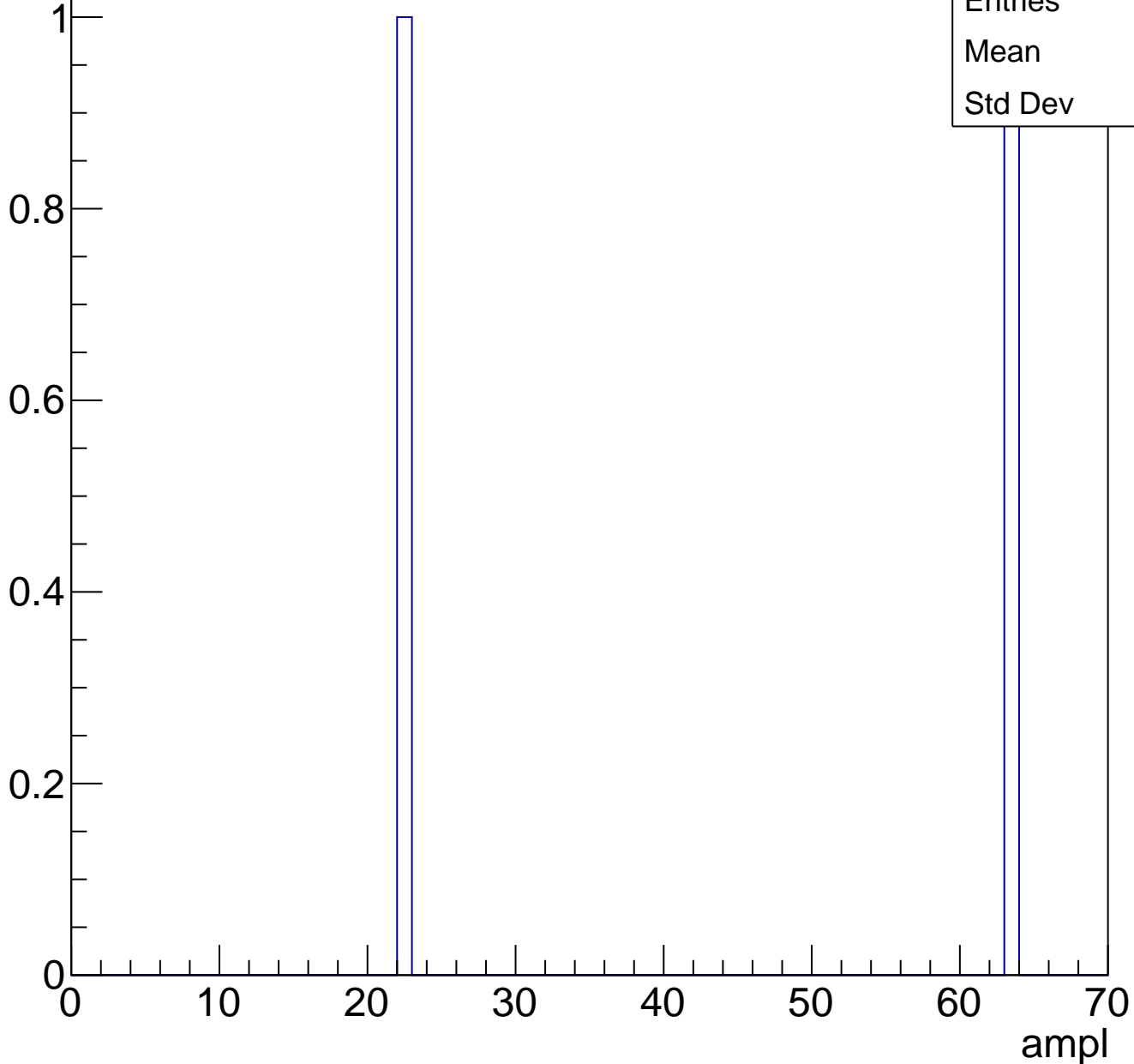
Entry



B1L101S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch30, adc0

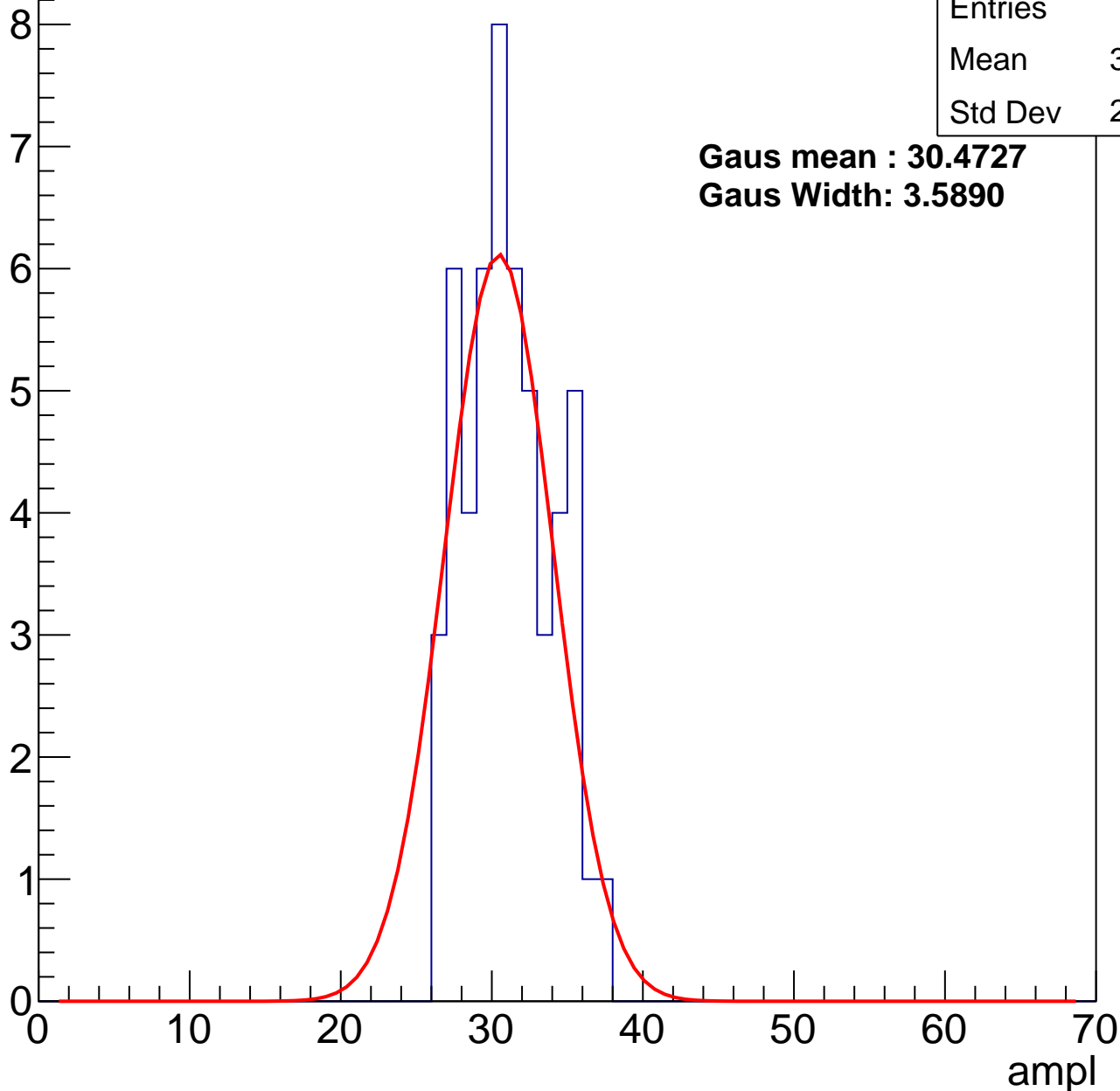
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	30.67
Std Dev	2.867

Gaus mean : 30.4727

Gaus Width: 3.5890



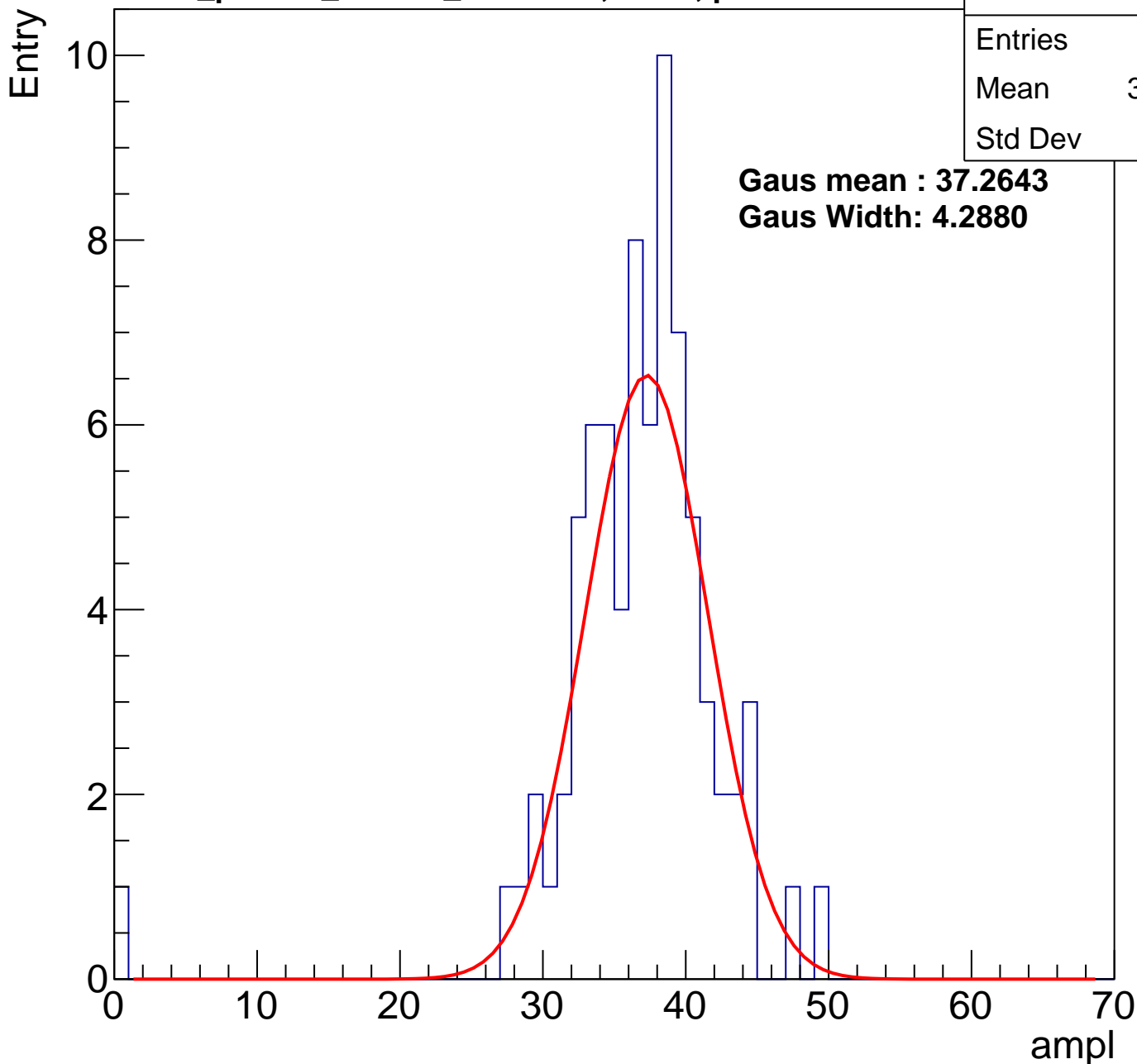
B1L101S, U14-ch30, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	36.25
Std Dev	5.92

Gaus mean : 37.2643

Gaus Width: 4.2880



B1L101S, U14-ch30, adc2

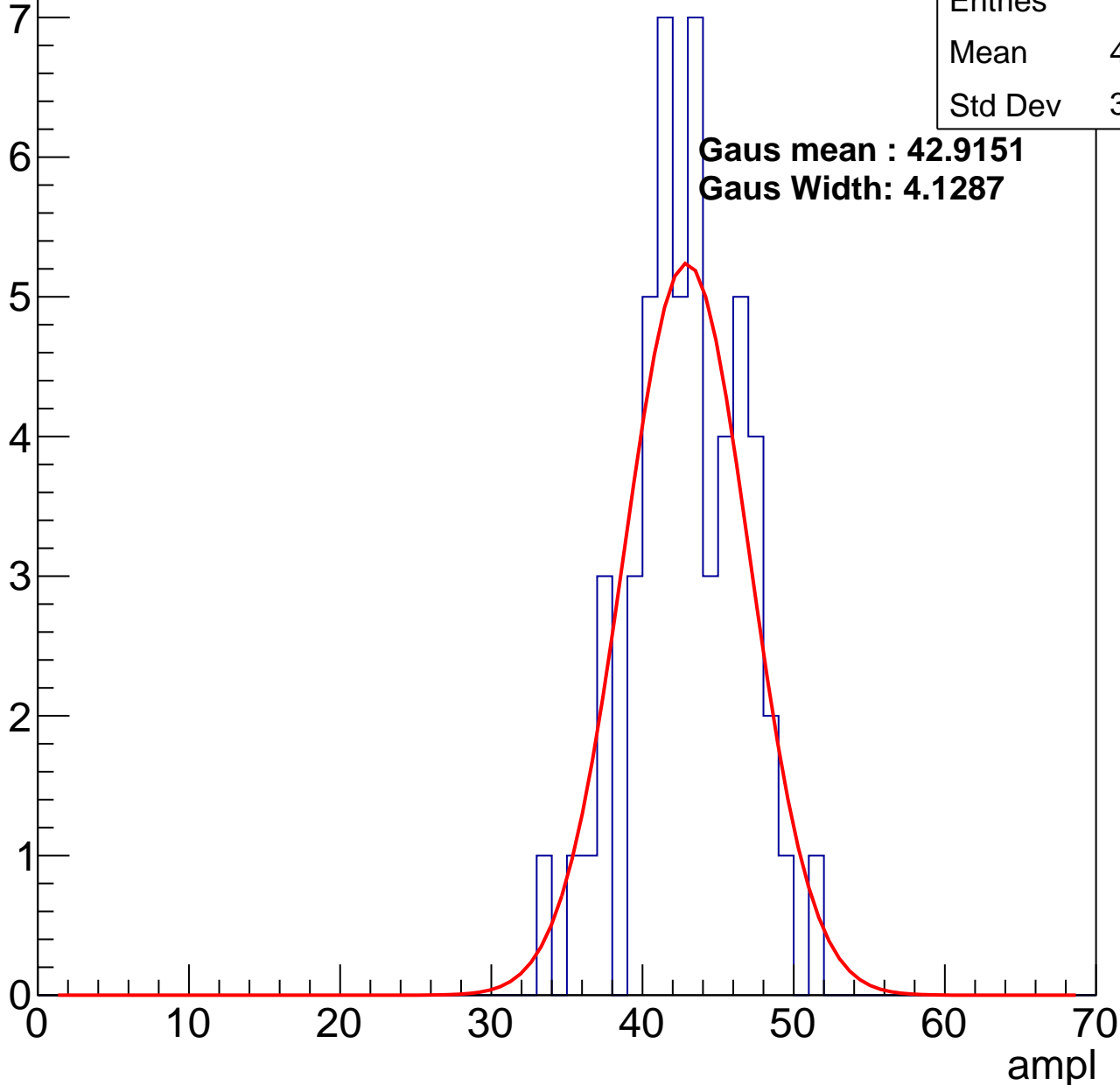
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.57
Std Dev	3.673

Gaus mean : 42.9151

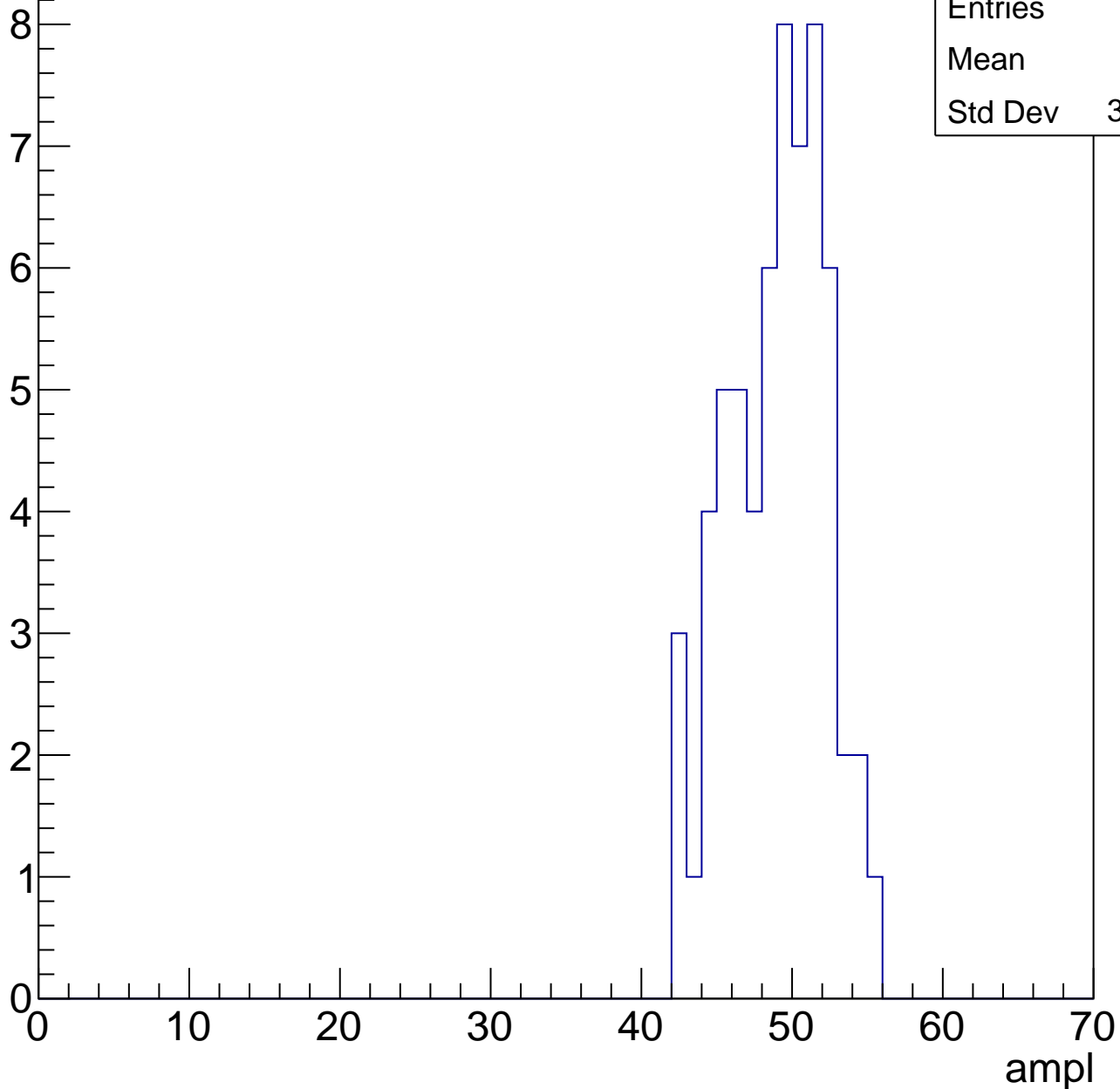
Gaus Width: 4.1287



B1L101S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



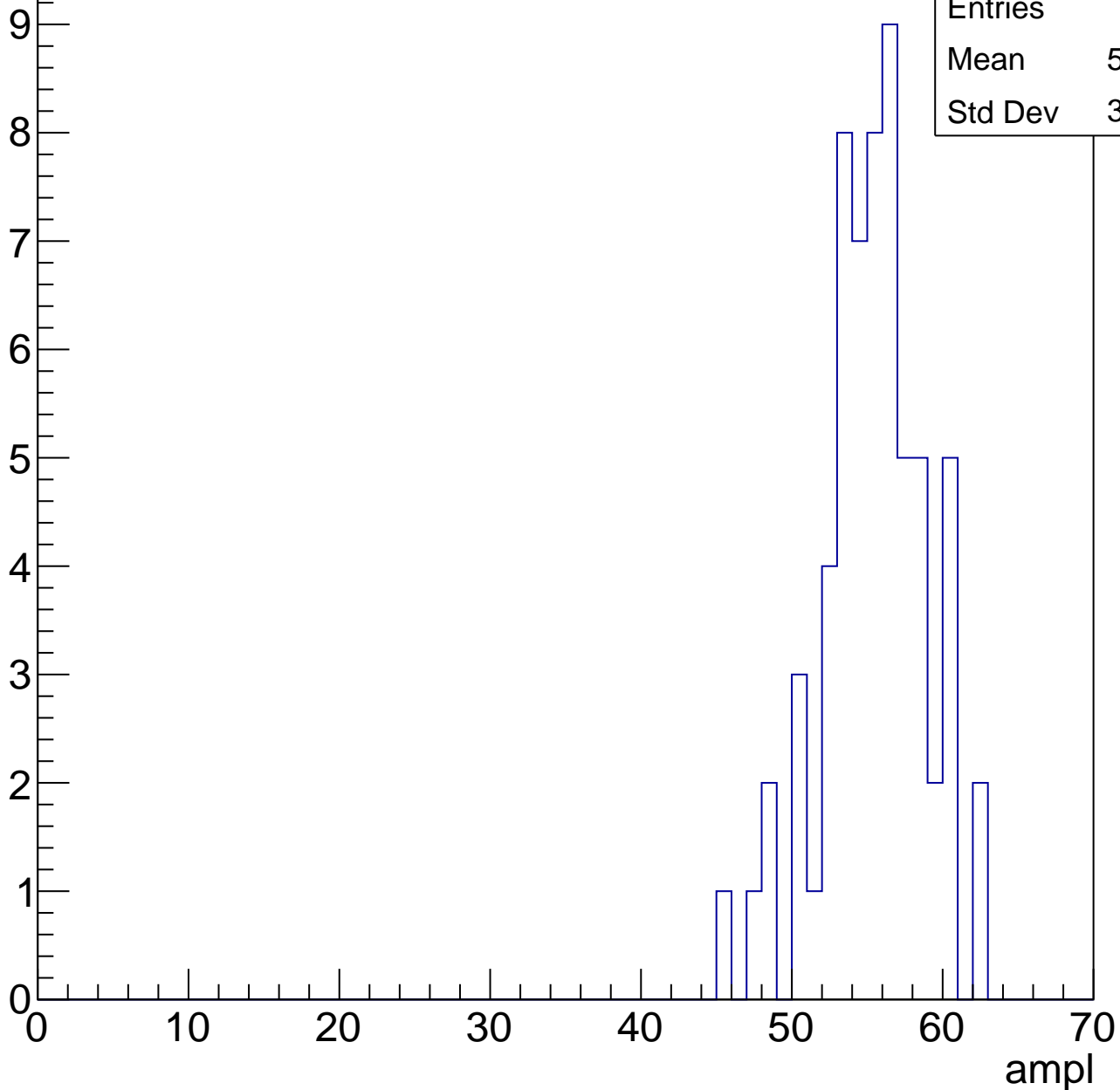
Entries	62
Mean	48.5
Std Dev	3.176

B1L101S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	54.92
Std Dev	3.465

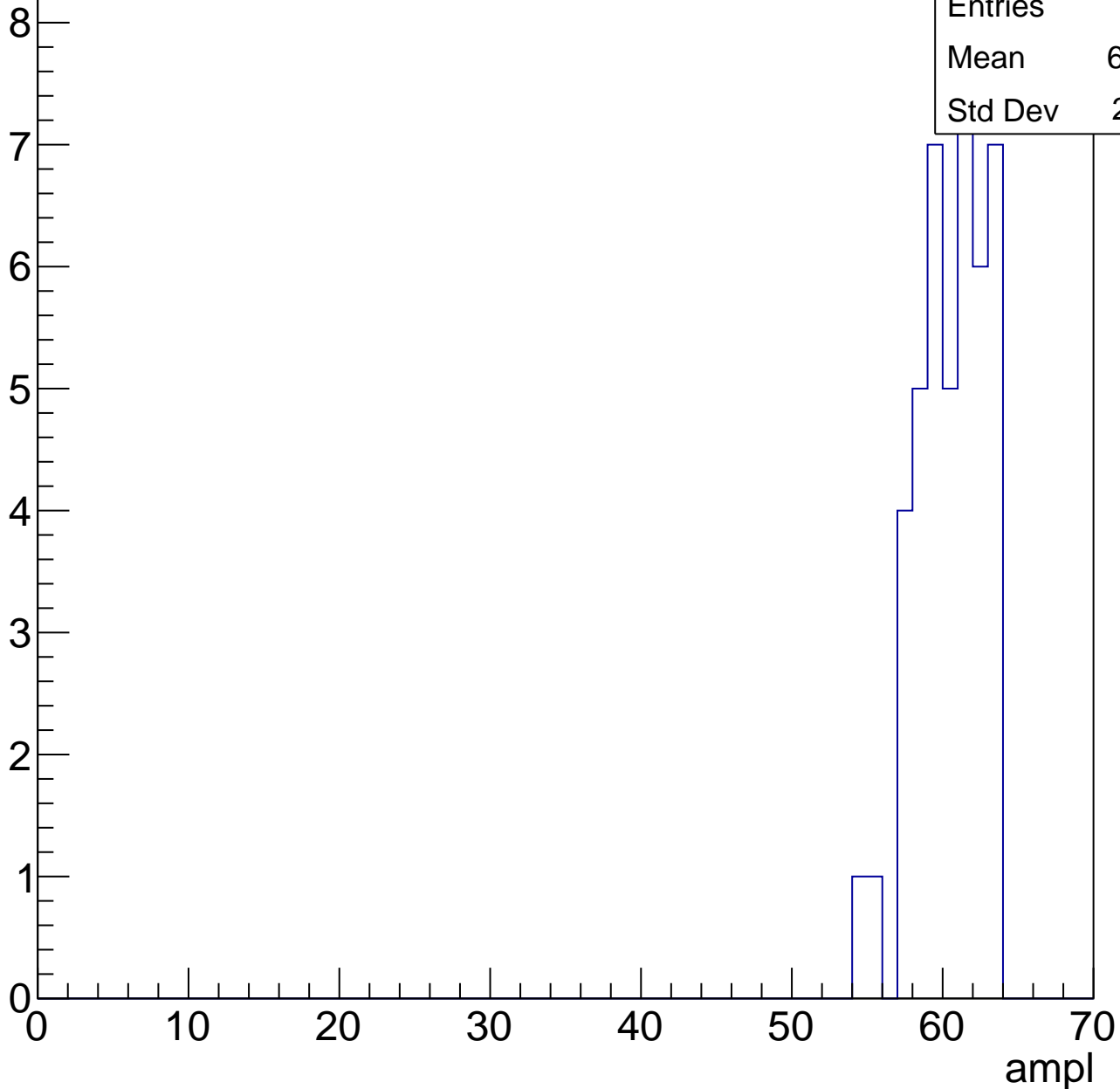


B1L101S, U14-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

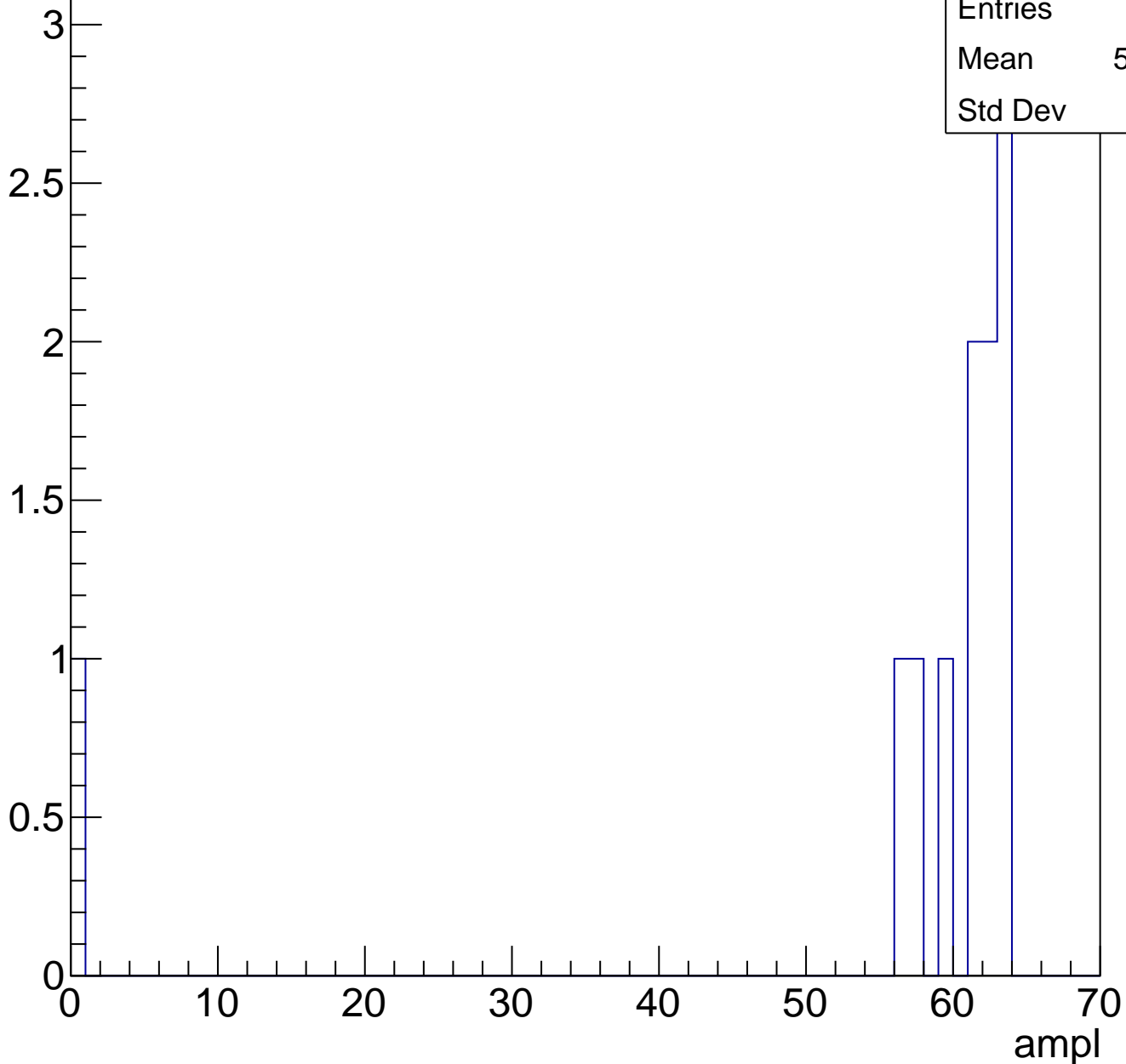
Entries	44
Mean	60.02
Std Dev	2.231



B1L101S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

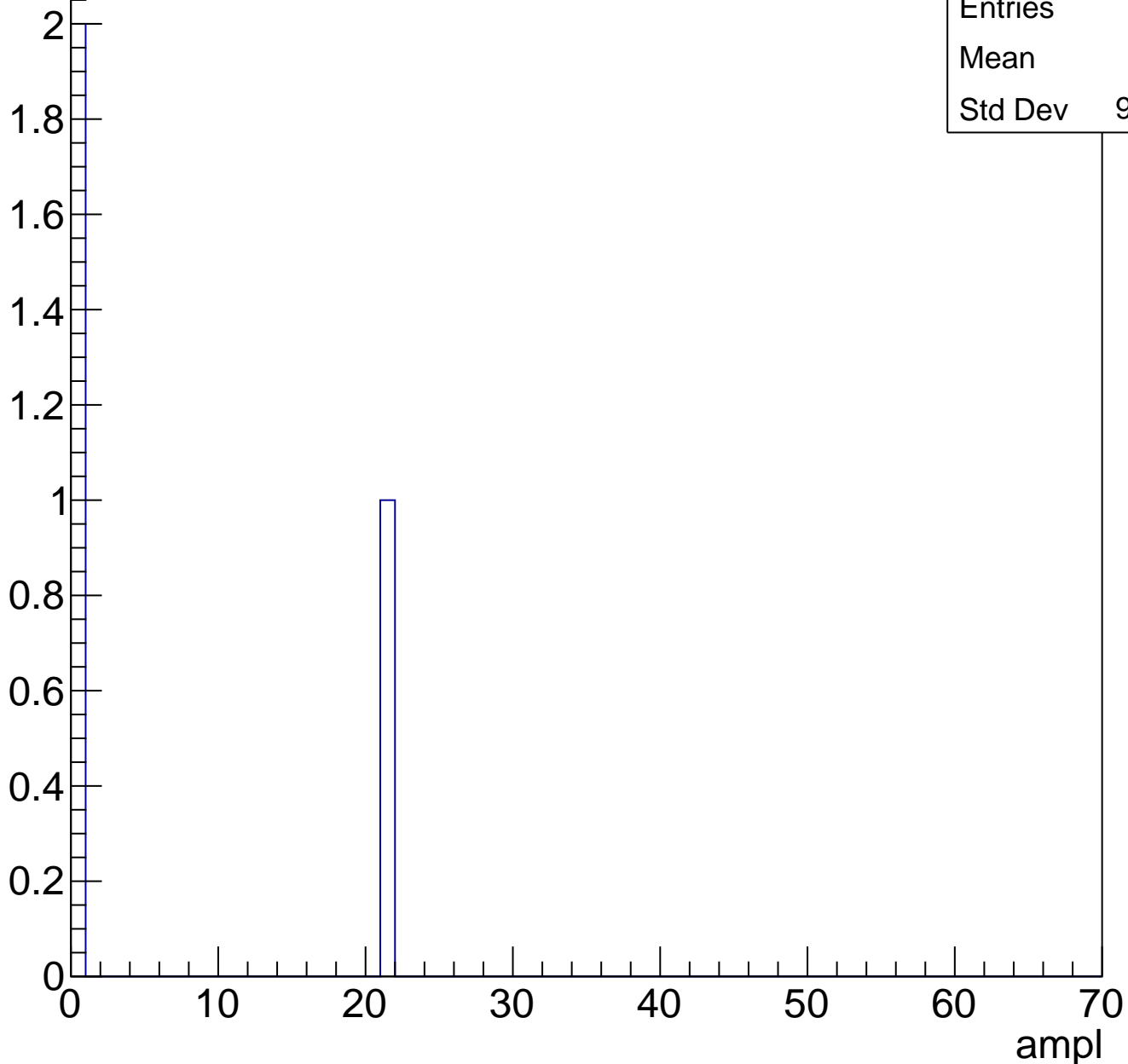
Entry



B1L101S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U14-ch31, adc0

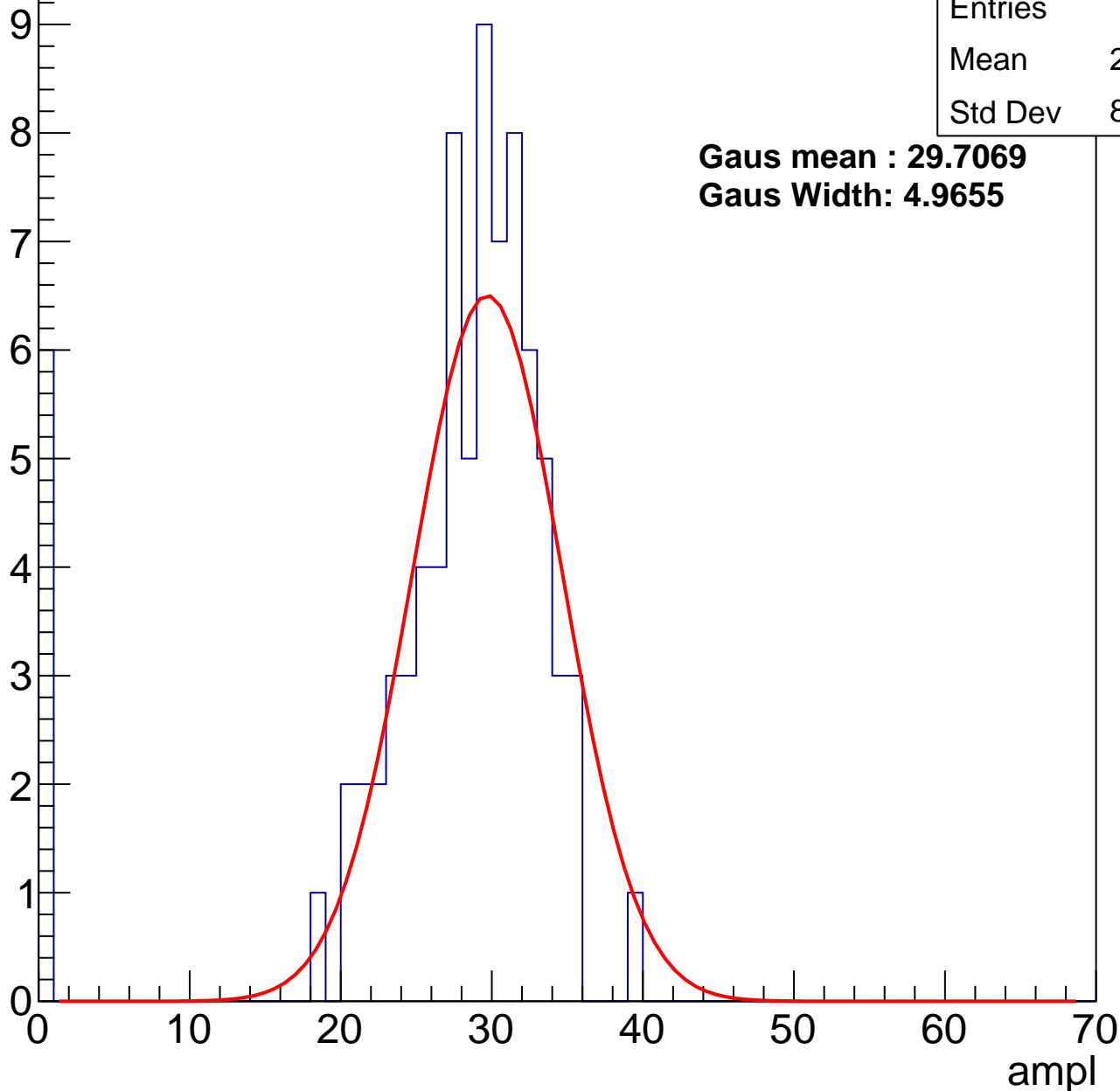
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	26.43
Std Dev	8.402

Gaus mean : 29.7069

Gaus Width: 4.9655



B1L101S, U14-ch31, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	36.08
Std Dev	3.574

Gaus mean : 36.9963

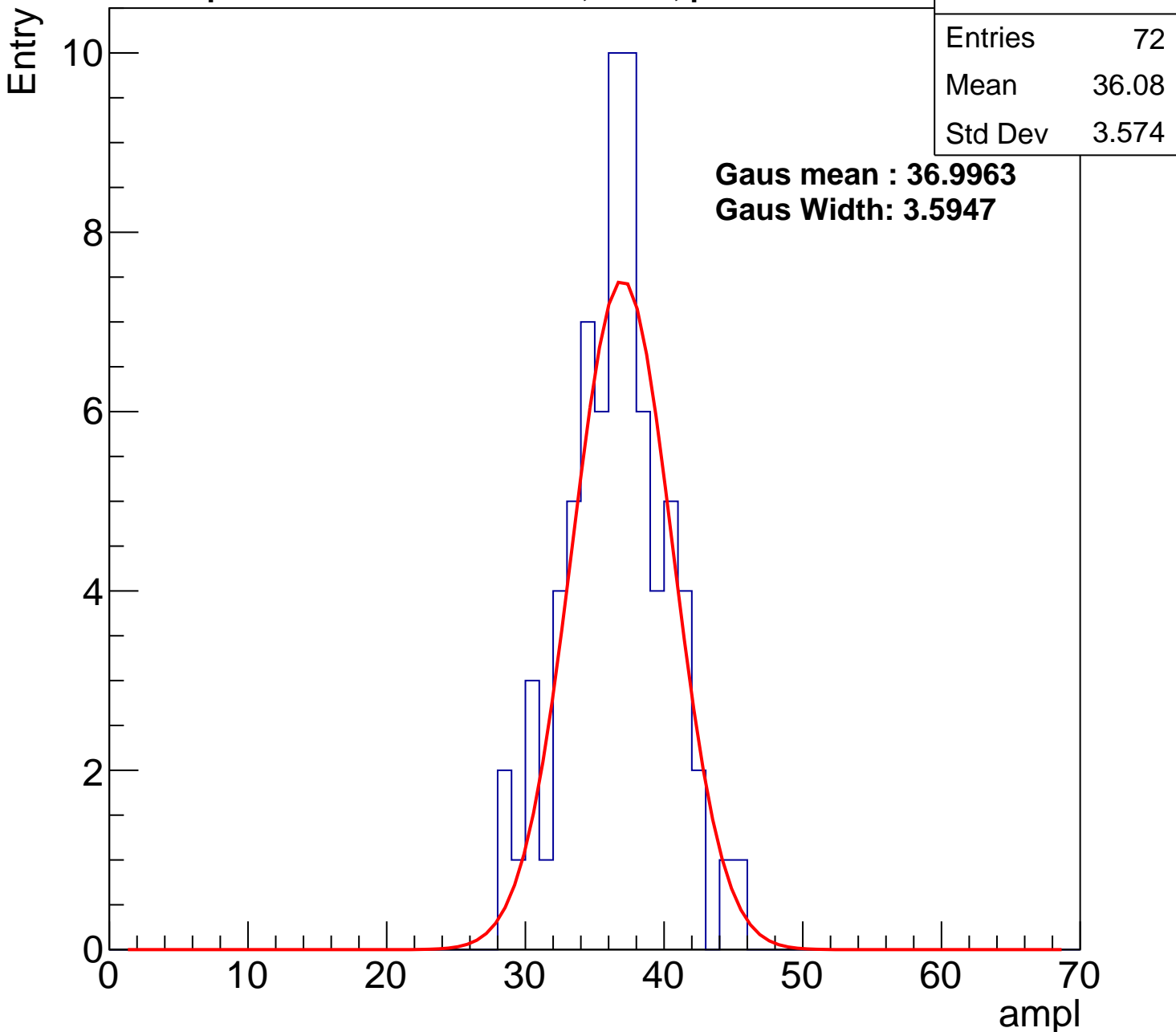
Gaus Width: 3.5947

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch31, adc2

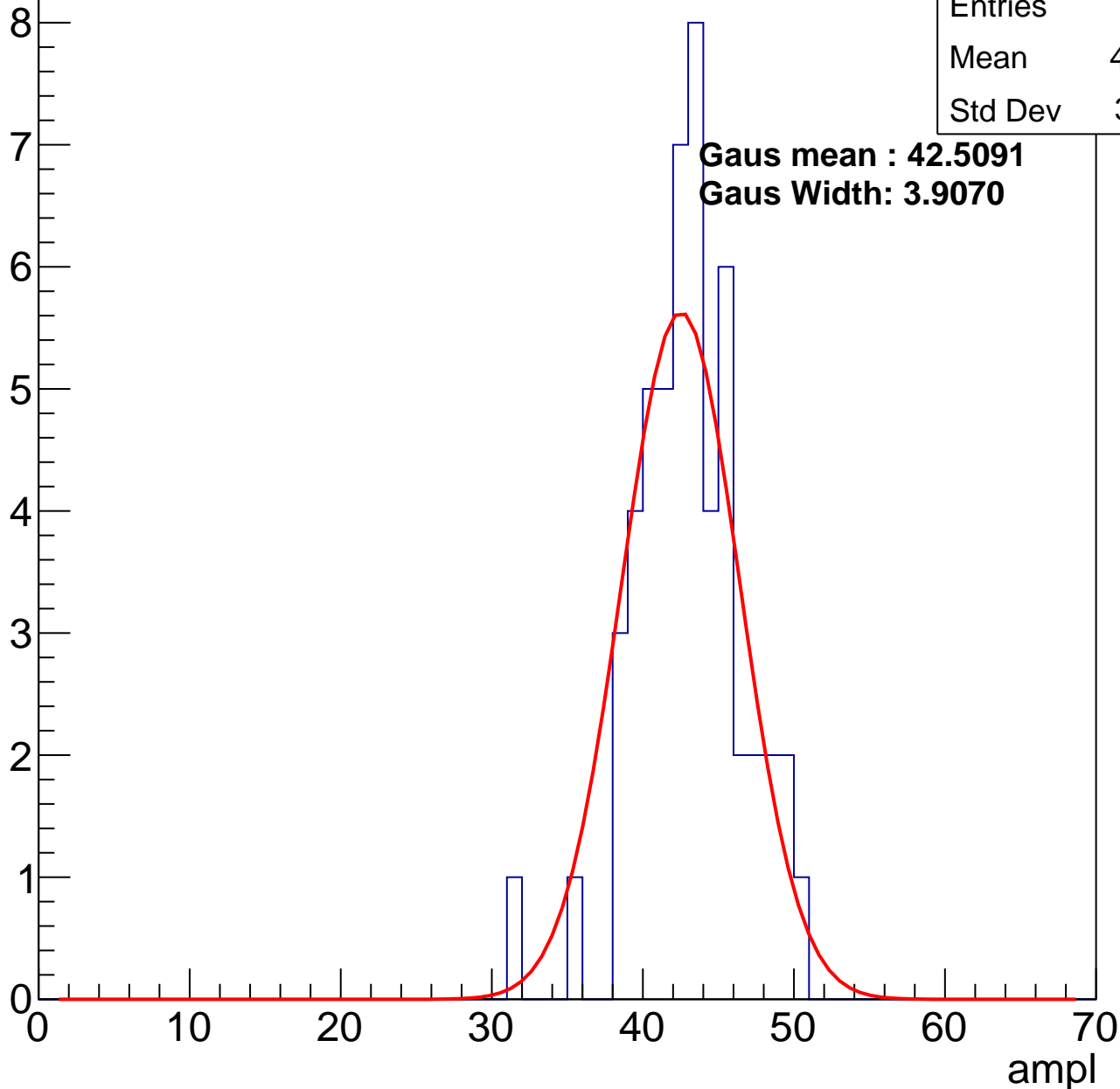
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.55
Std Dev	3.521

Gaus mean : 42.5091

Gaus Width: 3.9070

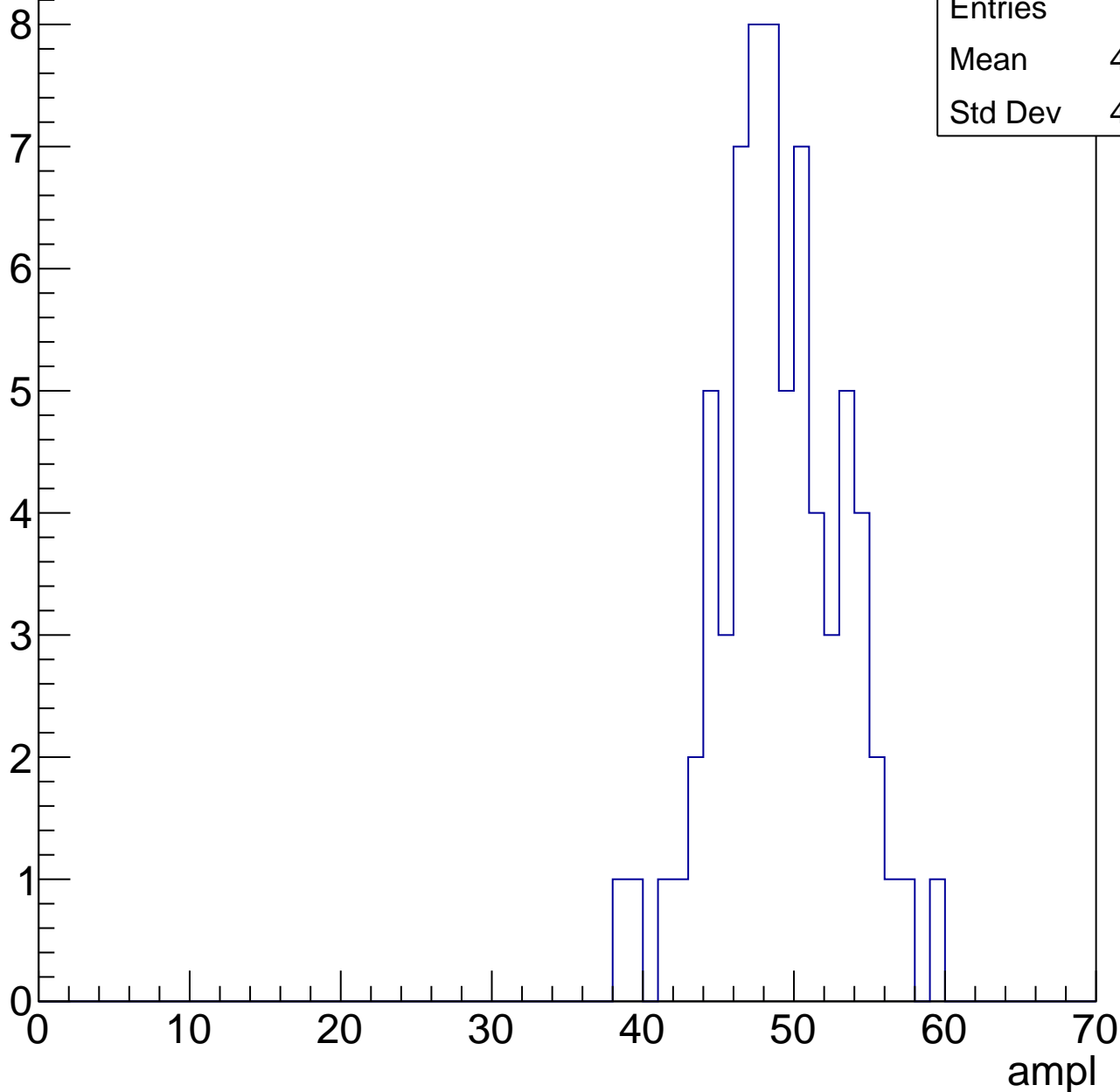


B1L101S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.59
Std Dev	4.114

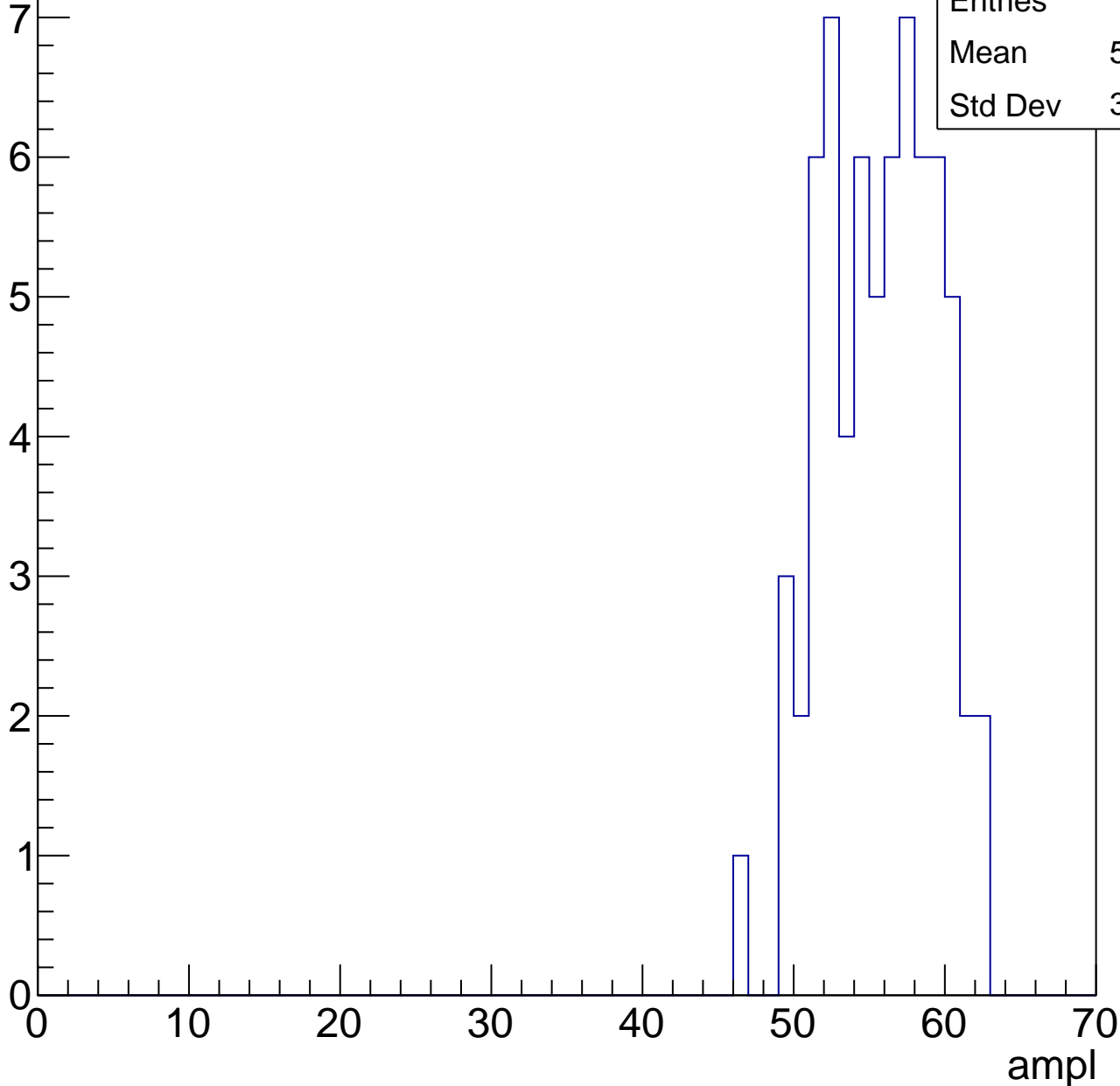


B1L101S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	55.25
Std Dev	3.627

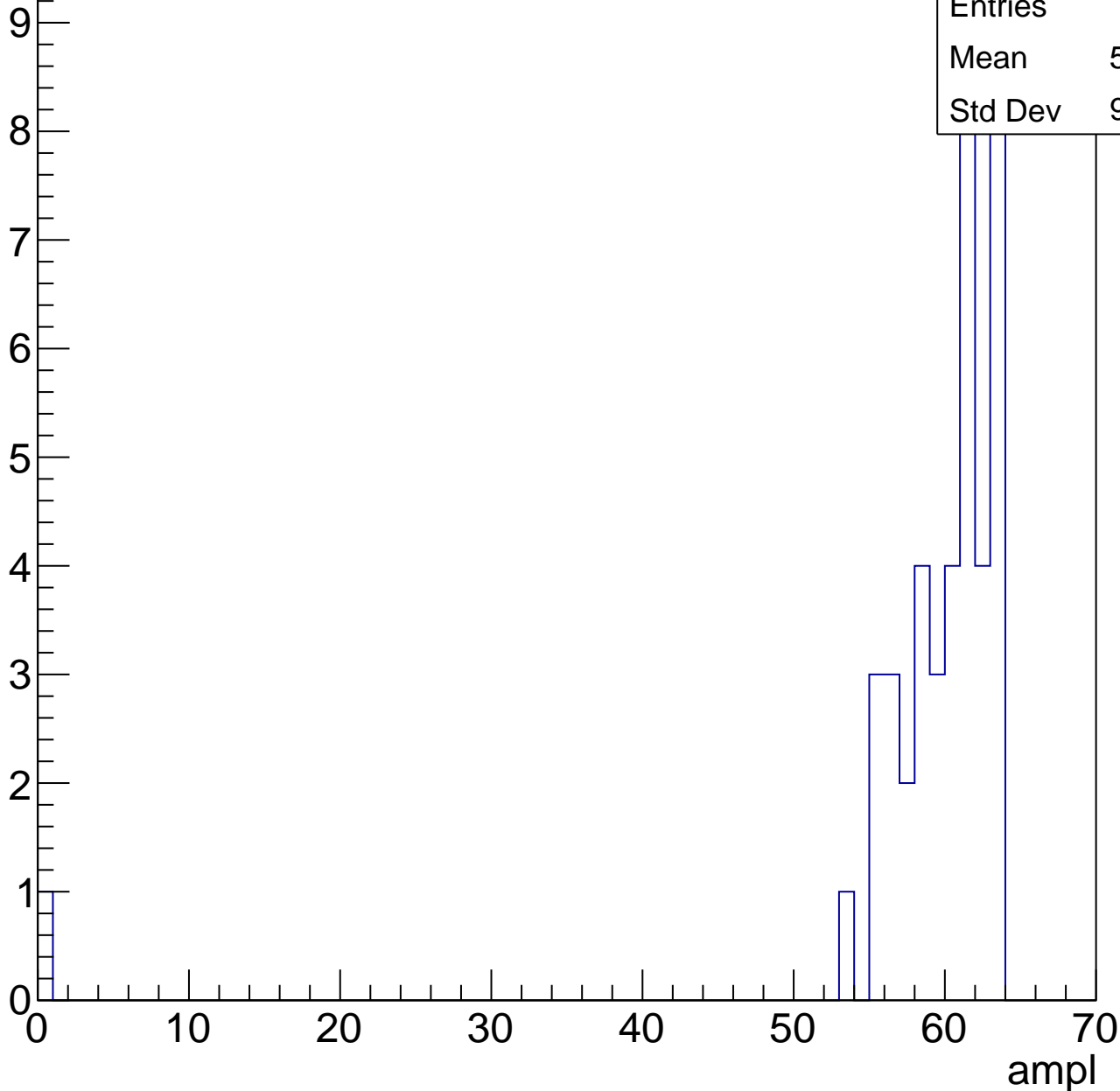


B1L101S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

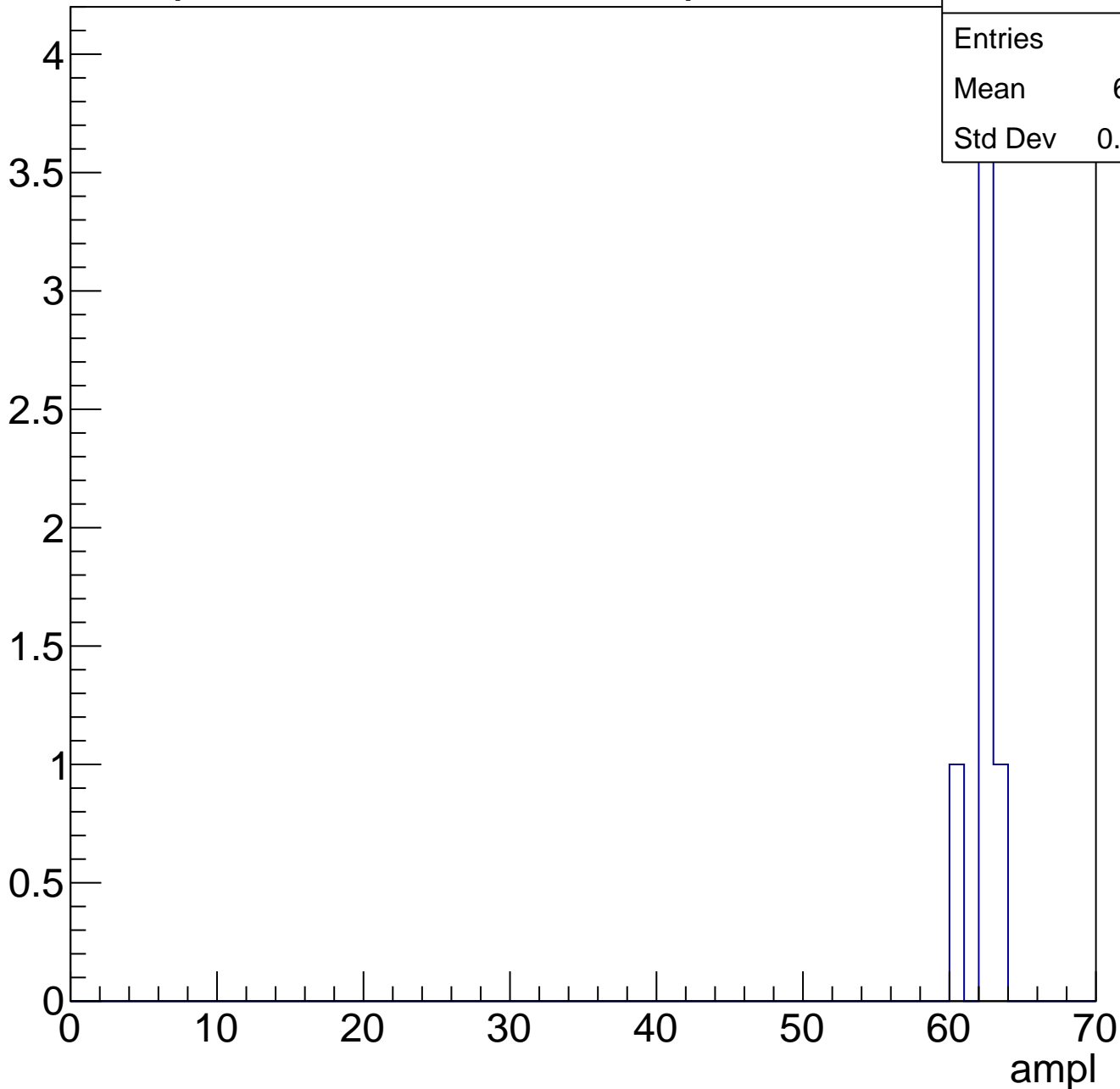
Entries	42
Mean	58.38
Std Dev	9.517



B1L101S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch32, adc0

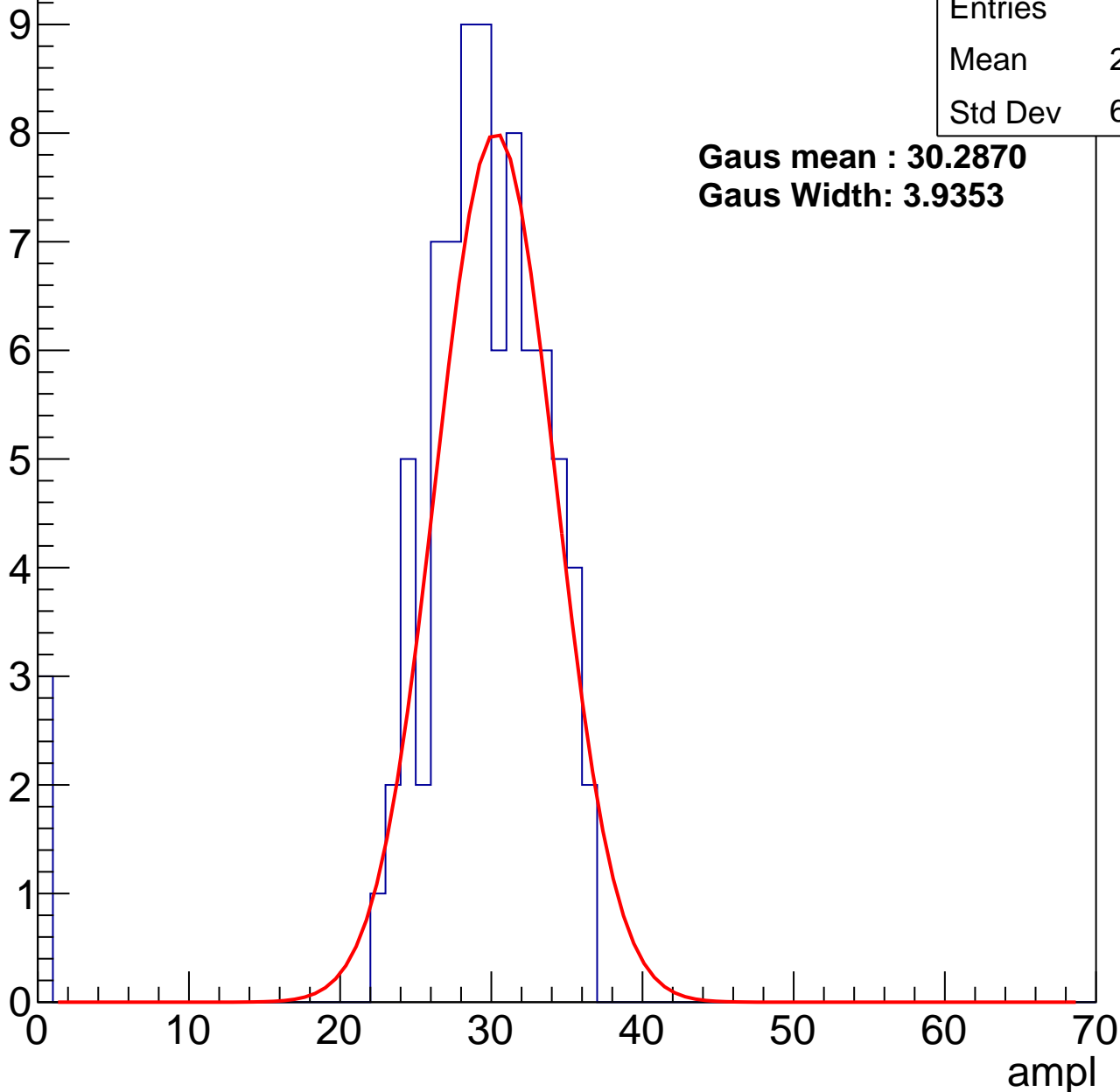
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	28.32
Std Dev	6.456

Gaus mean : 30.2870

Gaus Width: 3.9353



B1L101S, U14-ch32, adc1

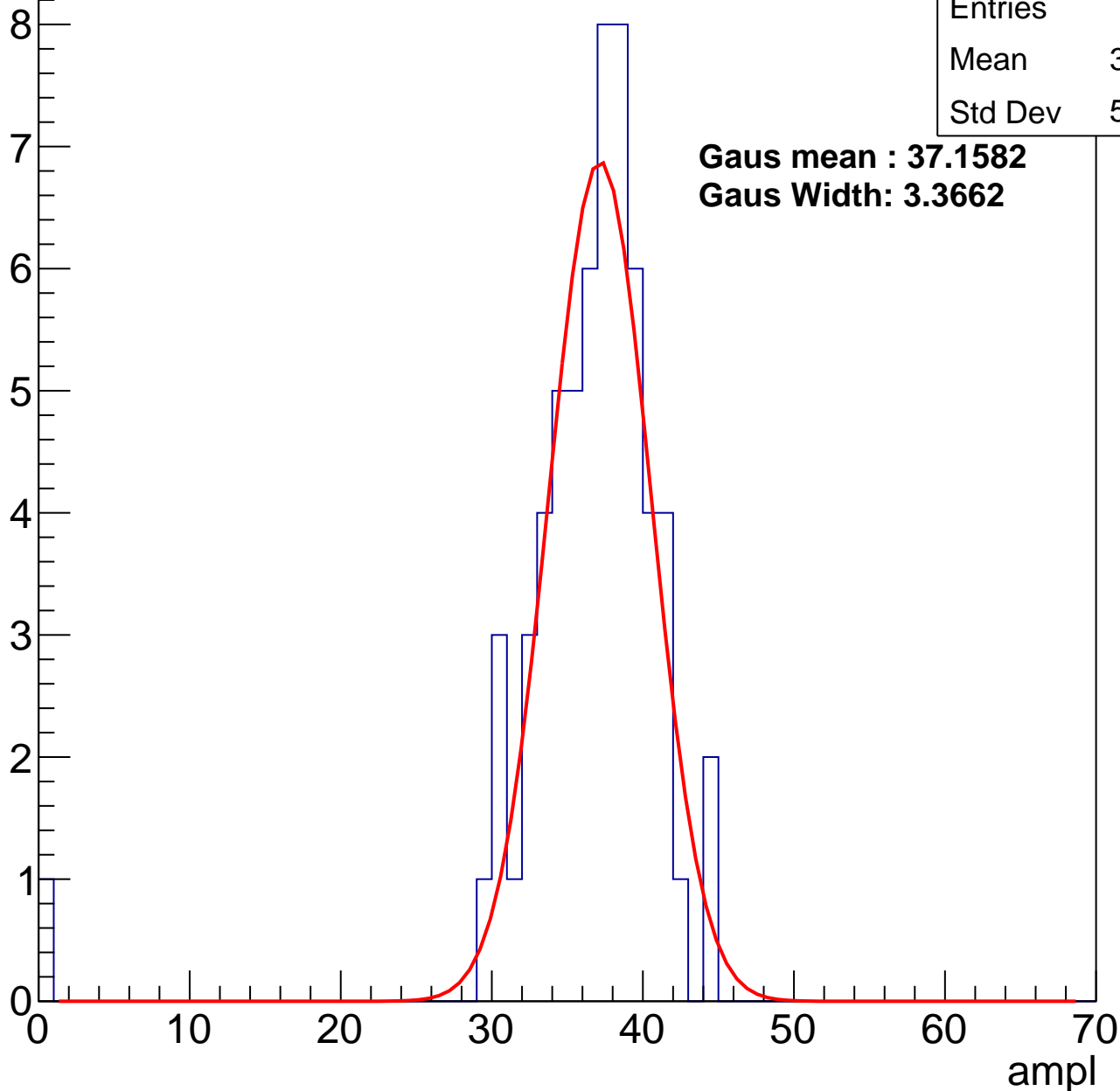
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	35.92
Std Dev	5.692

Gaus mean : 37.1582

Gaus Width: 3.3662



B1L101S, U14-ch32, adc2

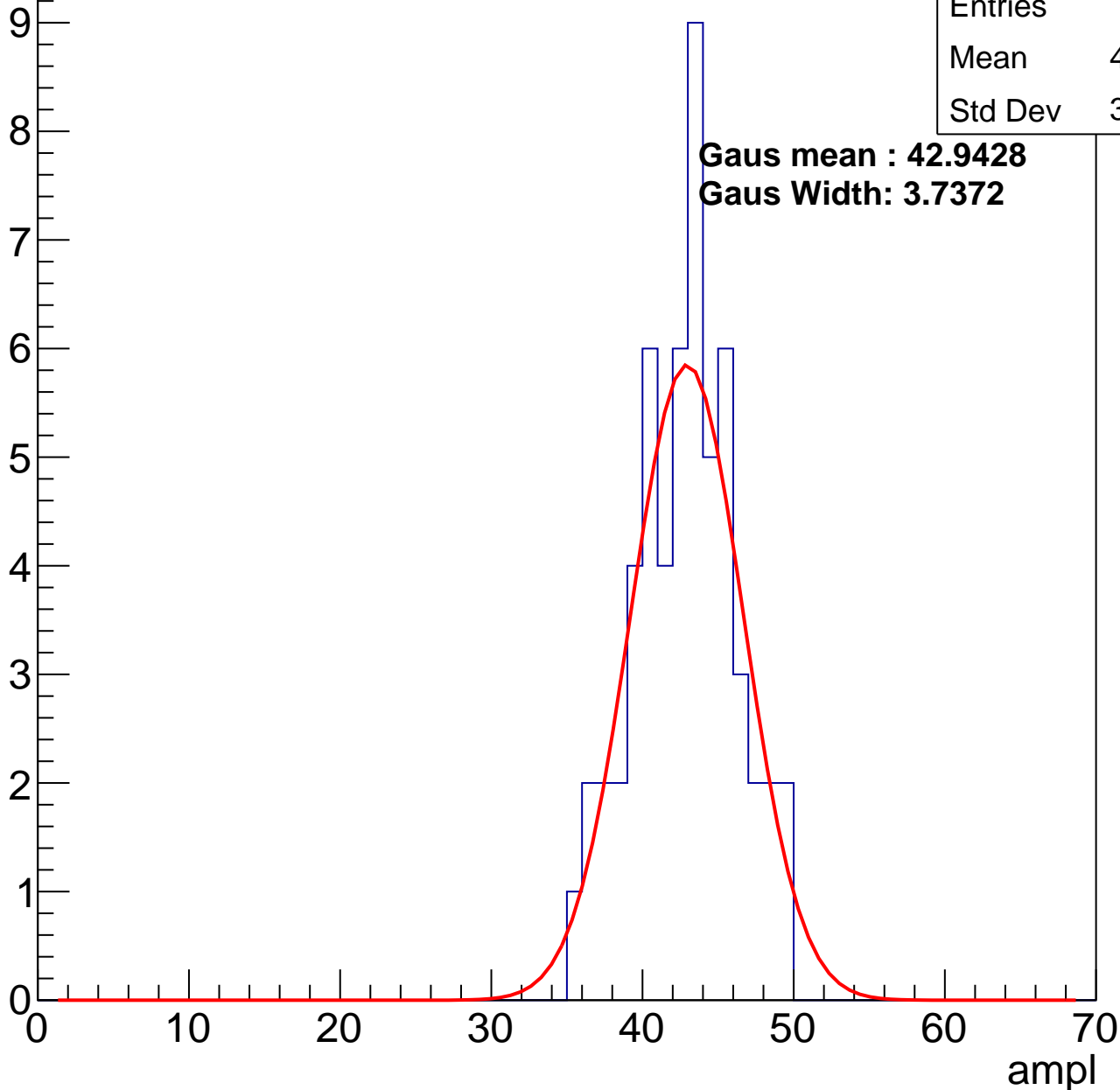
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.36
Std Dev	3.303

Gaus mean : 42.9428

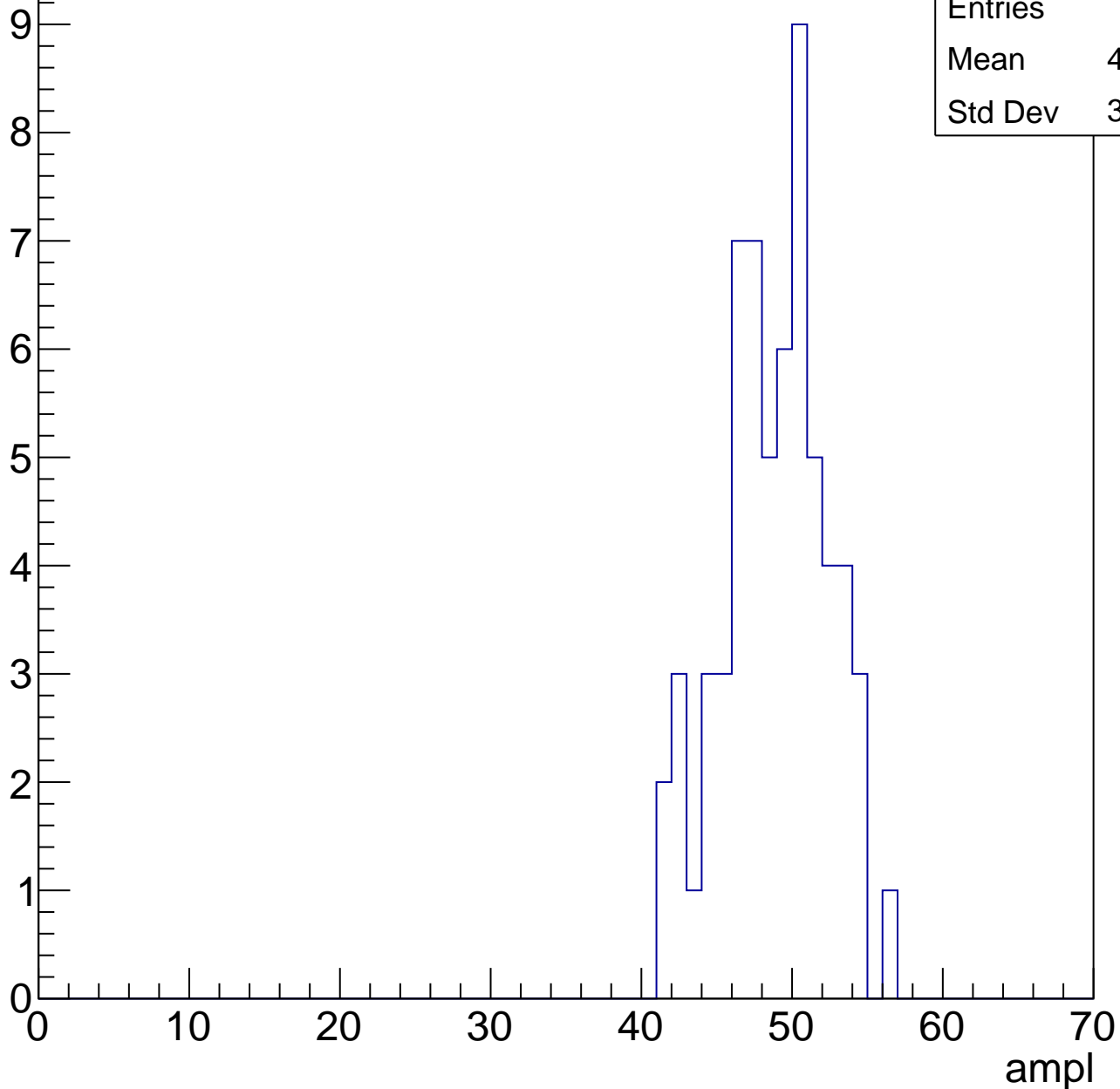
Gaus Width: 3.7372



B1L101S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

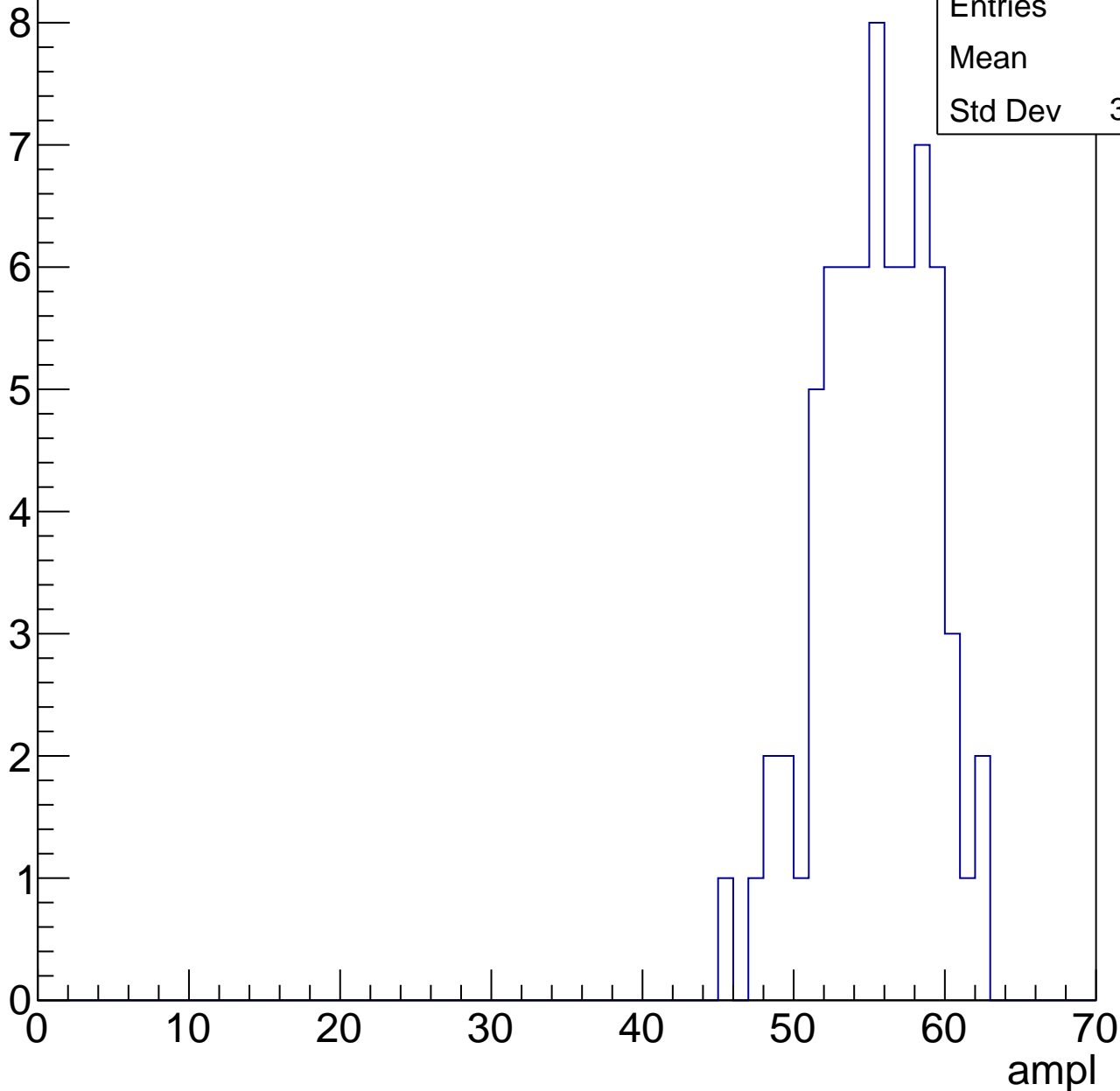


B1L101S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	54.9
Std Dev	3.652



B1L101S, U14-ch32, adc5

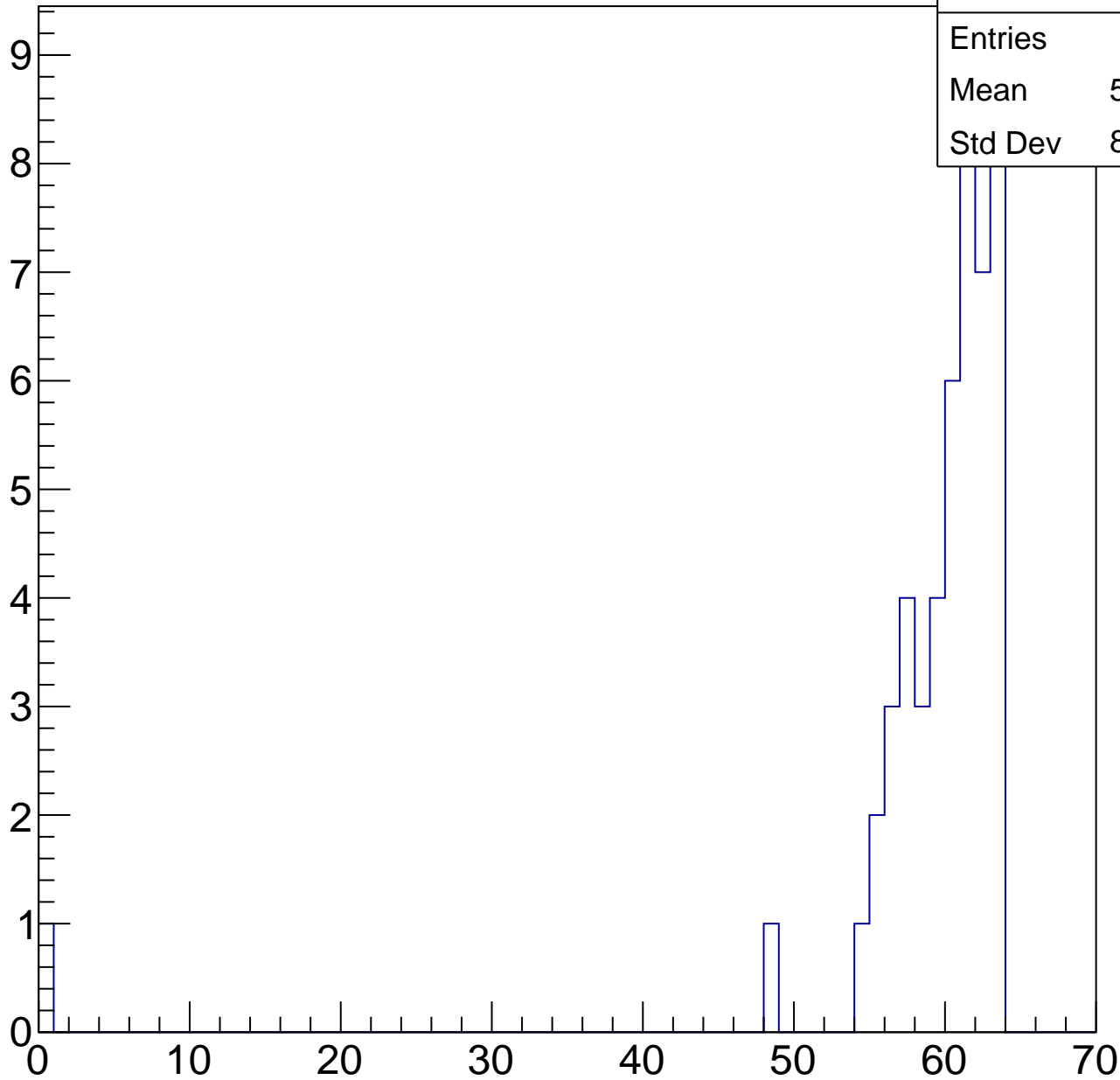
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.47
Std Dev	8.949

ampl



B1L101S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B1L101S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch33, adc0

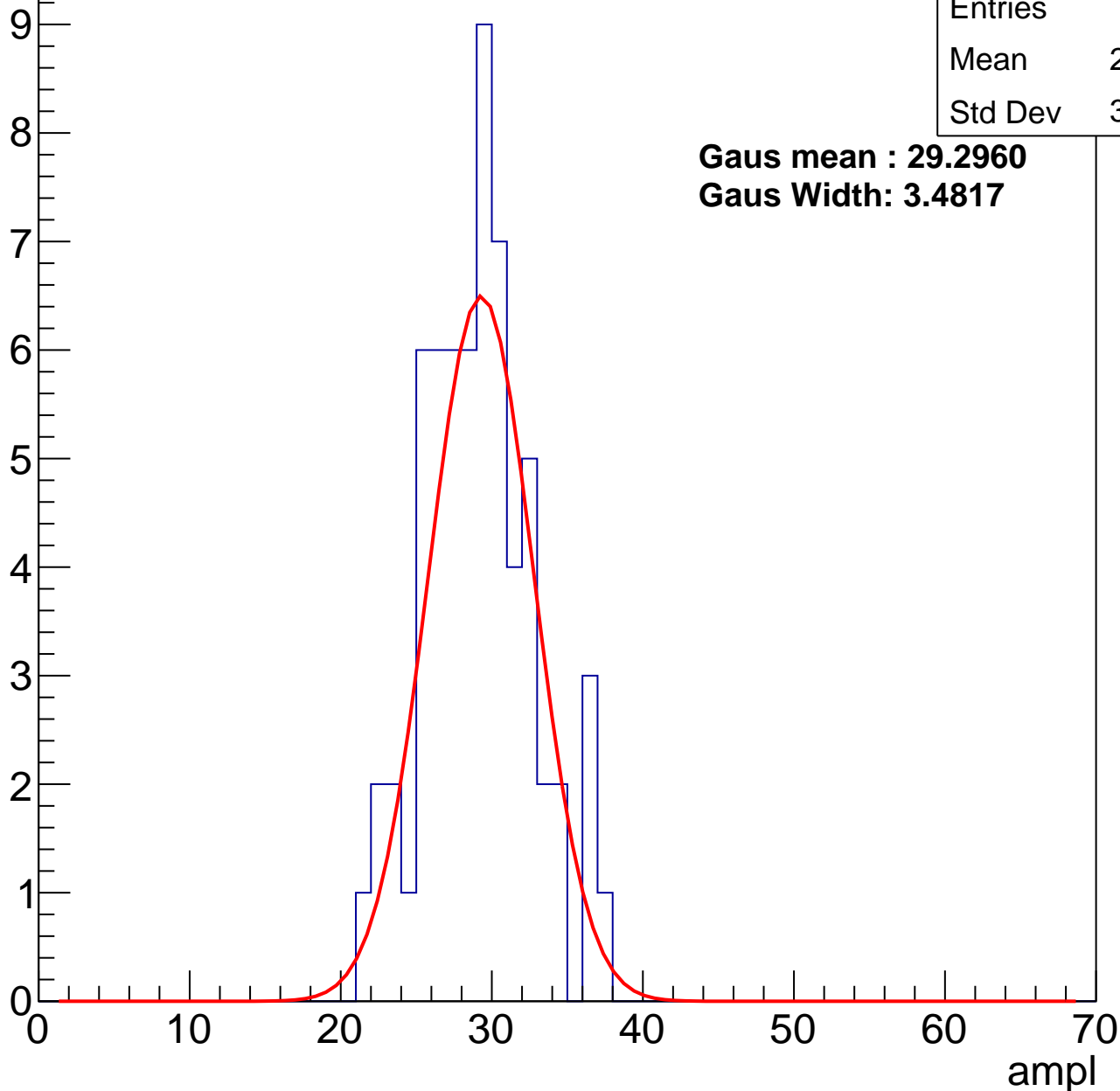
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.65
Std Dev	3.546

Gaus mean : 29.2960

Gaus Width: 3.4817



B1L101S, U14-ch33, adc1

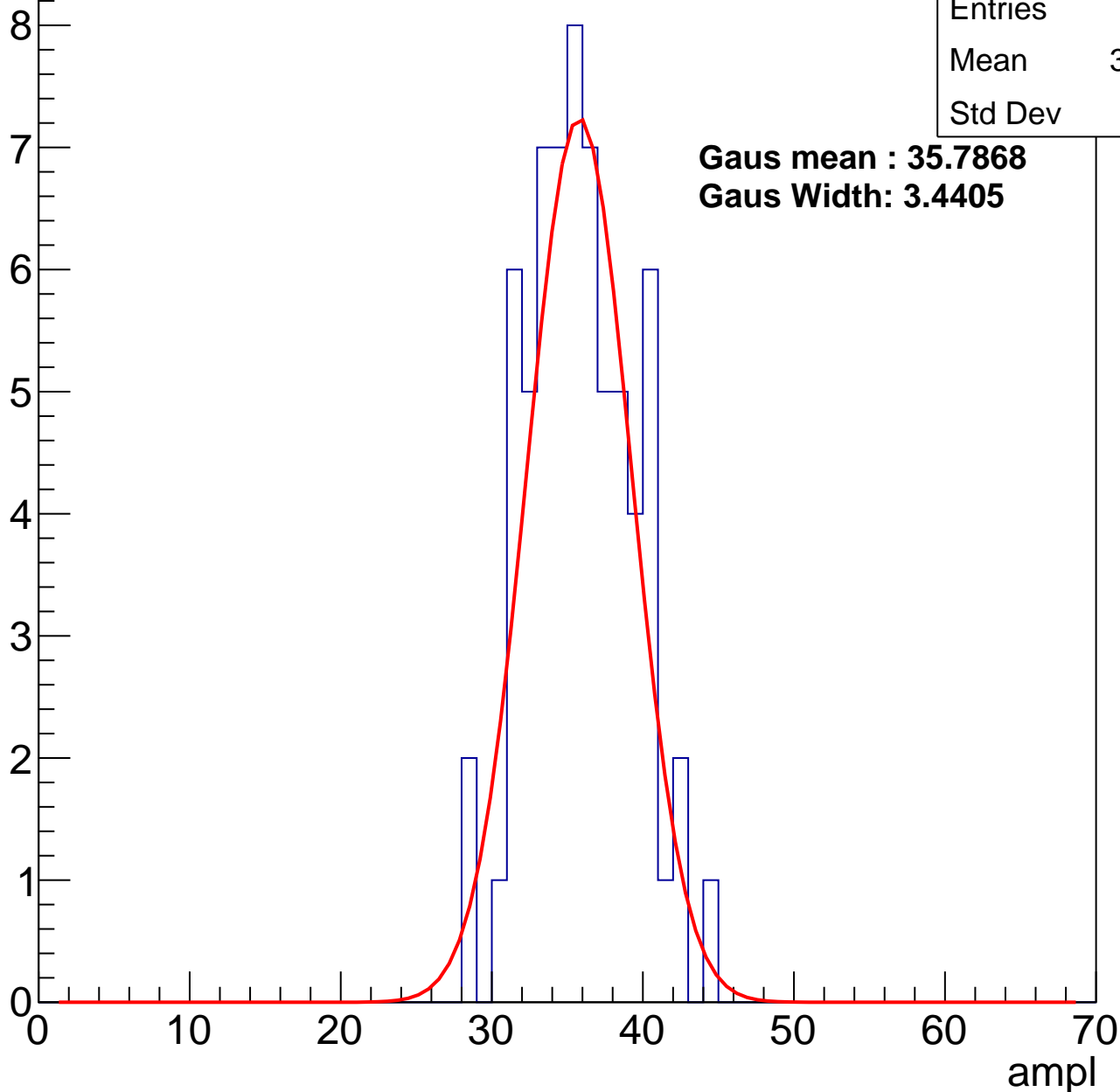
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.42
Std Dev	3.43

Gaus mean : 35.7868

Gaus Width: 3.4405



B1L101S, U14-ch33, adc2

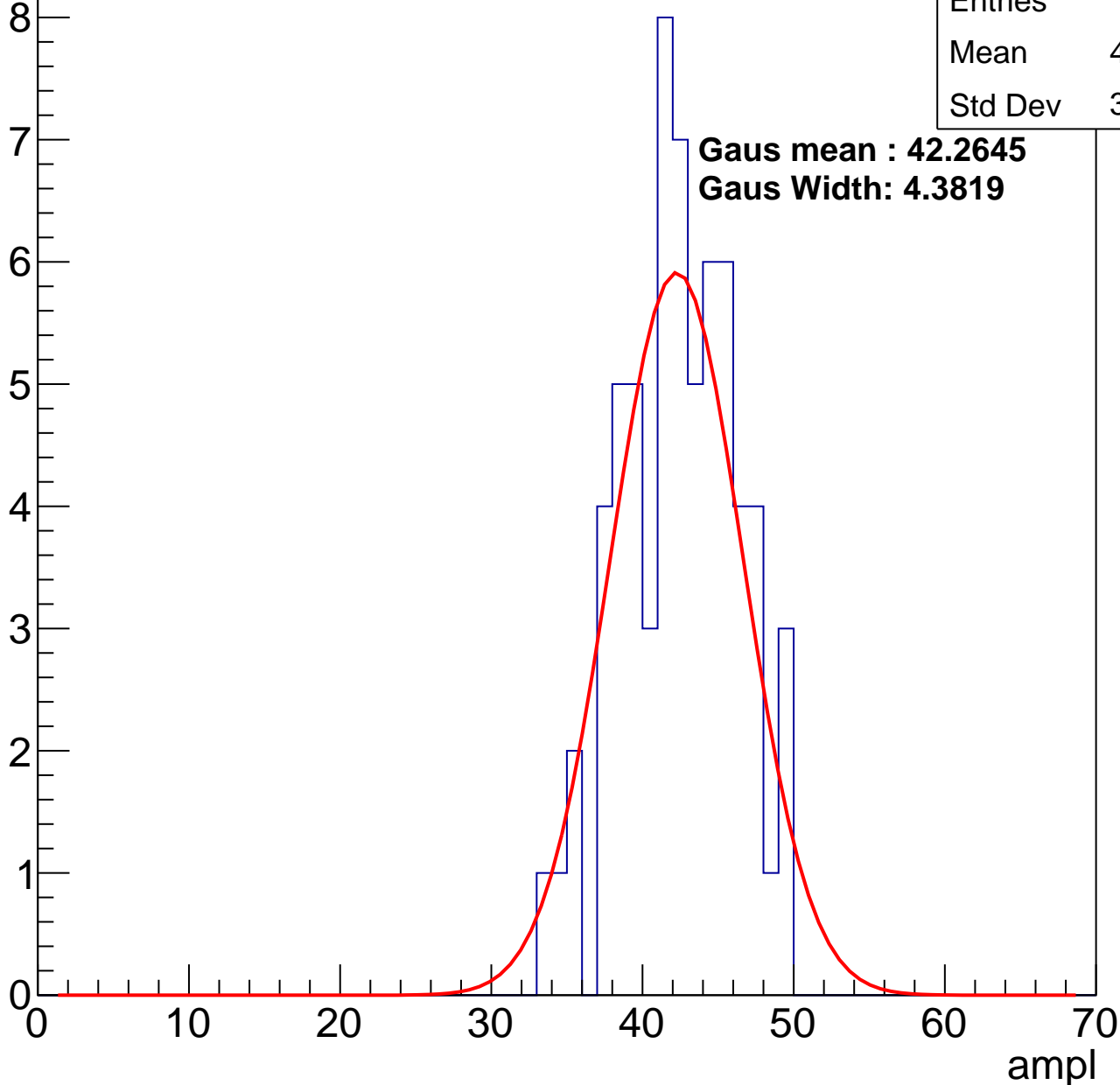
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.97
Std Dev	3.758

Gaus mean : 42.2645

Gaus Width: 4.3819

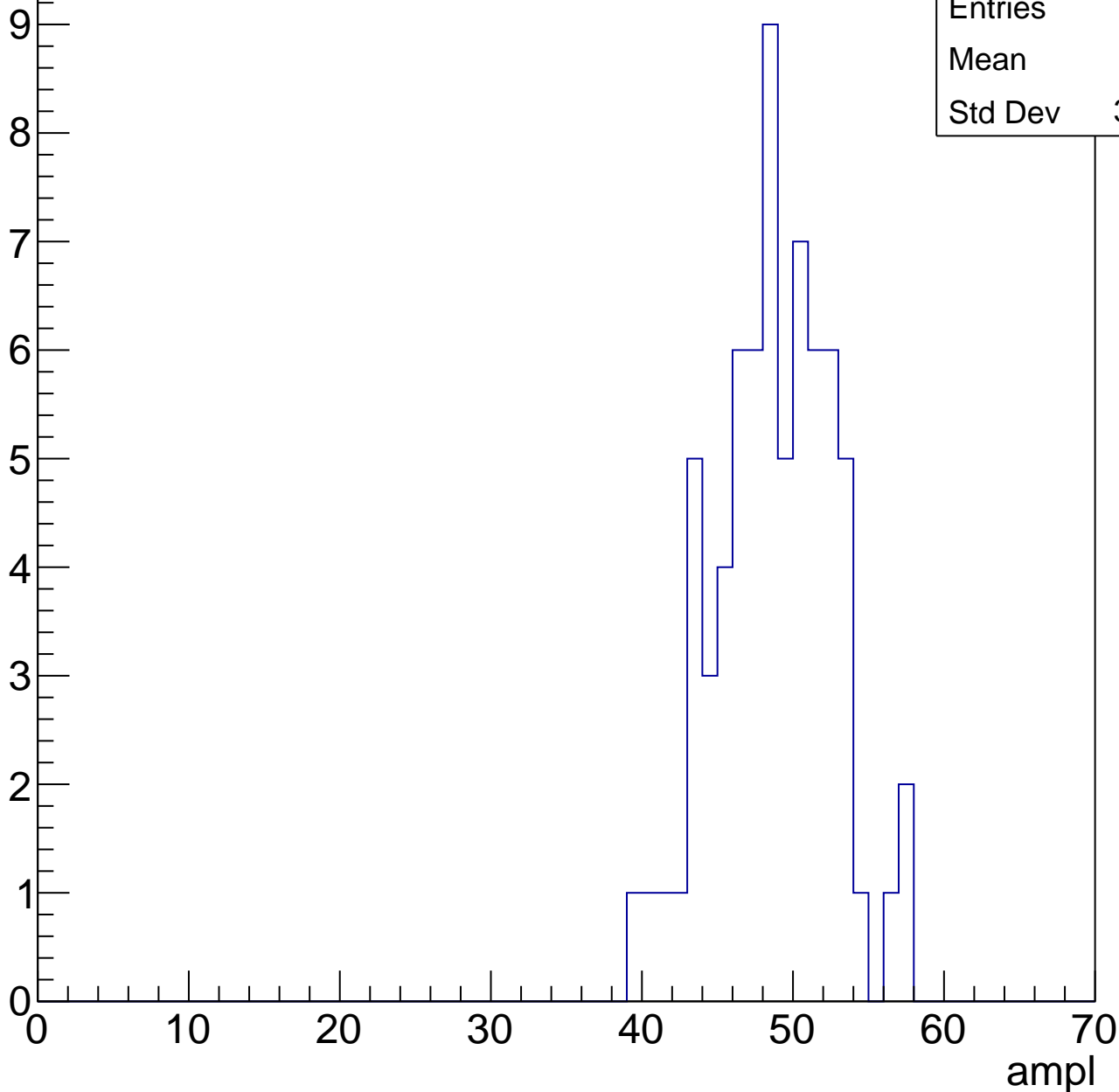


B1L101S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

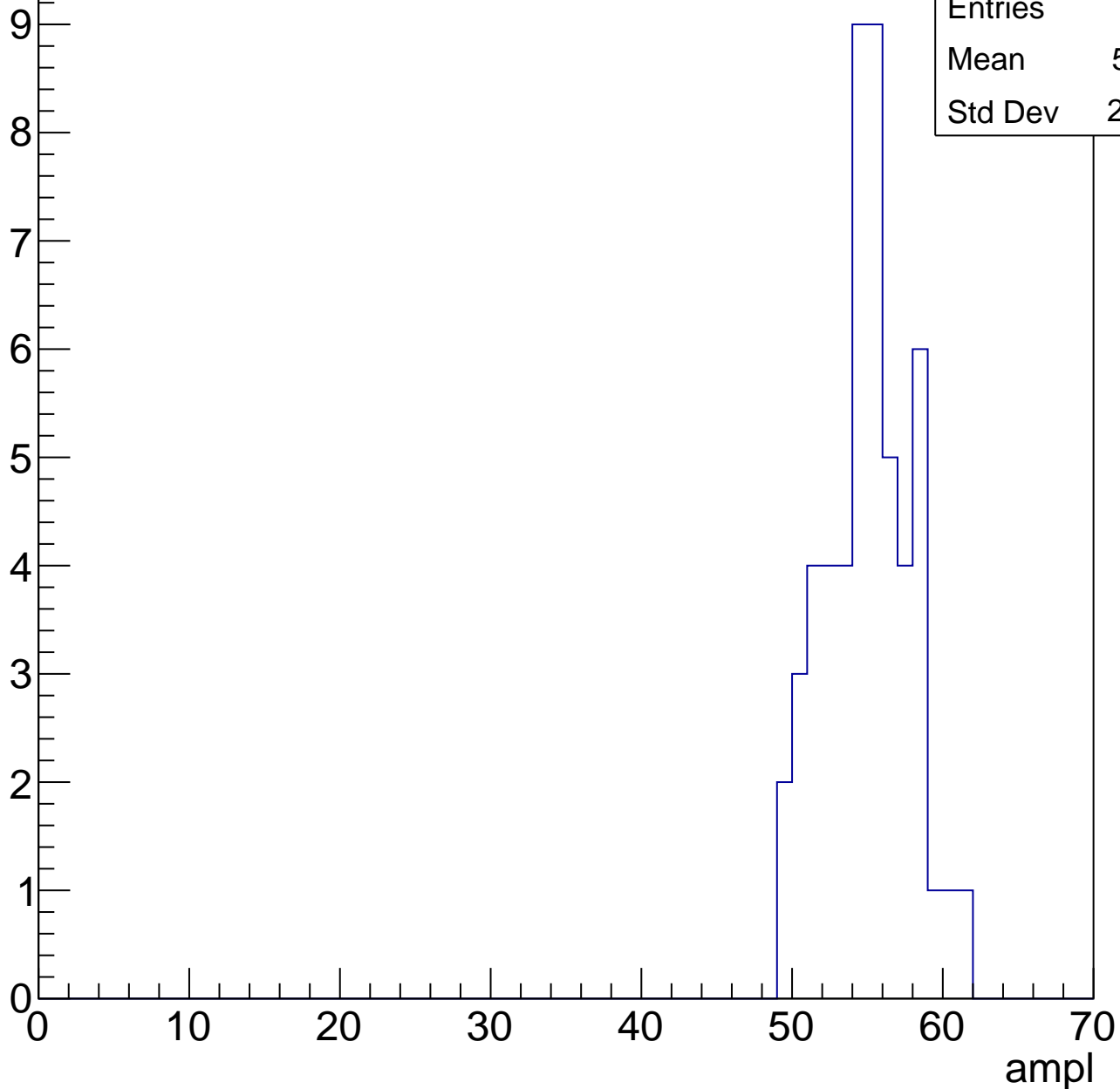
Entries	70
Mean	48.3
Std Dev	3.841



B1L101S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

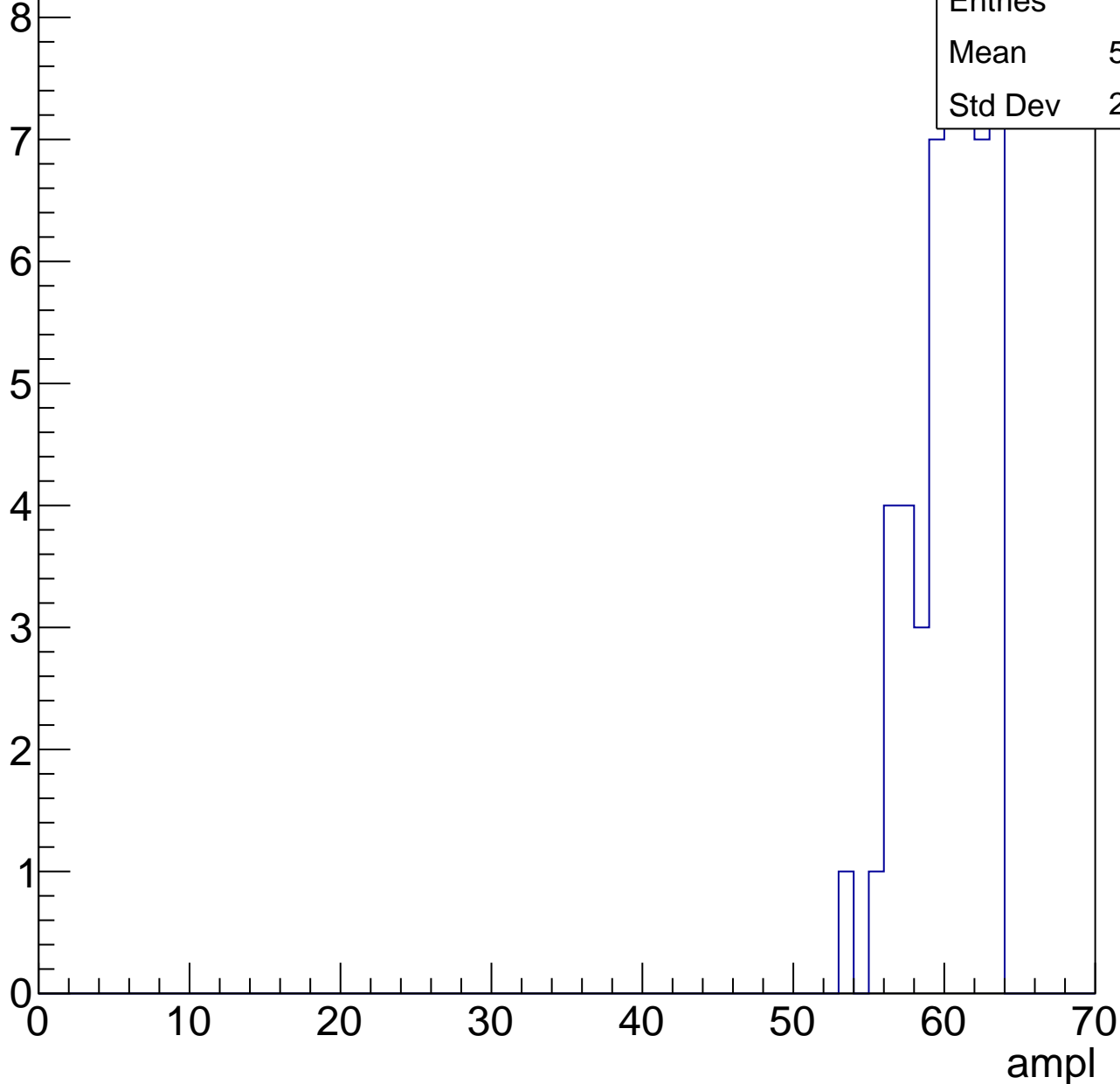


B1L101S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

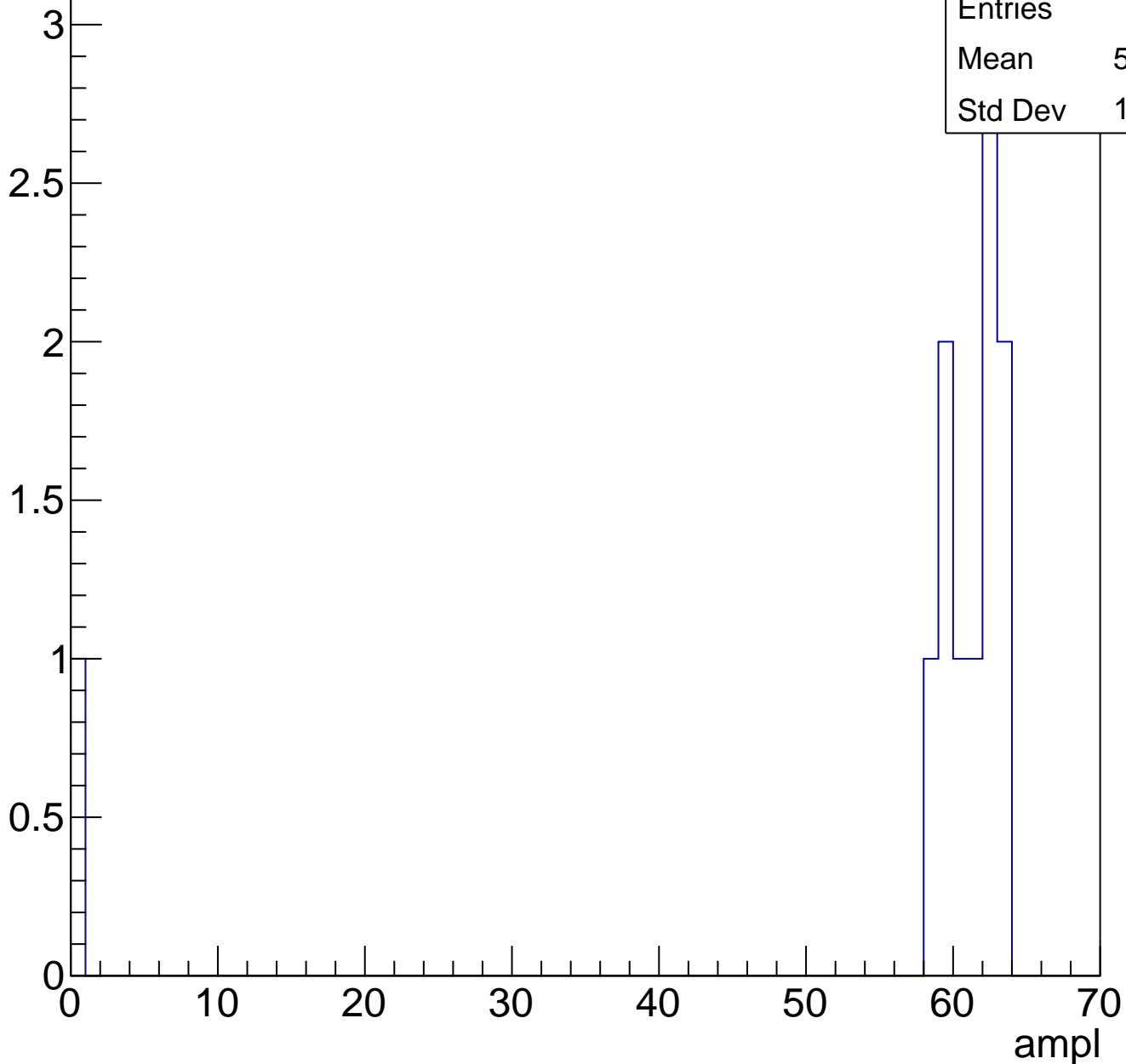
Entries	51
Mean	59.86
Std Dev	2.426



B1L101S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch34, adc0

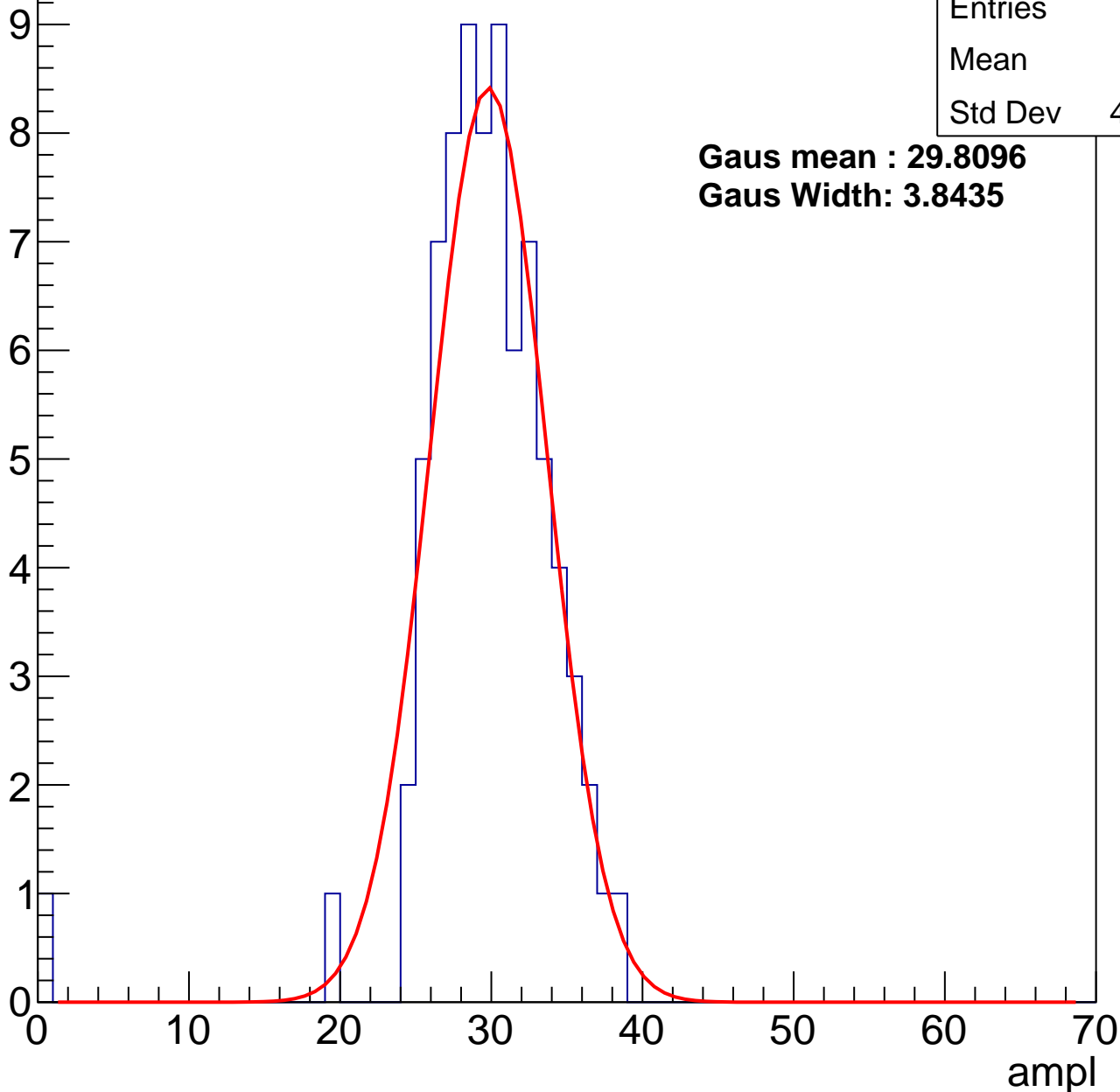
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.2
Std Dev	4.772

Gaus mean : 29.8096

Gaus Width: 3.8435



B1L101S, U14-ch34, adc1

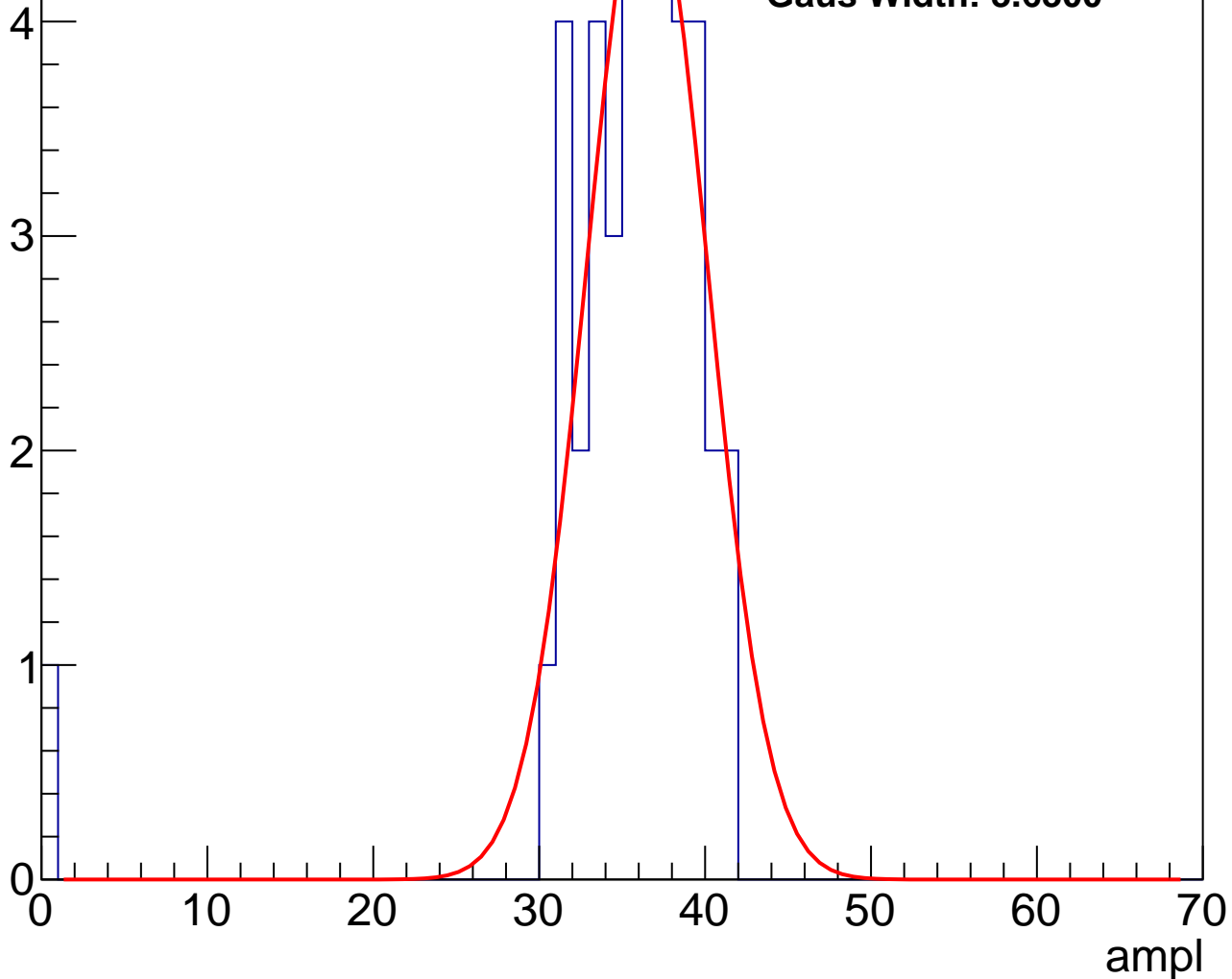
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	34.81
Std Dev	6.165

Gaus mean : 36.5024

Gaus Width: 3.6300



B1L101S, U14-ch34, adc2

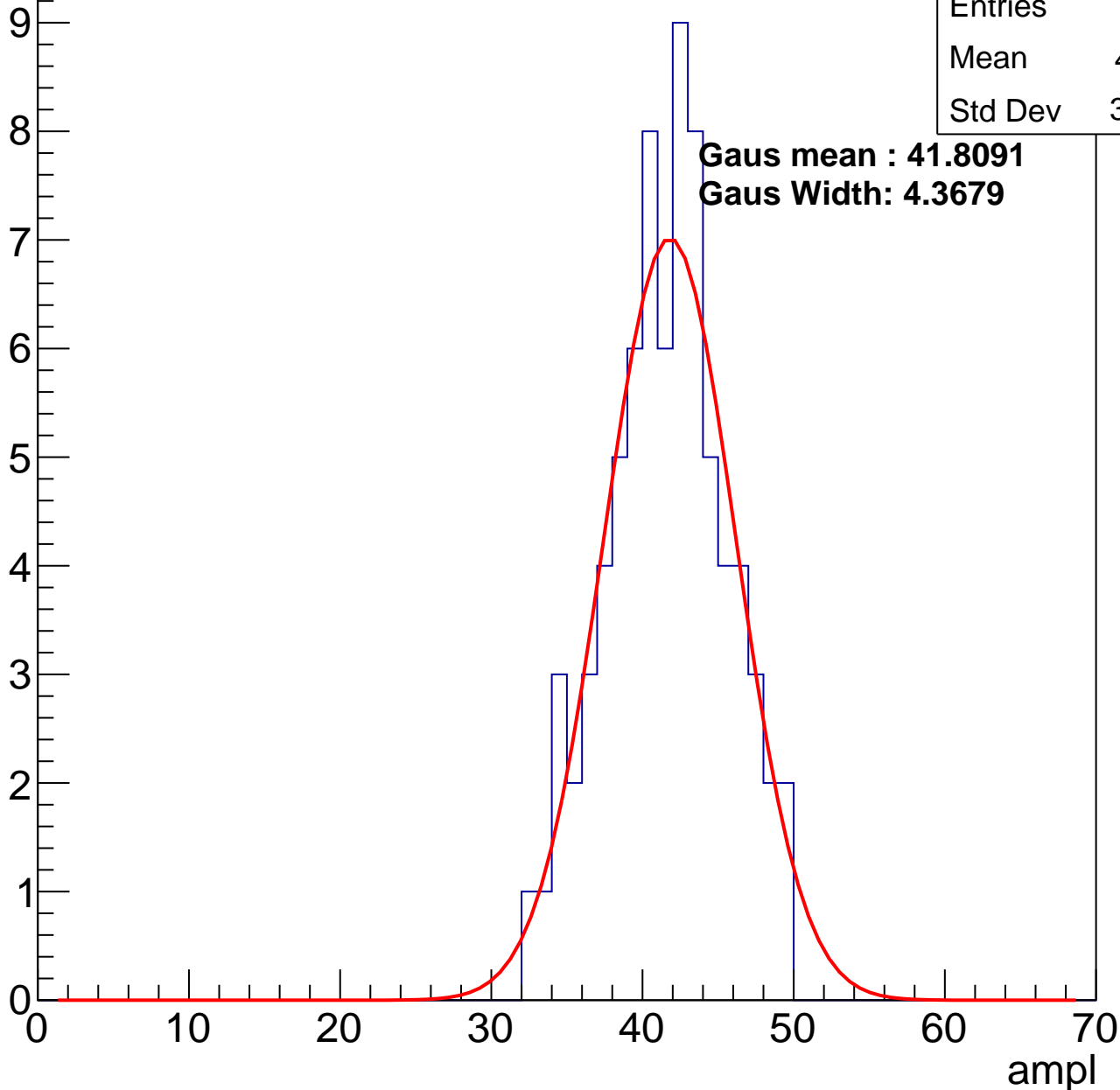
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	41.11
Std Dev	3.912

Gaus mean : 41.8091

Gaus Width: 4.3679

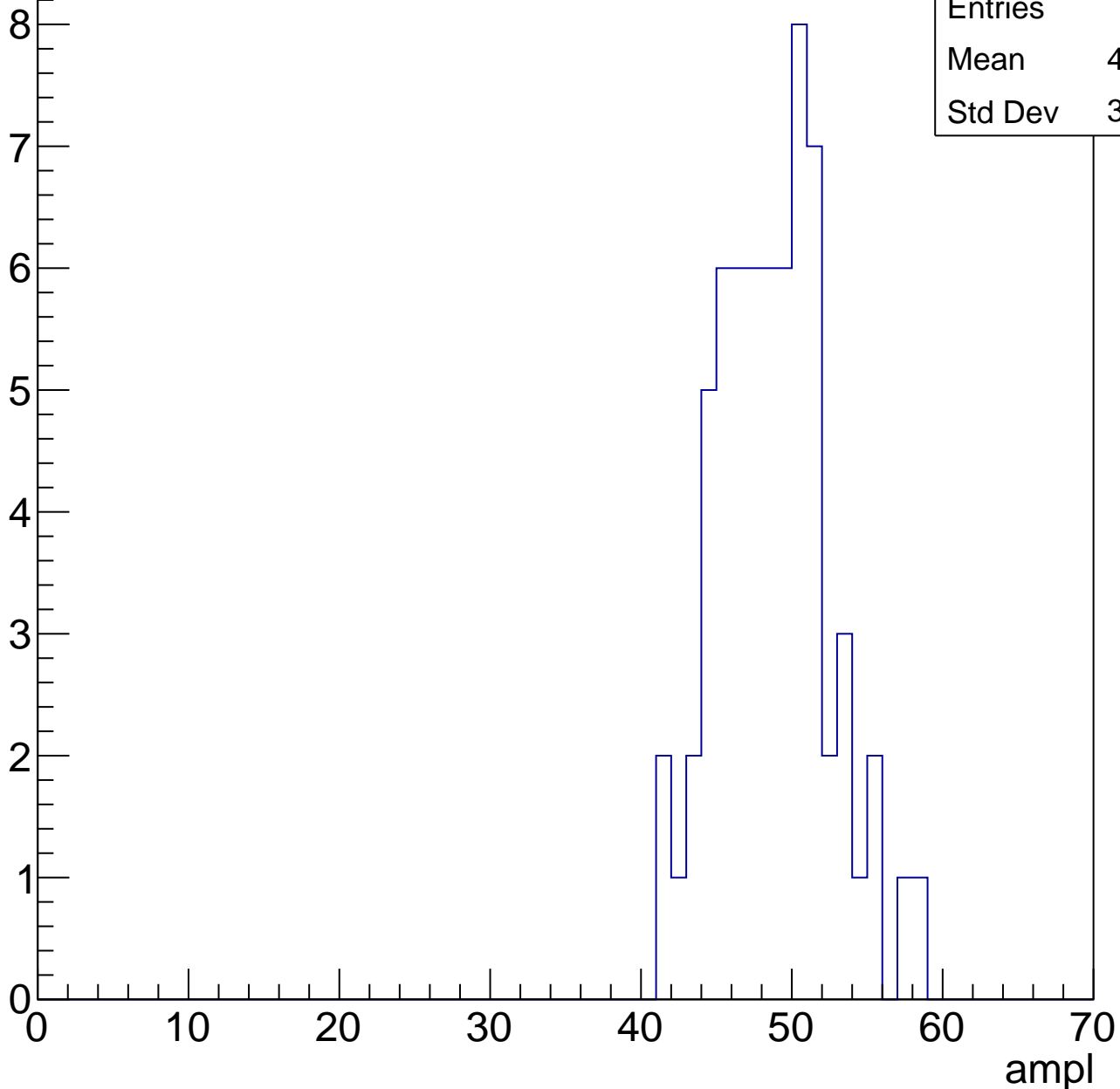


B1L101S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.29
Std Dev	3.649

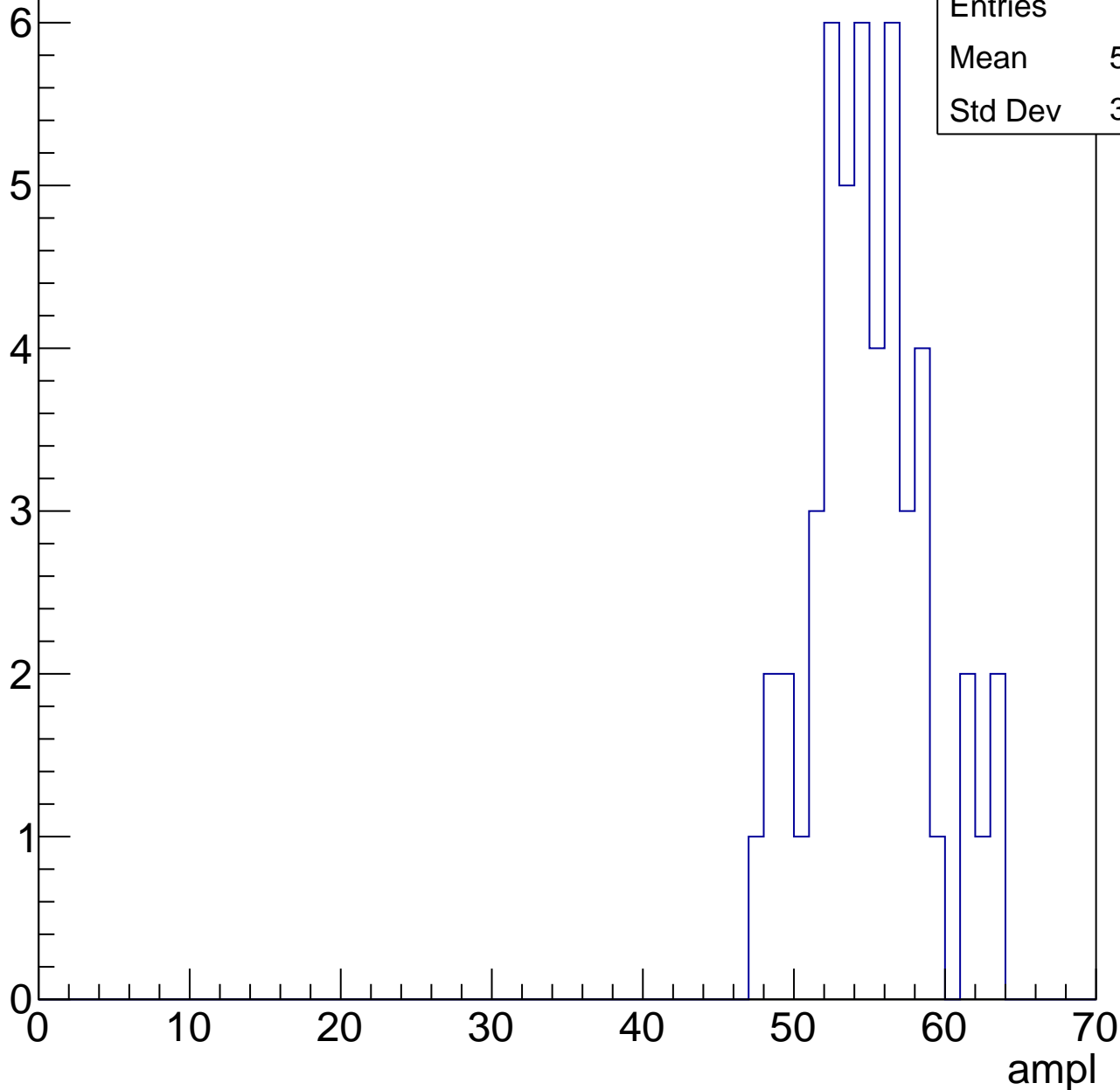


B1L101S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	54.55
Std Dev	3.769



B1L101S, U14-ch34, adc5

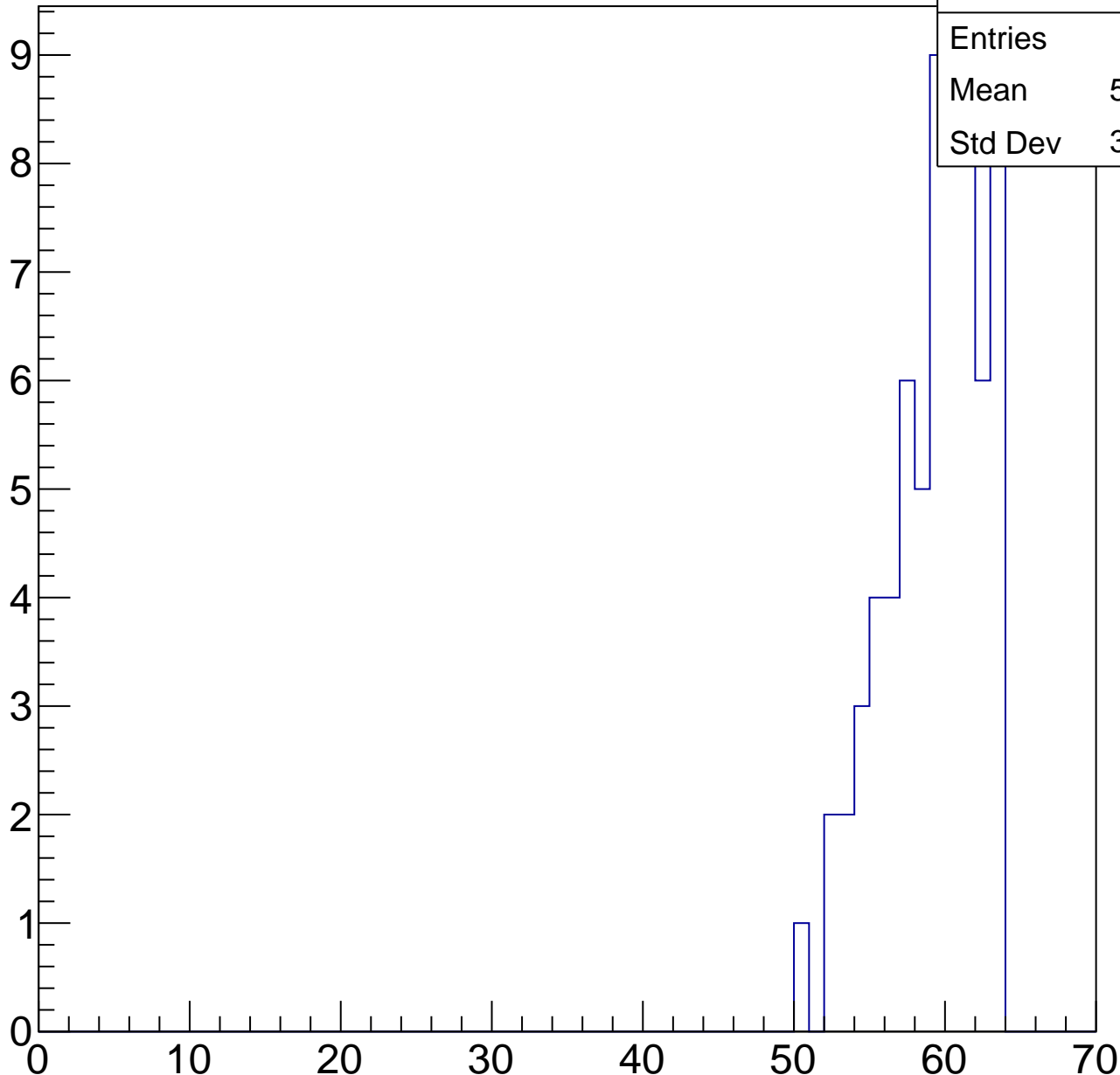
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	58.68
Std Dev	3.187

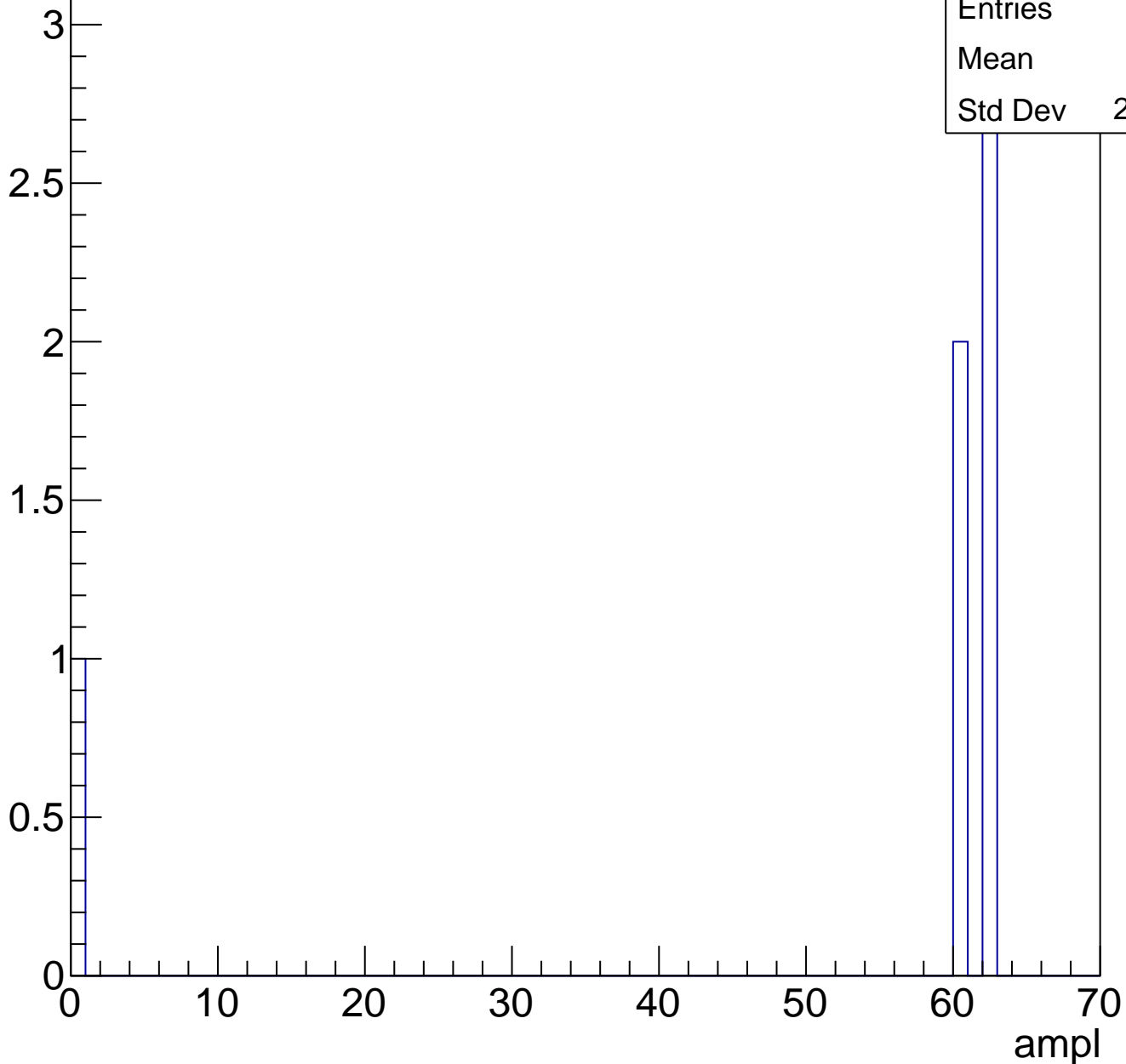
ampl



B1L101S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch35, adc0

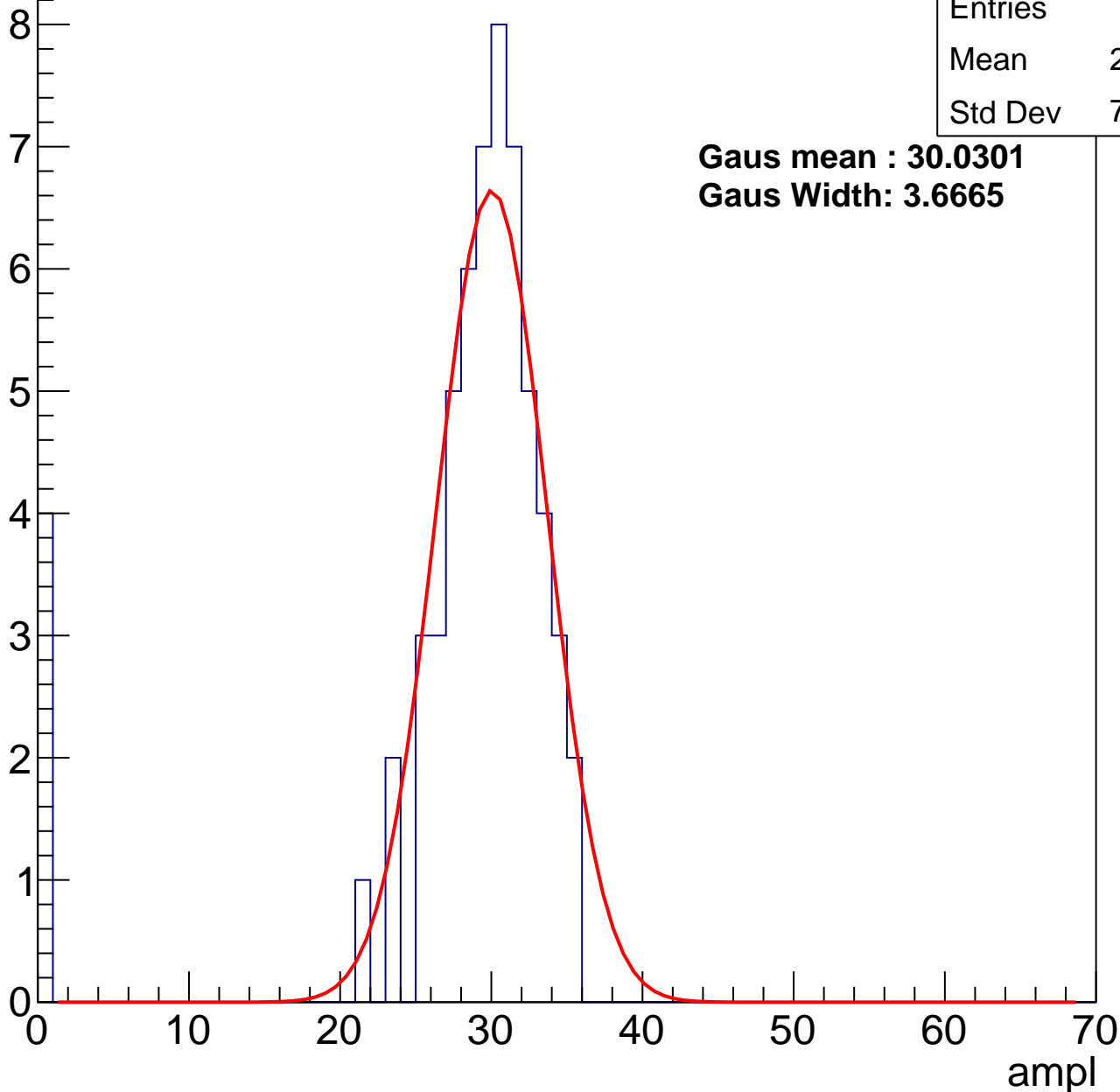
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	27.45
Std Dev	7.904

Gaus mean : 30.0301

Gaus Width: 3.6665

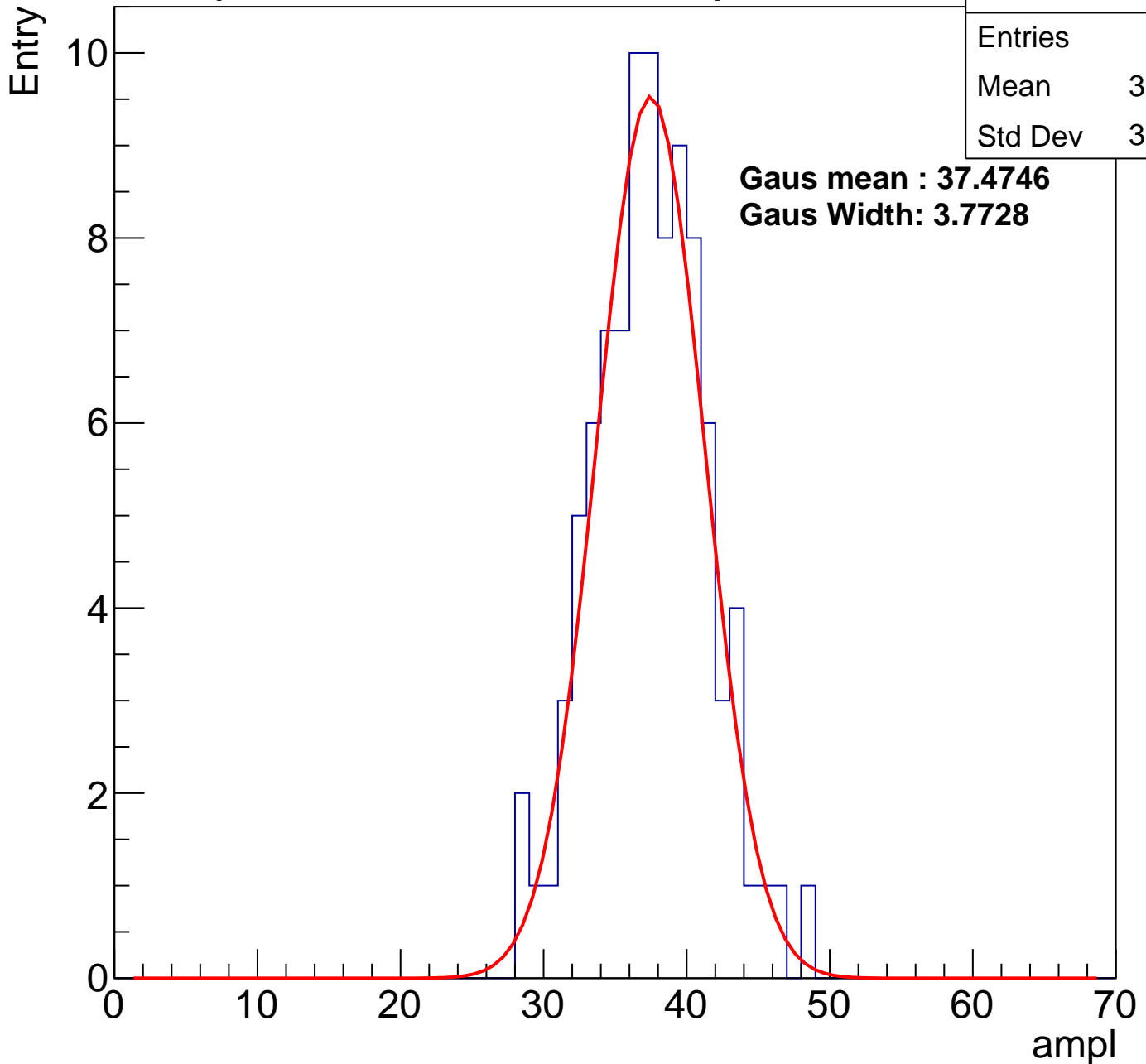


B1L101S, U14-ch35, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	94
Mean	37.03
Std Dev	3.929

Gaus mean : 37.4746
Gaus Width: 3.7728



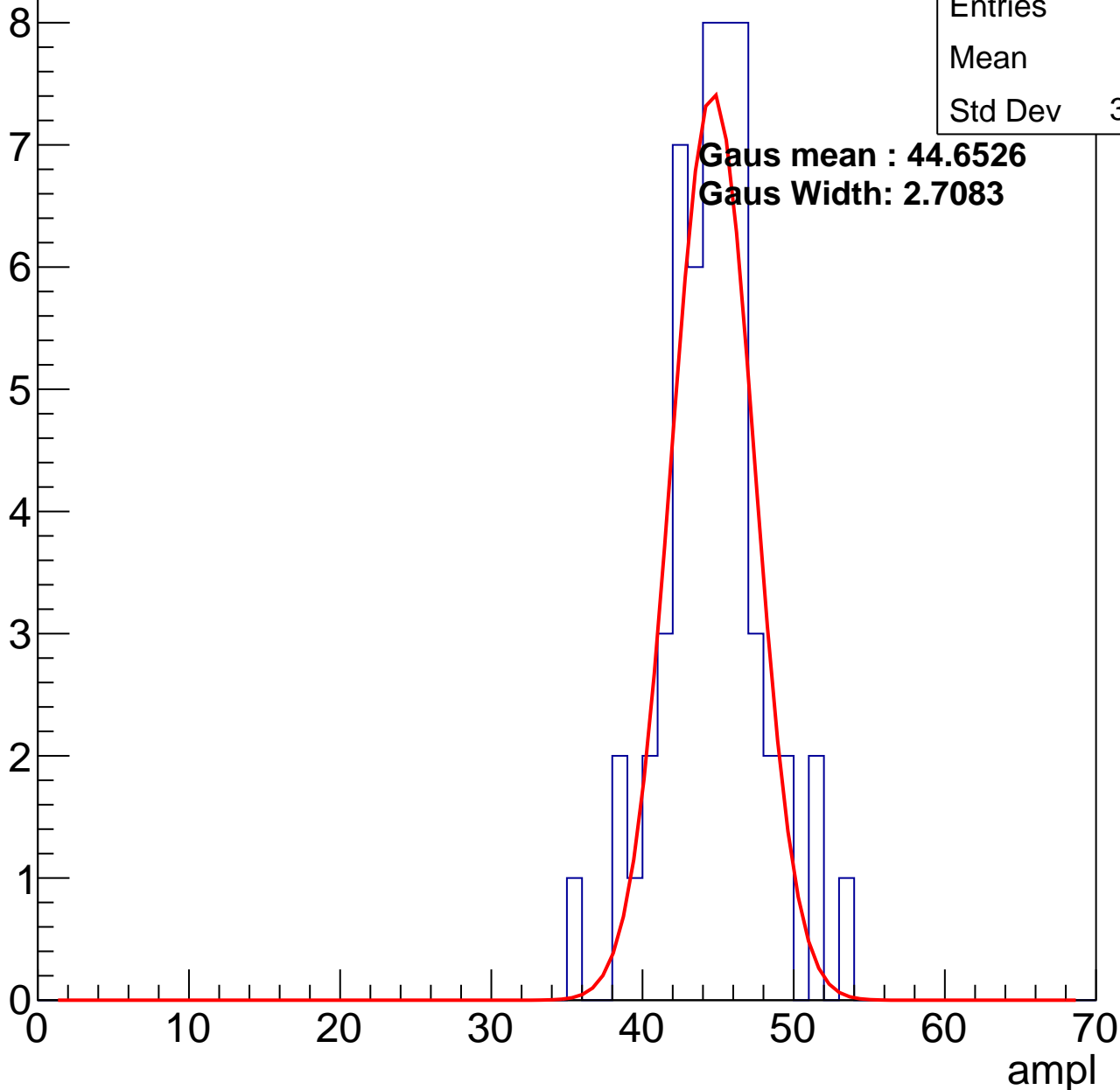
B1L101S, U14-ch35, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	44.2
Std Dev	3.265

Gaus mean : 44.6526
Gaus Width: 2.7083

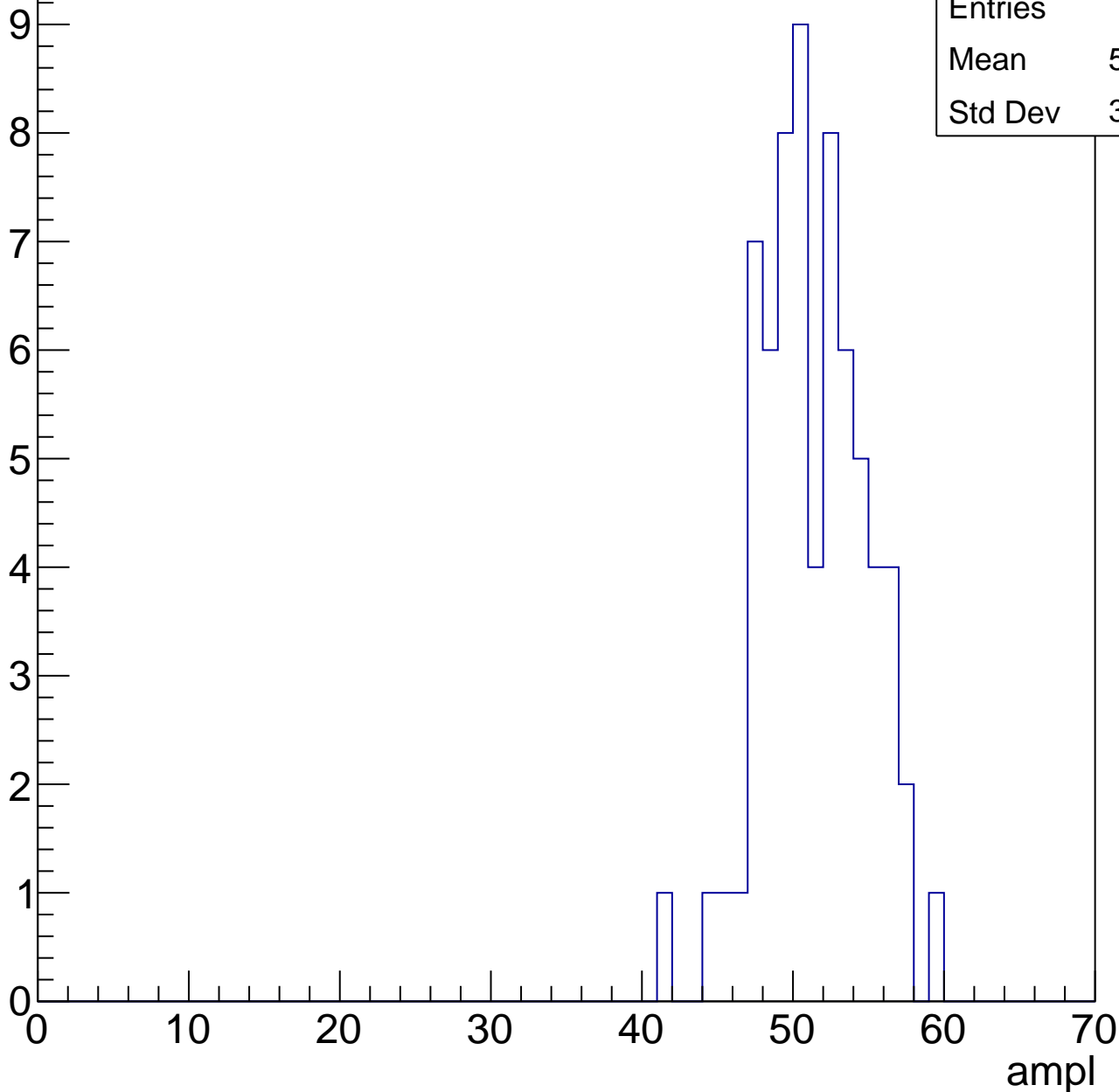


B1L101S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	50.88
Std Dev	3.415

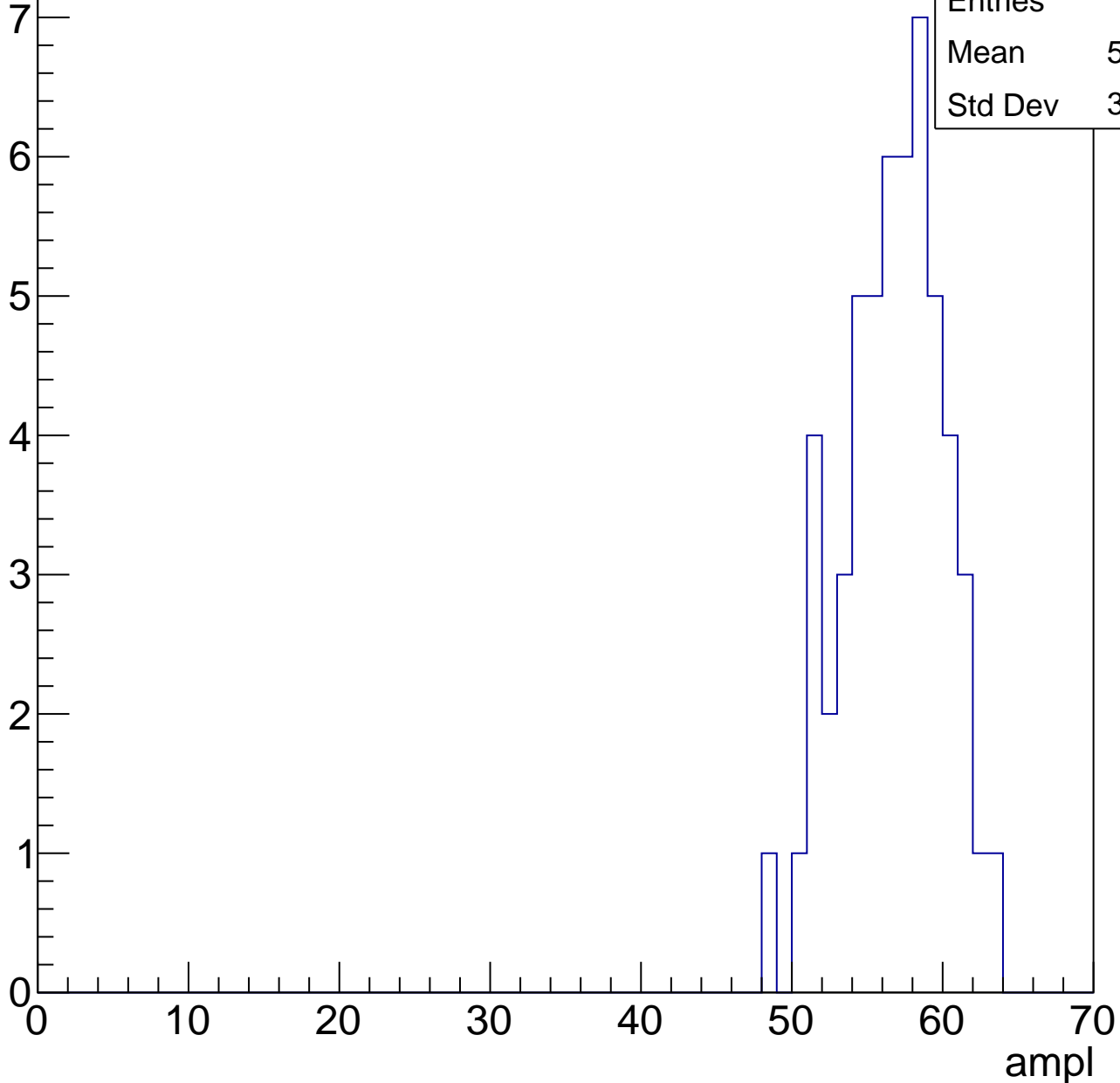


B1L101S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.24
Std Dev	3.288

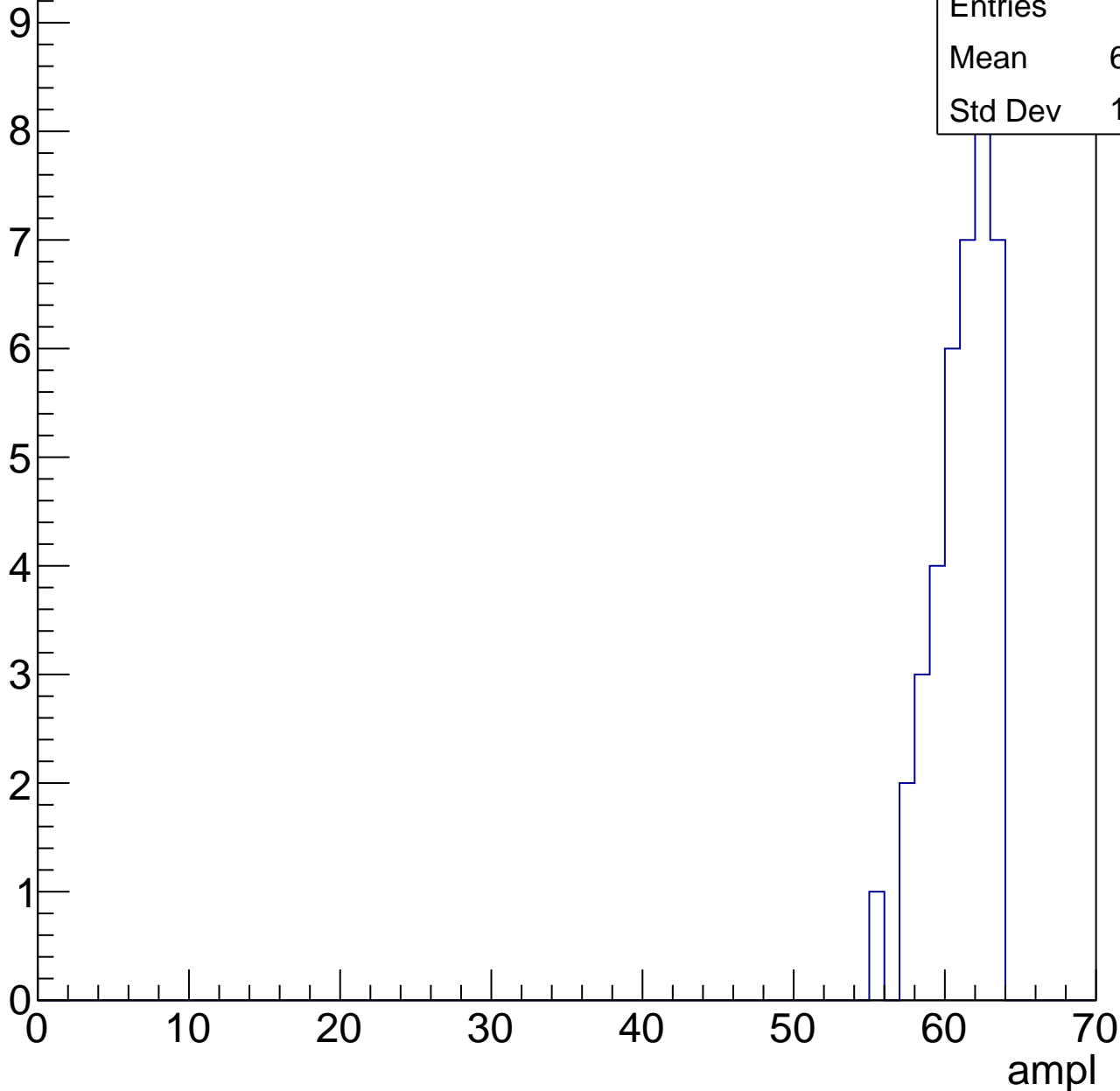


B1L101S, U14-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	60.64
Std Dev	1.954



B1L101S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch36, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	31.01
Std Dev	3.969

Gaus mean : 31.7200

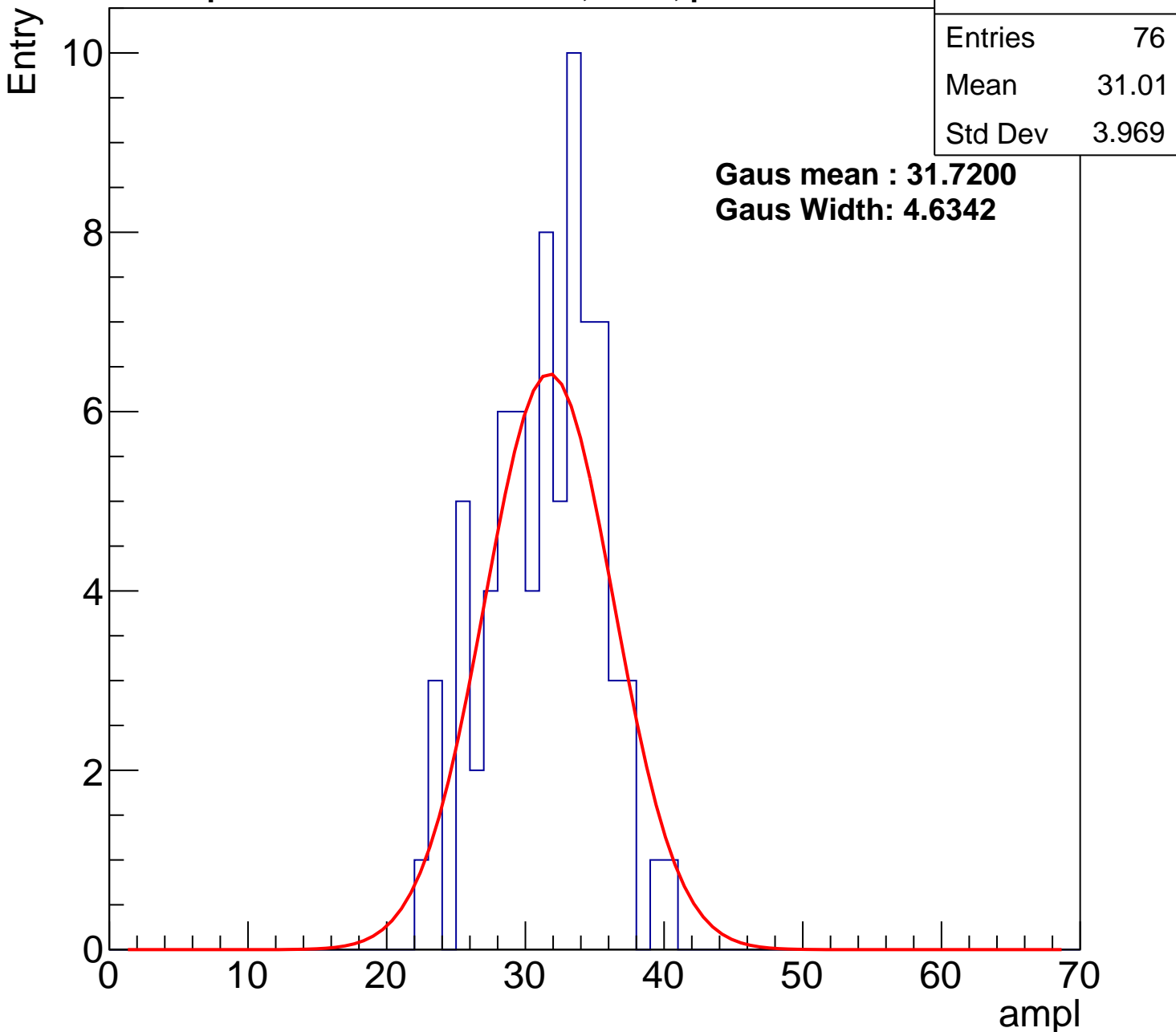
Gaus Width: 4.6342

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



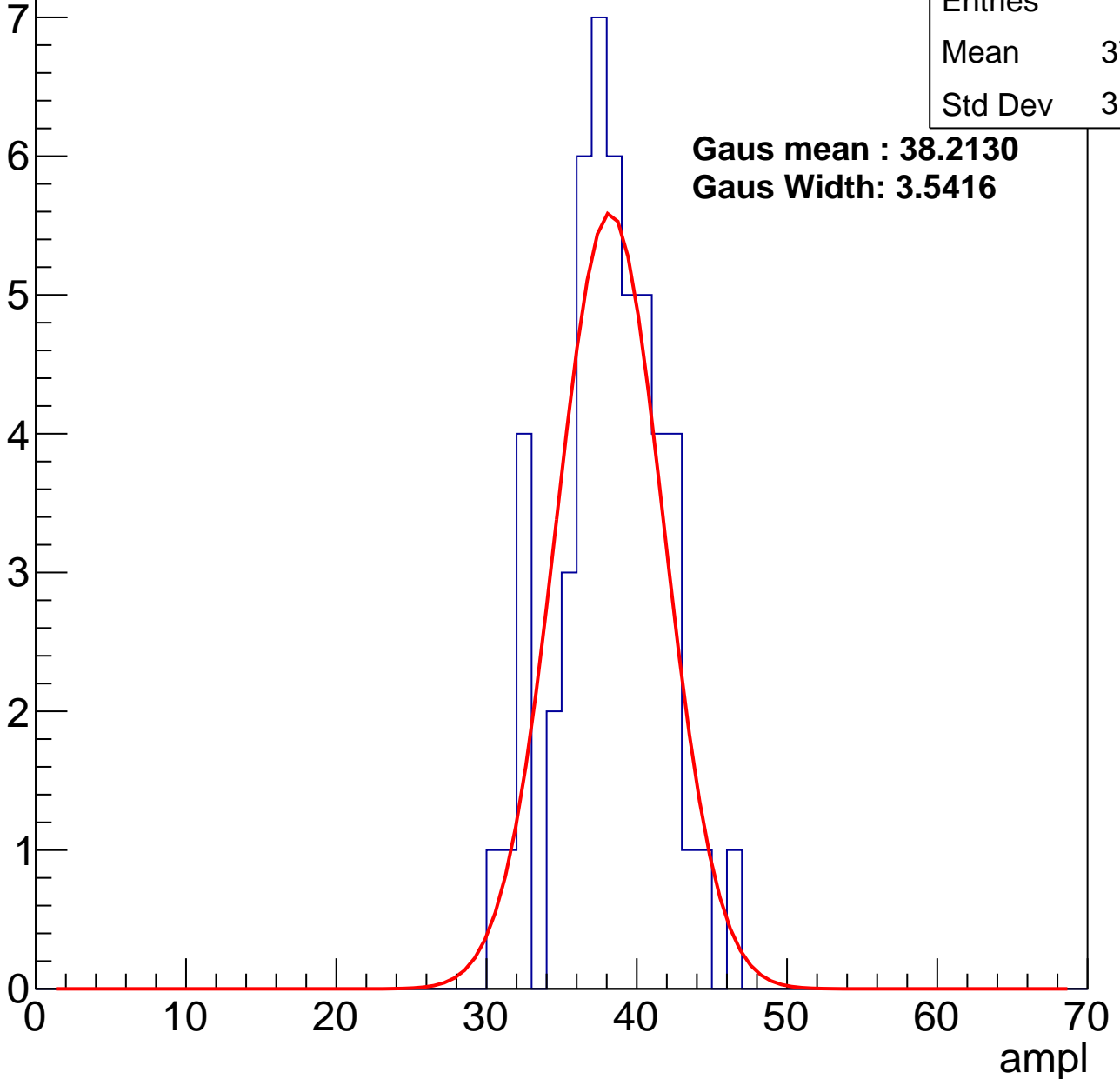
B1L101S, U14-ch36, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	37.75
Std Dev	3.412

Gaus mean : 38.2130
Gaus Width: 3.5416



B1L101S, U14-ch36, adc2

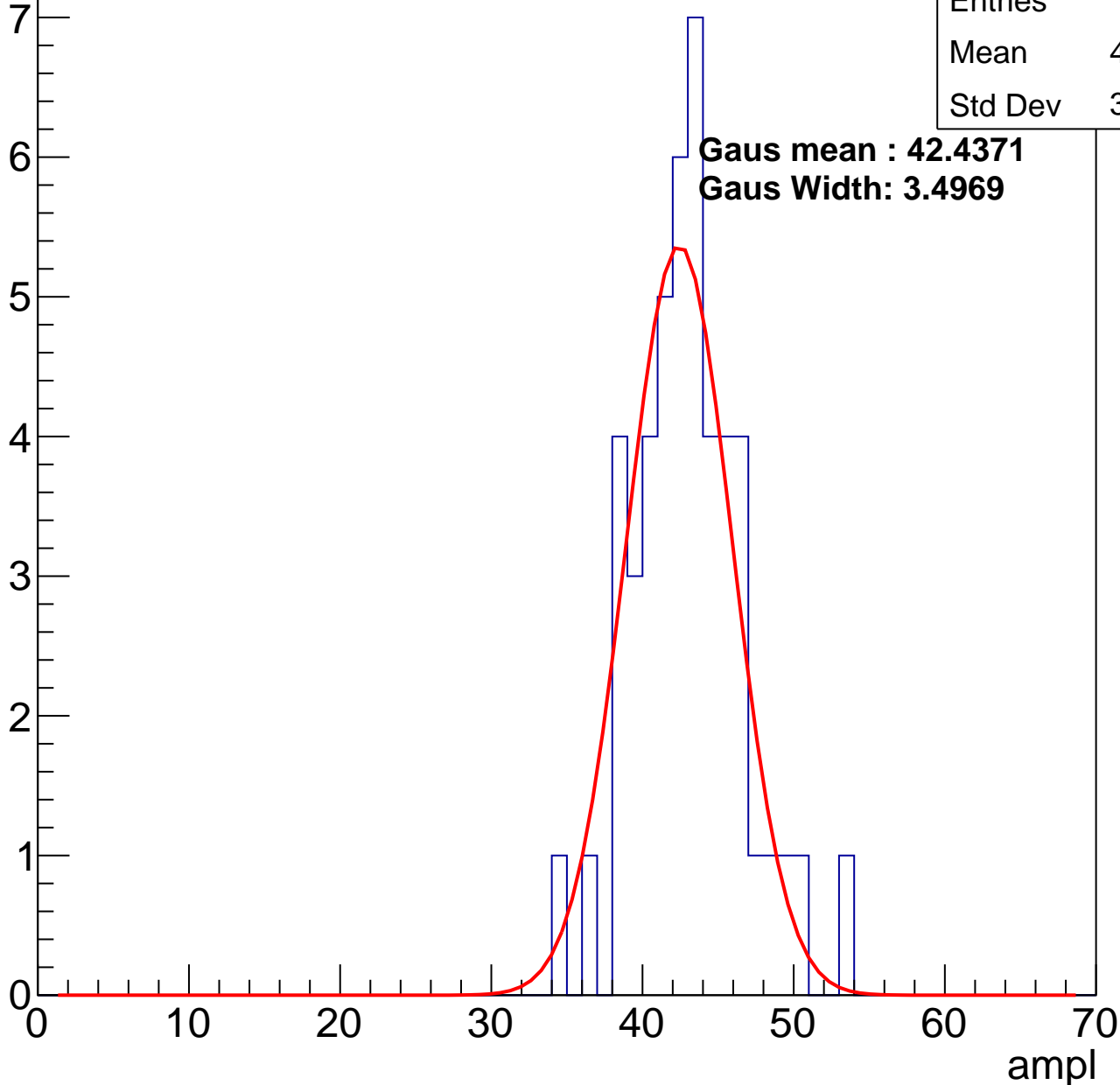
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	42.58
Std Dev	3.576

Gaus mean : 42.4371

Gaus Width: 3.4969

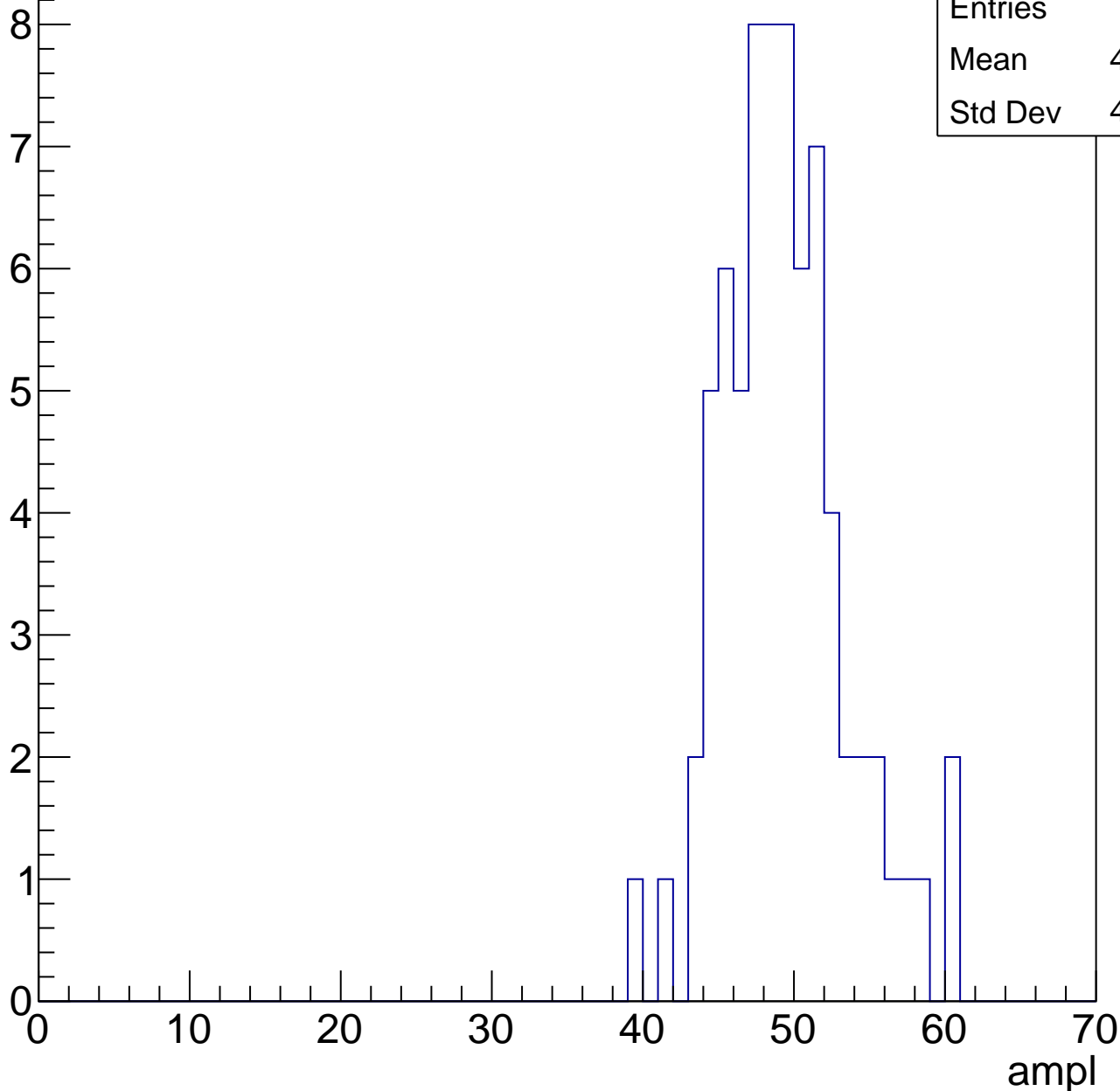


B1L101S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	48.86
Std Dev	4.084

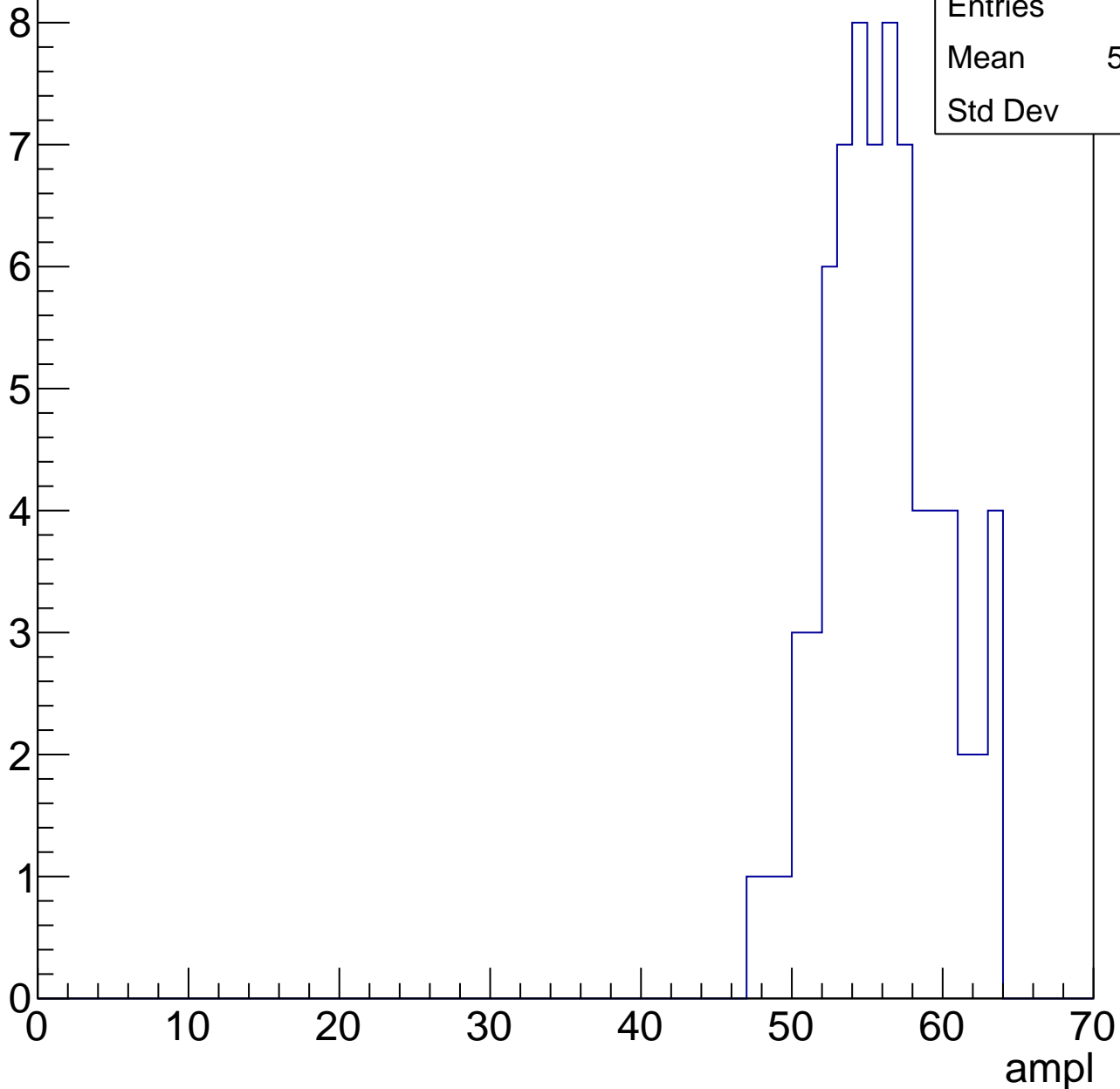


B1L101S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

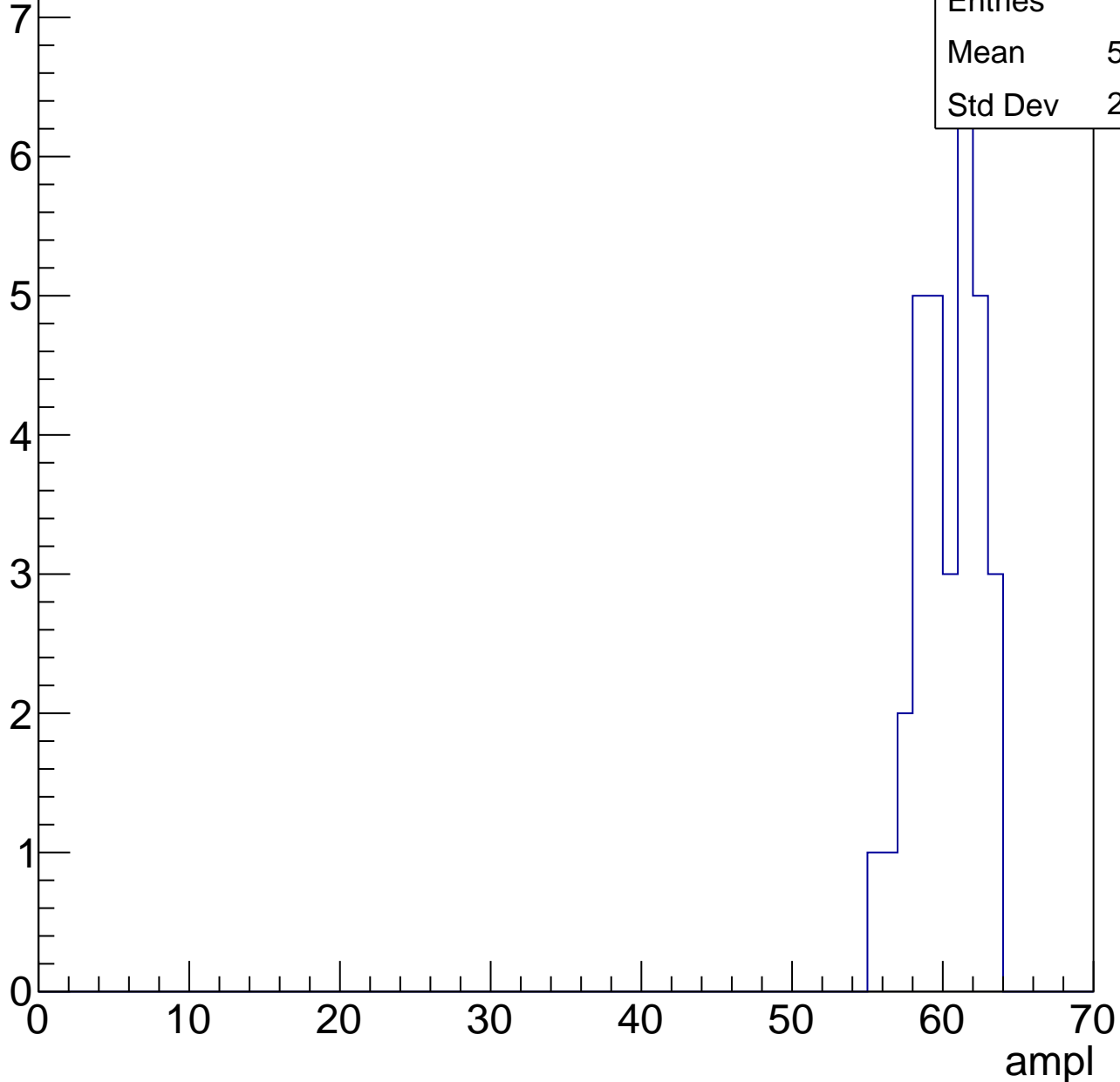
Entries	72
Mean	55.56
Std Dev	3.73



B1L101S, U14-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

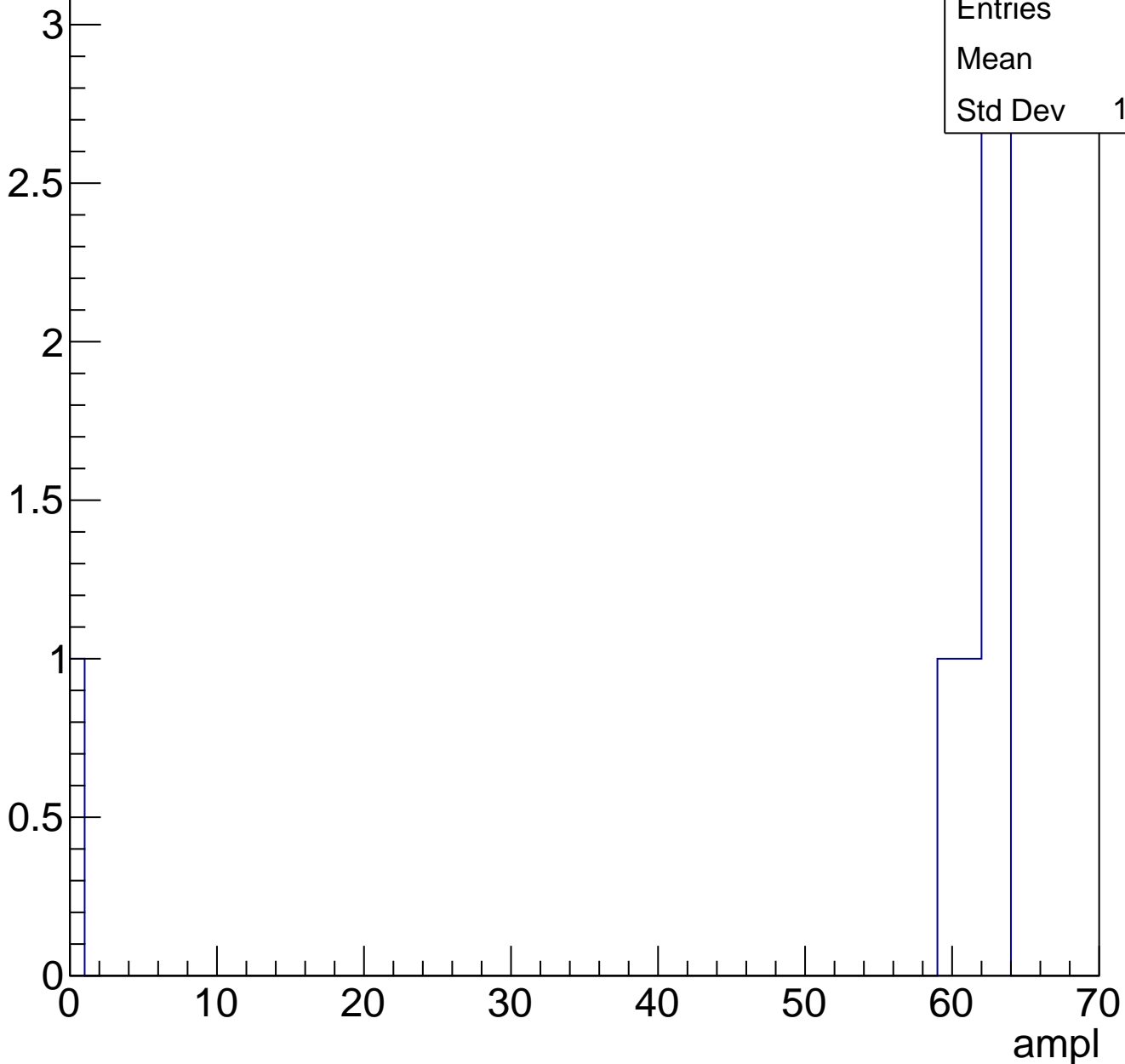


Entries	32
Mean	59.88
Std Dev	2.073

B1L101S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch37, adc0

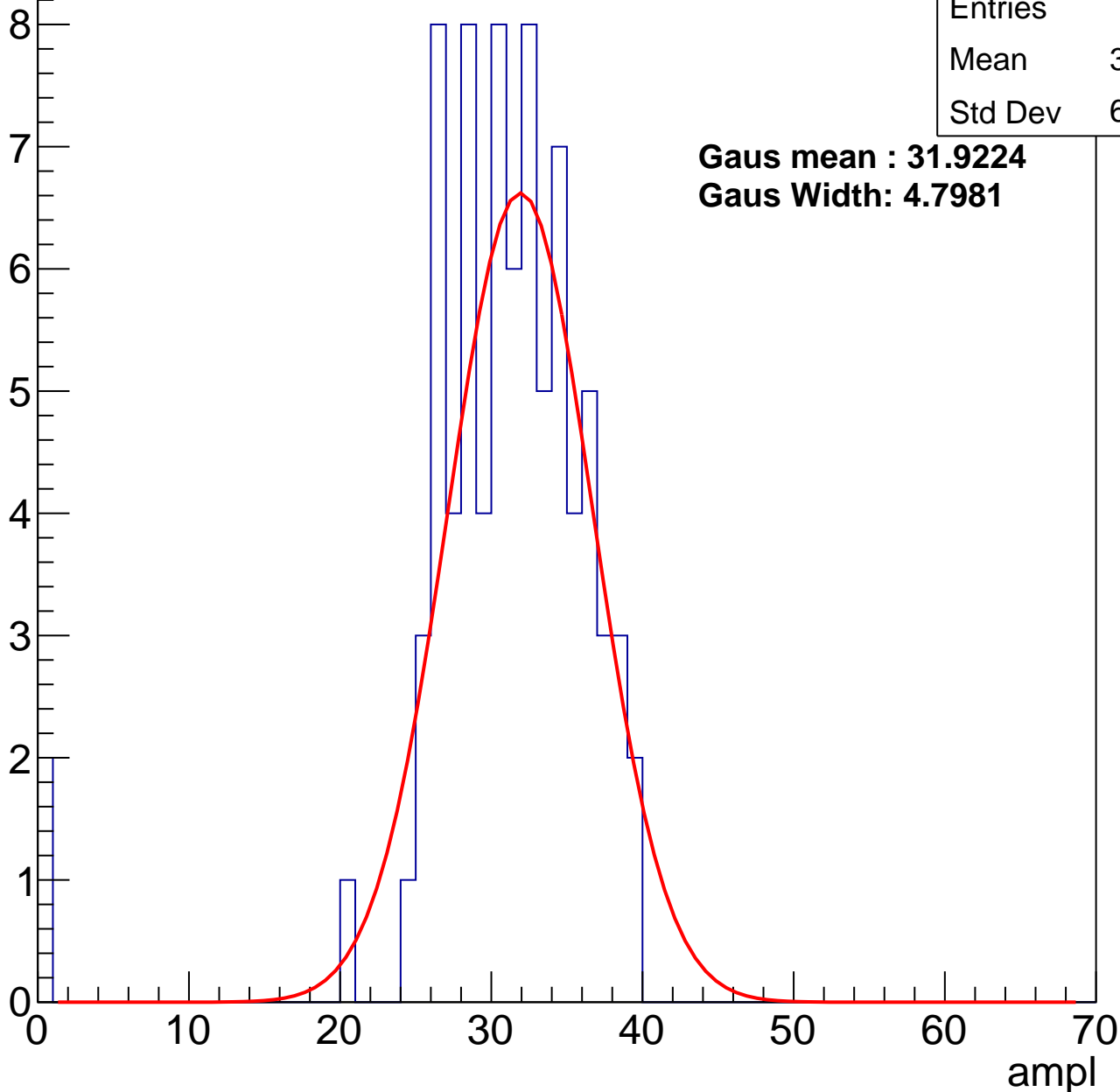
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.28
Std Dev	6.228

Gaus mean : 31.9224

Gaus Width: 4.7981



B1L101S, U14-ch37, adc1

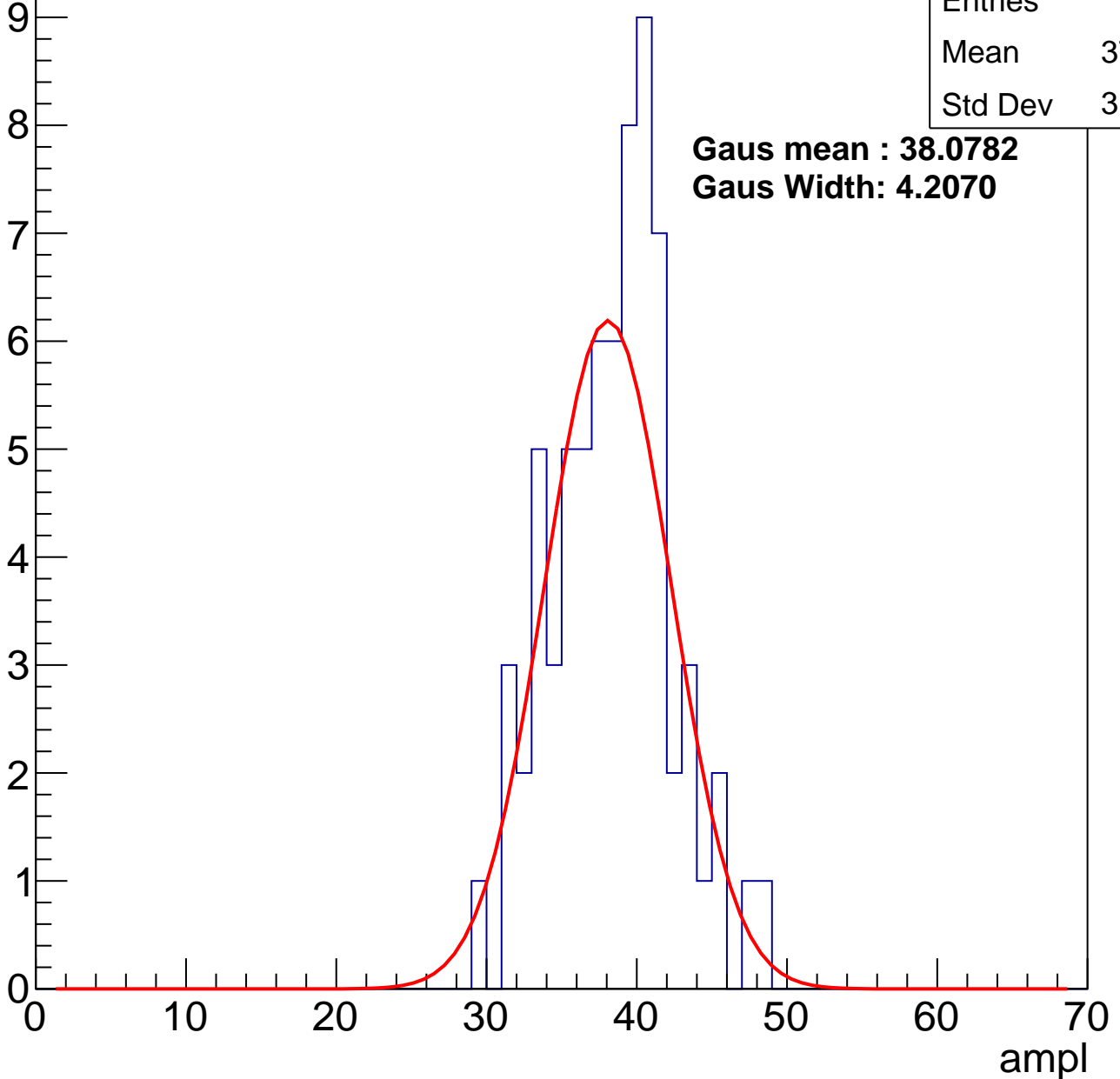
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.99
Std Dev	3.915

Gaus mean : 38.0782

Gaus Width: 4.2070



B1L101S, U14-ch37, adc2

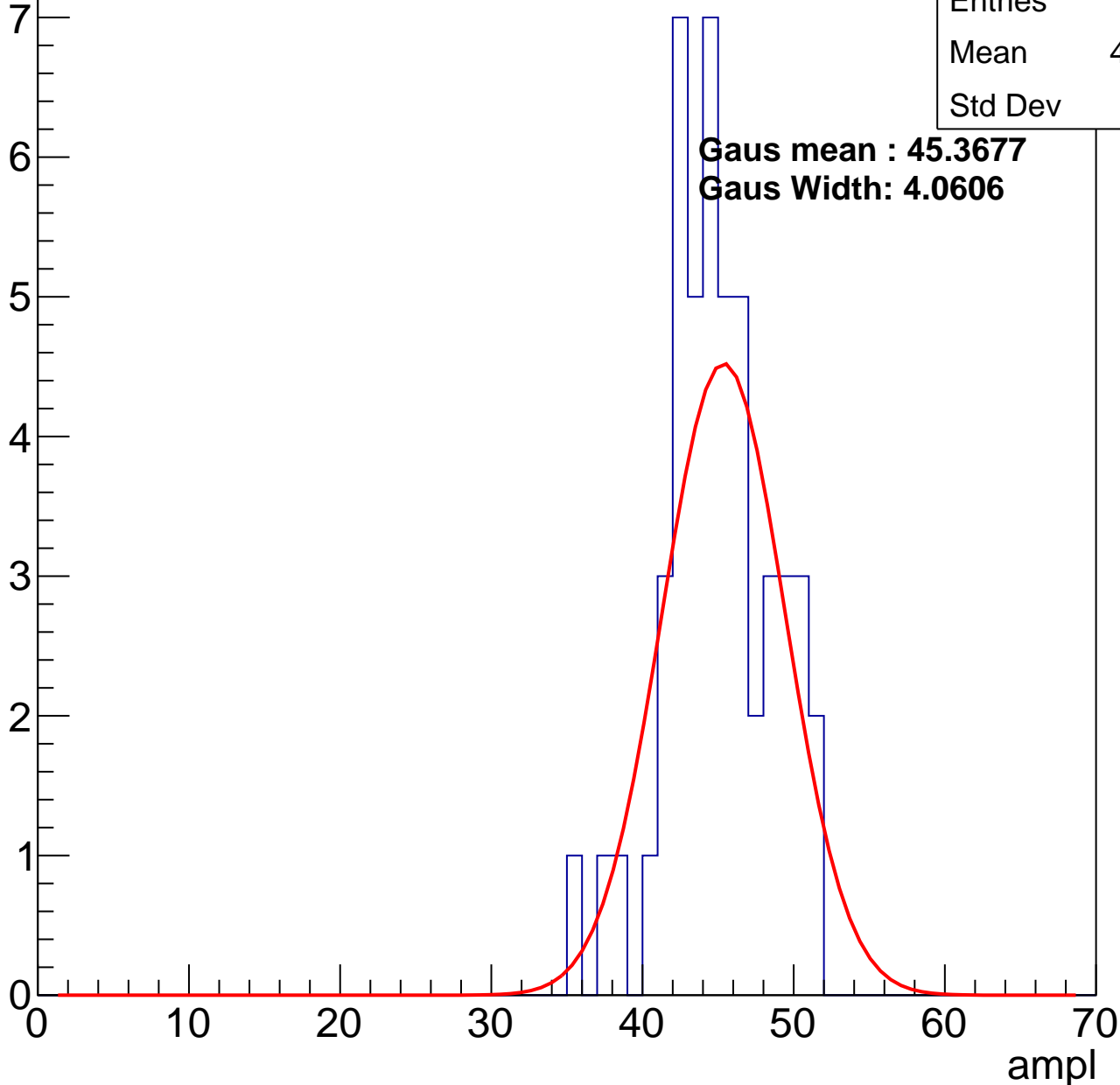
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	44.53
Std Dev	3.5

Gaus mean : 45.3677

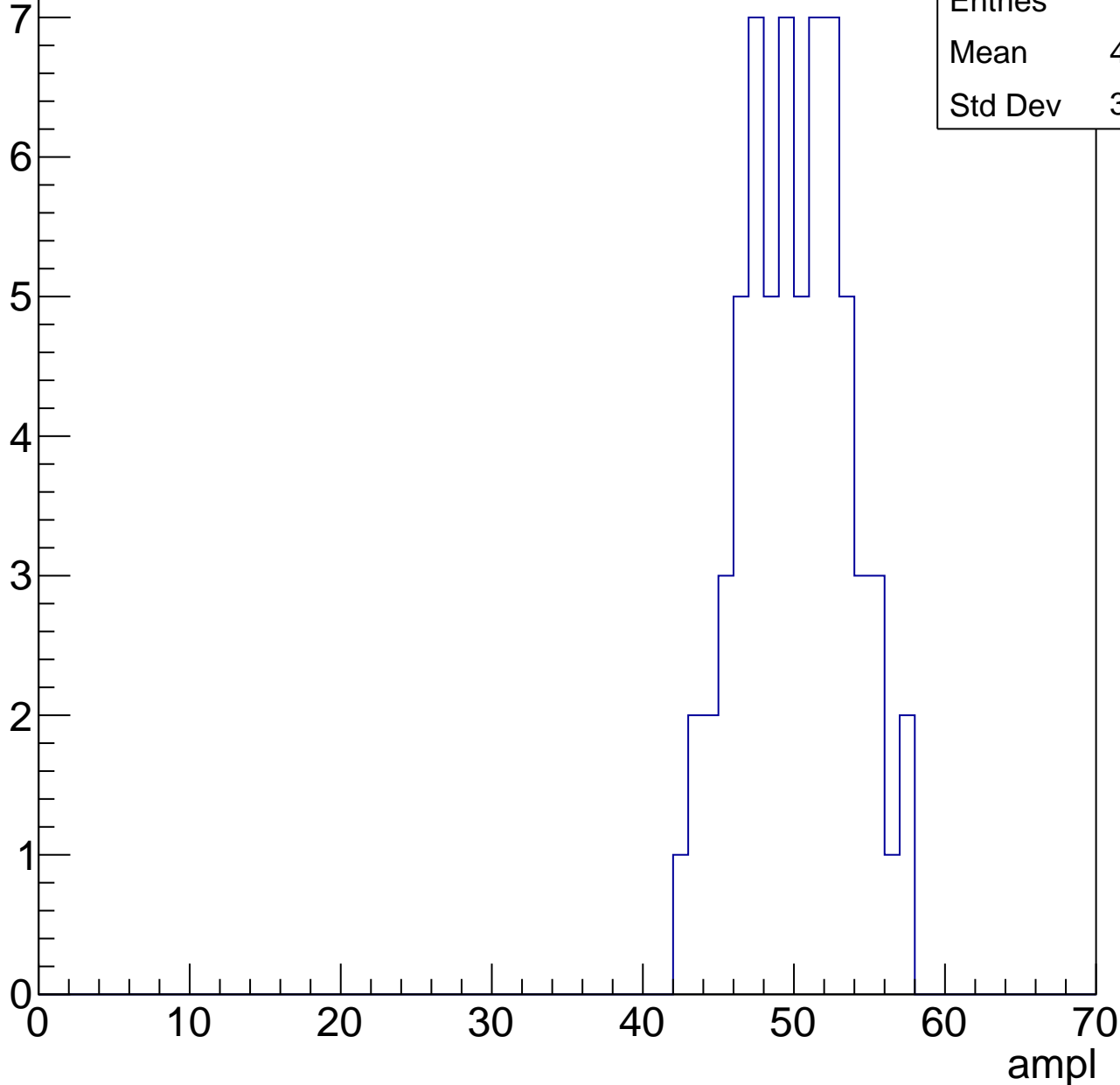
Gaus Width: 4.0606



B1L101S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



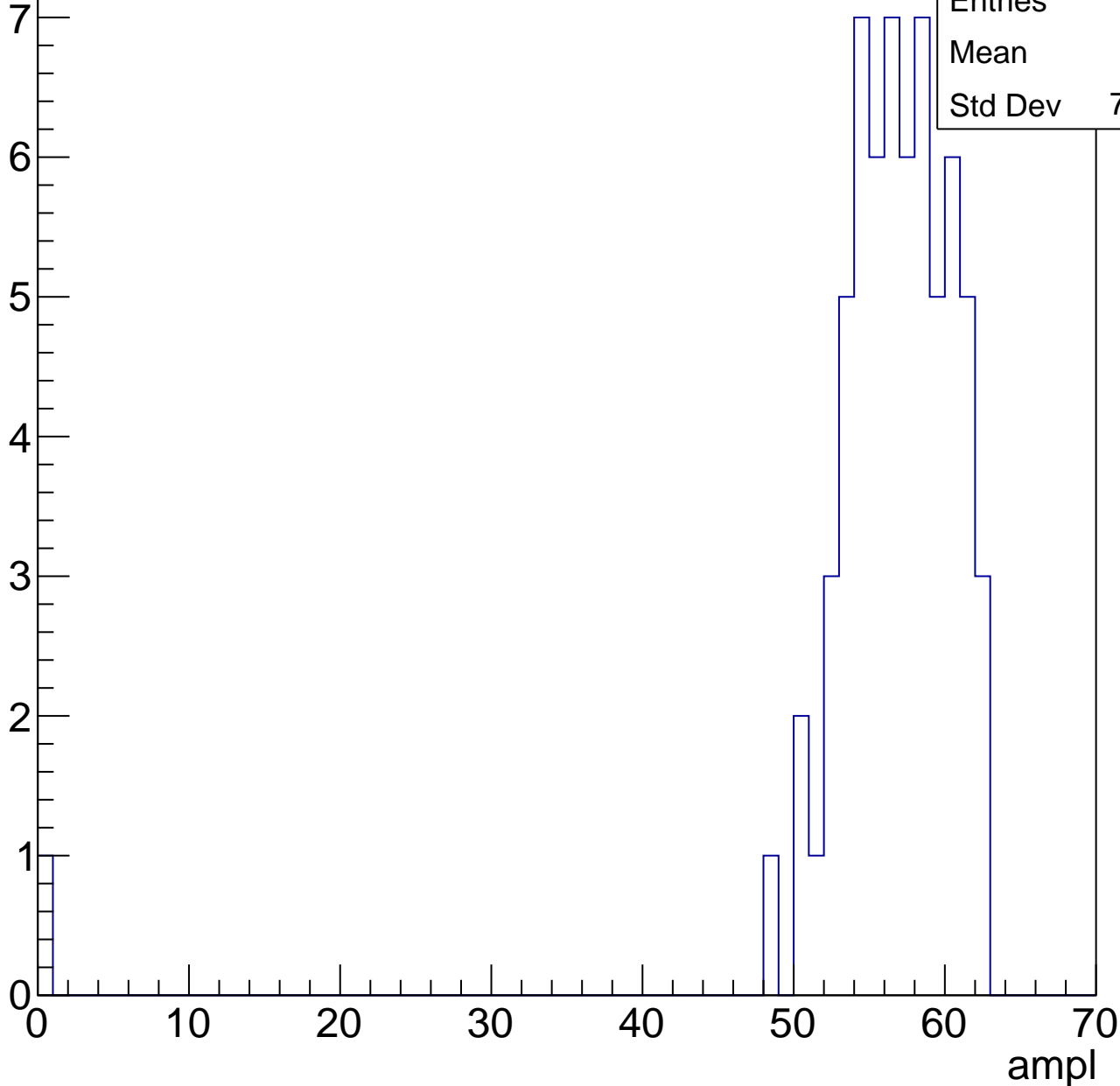
Entries	65
Mean	49.63
Std Dev	3.524

B1L101S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.6
Std Dev	7.668

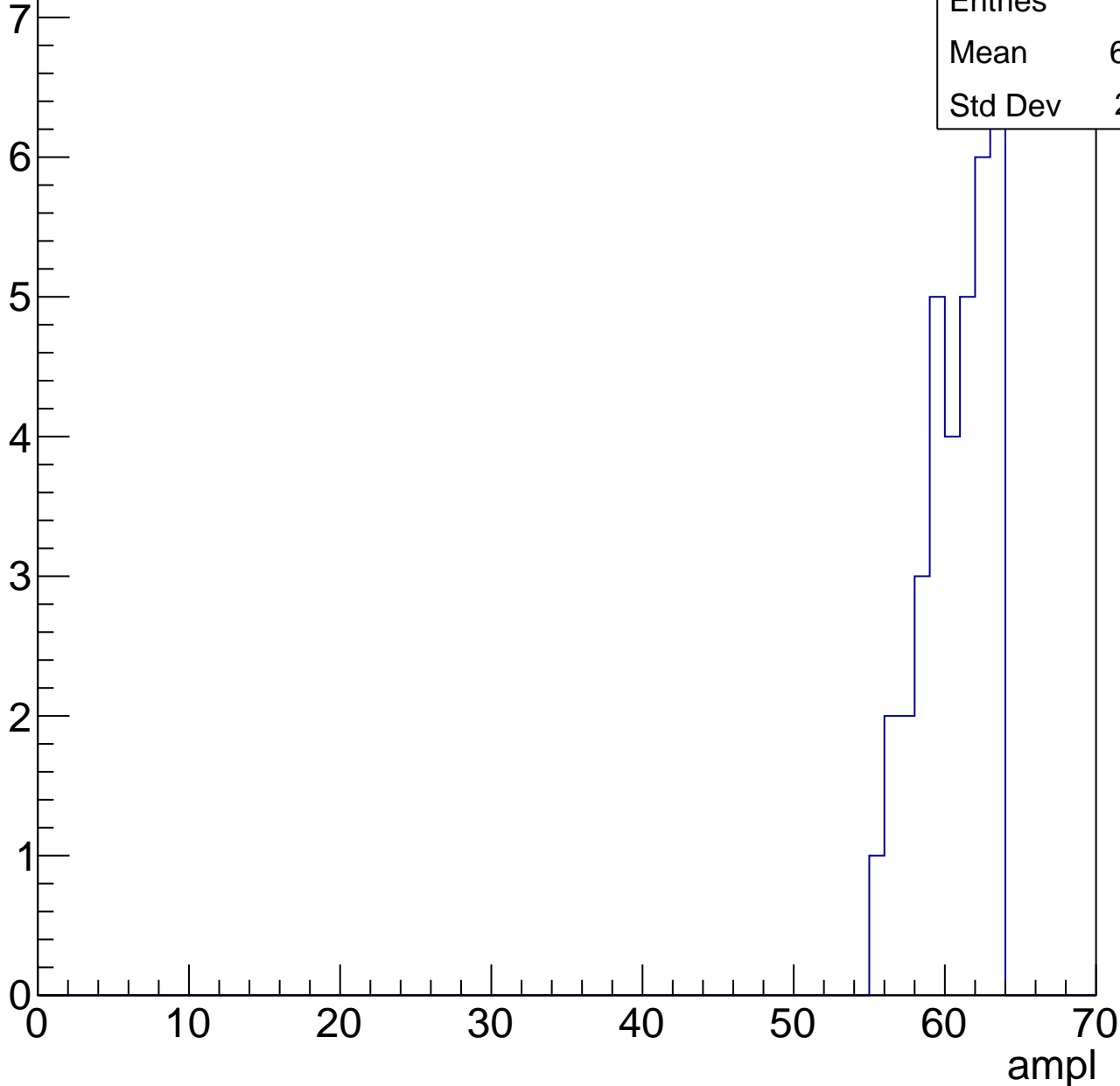


B1L101S, U14-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.23
Std Dev	2.281



B1L101S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch38, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	27.79
Std Dev	6.731

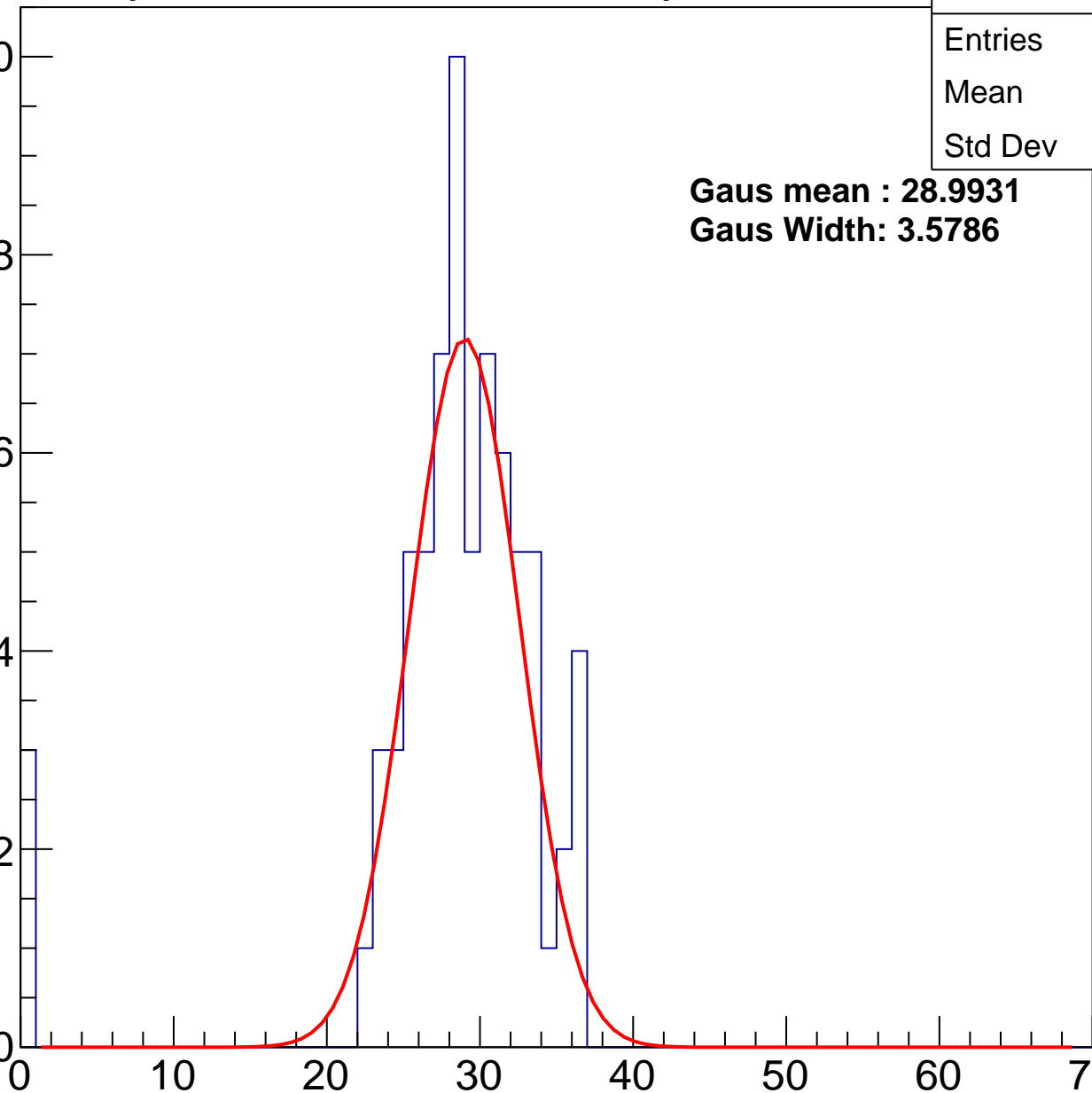
Gaus mean : 28.9931

Gaus Width: 3.5786

Entry

10
8
6
4
2
0

ampl



B1L101S, U14-ch38, adc1

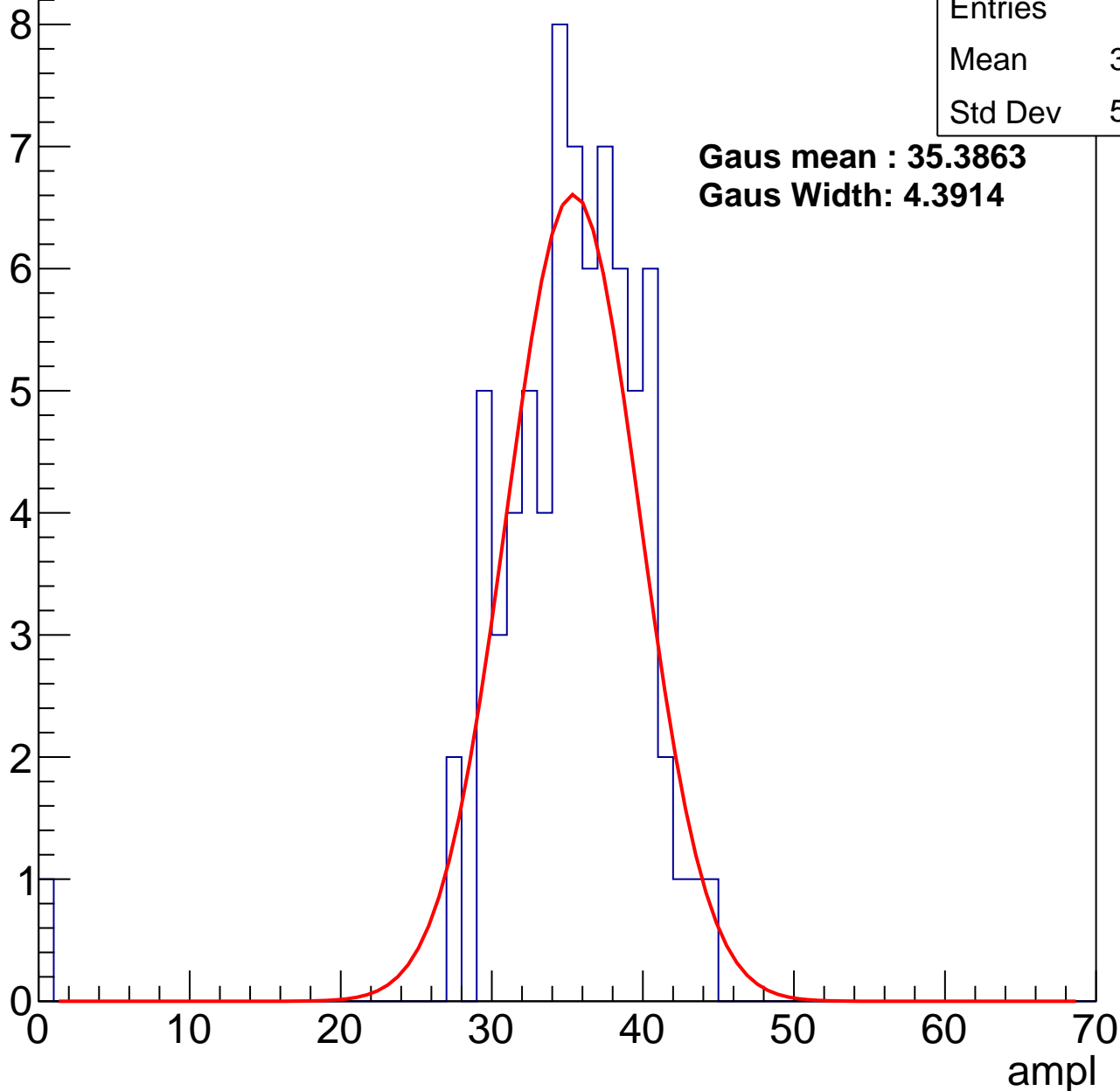
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.74
Std Dev	5.599

Gaus mean : 35.3863

Gaus Width: 4.3914



B1L101S, U14-ch38, adc2

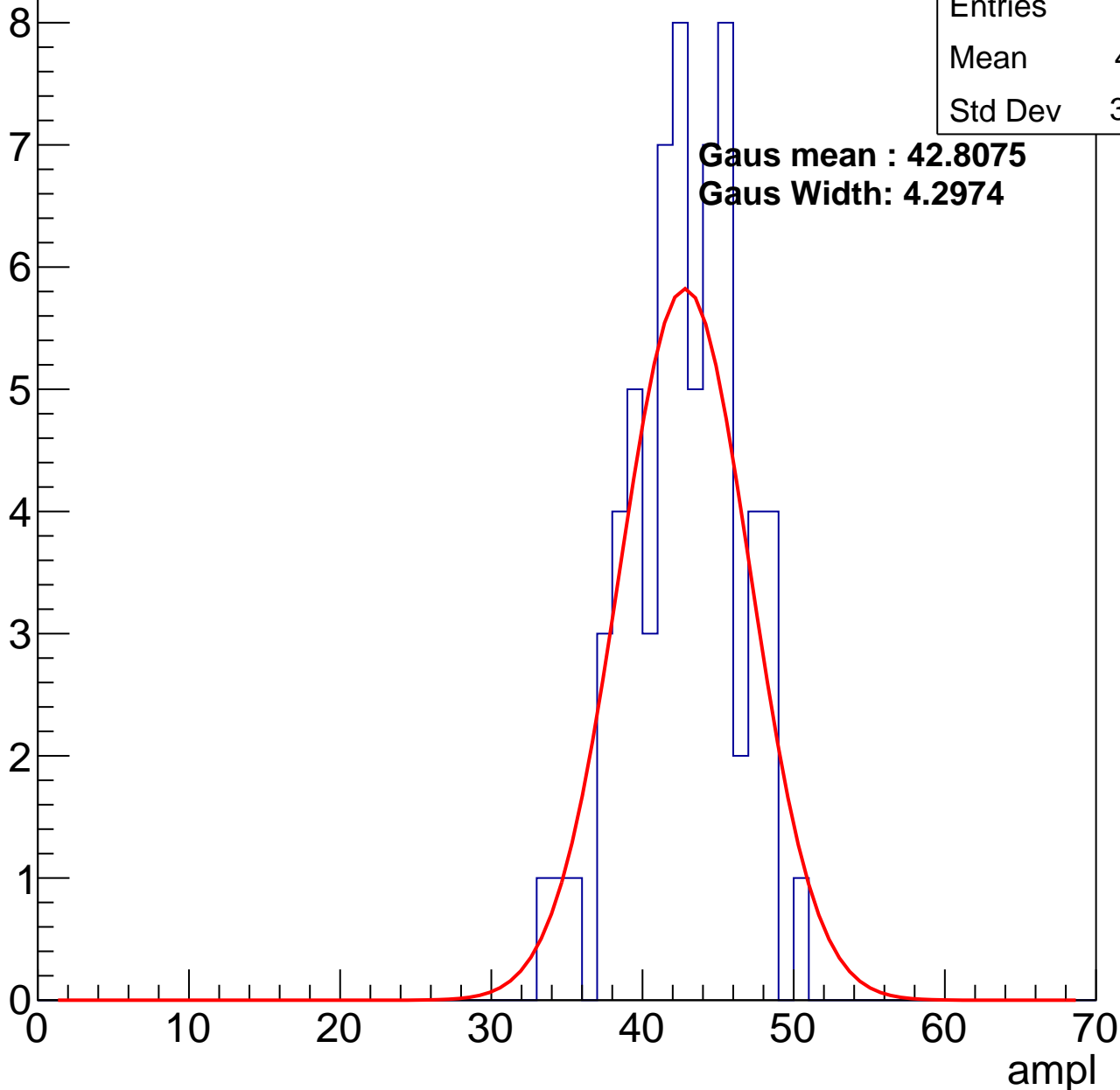
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.31
Std Dev	3.614

Gaus mean : 42.8075

Gaus Width: 4.2974

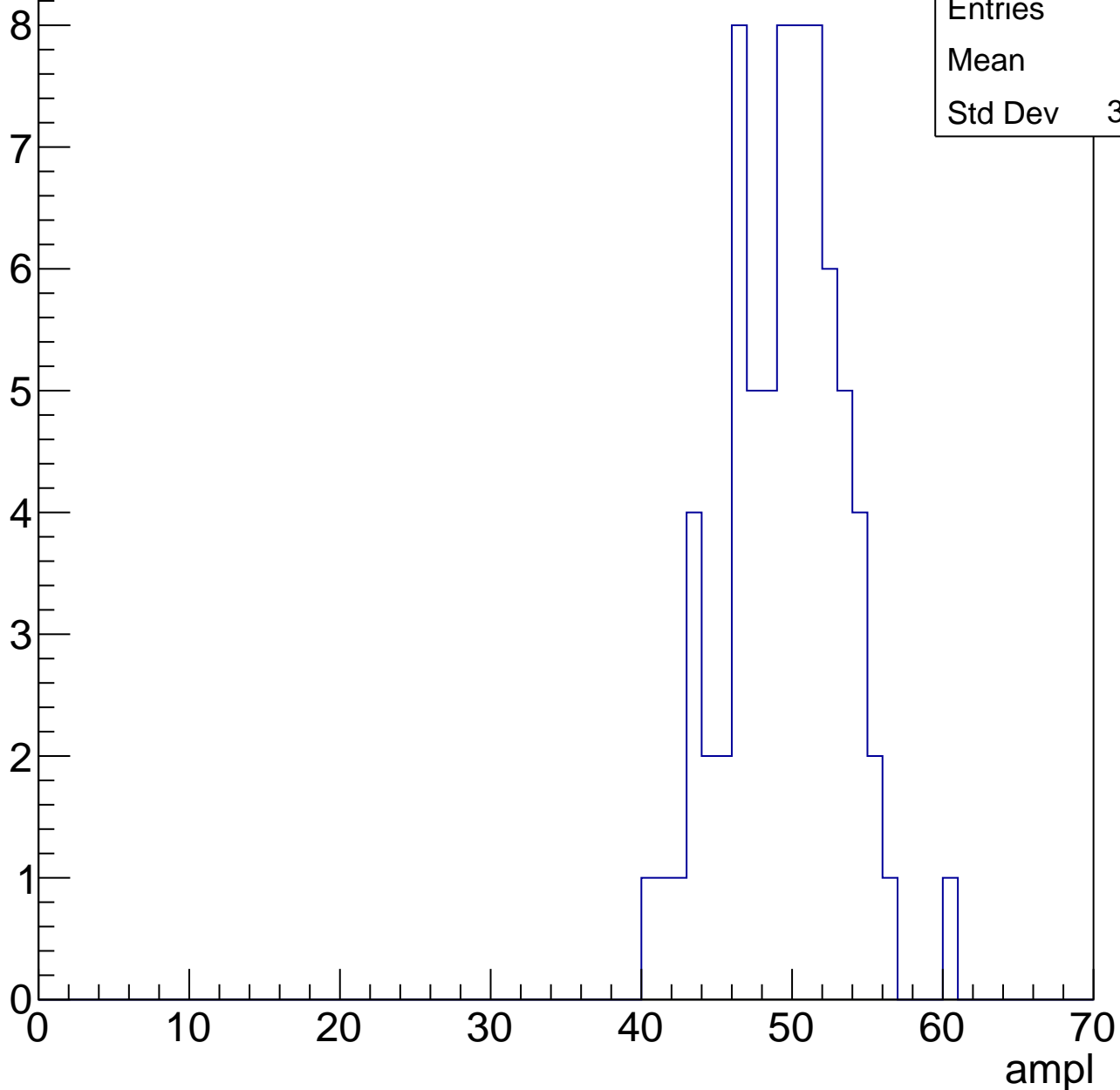


B1L101S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	49.1
Std Dev	3.787

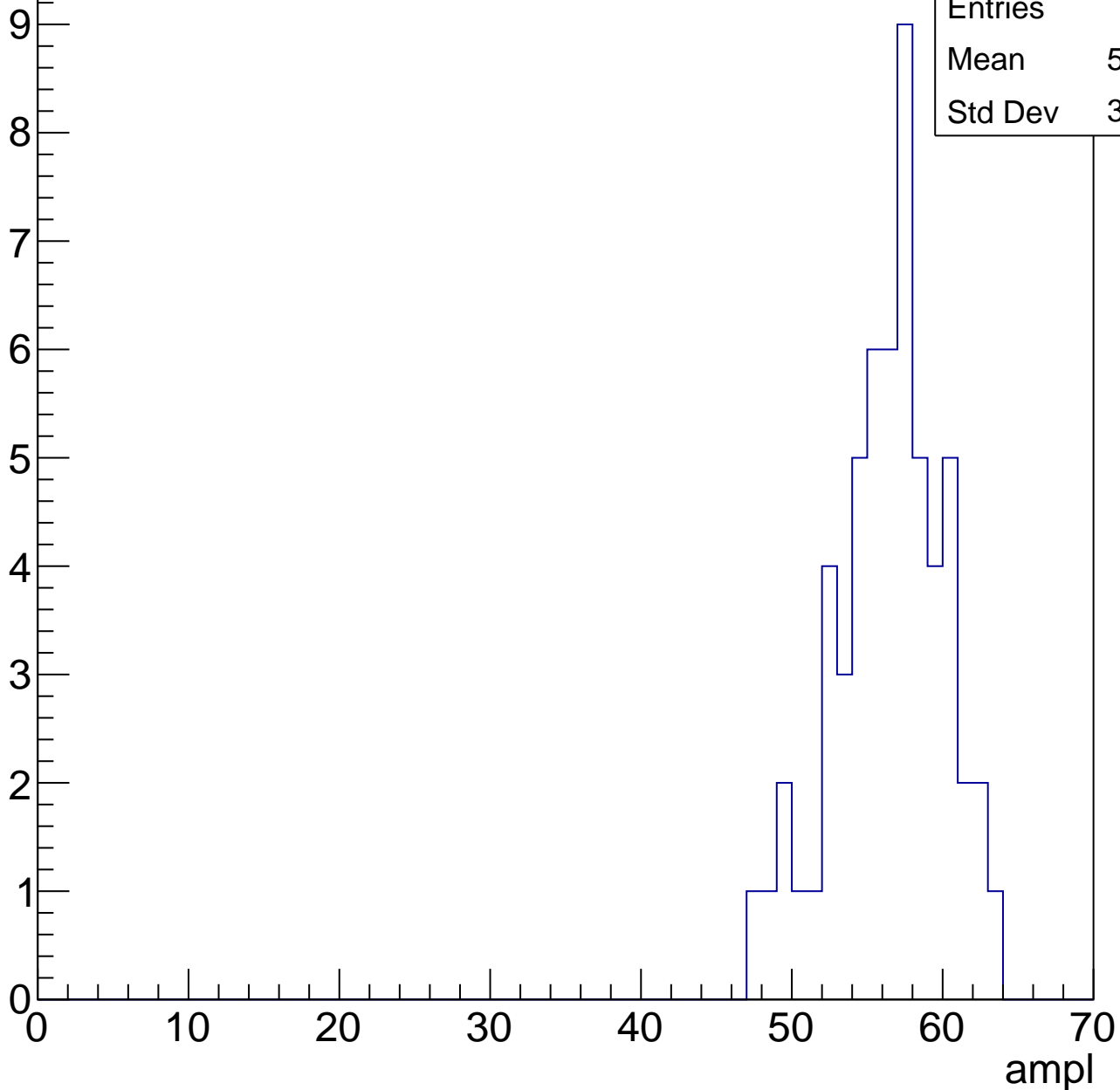


B1L101S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.95
Std Dev	3.564

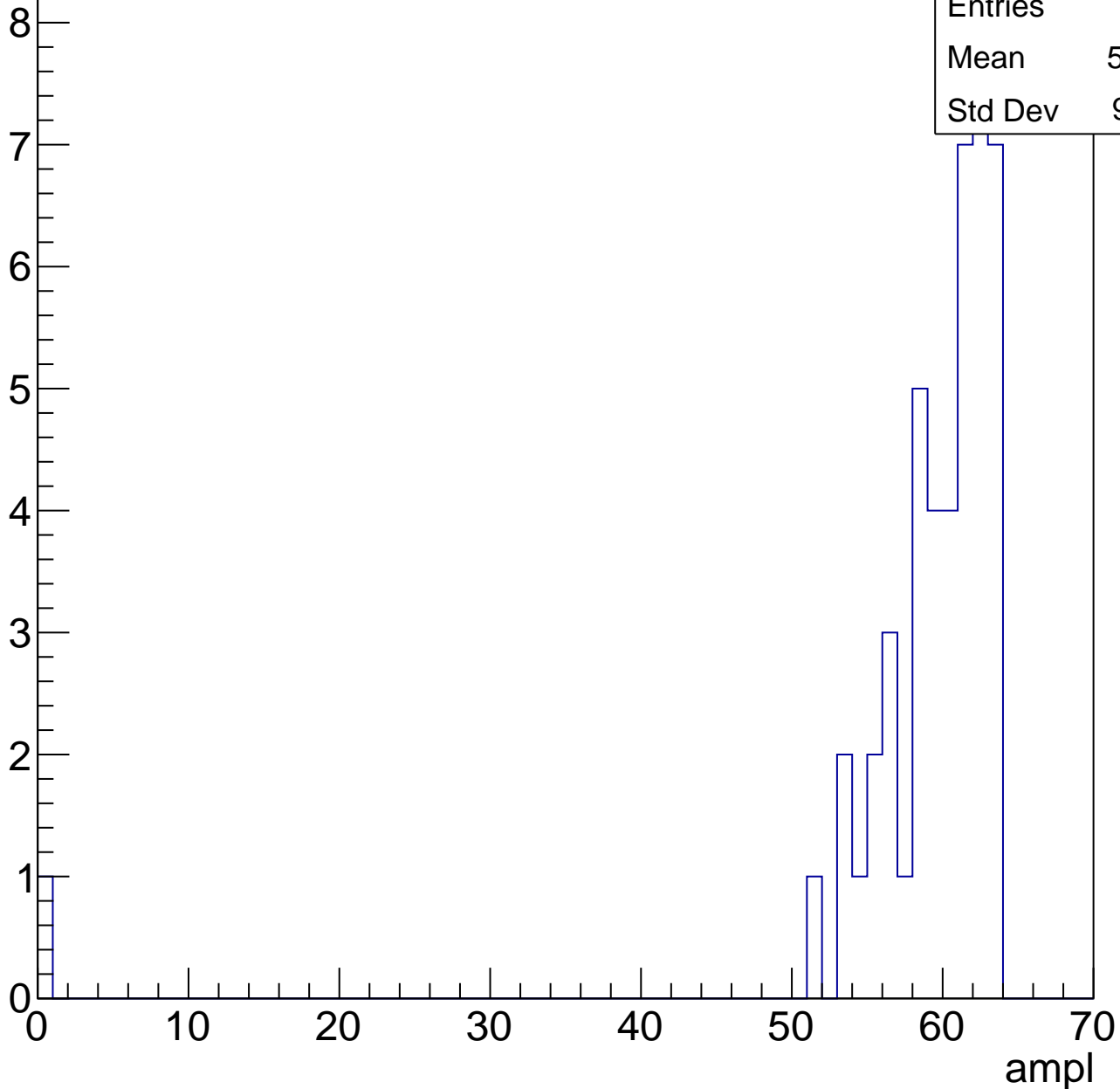


B1L101S, U14-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

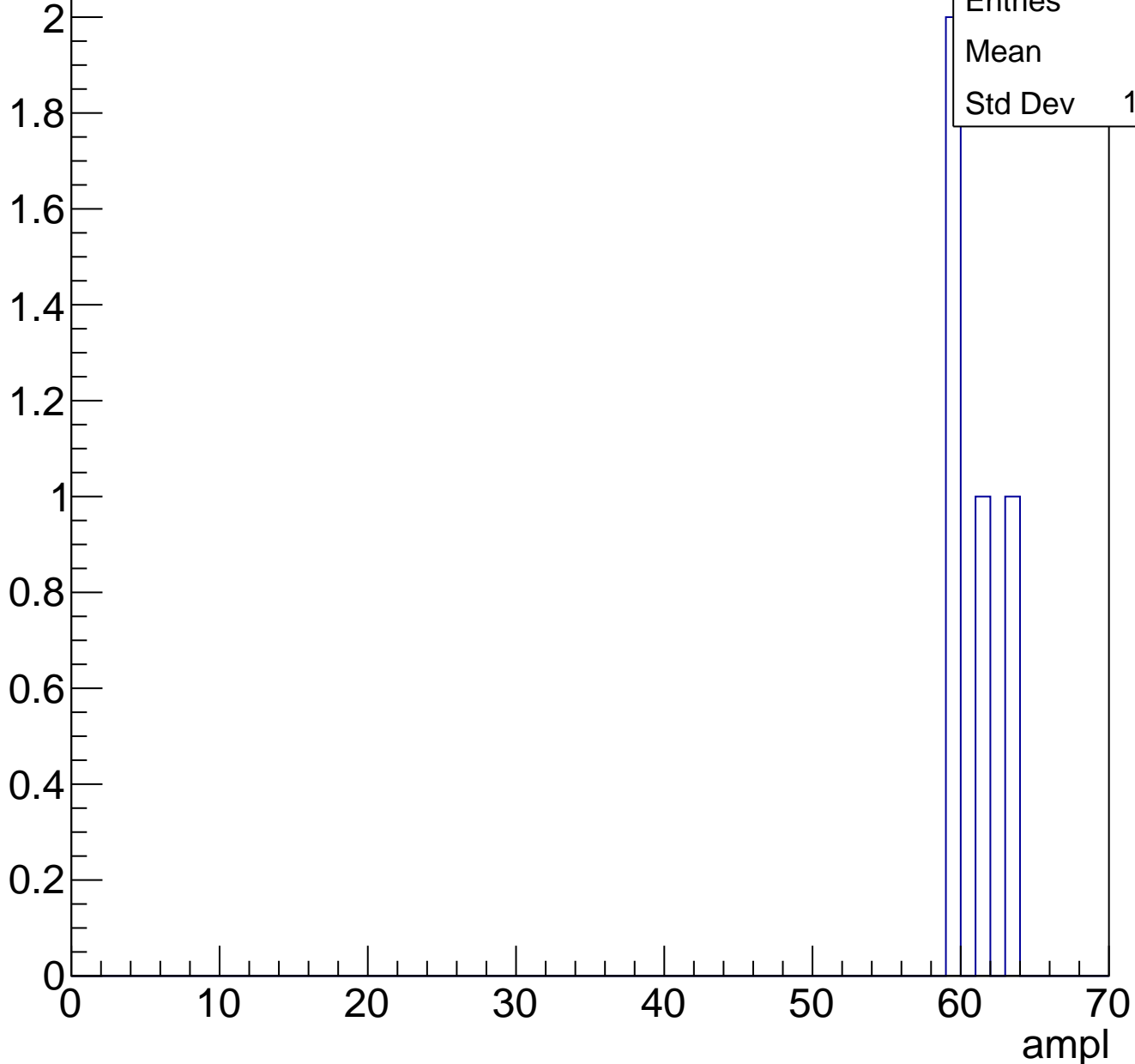
Entries	46
Mean	58.17
Std Dev	9.201



B1L101S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch39, adc0

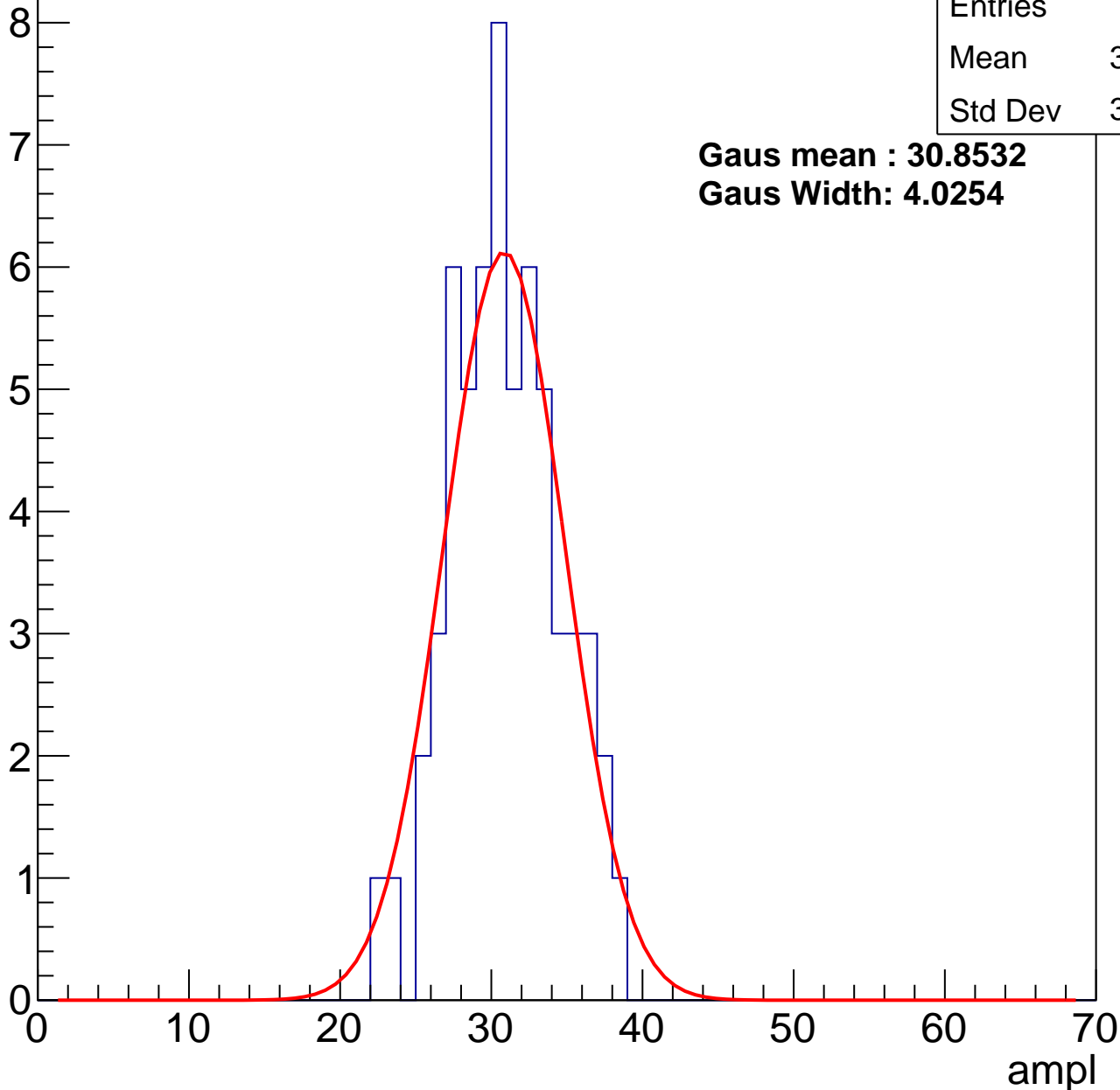
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.47
Std Dev	3.524

Gaus mean : 30.8532

Gaus Width: 4.0254



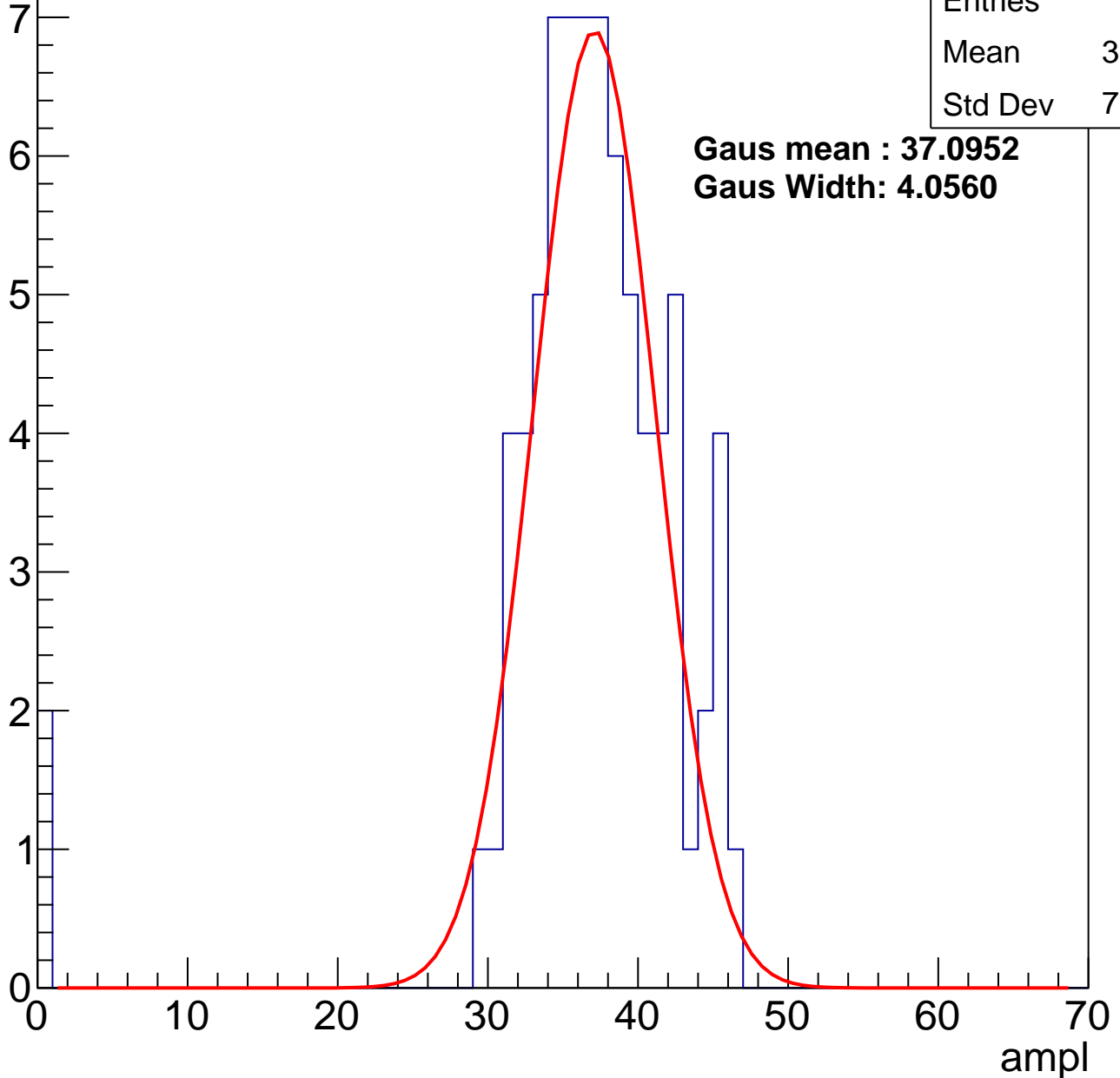
B1L101S, U14-ch39, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.16
Std Dev	7.153

Gaus mean : 37.0952
Gaus Width: 4.0560



B1L101S, U14-ch39, adc2

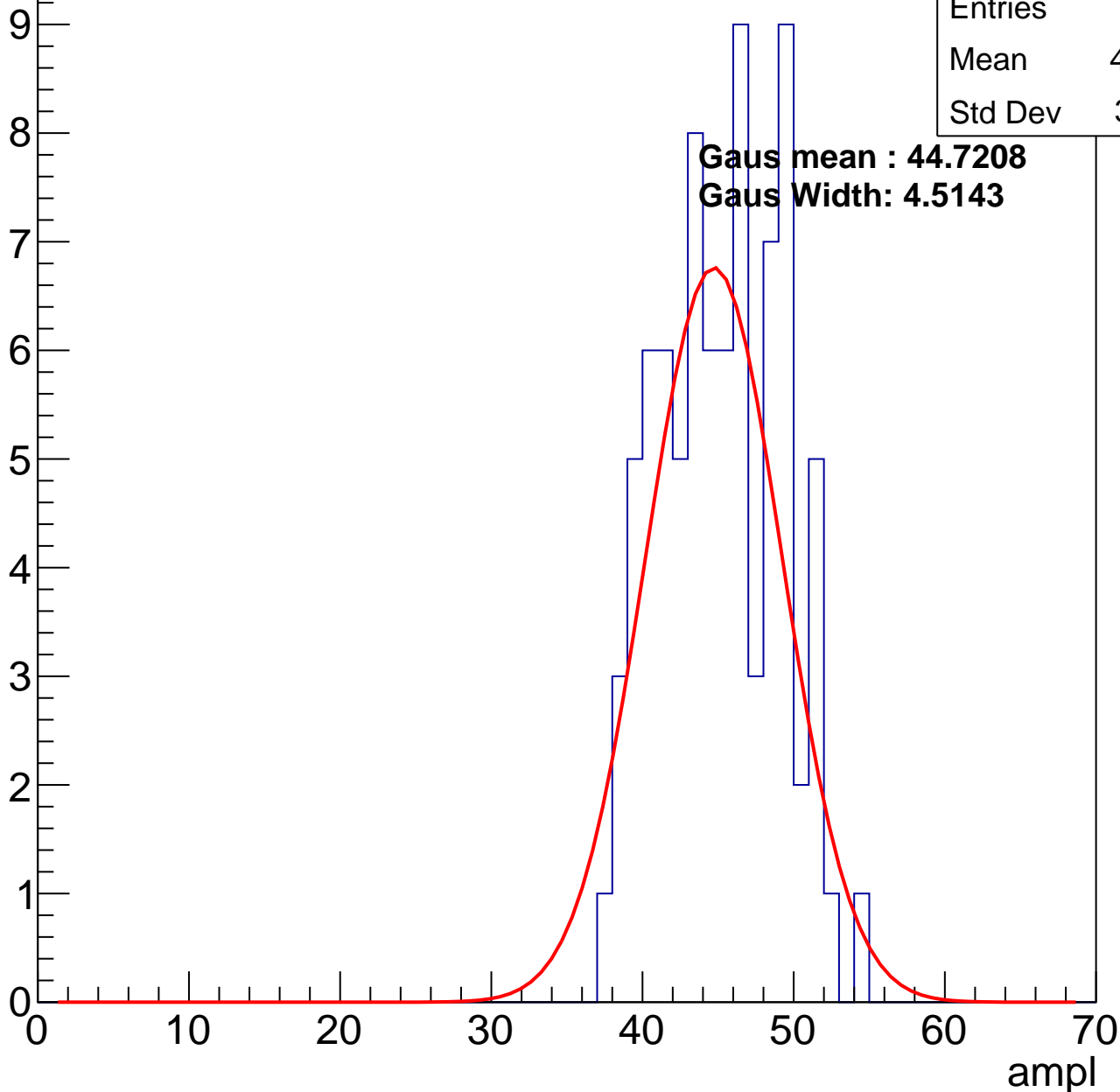
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	44.73
Std Dev	3.961

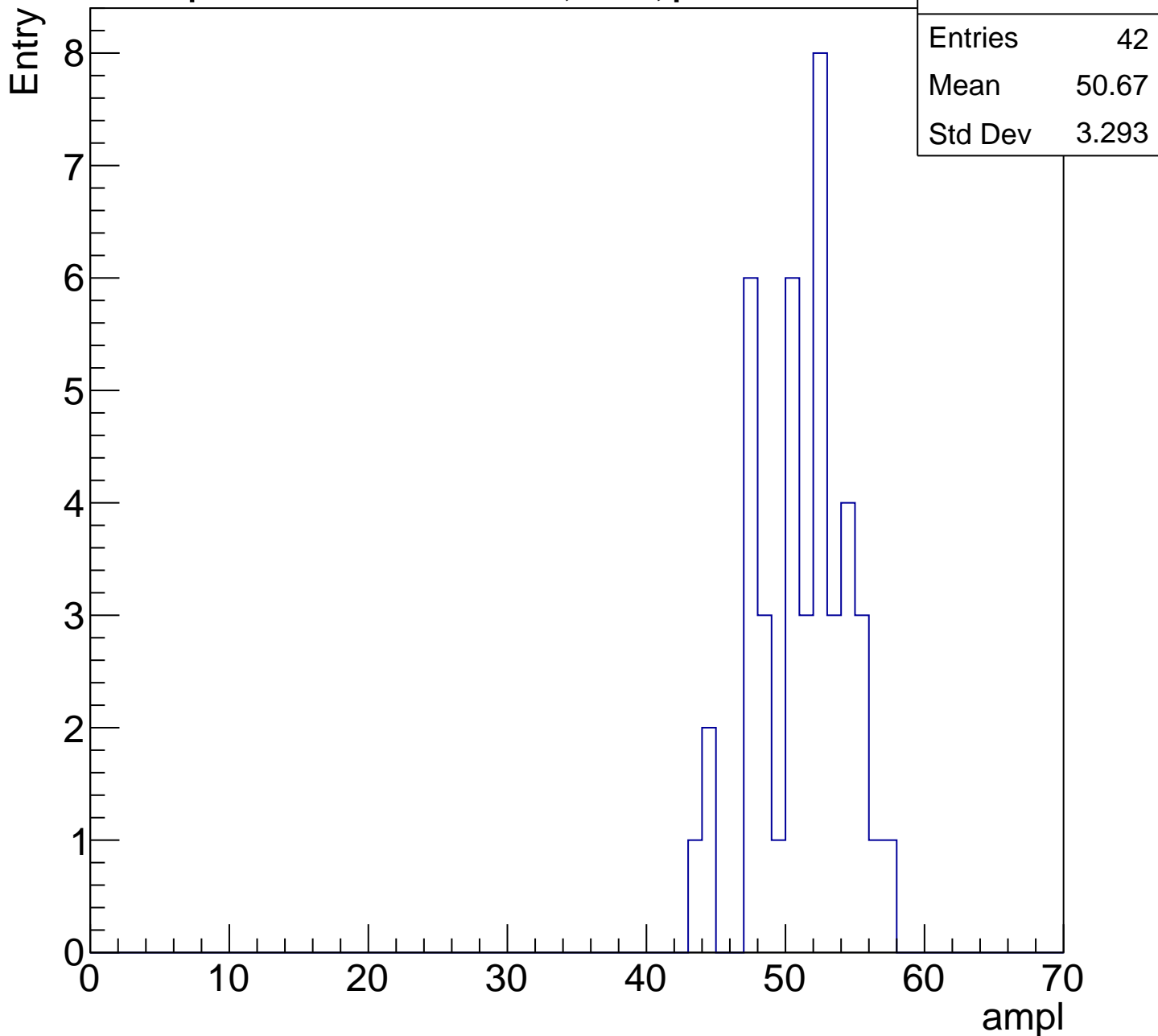
Gaus mean : 44.7208

Gaus Width: 4.5143



B1L101S, U14-ch39, adc3

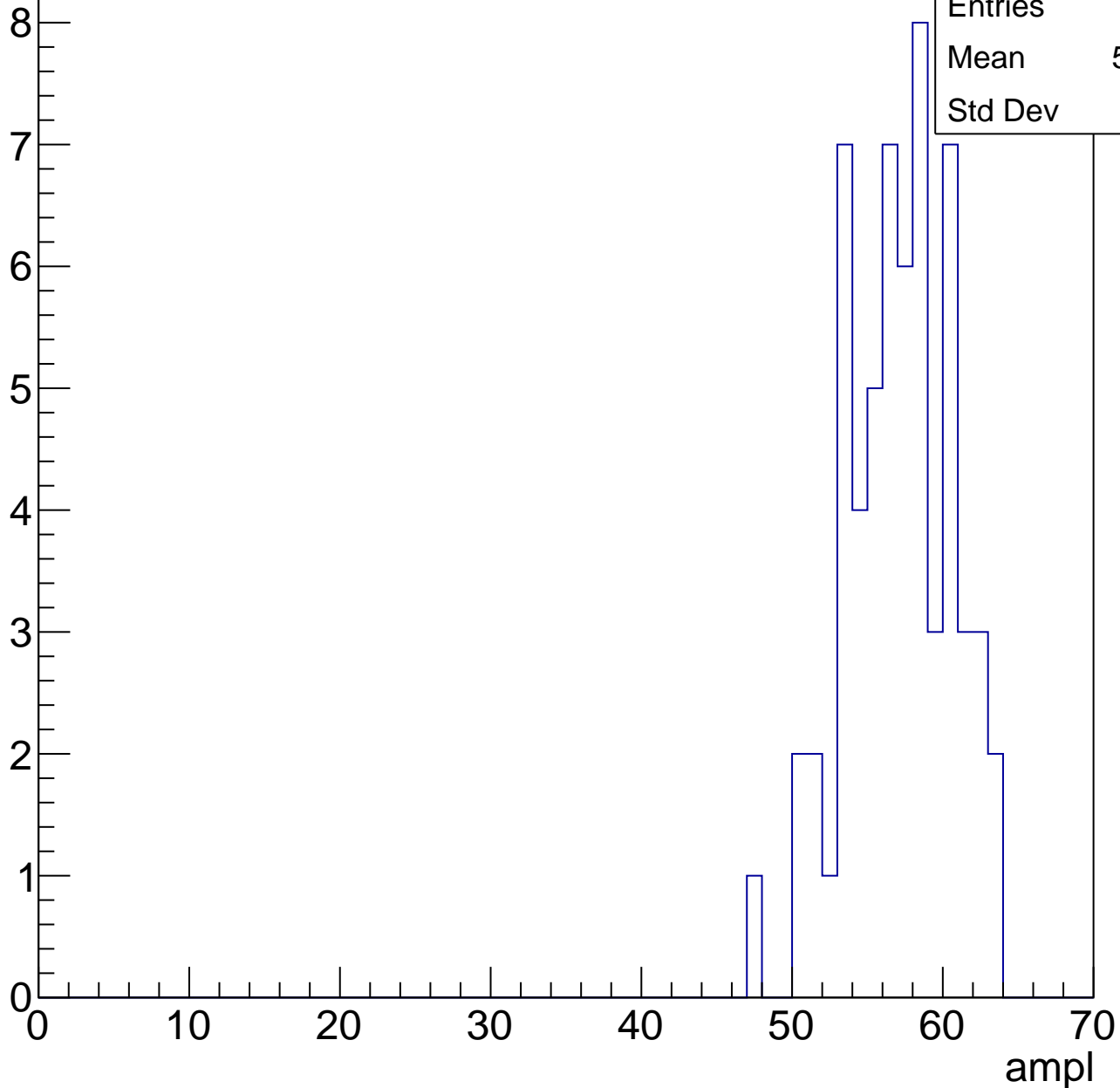
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



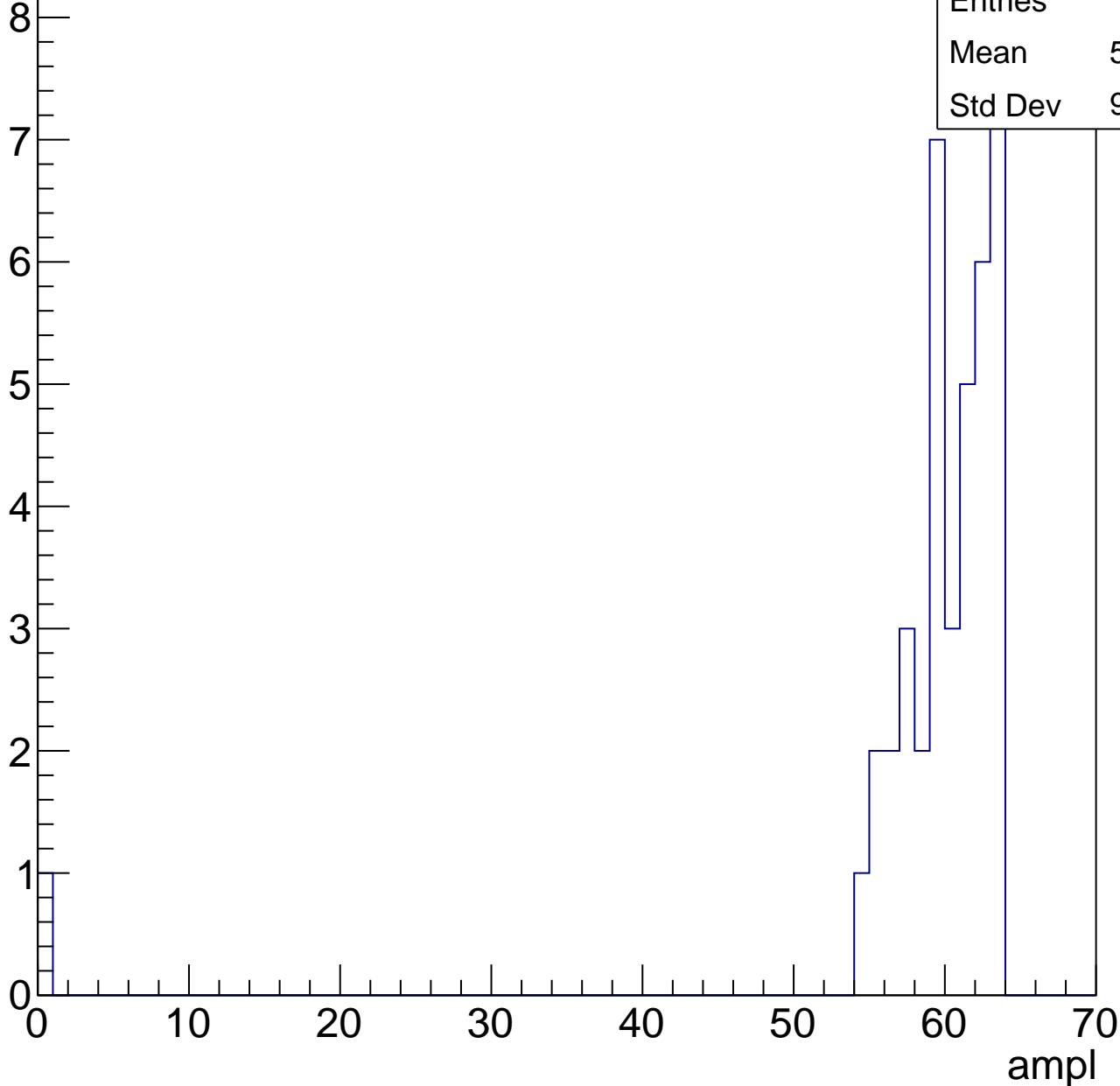
Entries	61
Mean	56.61
Std Dev	3.48

B1L101S, U14-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.42
Std Dev	9.698



B1L101S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch40, adc0

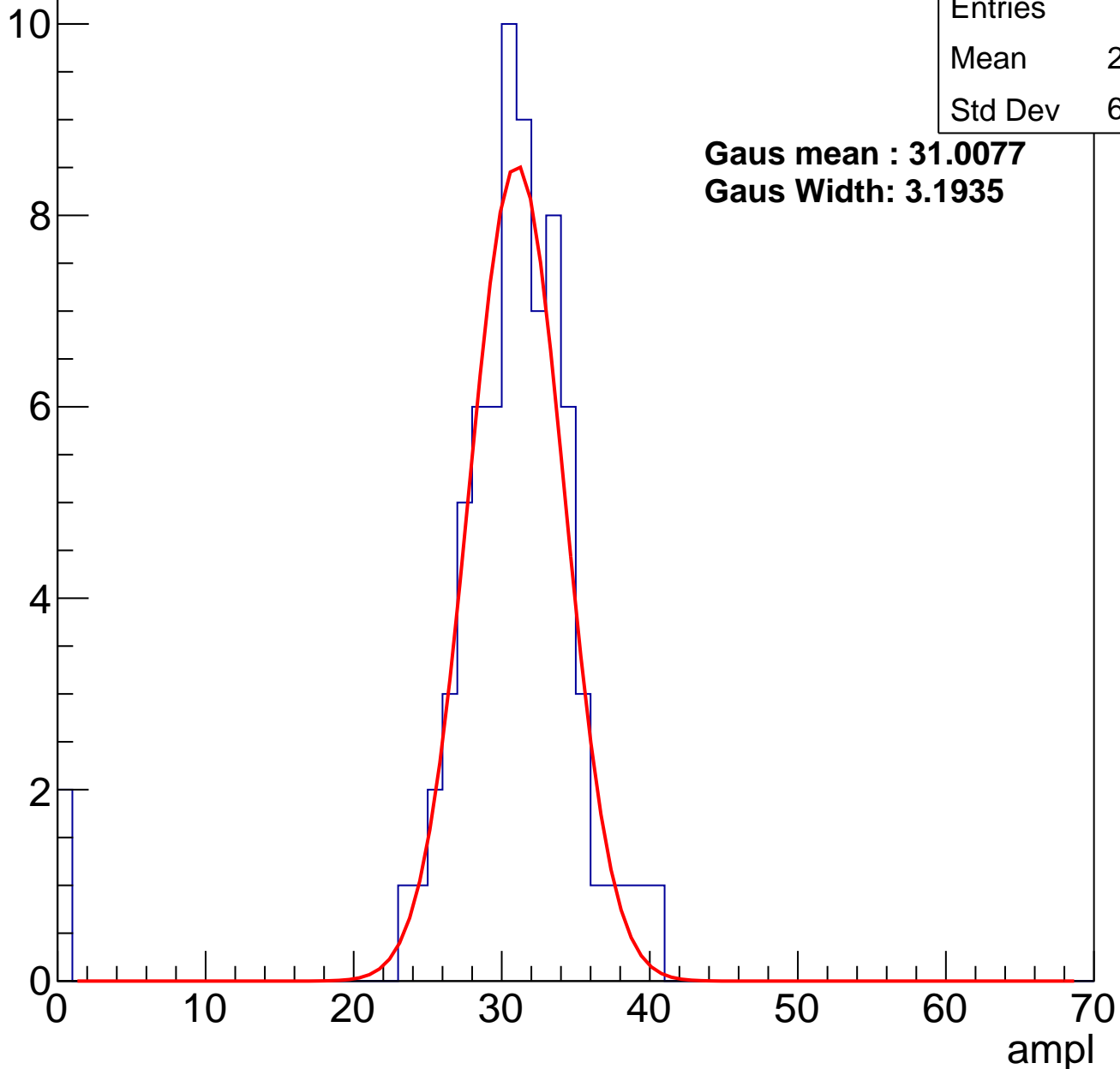
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.97
Std Dev	6.004

Gaus mean : 31.0077

Gaus Width: 3.1935

Entry



B1L101S, U14-ch40, adc1

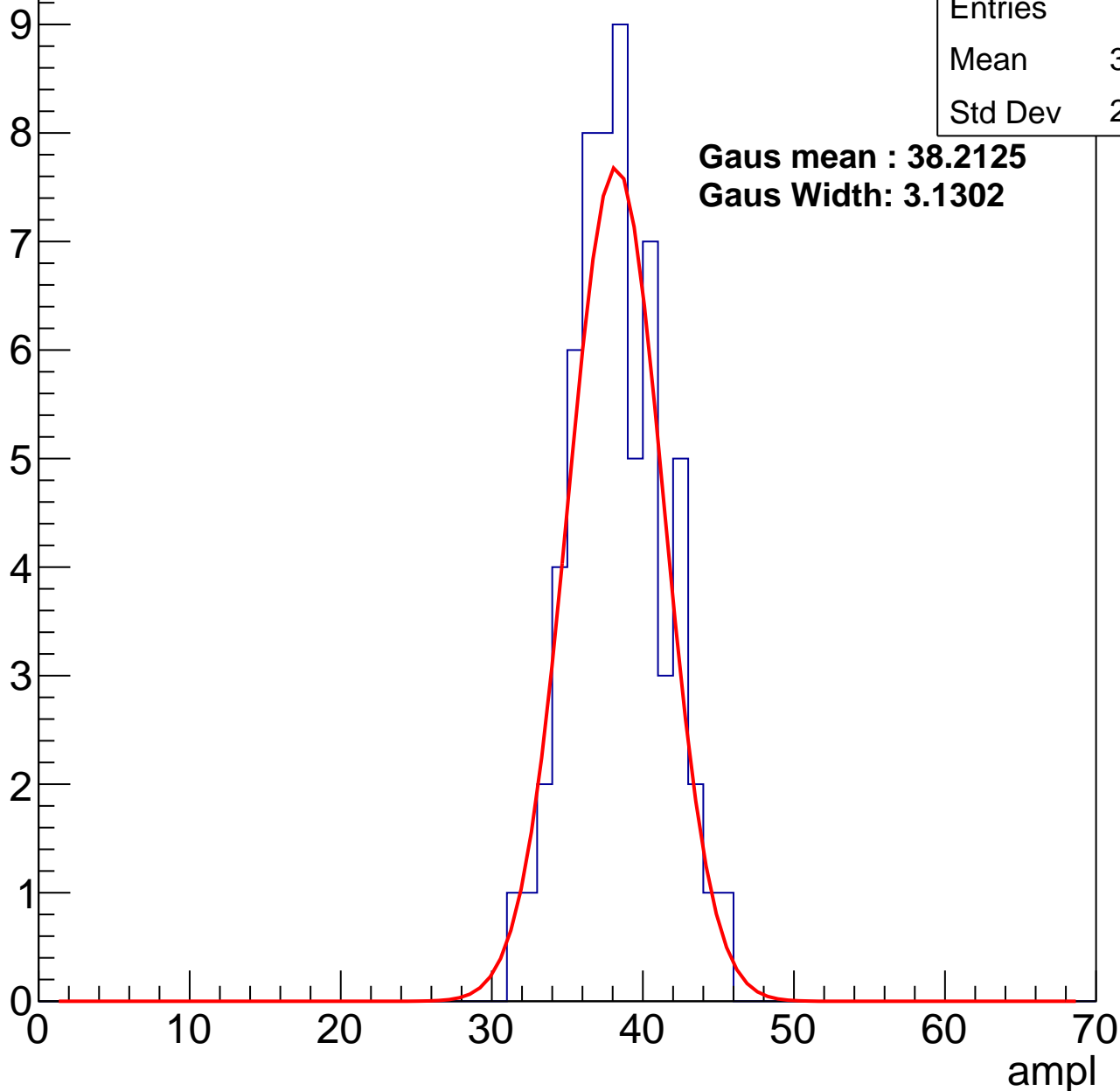
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.84
Std Dev	2.998

Gaus mean : 38.2125

Gaus Width: 3.1302



B1L101S, U14-ch40, adc2

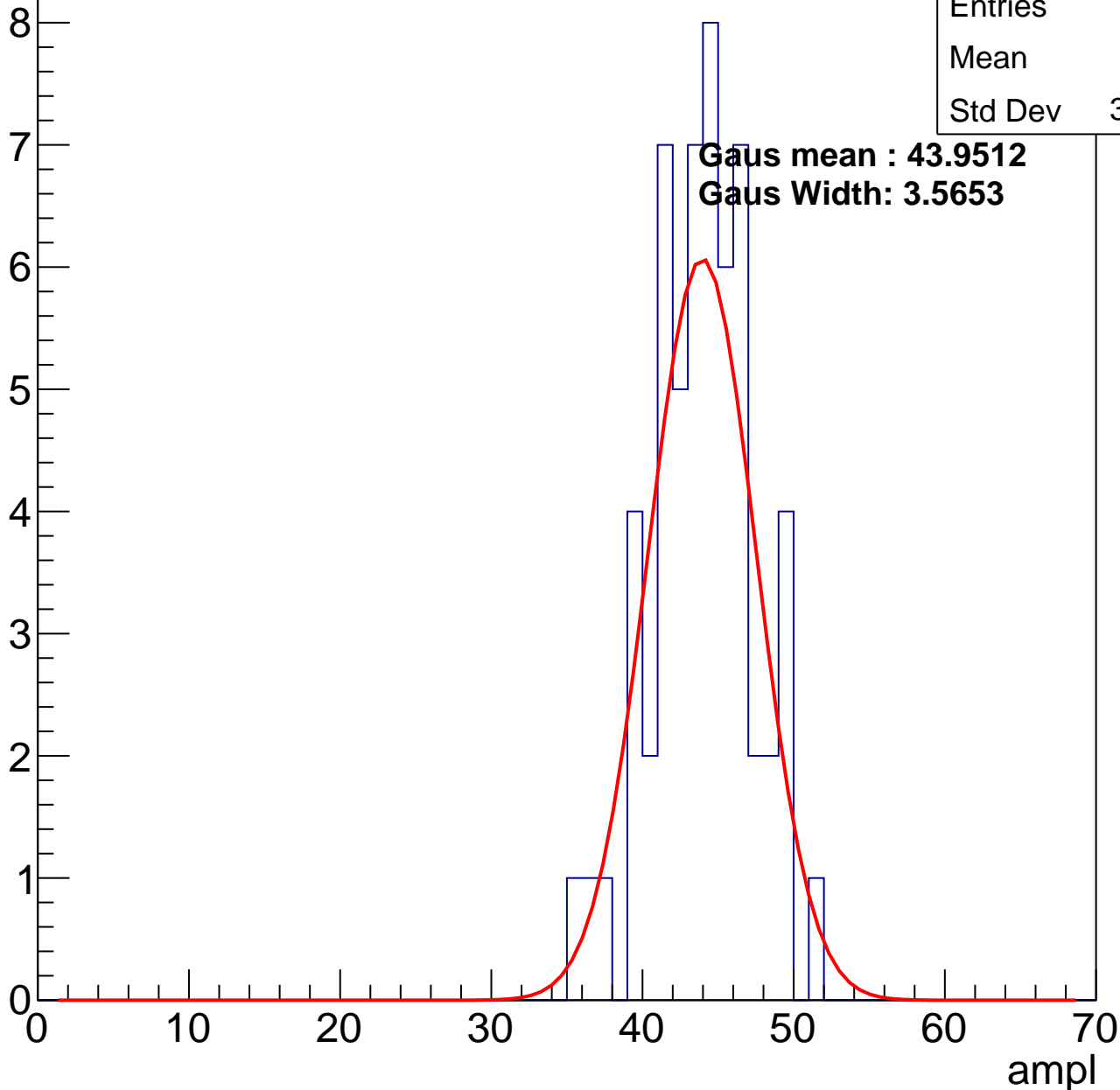
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.5
Std Dev	3.308

Gaus mean : 43.9512

Gaus Width: 3.5653

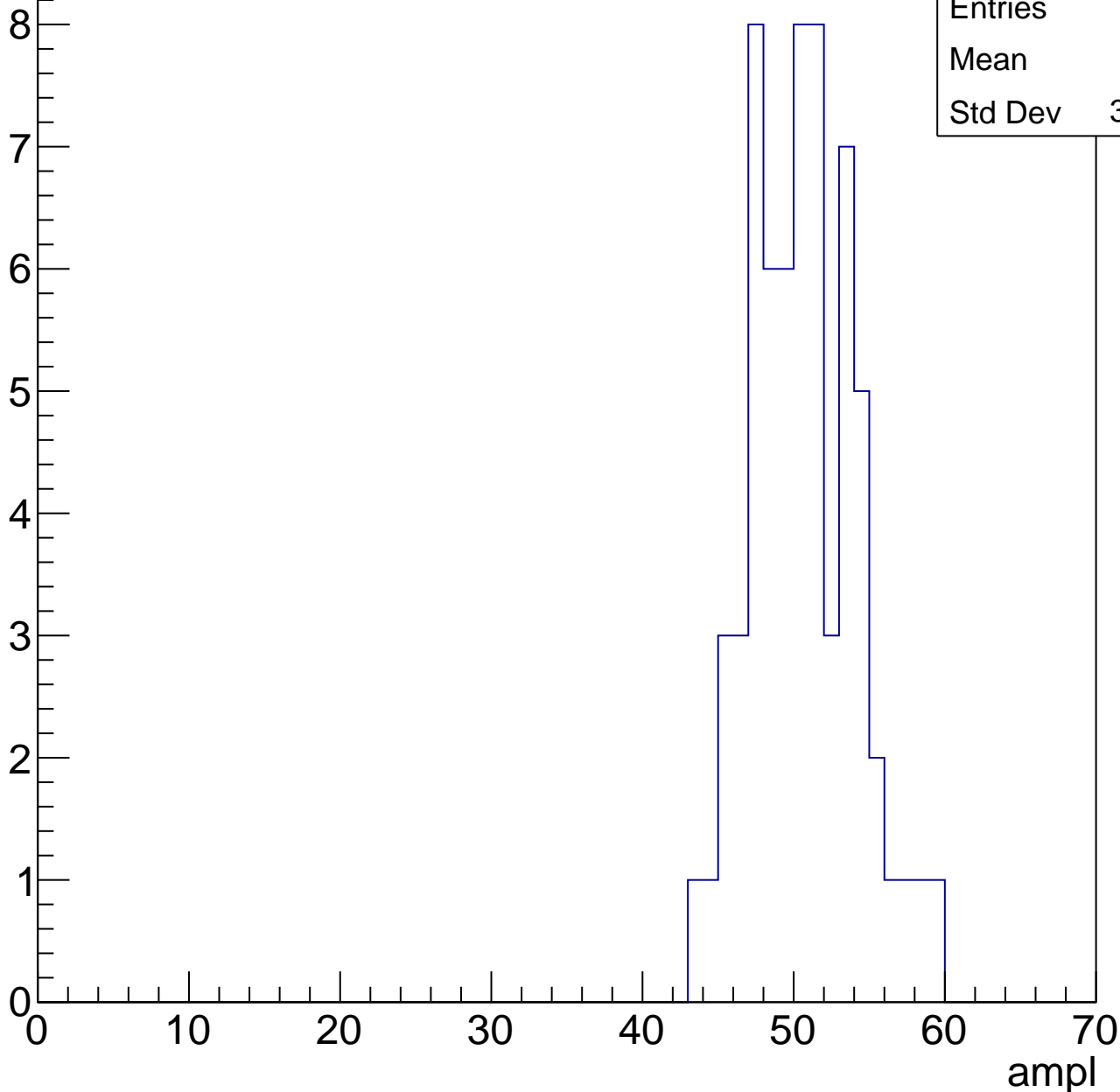


B1L101S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

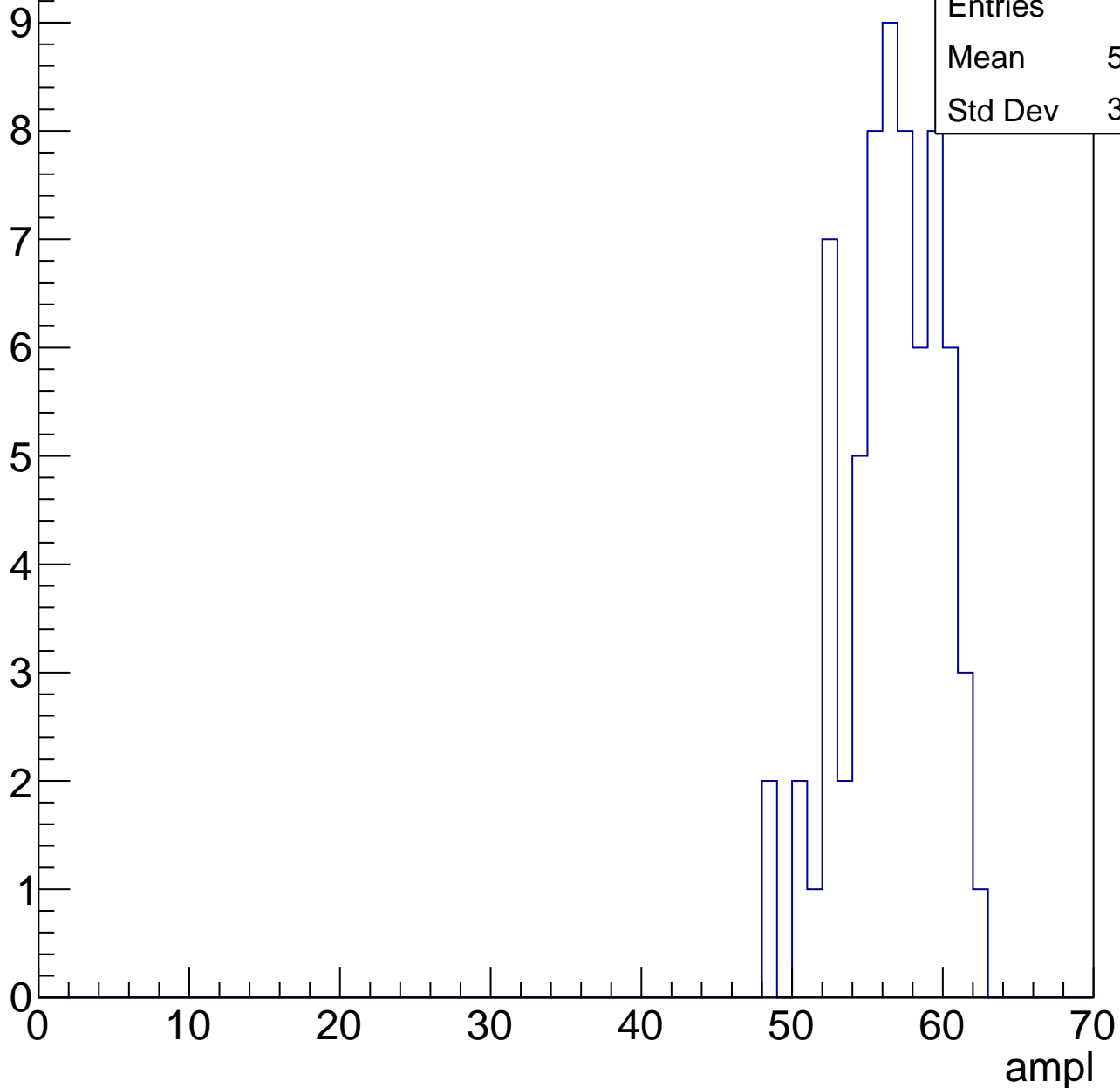
Entries	65
Mean	50.2
Std Dev	3.398



B1L101S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

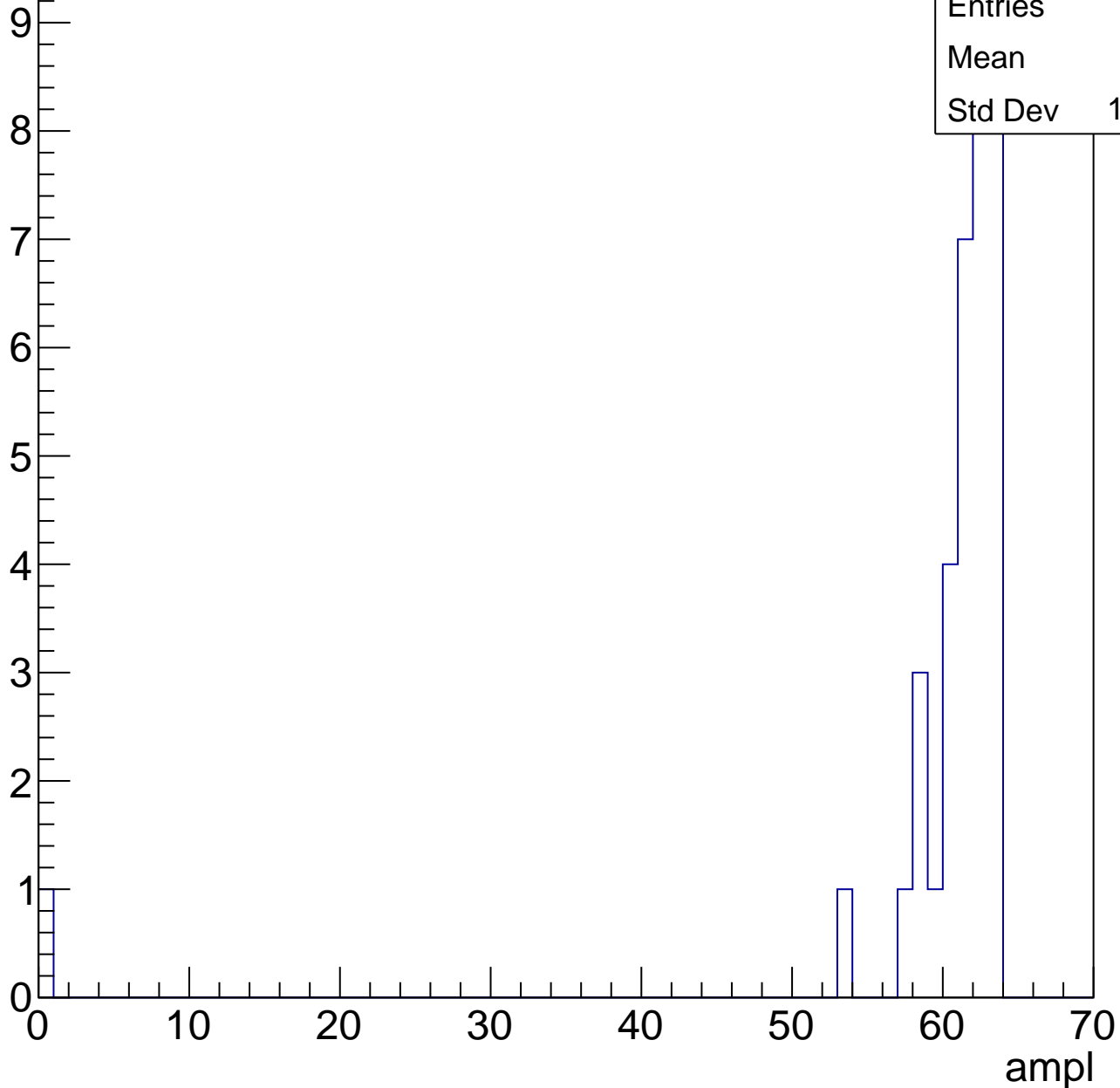
Entry



B1L101S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch41, adc0

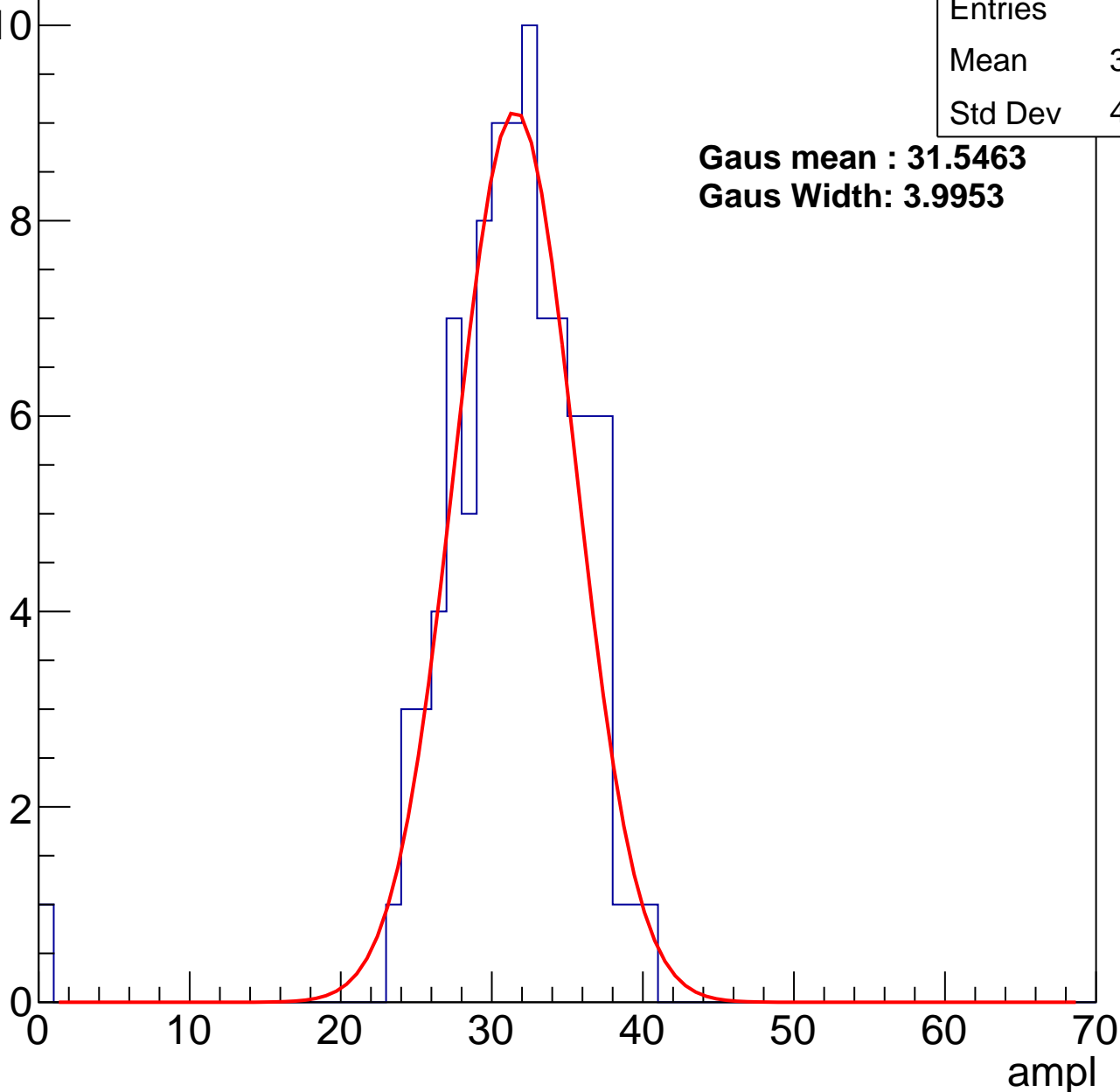
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	95
Mean	30.93
Std Dev	4.957

Gaus mean : 31.5463

Gaus Width: 3.9953



B1L101S, U14-ch41, adc1

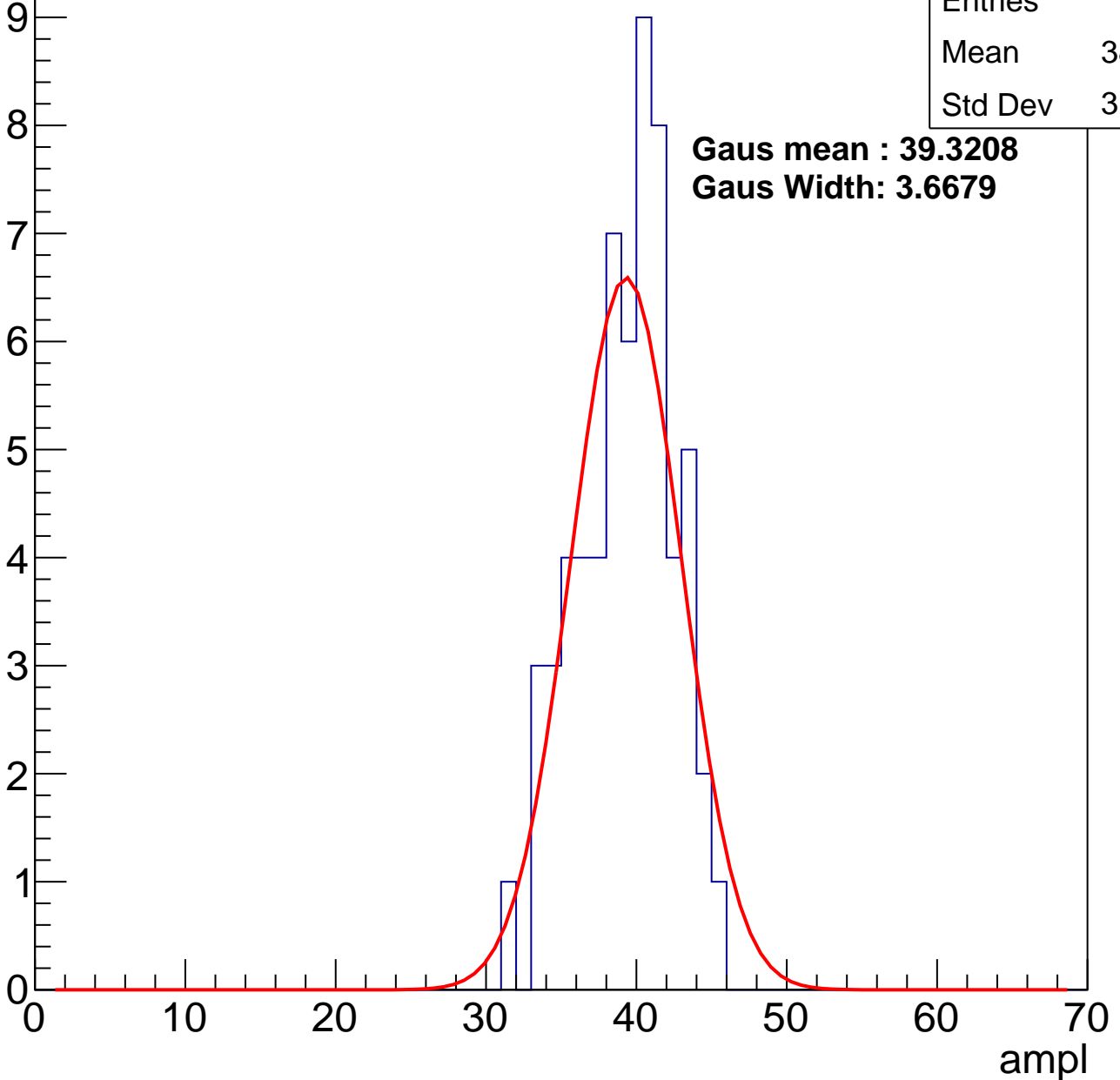
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	38.82
Std Dev	3.165

Gaus mean : 39.3208

Gaus Width: 3.6679



B1L101S, U14-ch41, adc2

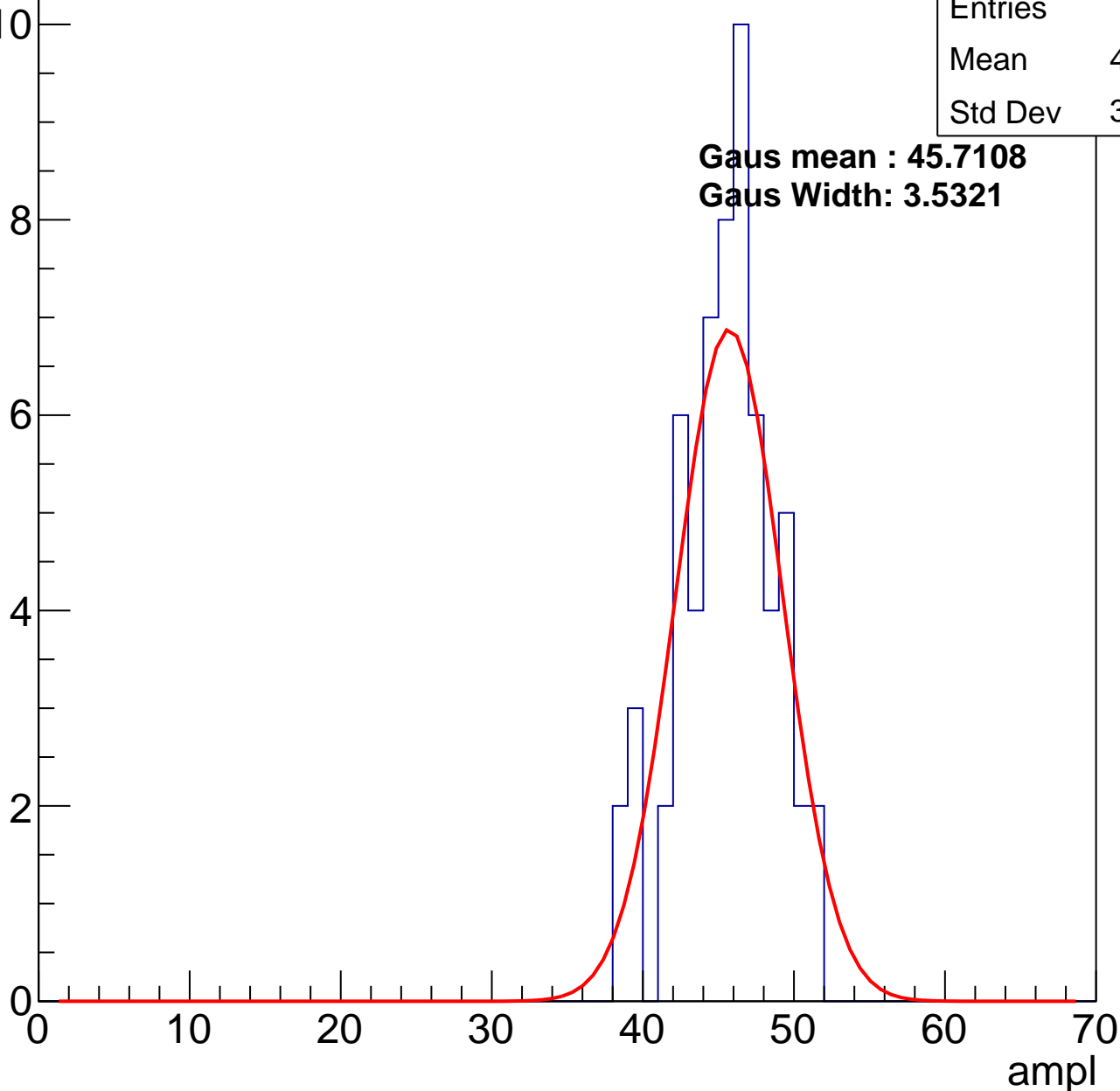
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	45.05
Std Dev	3.102

Gaus mean : 45.7108

Gaus Width: 3.5321

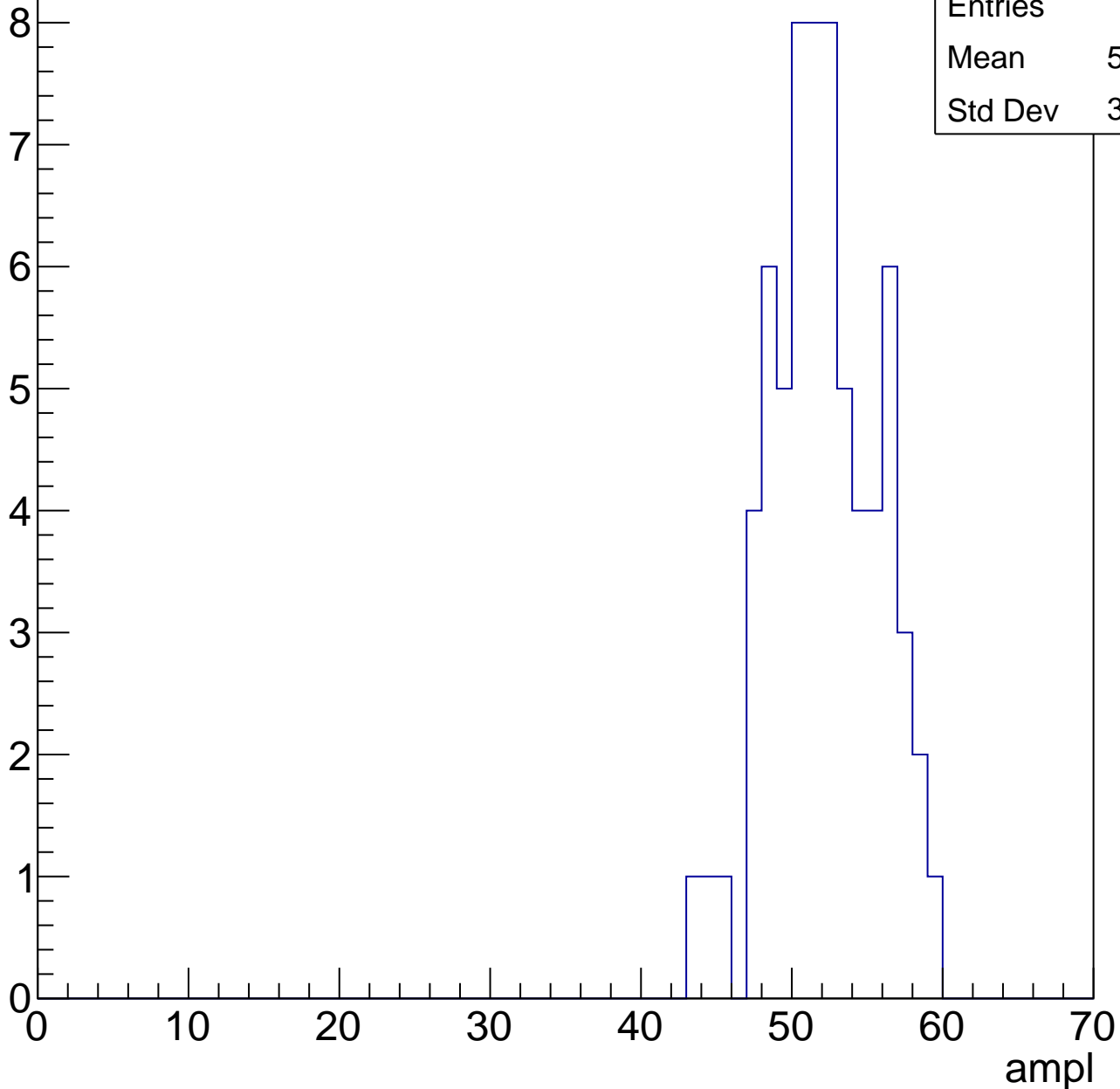


B1L101S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.64
Std Dev	3.489

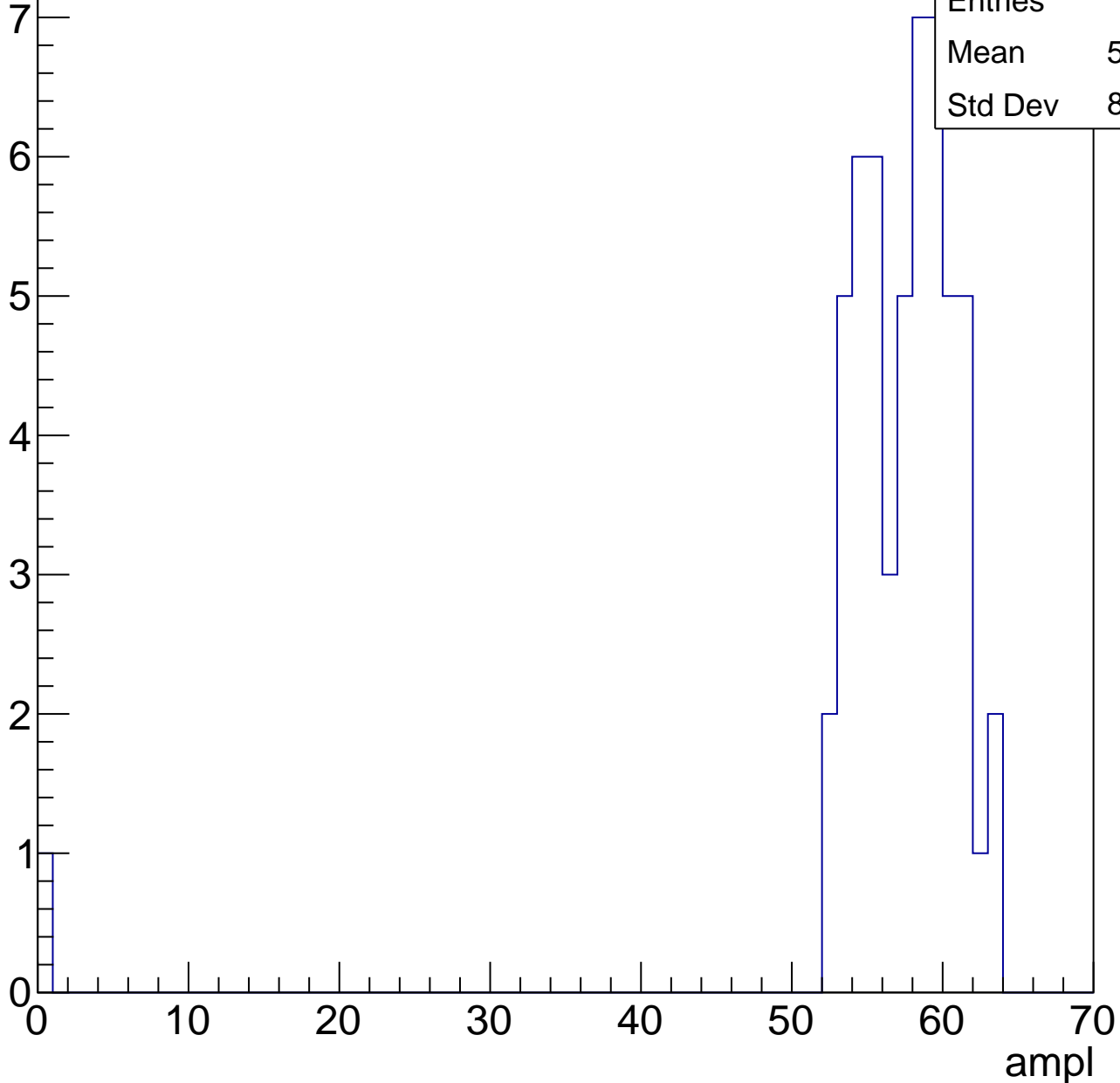


B1L101S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.15
Std Dev	8.176

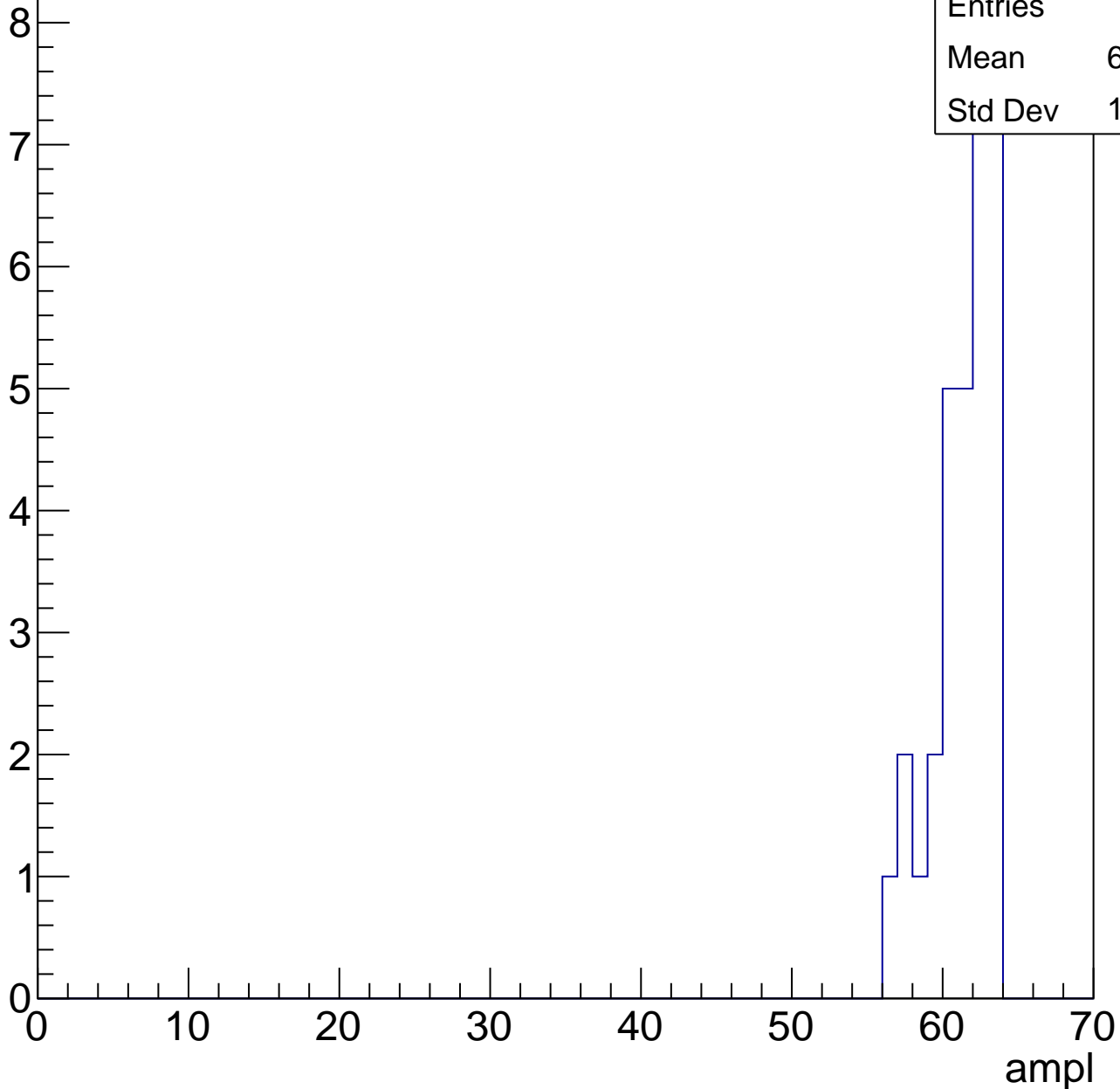


B1L101S, U14-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	60.97
Std Dev	1.928



B1L101S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch42, adc0

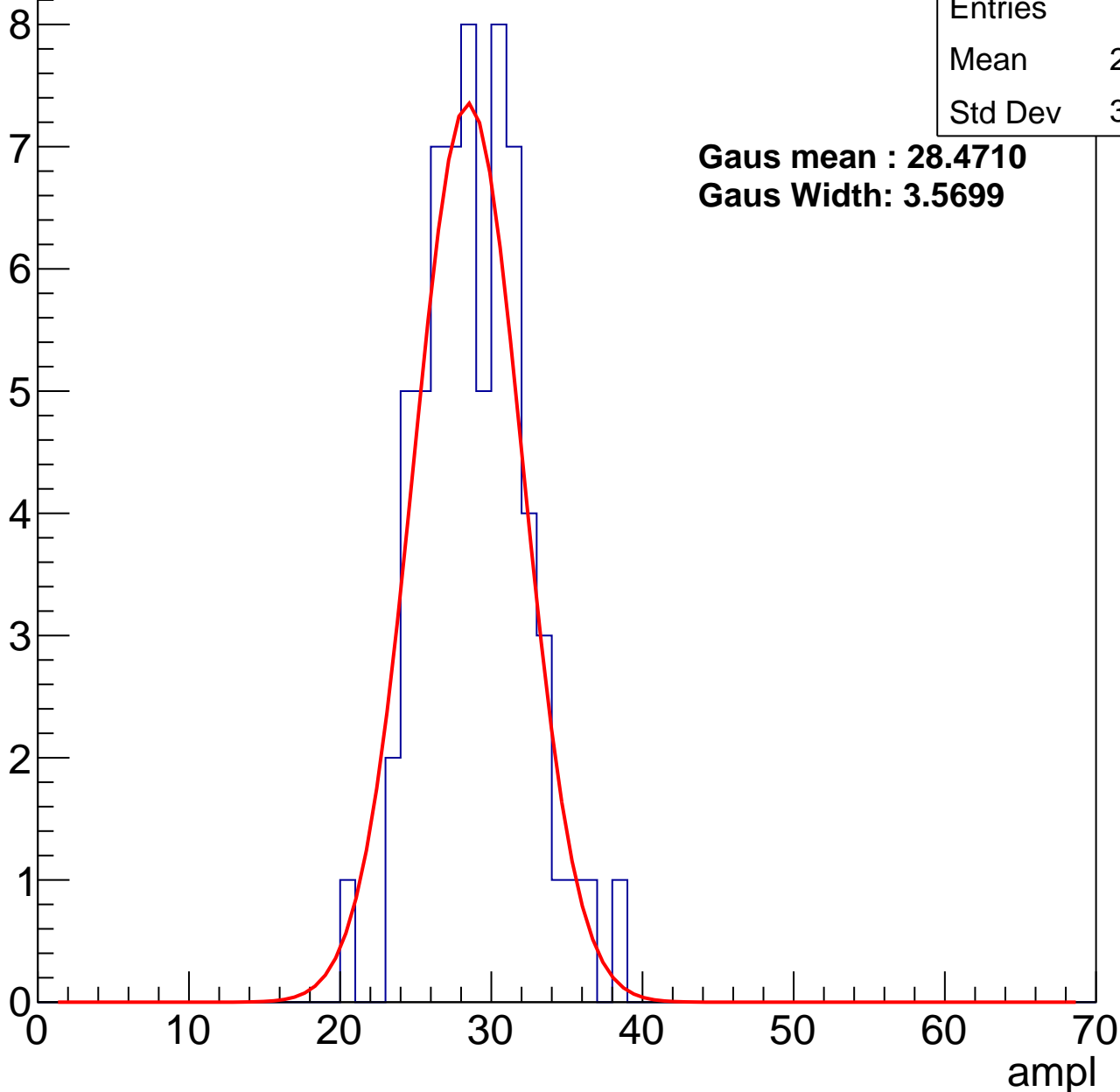
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.45
Std Dev	3.367

Gaus mean : 28.4710

Gaus Width: 3.5699



B1L101S, U14-ch42, adc1

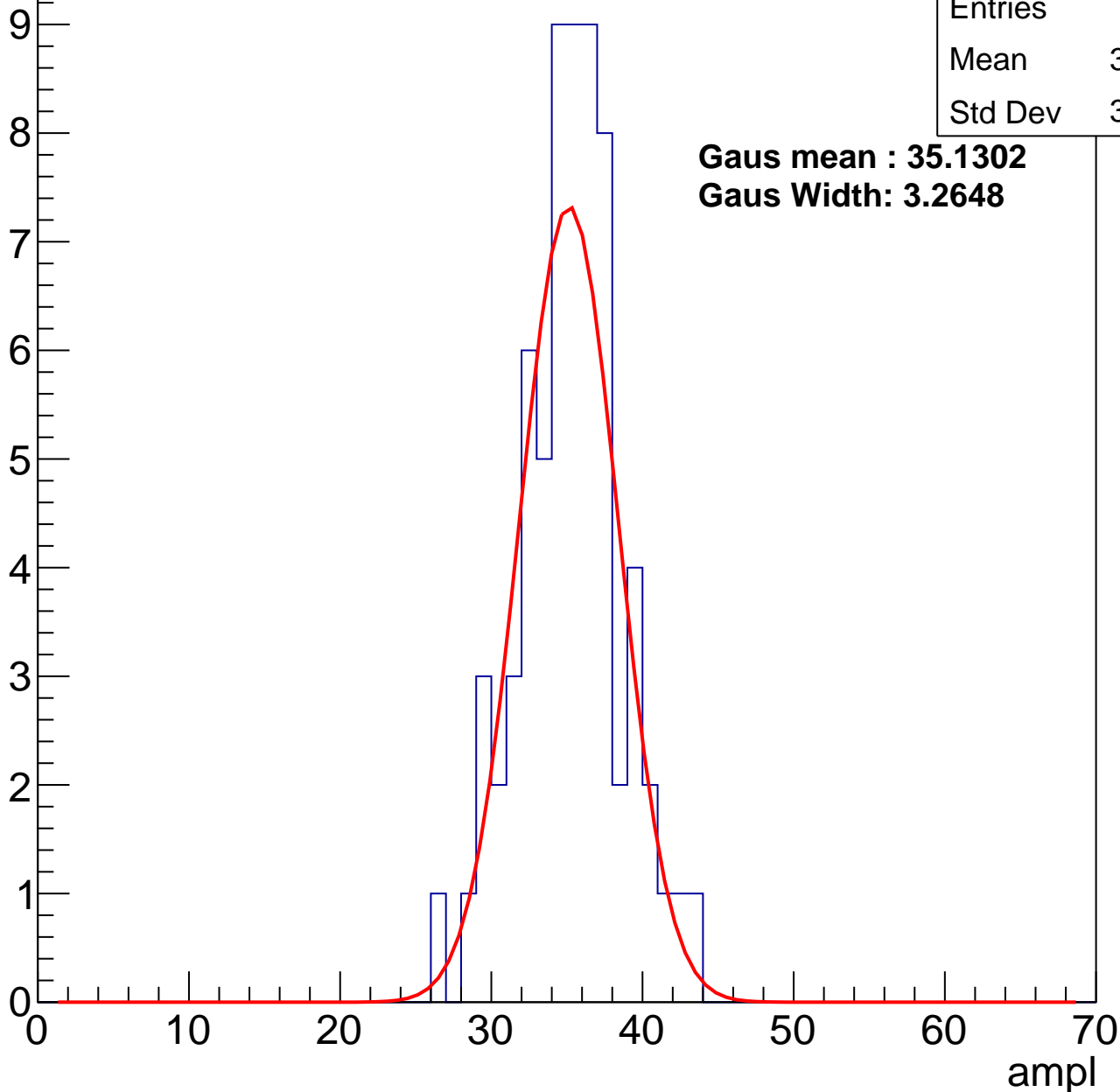
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.78
Std Dev	3.318

Gaus mean : 35.1302

Gaus Width: 3.2648



B1L101S, U14-ch42, adc2

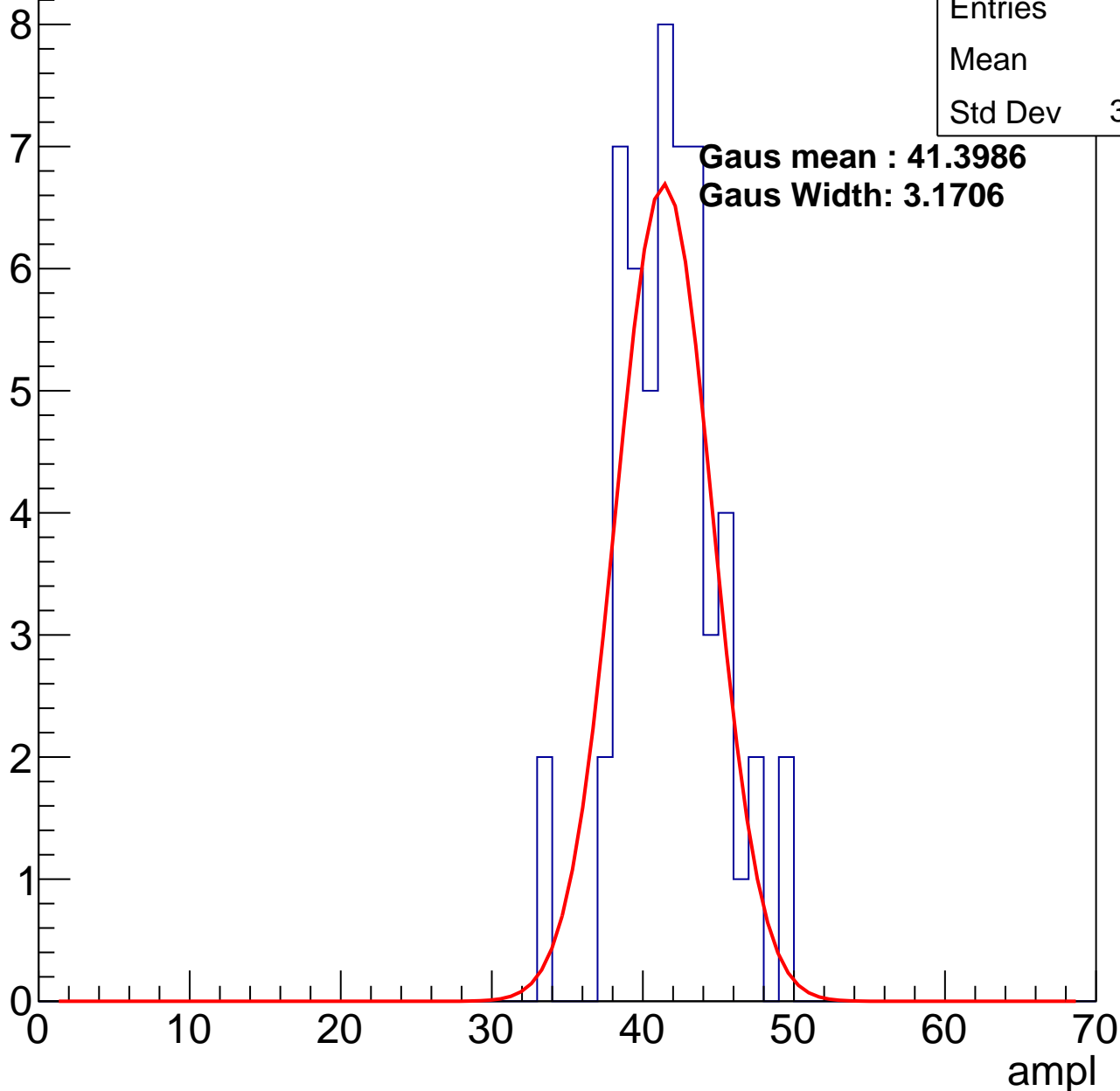
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.3
Std Dev	3.267

Gaus mean : 41.3986

Gaus Width: 3.1706

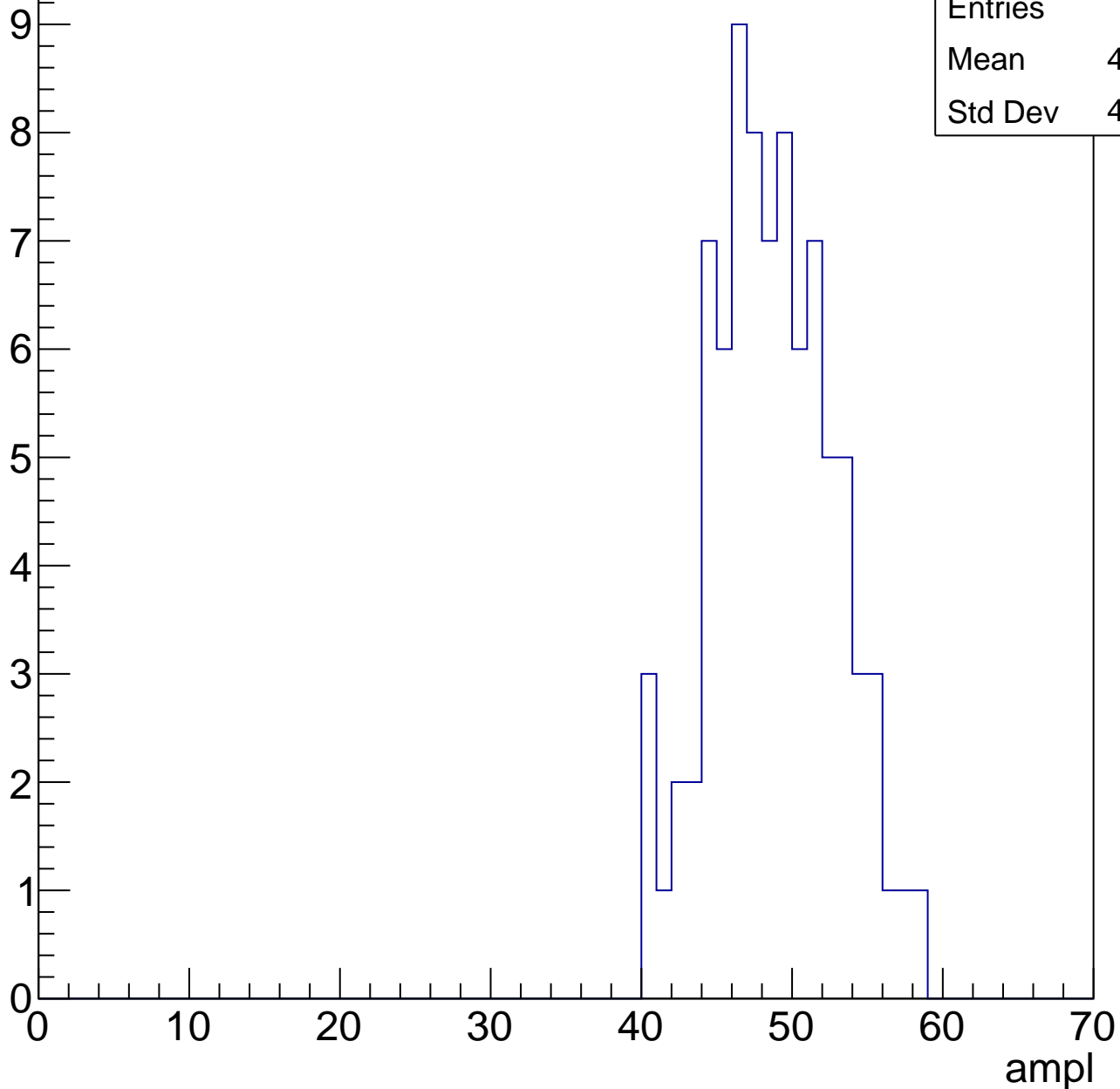


B1L101S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	48.32
Std Dev	4.006

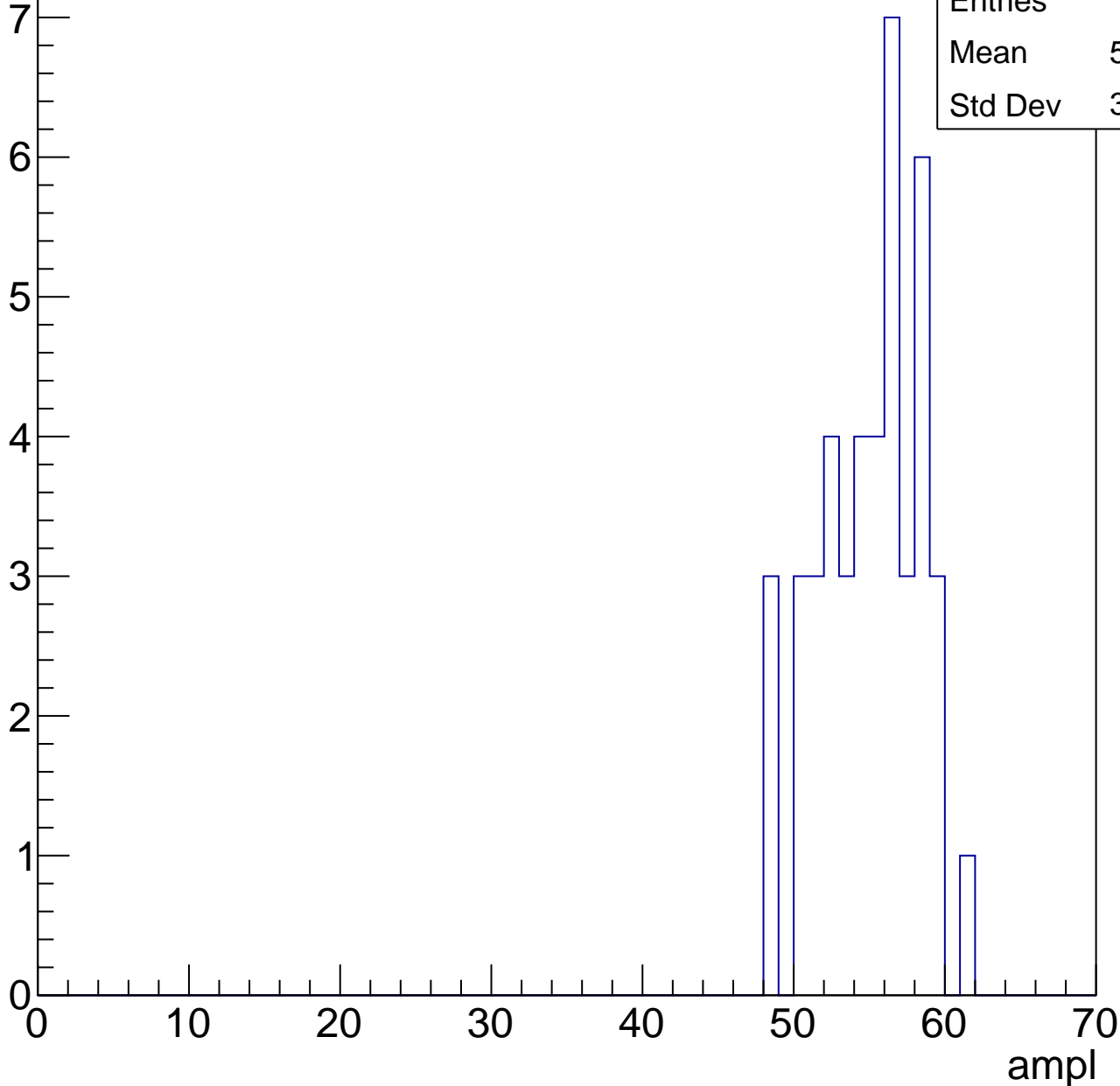


B1L101S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	54.52
Std Dev	3.265

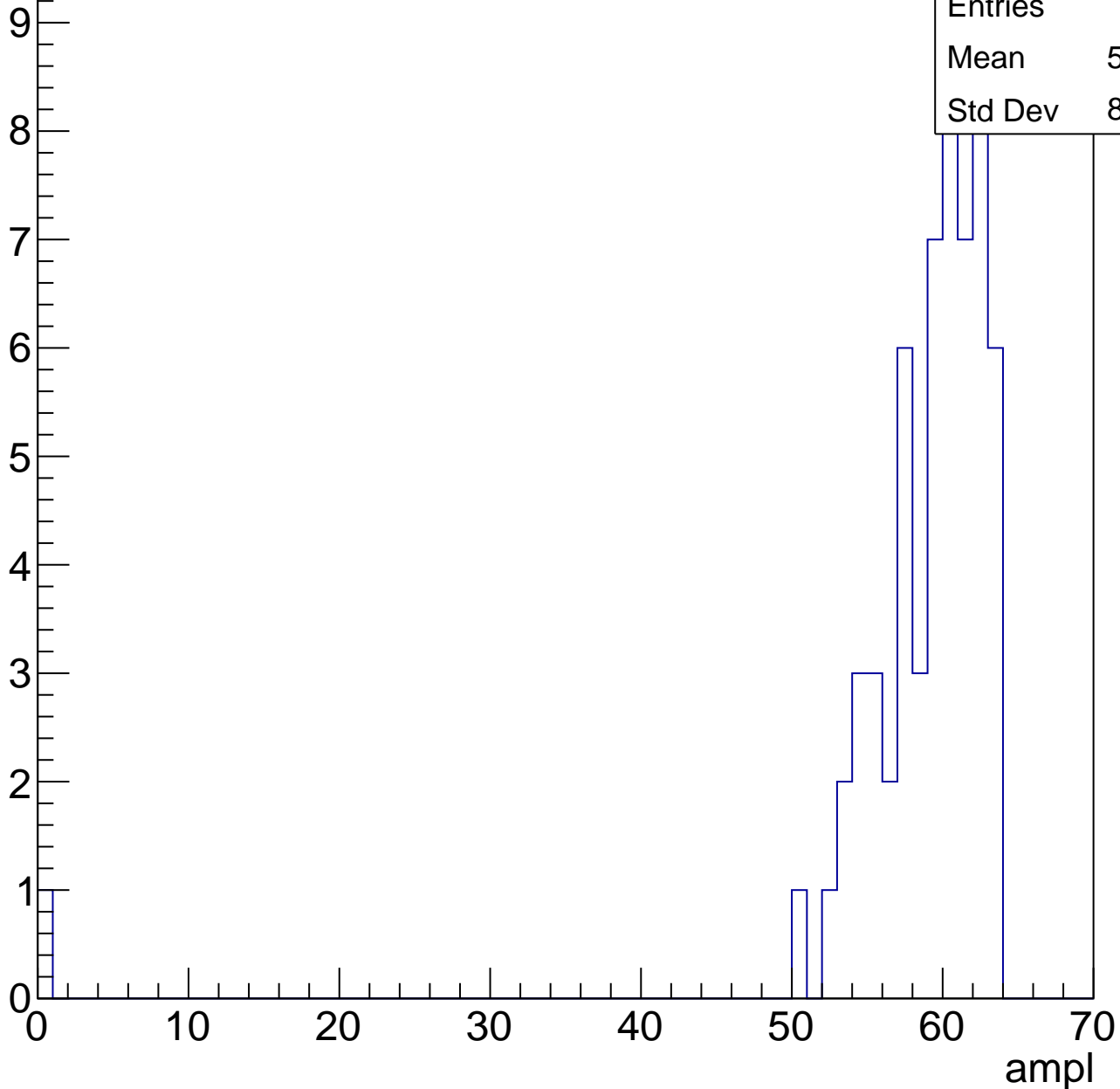


B1L101S, U14-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.98
Std Dev	8.168



B1L101S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch43, adc0

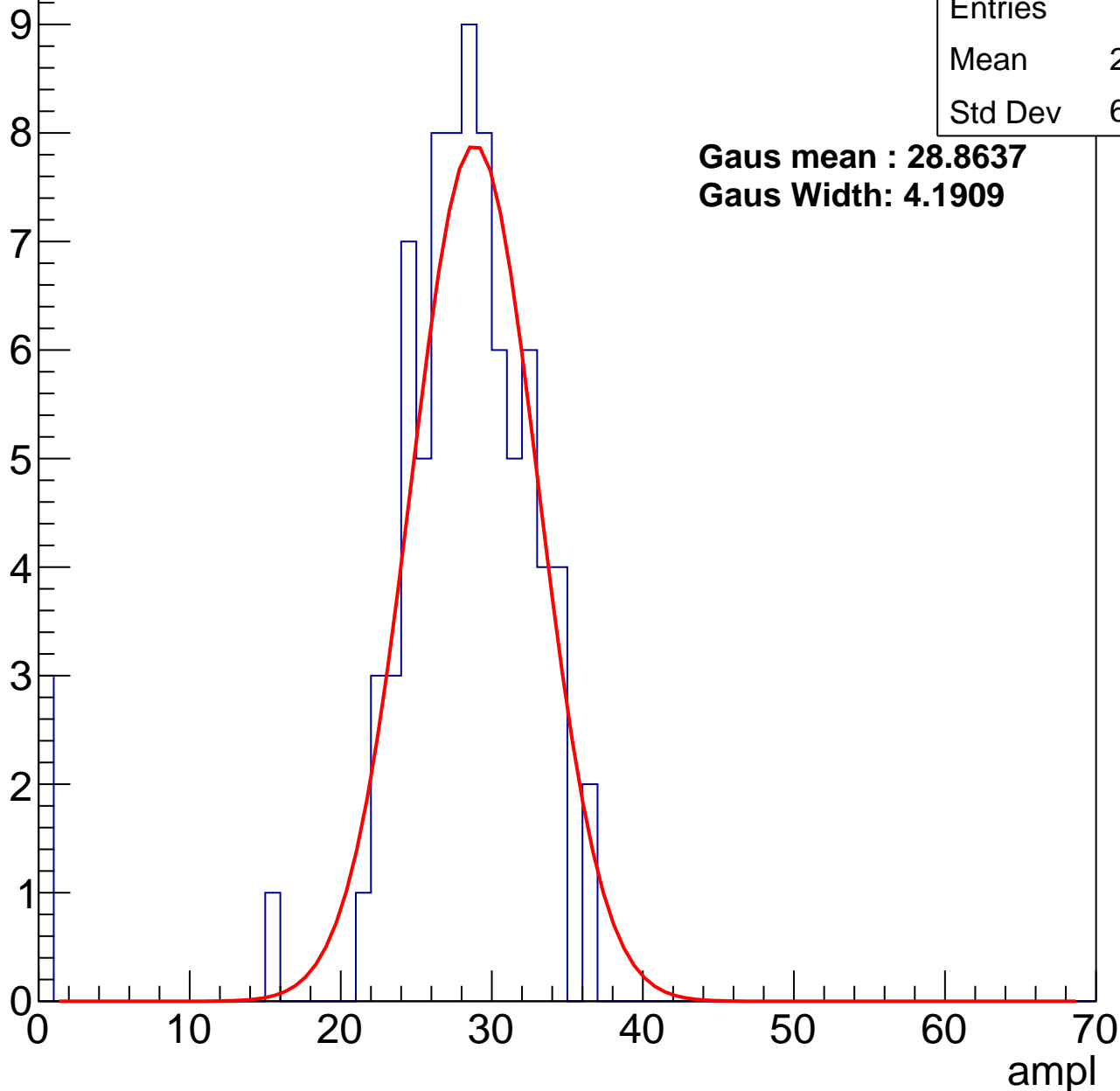
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	26.98
Std Dev	6.408

Gaus mean : 28.8637

Gaus Width: 4.1909



B1L101S, U14-ch43, adc1

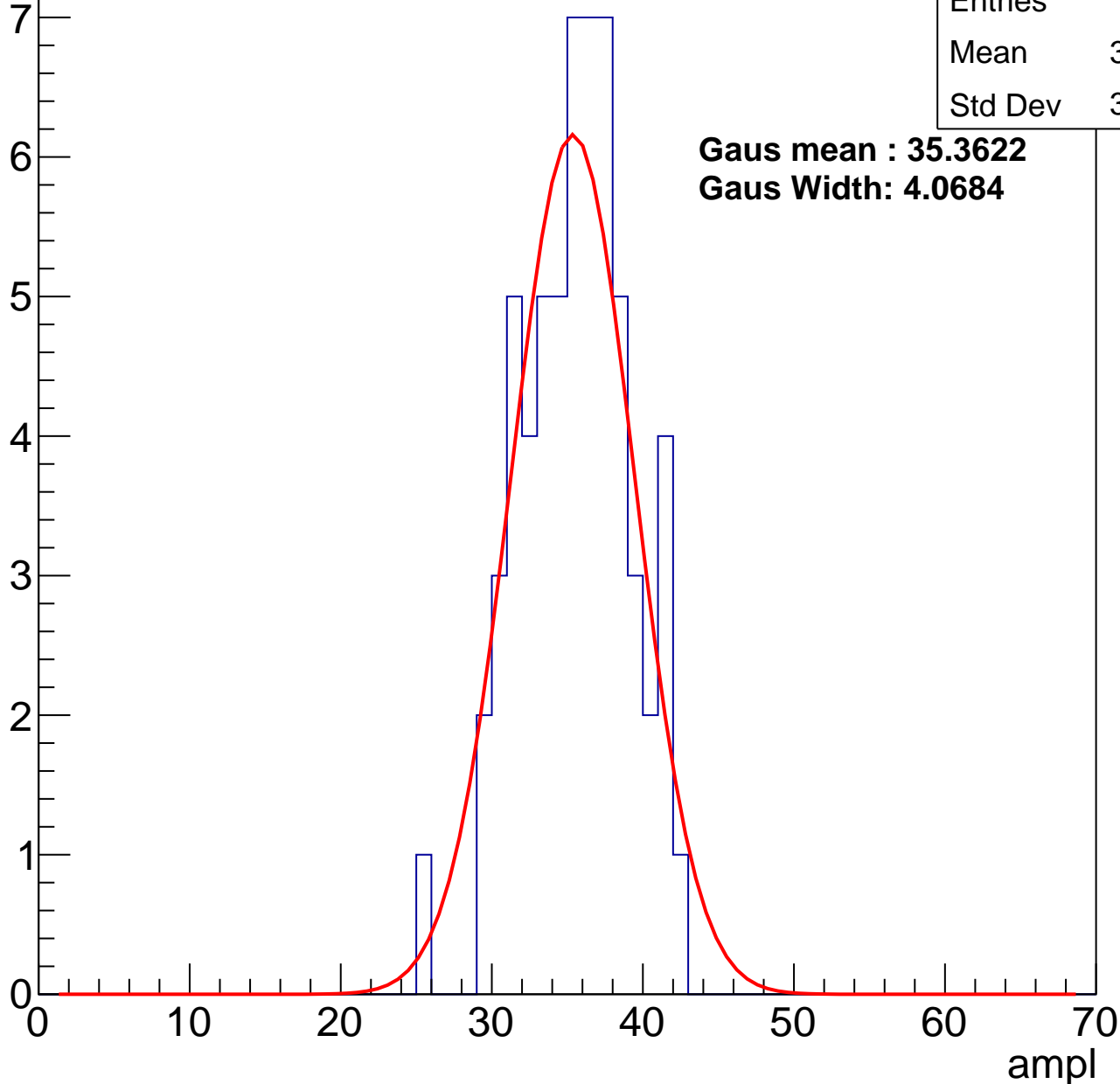
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.08
Std Dev	3.526

Gaus mean : 35.3622

Gaus Width: 4.0684



B1L101S, U14-ch43, adc2

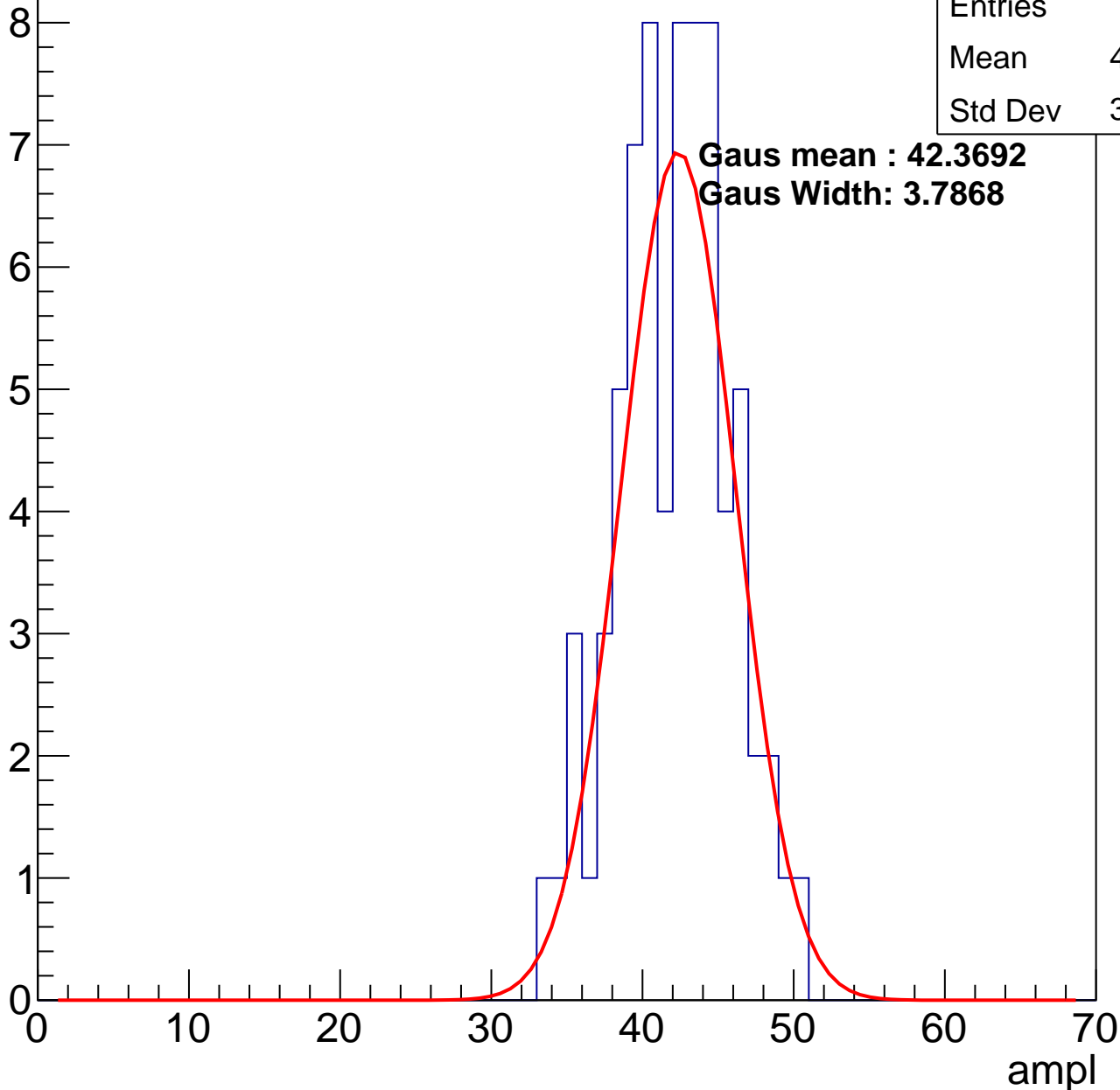
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	41.62
Std Dev	3.657

Gaus mean : 42.3692

Gaus Width: 3.7868

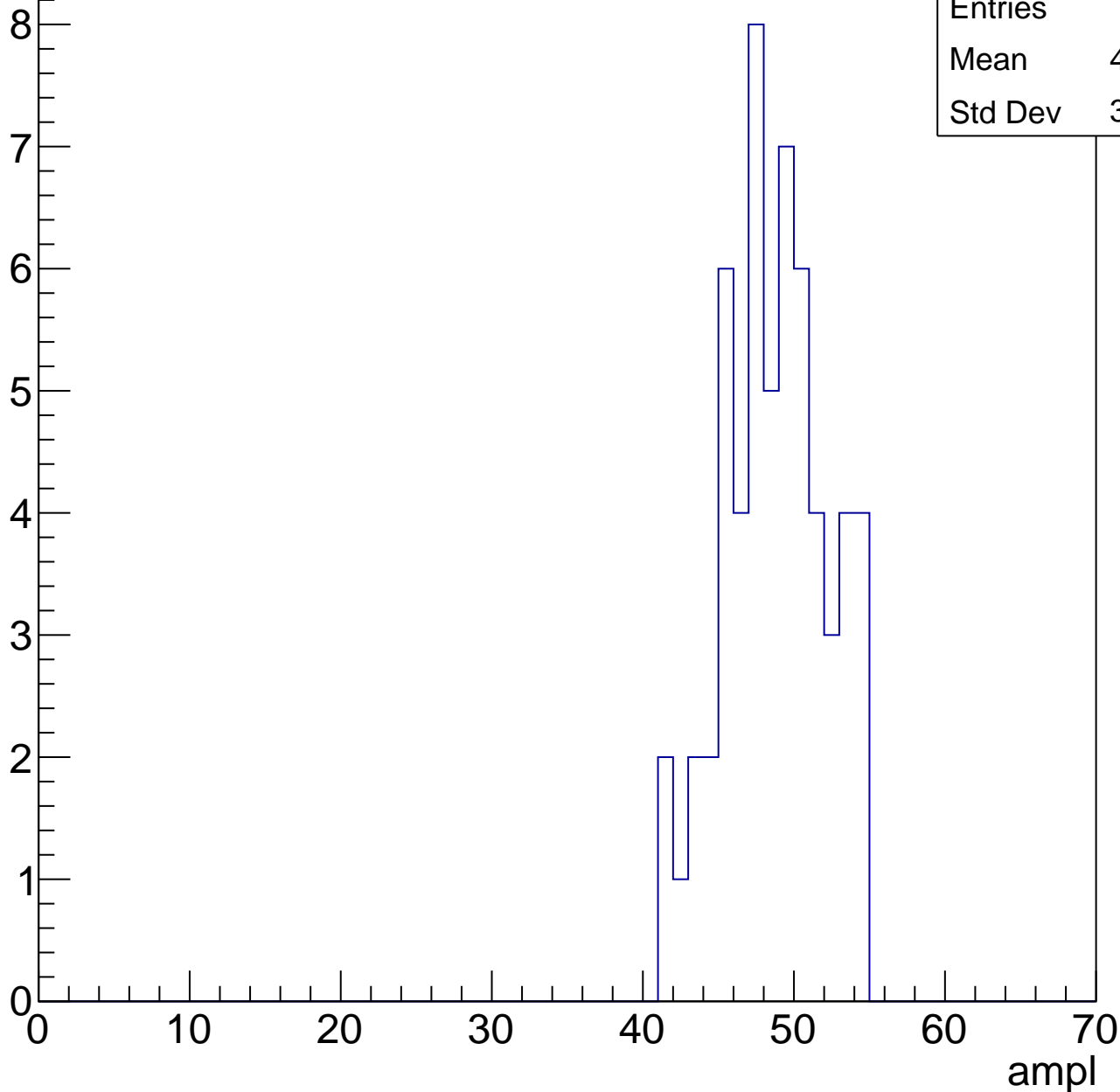


B1L101S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.26
Std Dev	3.345

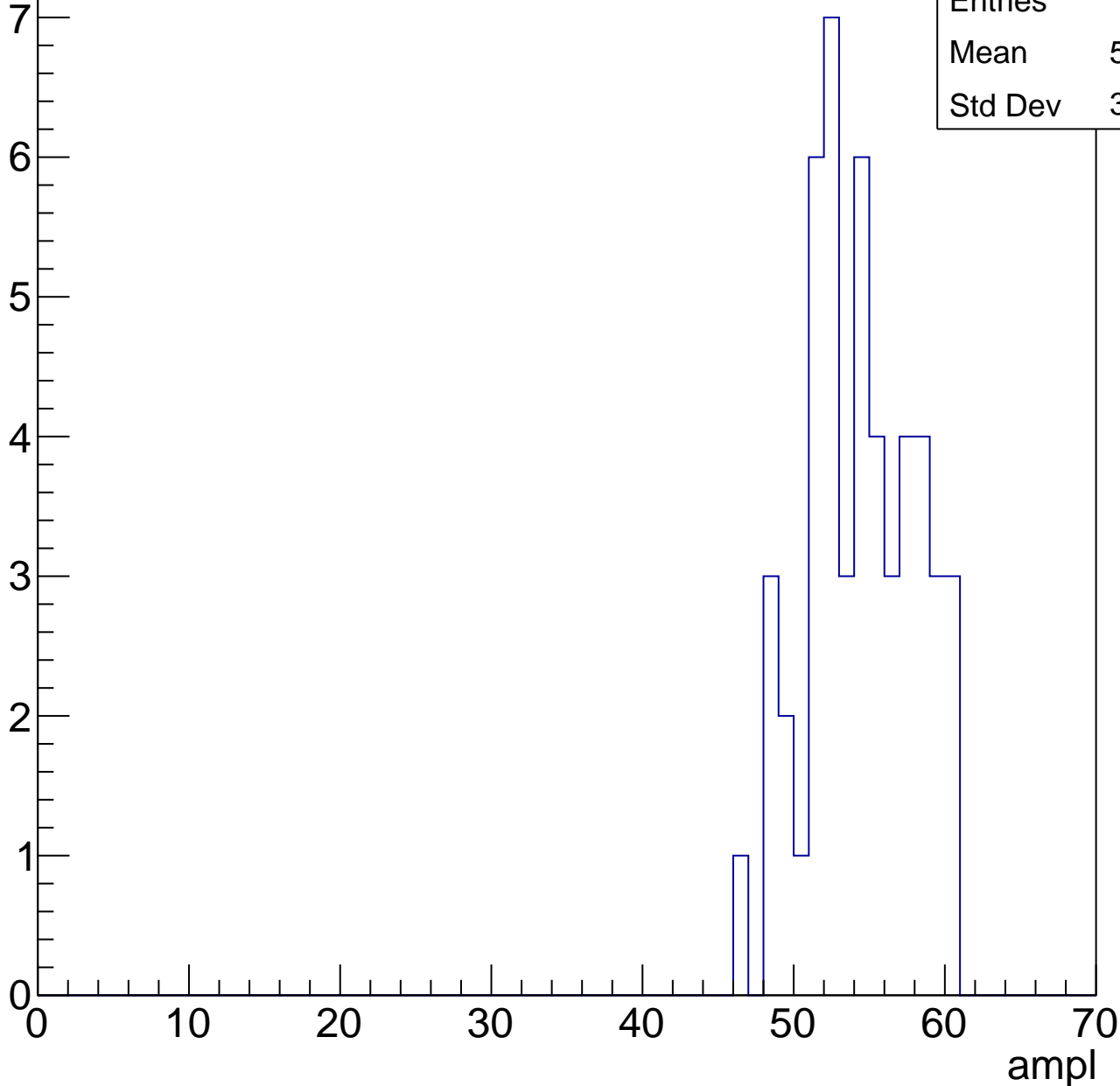


B1L101S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	53.92
Std Dev	3.526

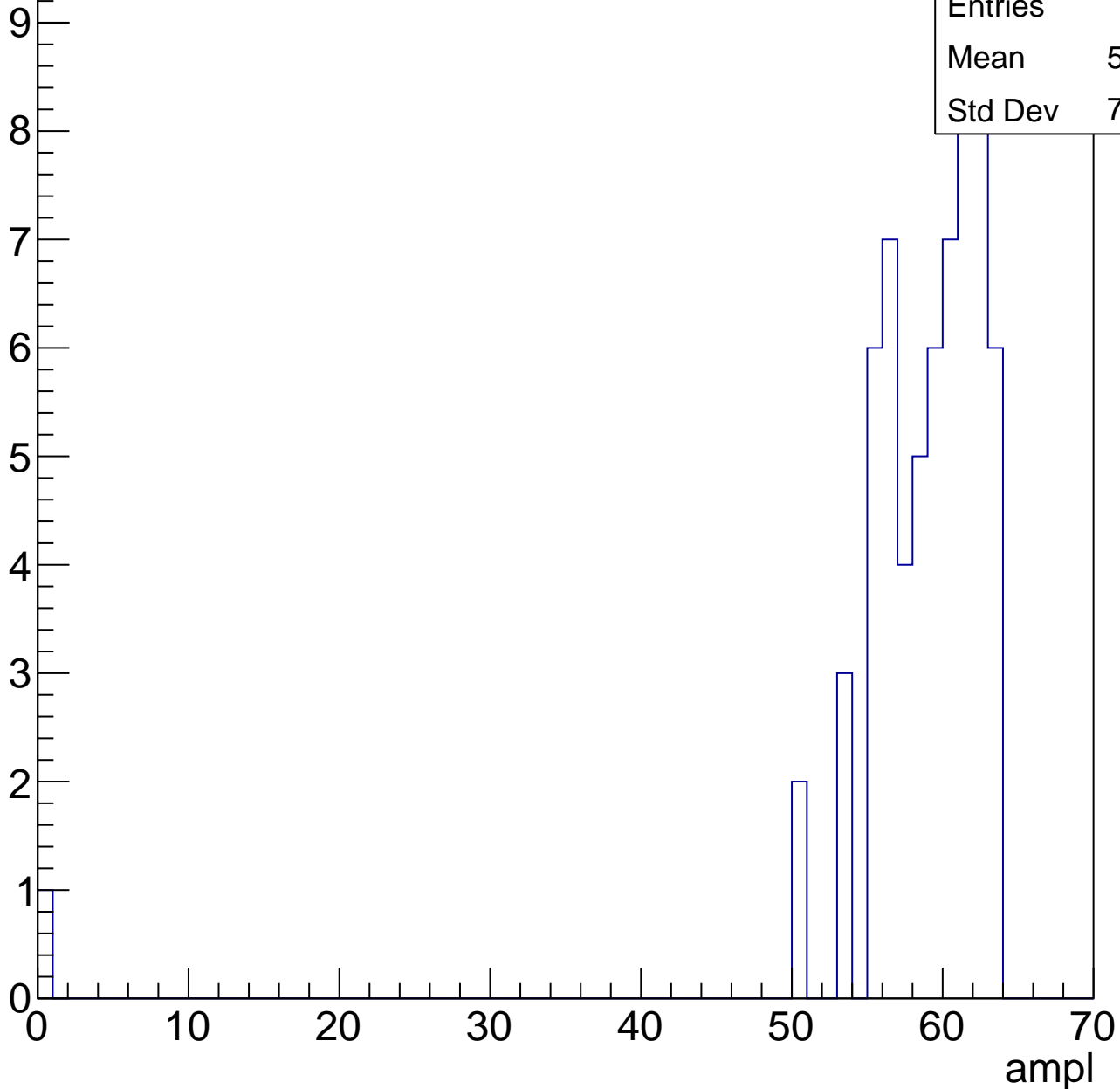


B1L101S, U14-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	57.82
Std Dev	7.903



B1L101S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

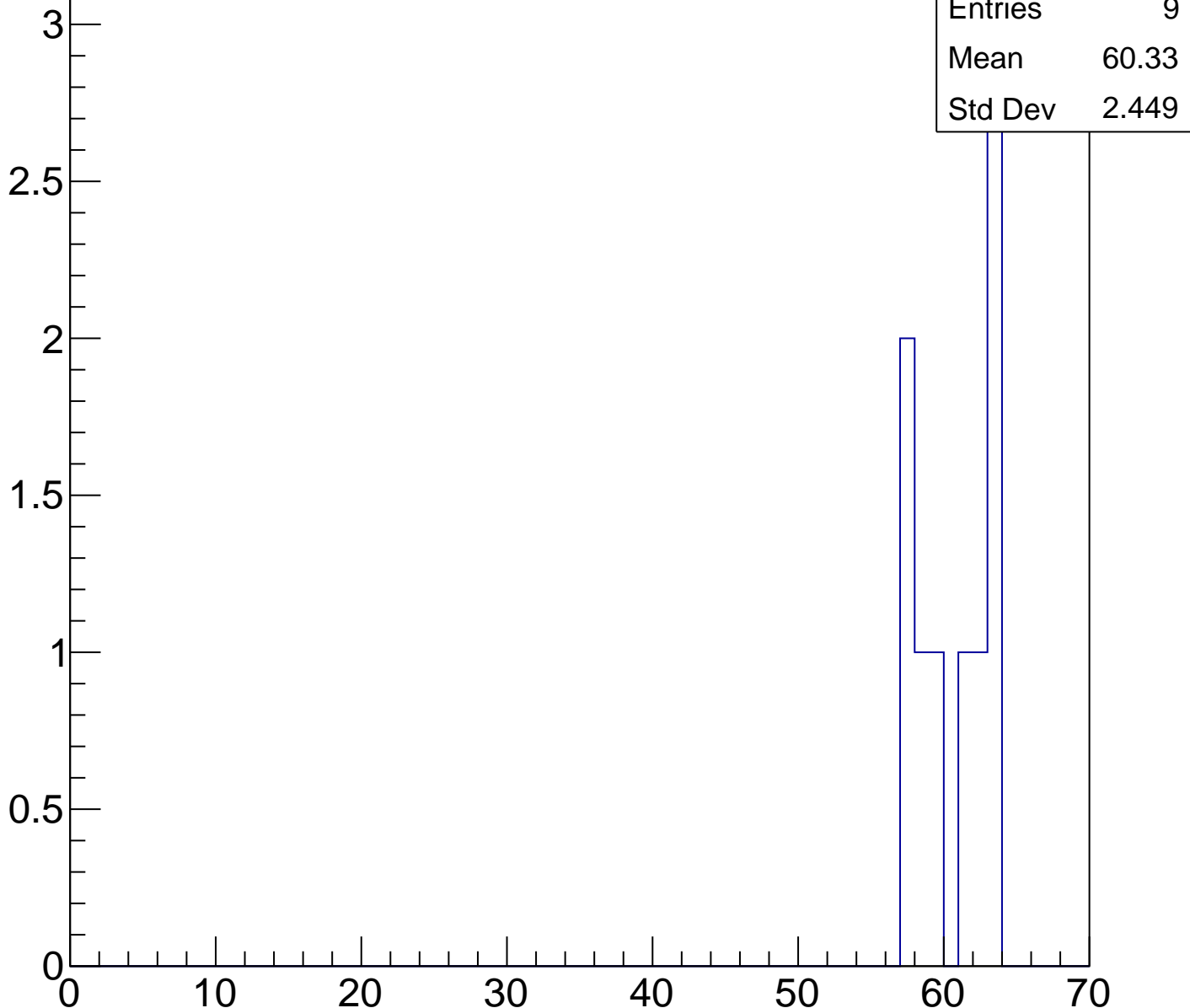
9

Mean

60.33

Std Dev

2.449



B1L101S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch44, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	30.2
Std Dev	4.983

Gaus mean : 30.6839

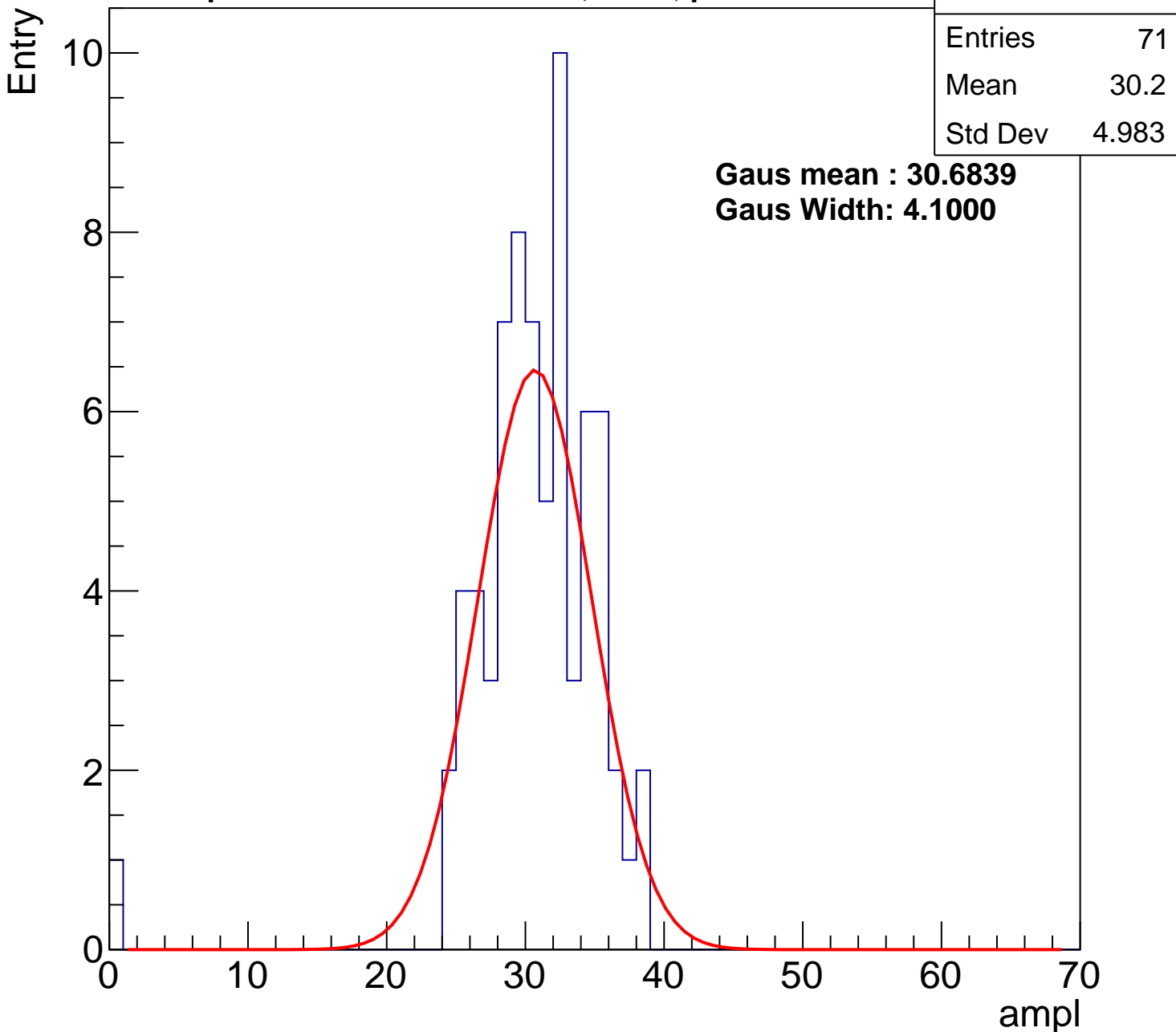
Gaus Width: 4.1000

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch44, adc1

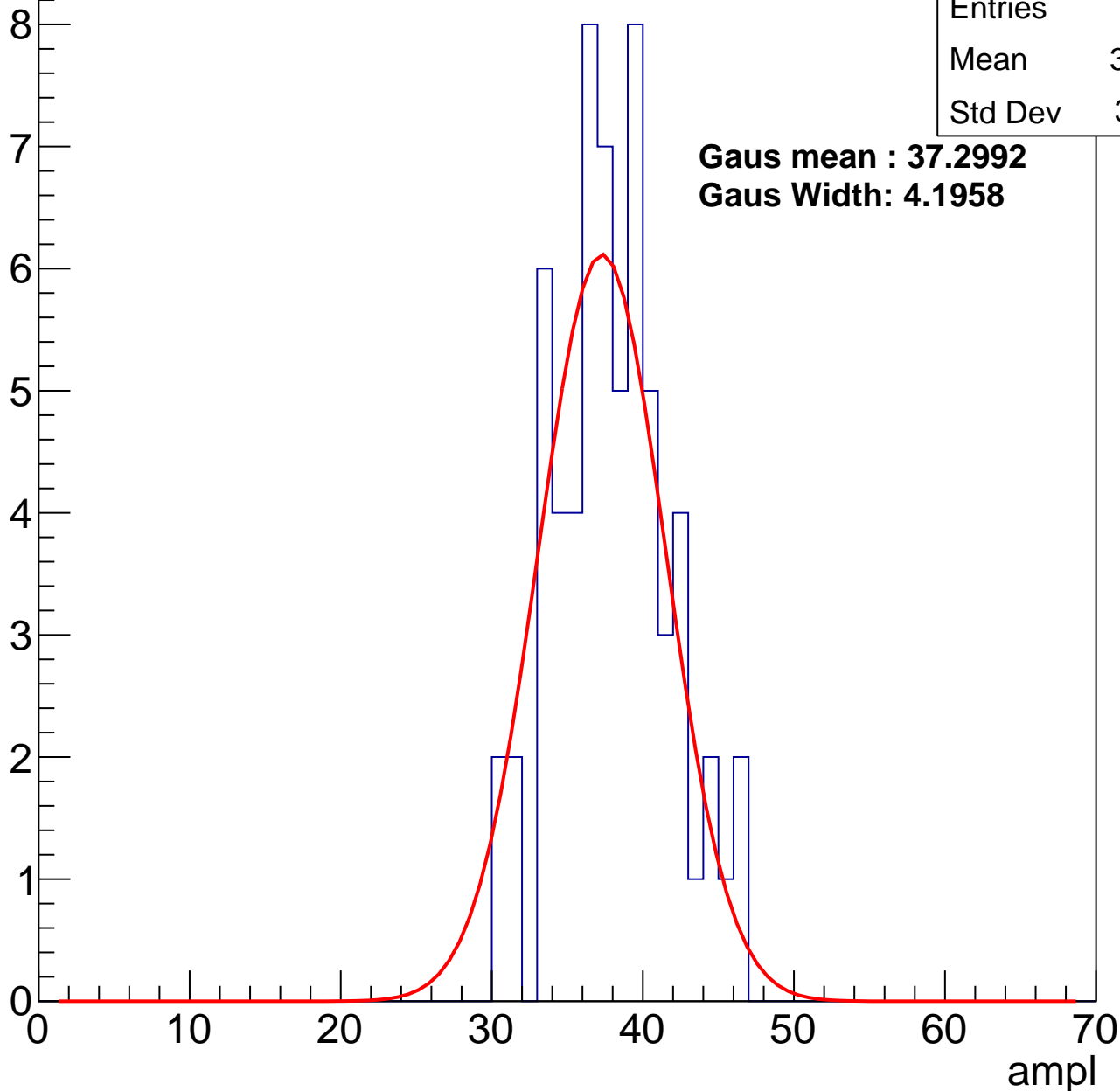
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.56
Std Dev	3.741

Gaus mean : 37.2992

Gaus Width: 4.1958



B1L101S, U14-ch44, adc2

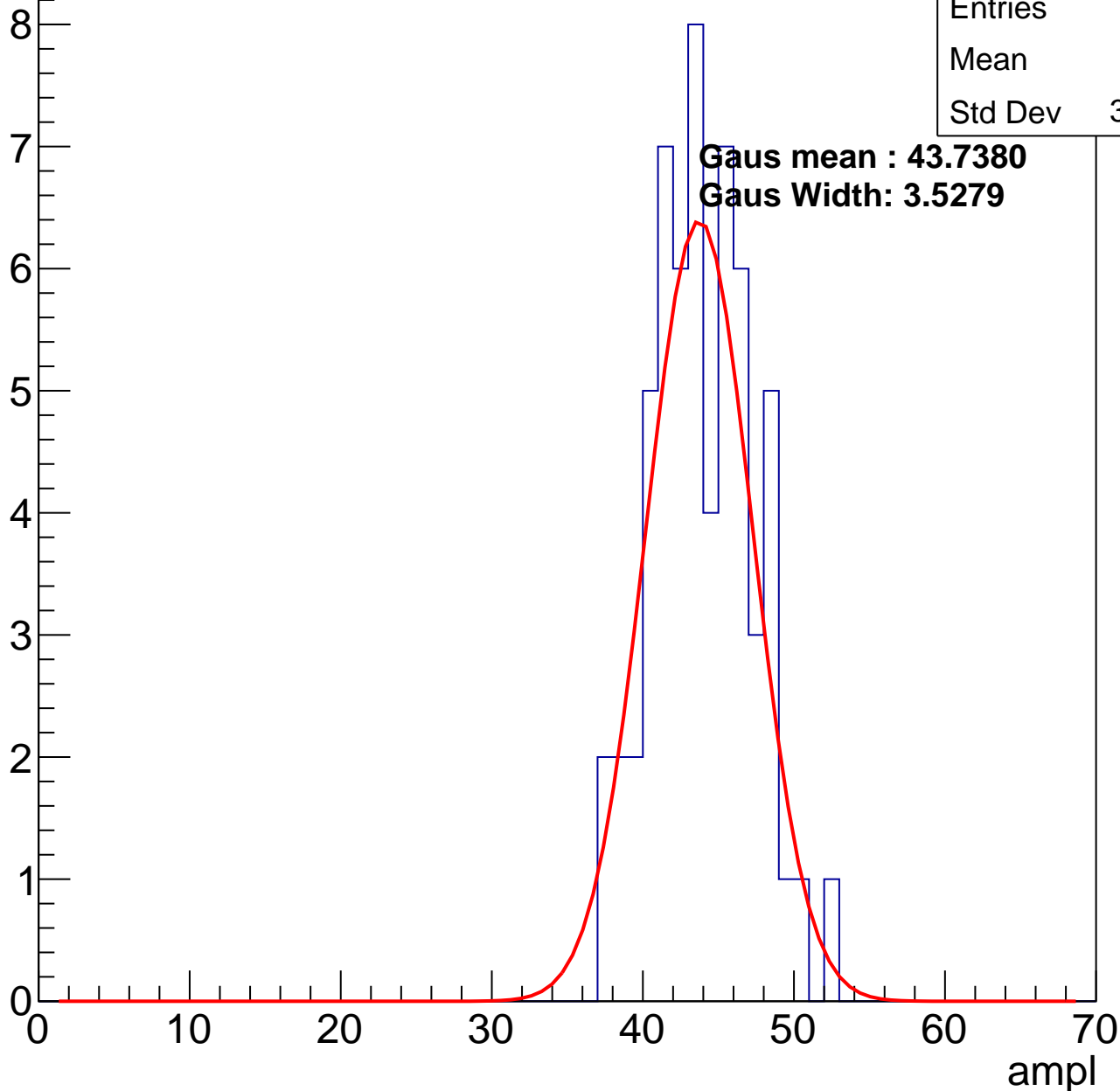
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.5
Std Dev	3.274

Gaus mean : 43.7380

Gaus Width: 3.5279

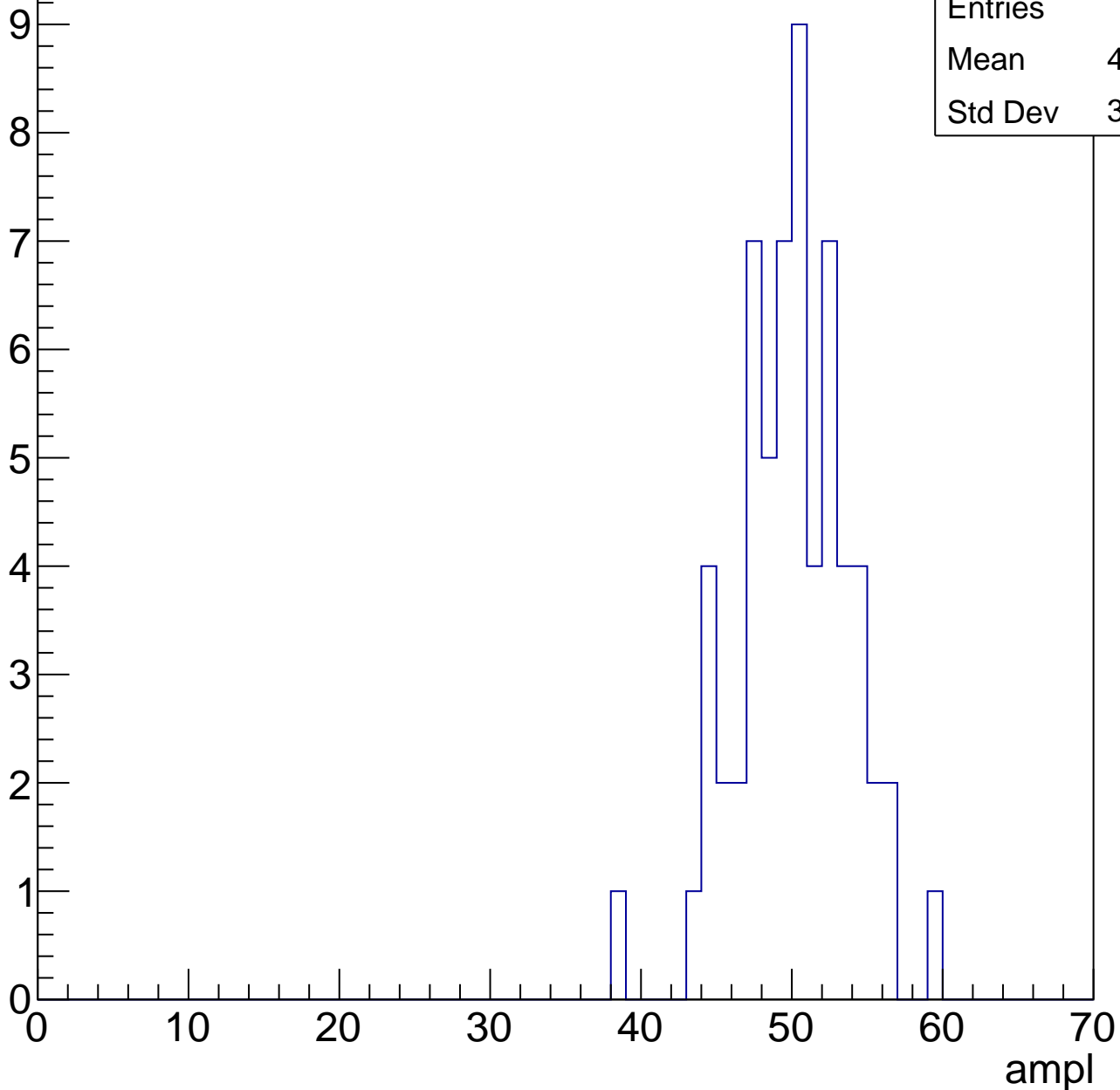


B1L101S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

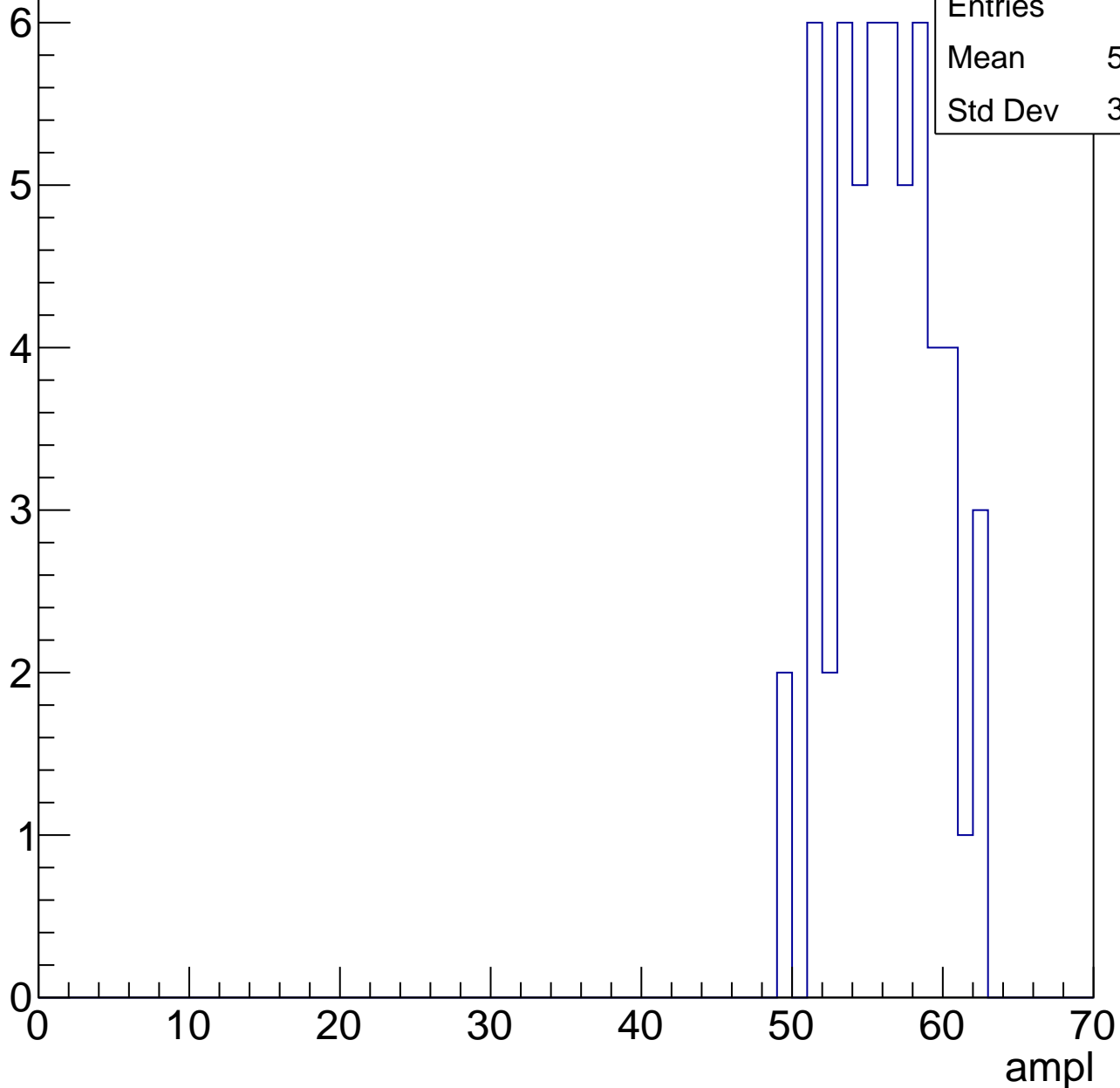
Entries	62
Mean	49.65
Std Dev	3.672



B1L101S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



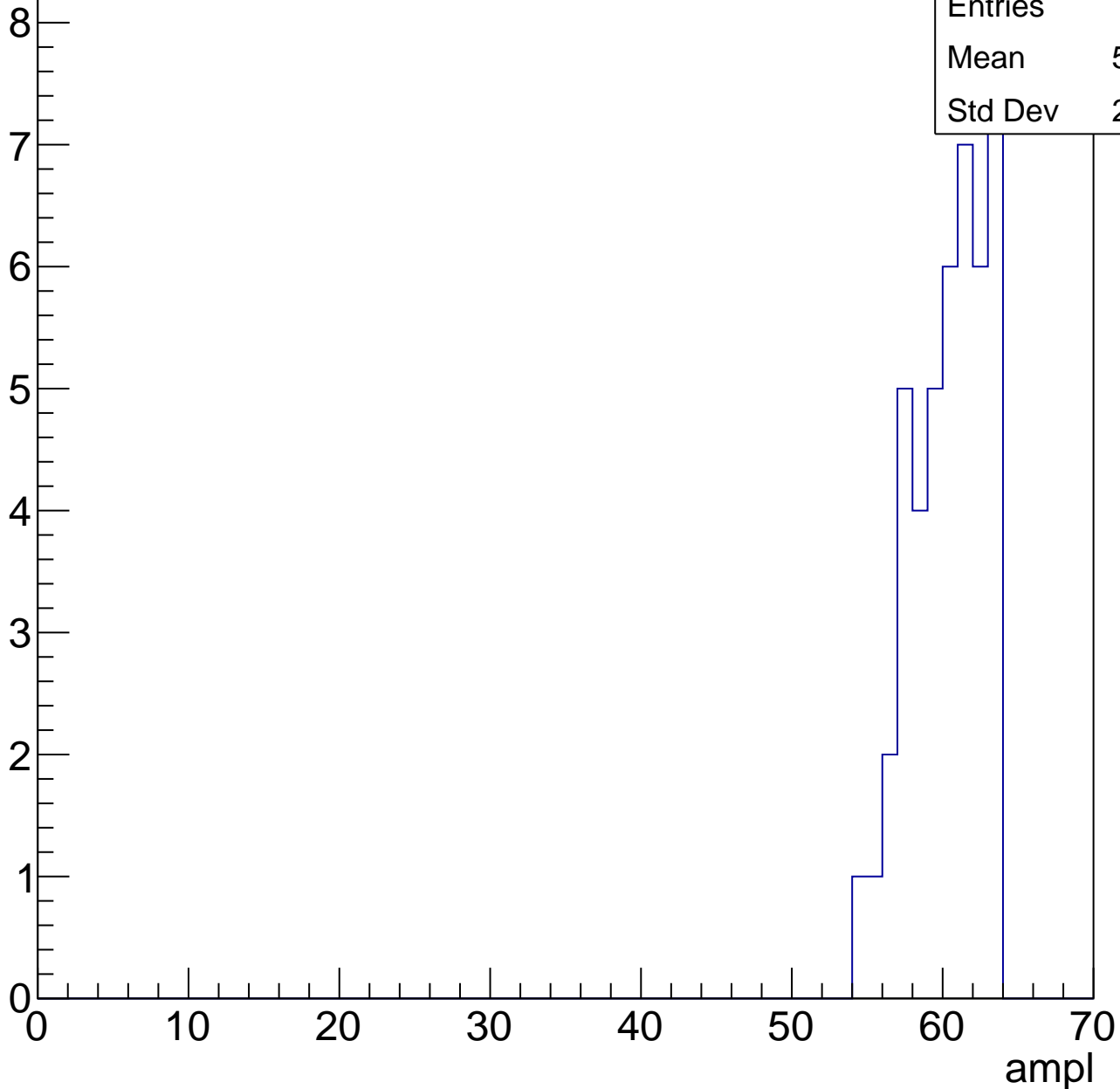
Entries	56
Mean	55.68
Std Dev	3.317

B1L101S, U14-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.91
Std Dev	2.411



B1L101S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

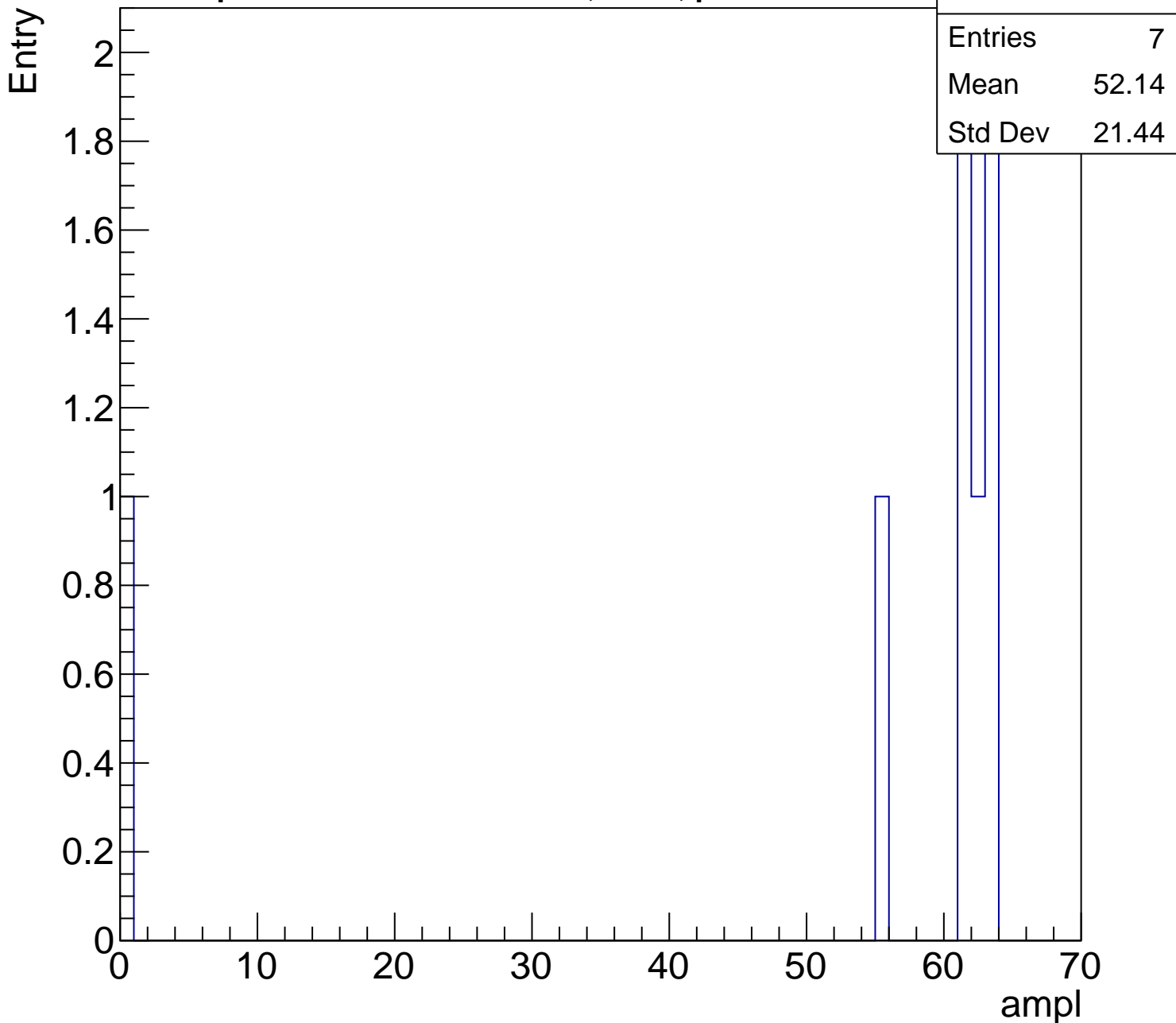
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.14
Std Dev	21.44

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch45, adc0

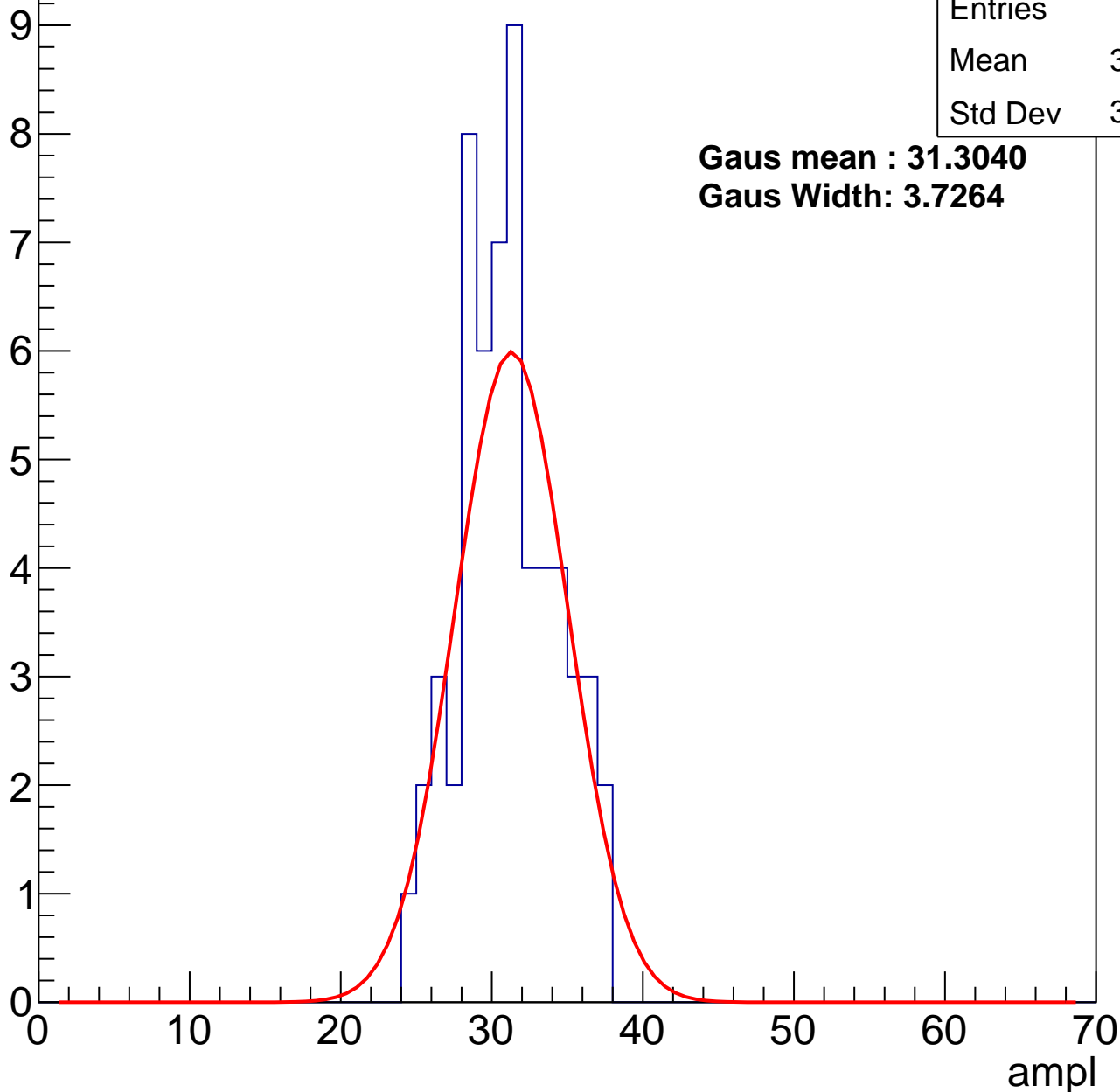
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	30.62
Std Dev	3.145

Gaus mean : 31.3040

Gaus Width: 3.7264



B1L101S, U14-ch45, adc1

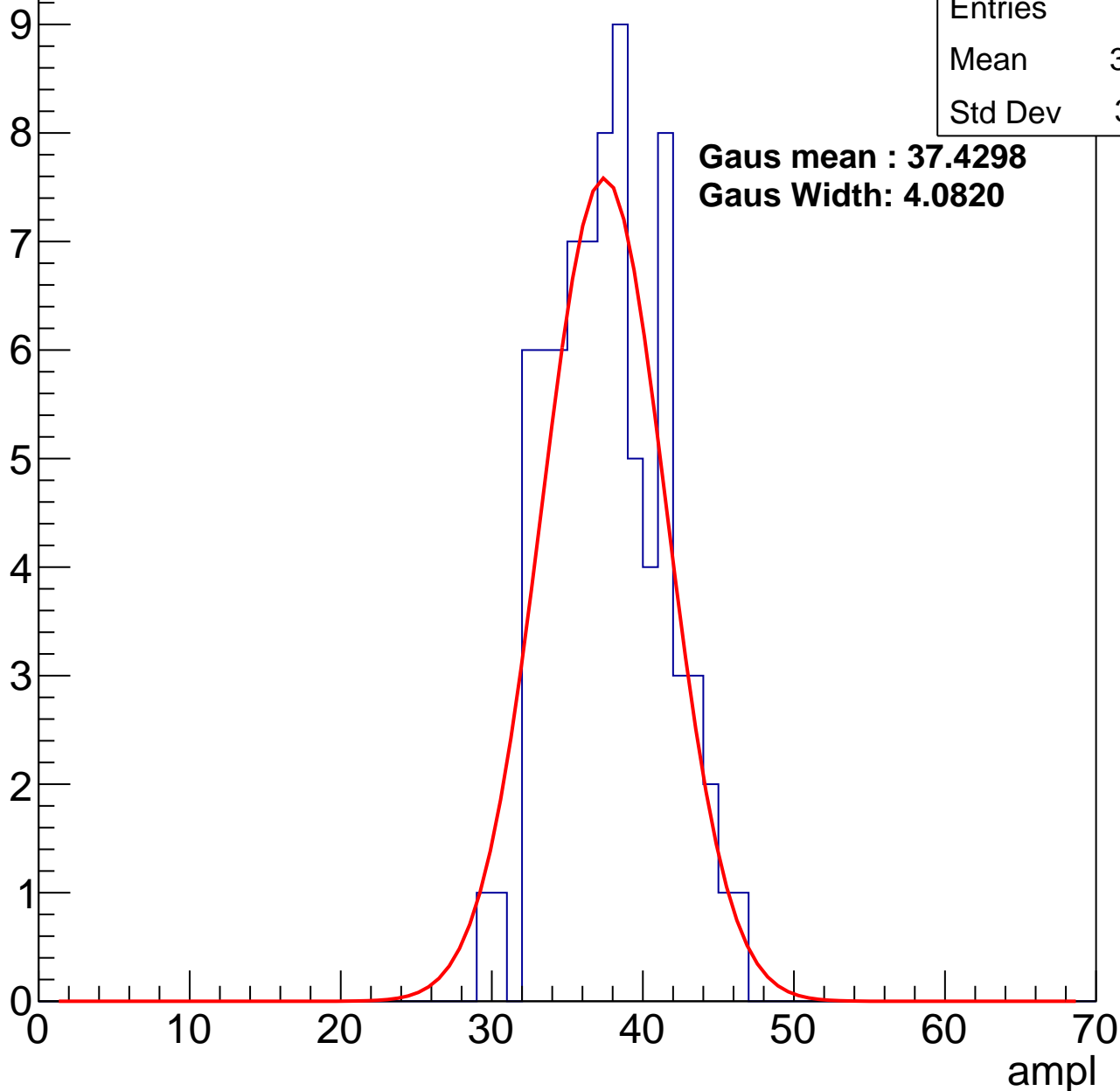
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.24
Std Dev	3.691

Gaus mean : 37.4298

Gaus Width: 4.0820

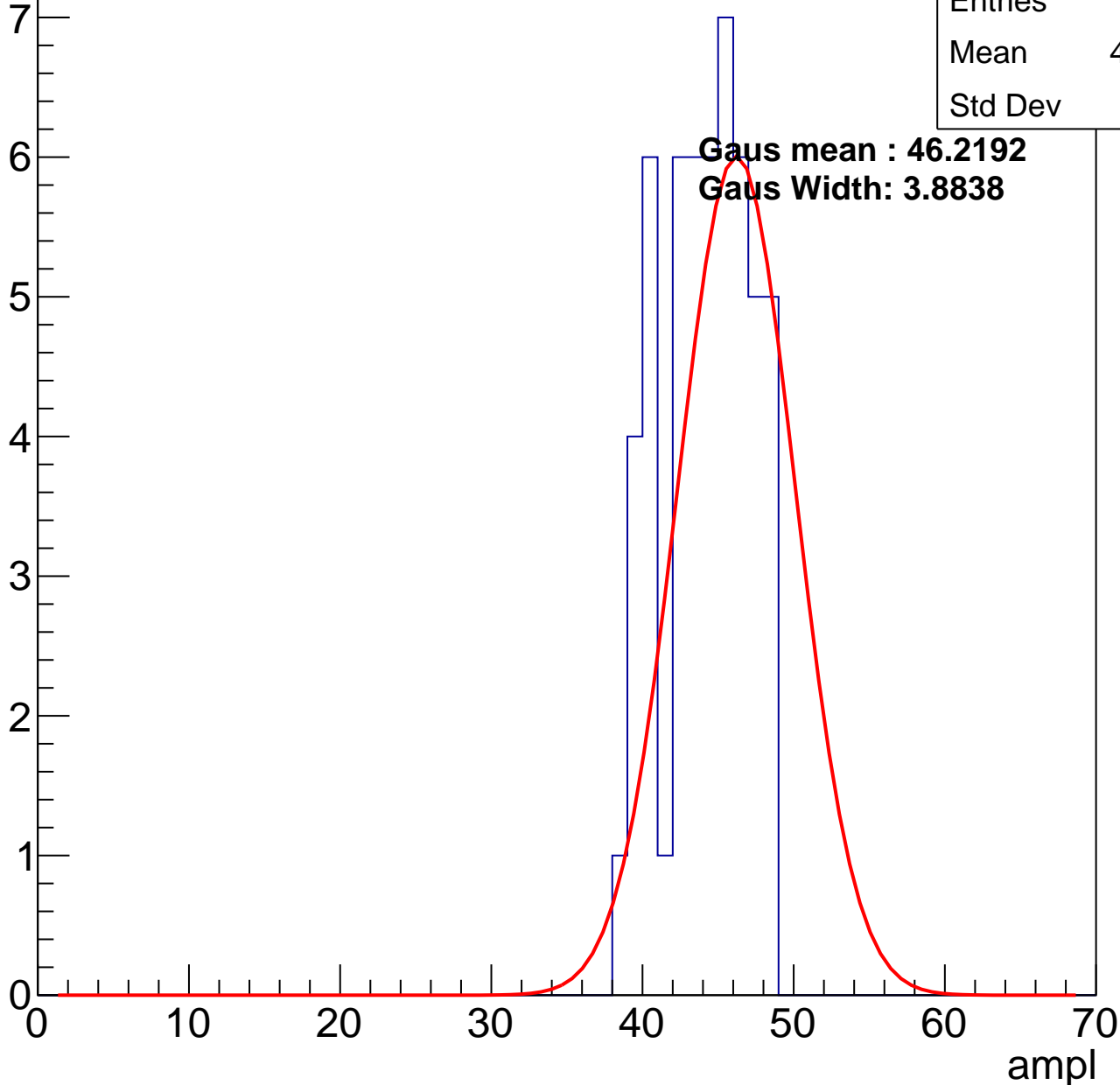


B1L101S, U14-ch45, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	43.68
Std Dev	2.82



B1L101S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	50.25
Std Dev	3.878

Entry

10

8

6

4

2

0

0

10

20

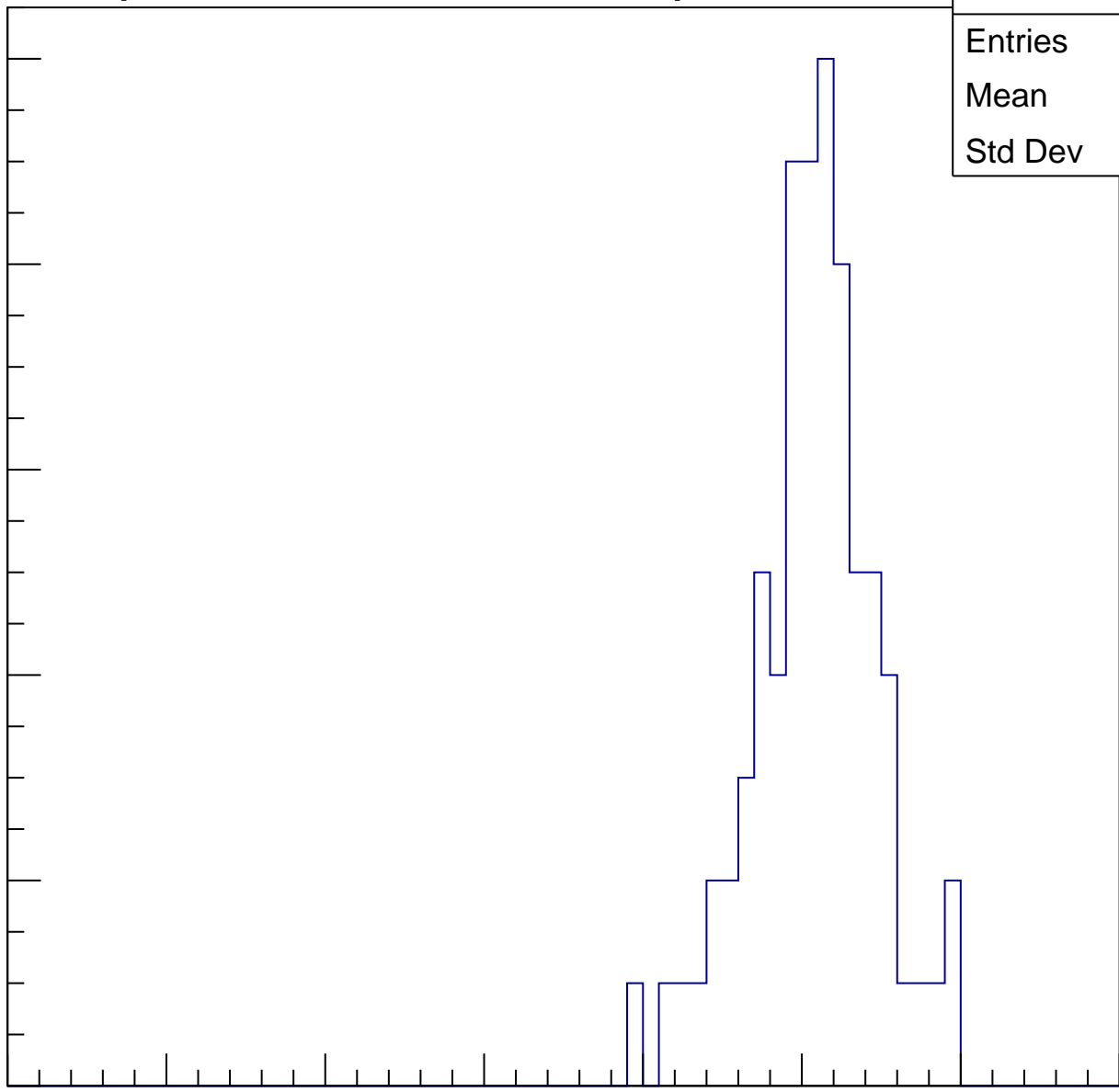
30

40

50

60

ampl

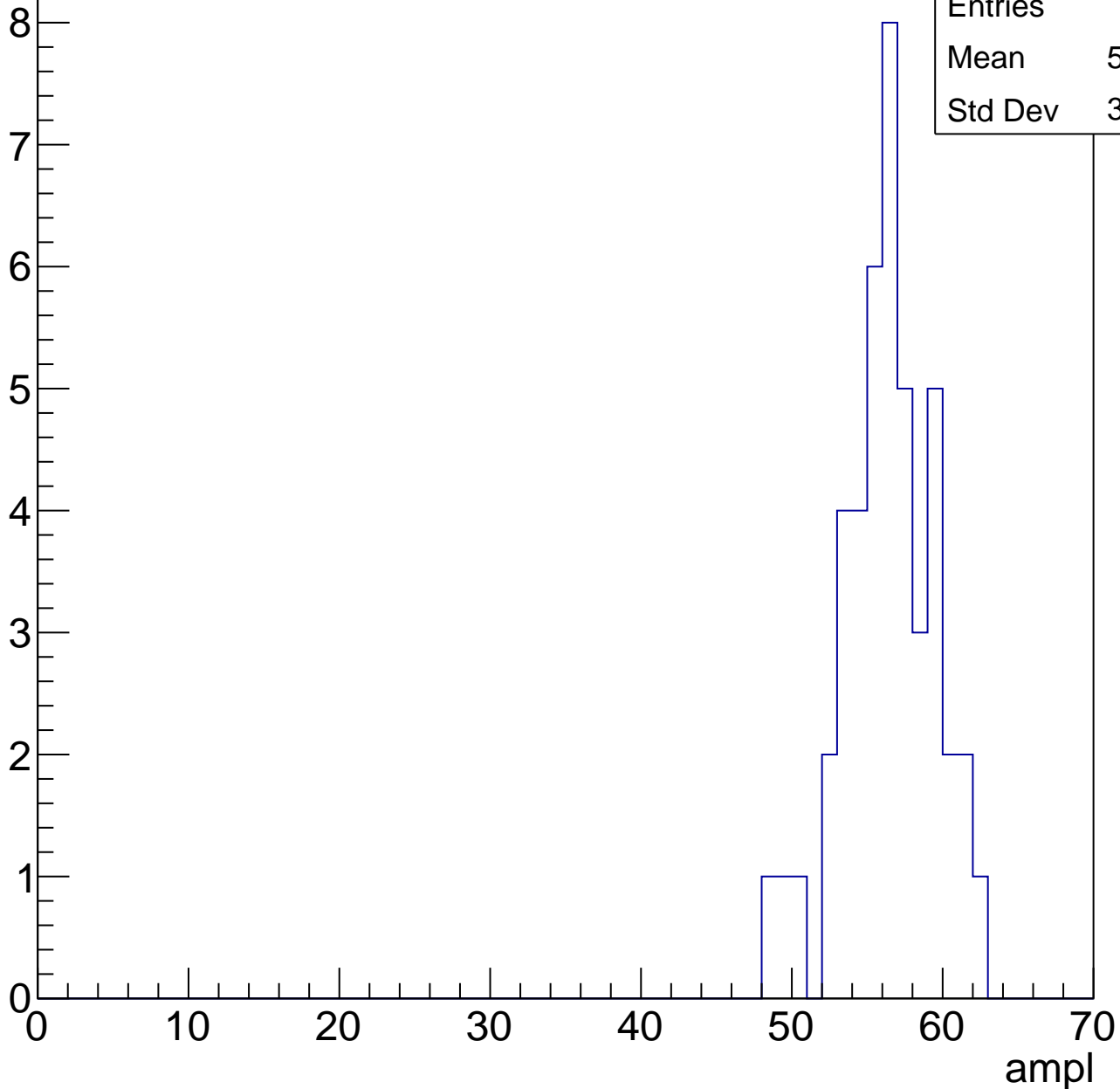


B1L101S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.89
Std Dev	3.049

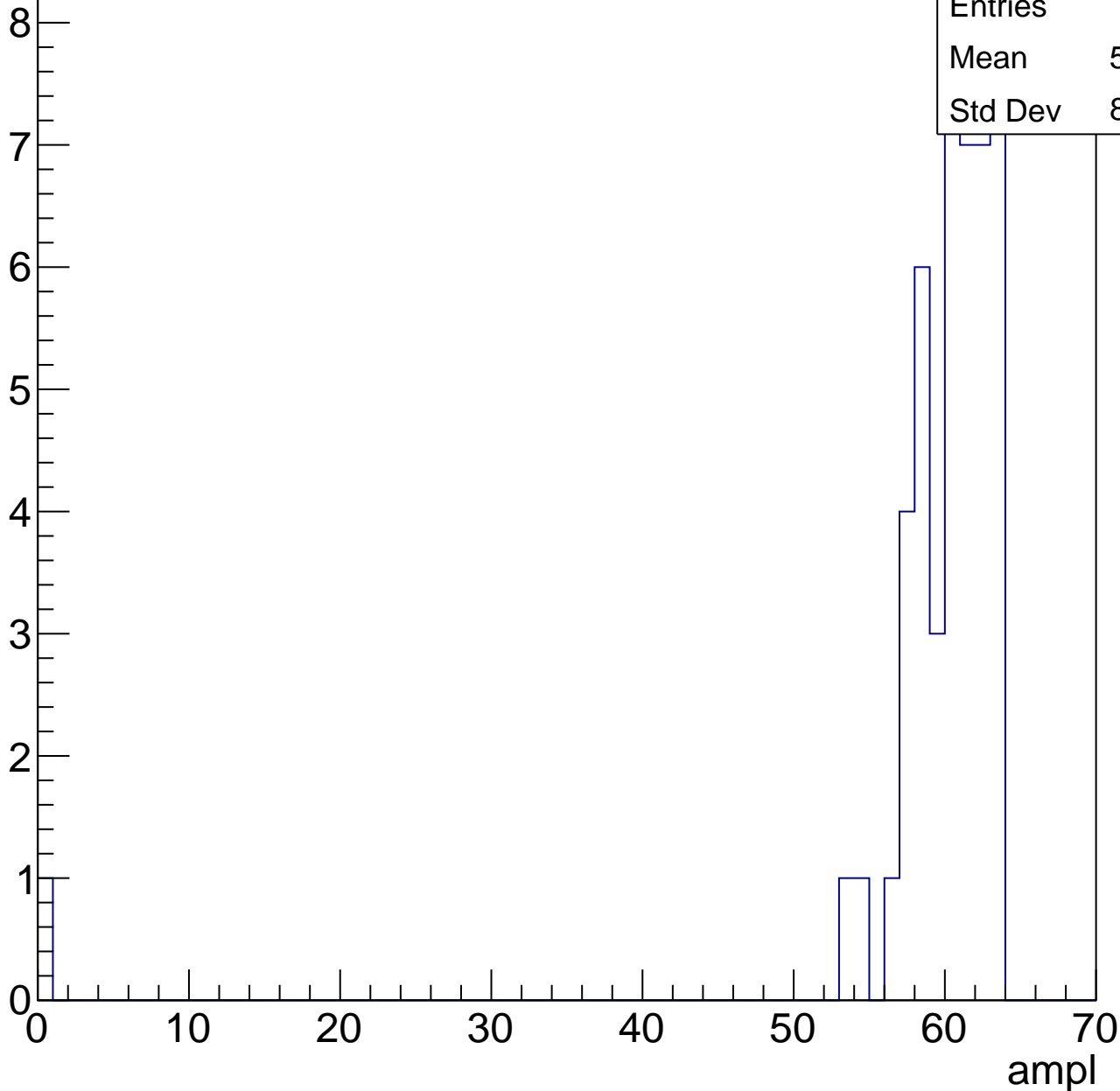


B1L101S, U14-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

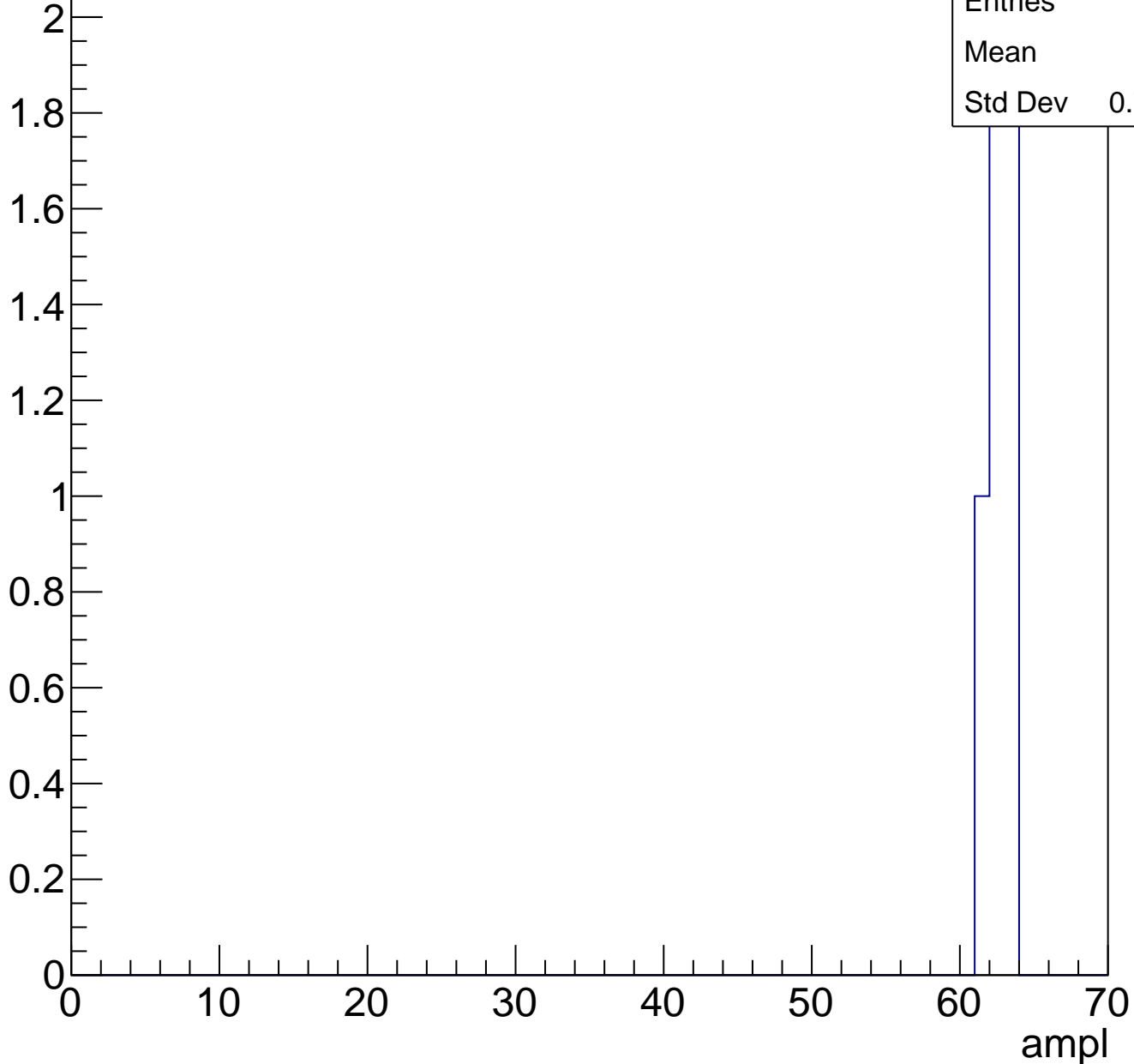
Entries	47
Mean	58.74
Std Dev	8.988



B1L101S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch46, adc0

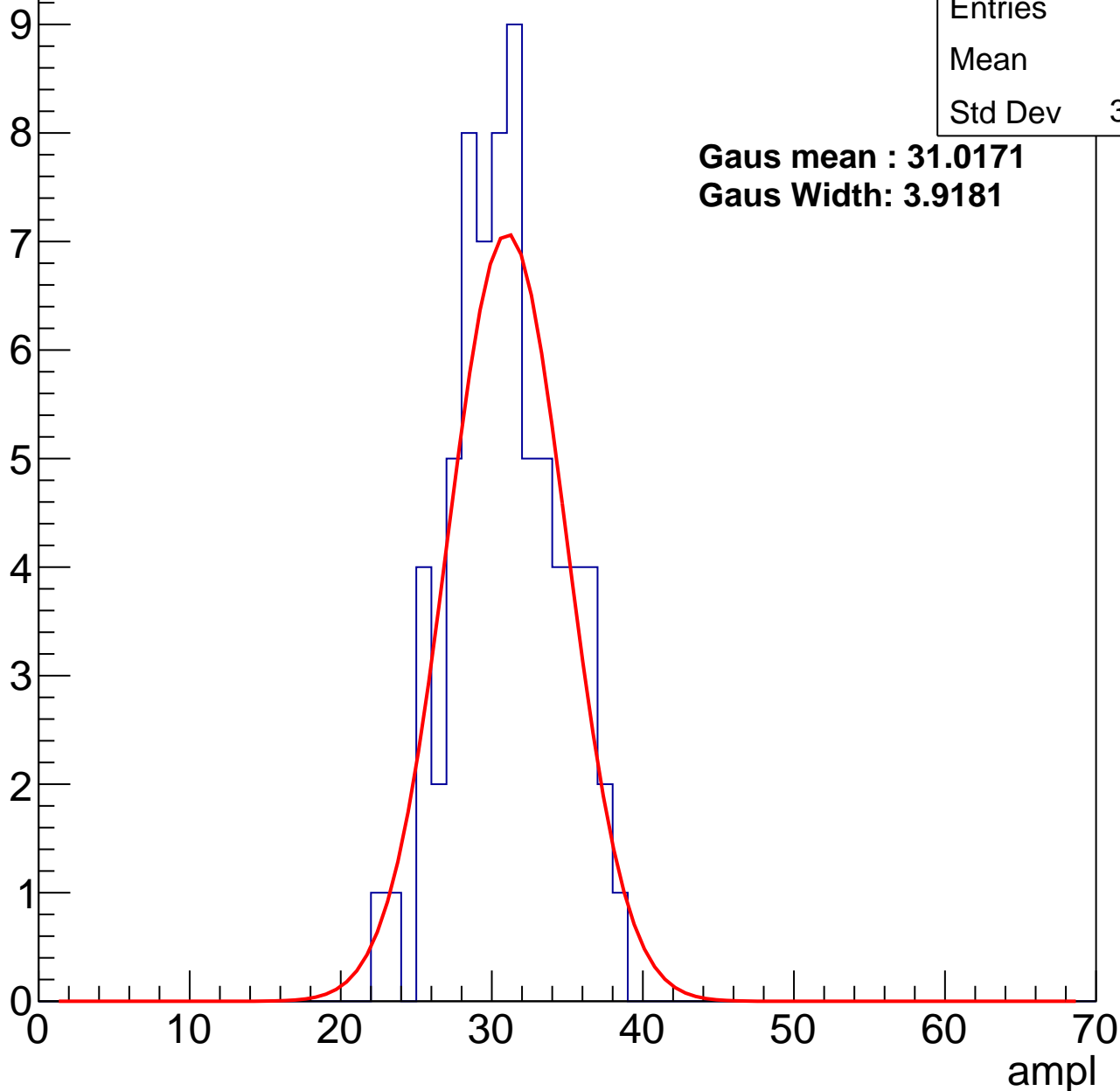
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.5
Std Dev	3.496

Gaus mean : 31.0171

Gaus Width: 3.9181



B1L101S, U14-ch46, adc1

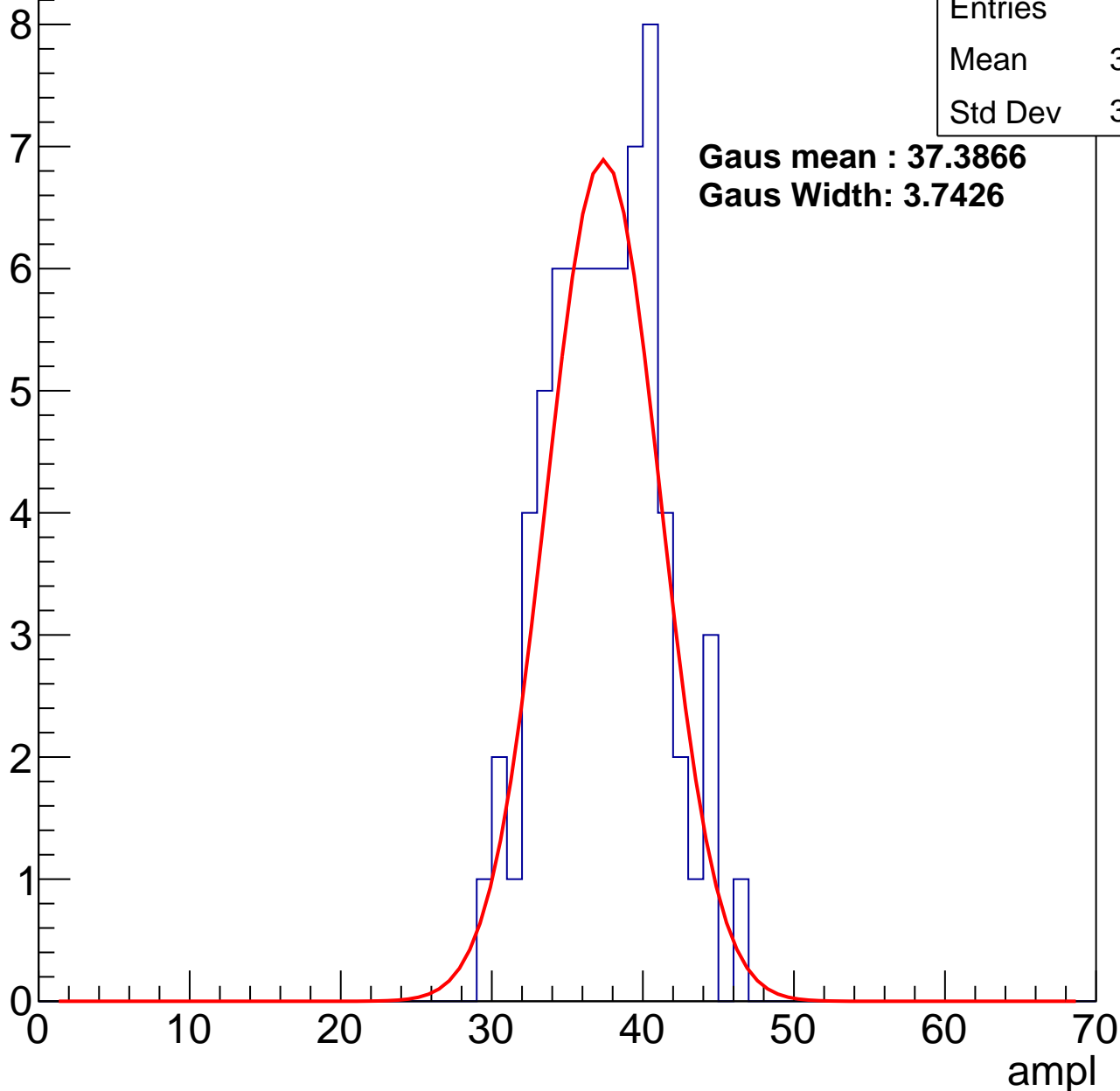
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.03
Std Dev	3.703

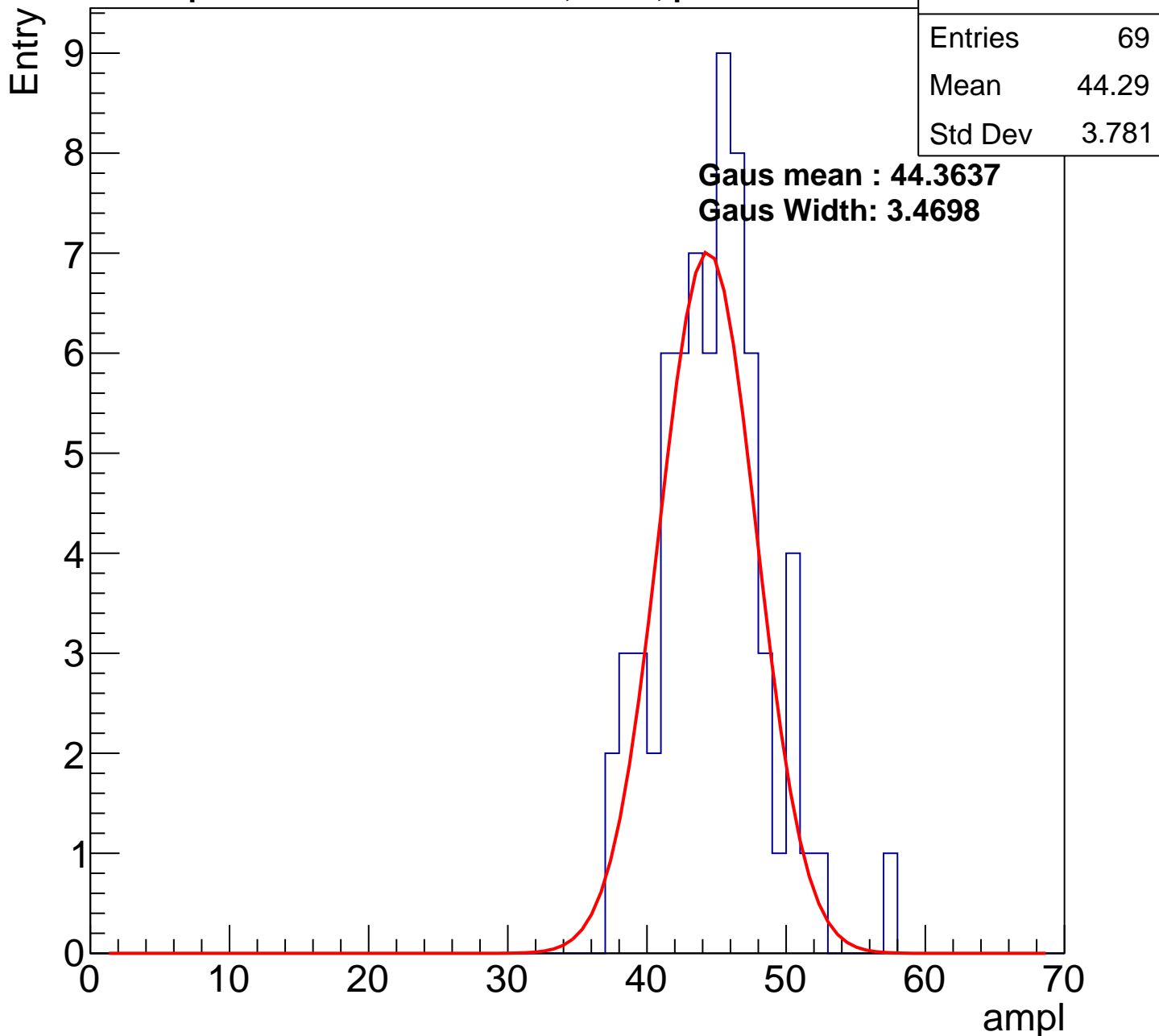
Gaus mean : 37.3866

Gaus Width: 3.7426



B1L101S, U14-ch46, adc2

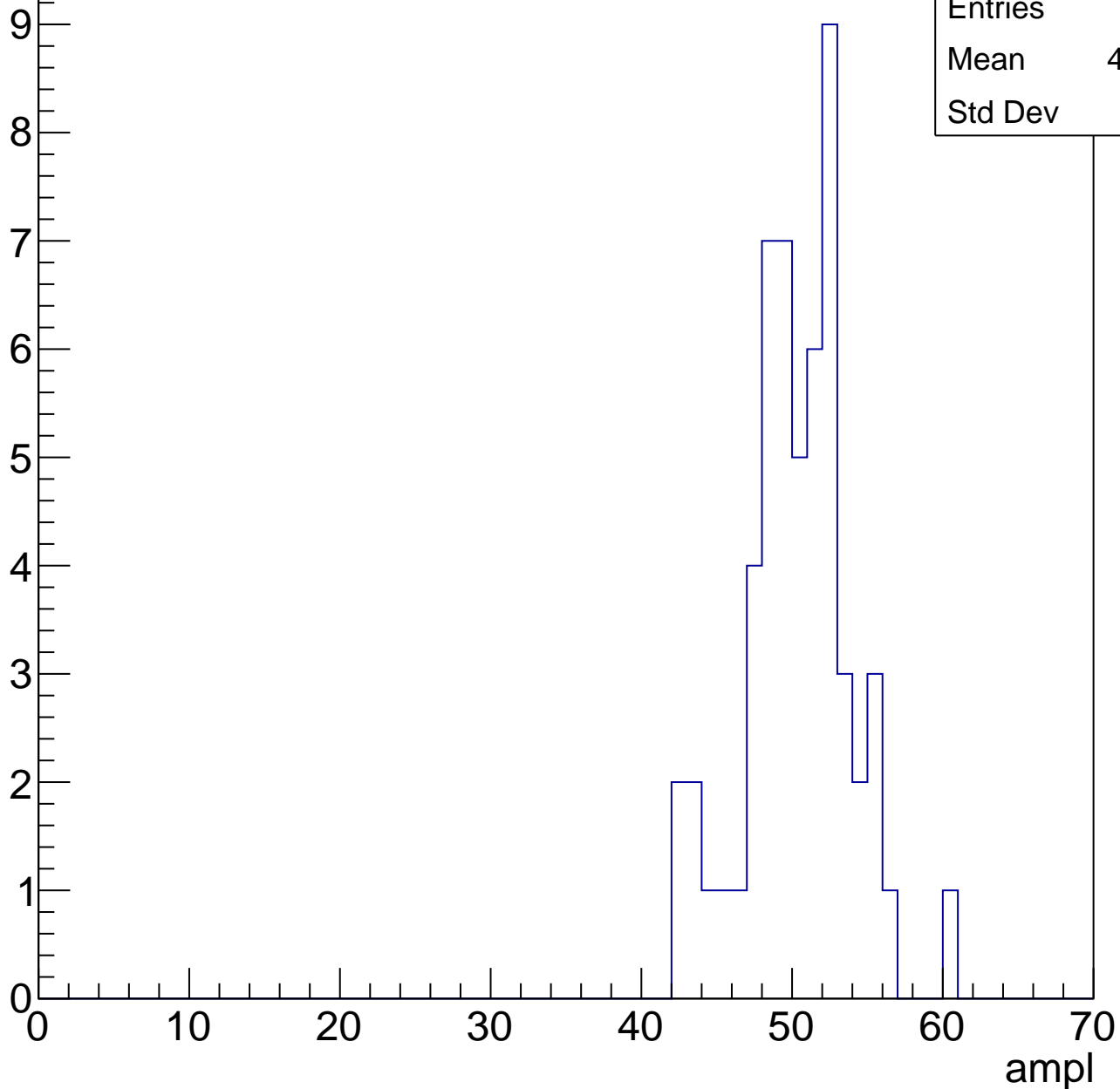
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



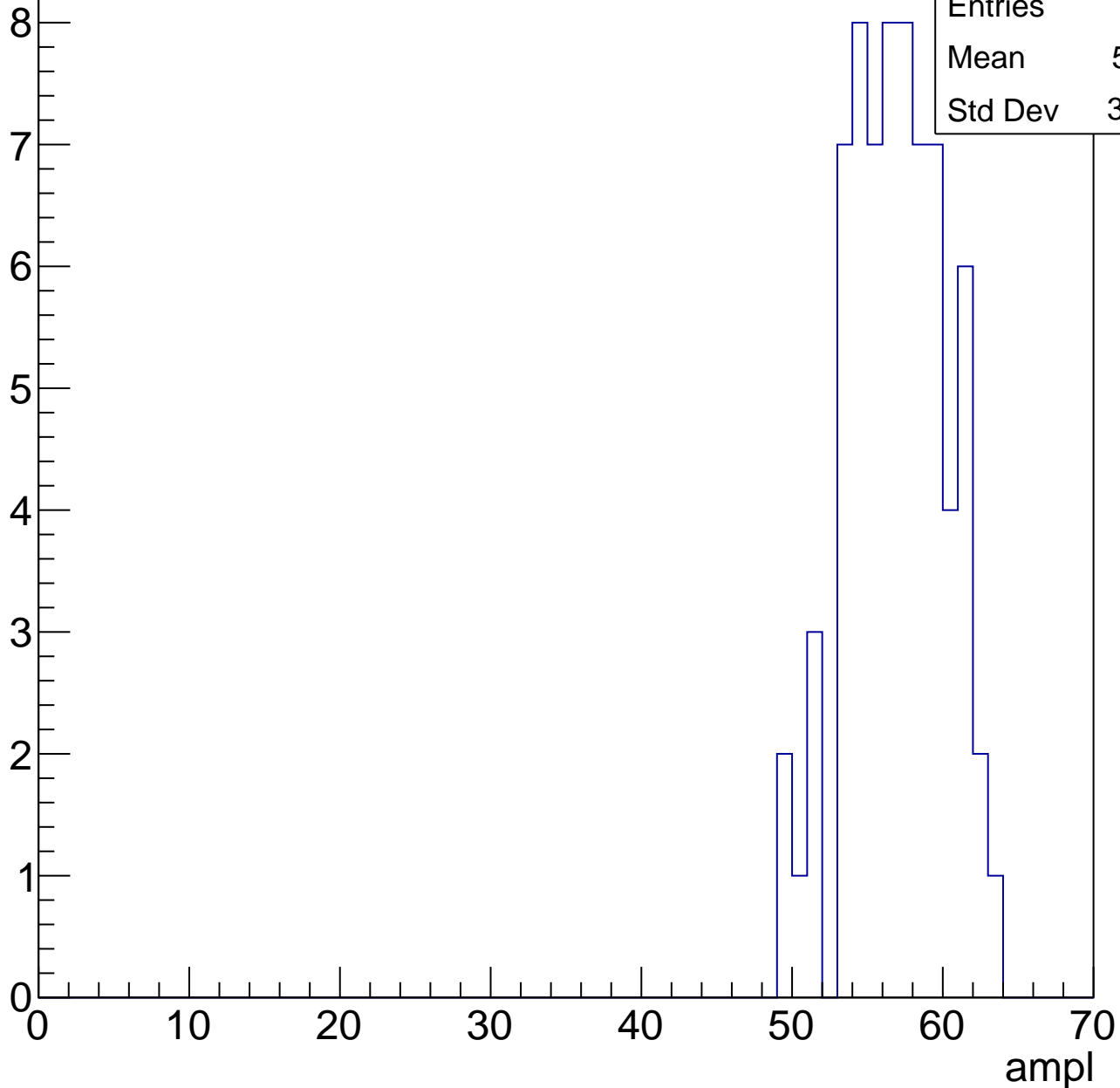
Entries	55
Mean	49.89
Std Dev	3.53

B1L101S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	56.41
Std Dev	3.213

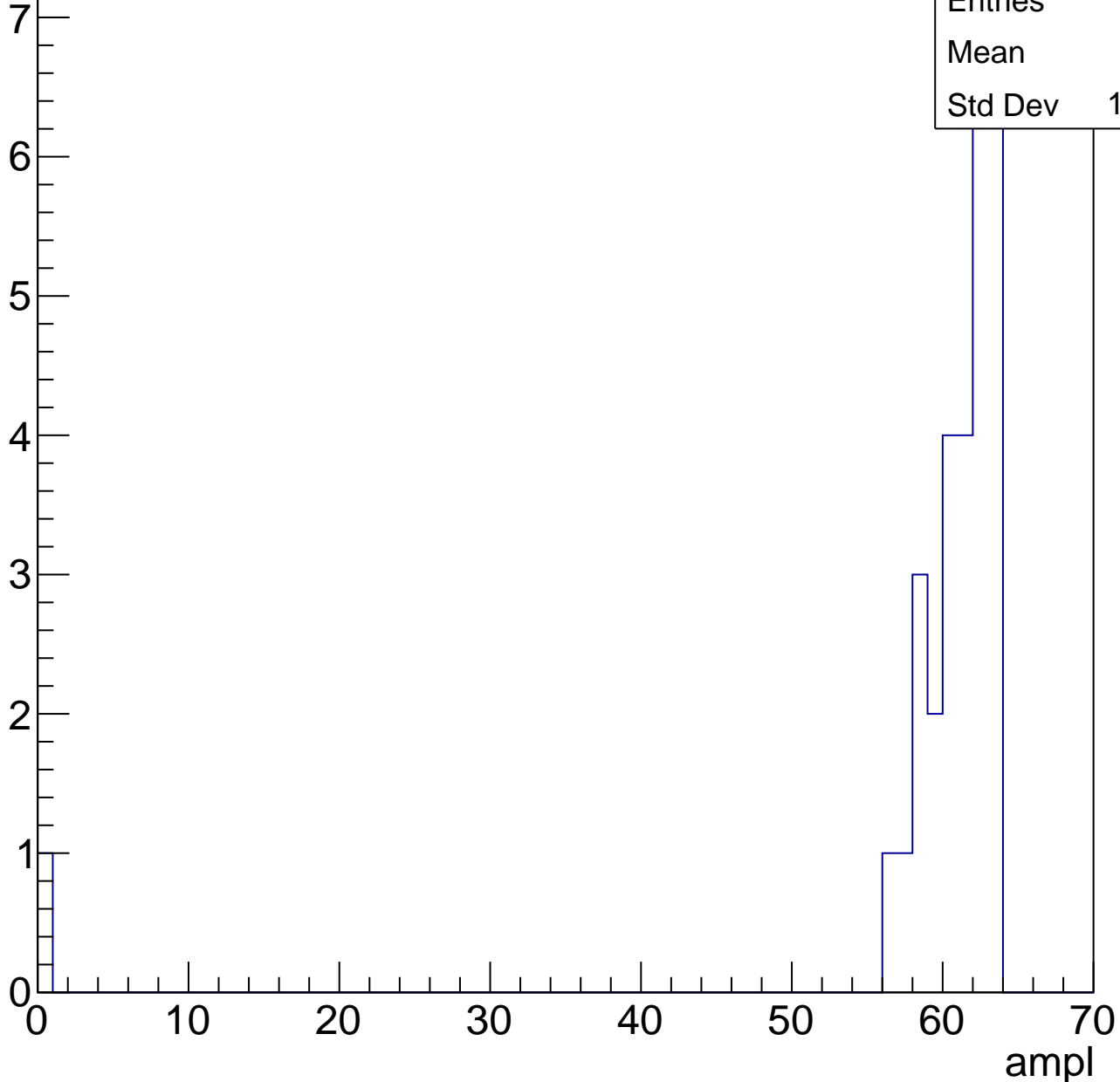


B1L101S, U14-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

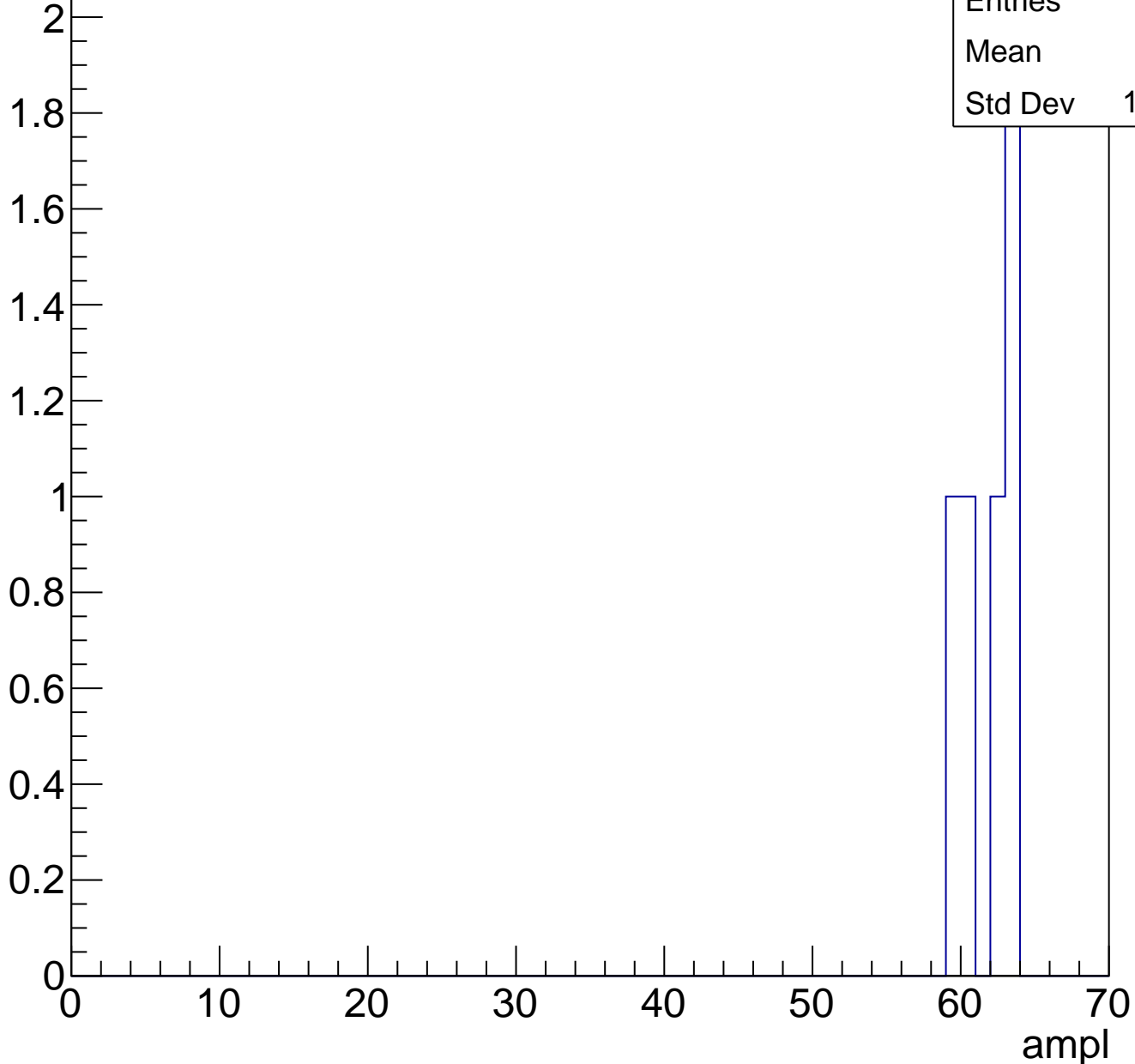
Entries	30
Mean	58.8
Std Dev	11.09



B1L101S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U14-ch47, adc0

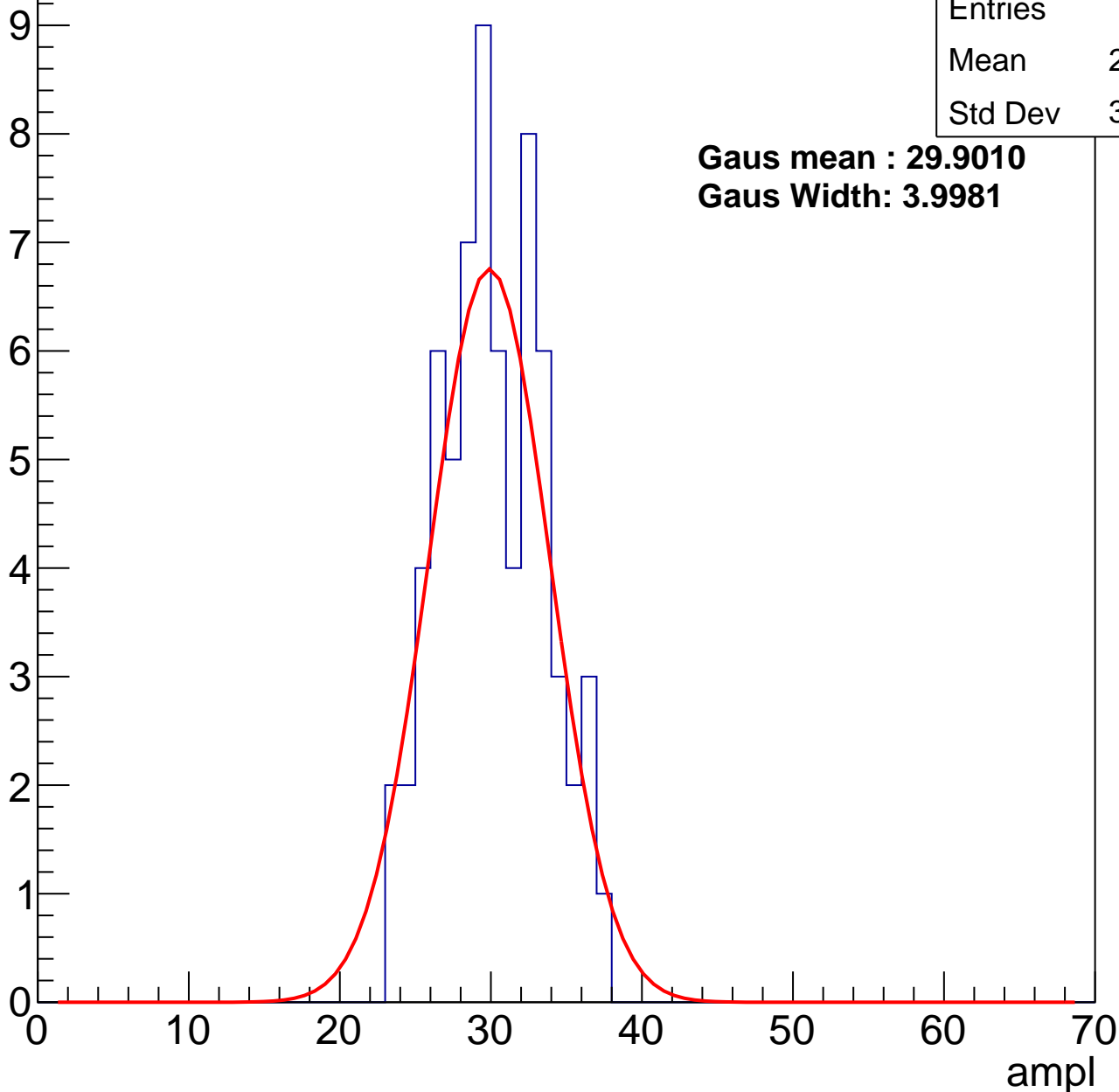
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.66
Std Dev	3.398

Gaus mean : 29.9010

Gaus Width: 3.9981



B1L101S, U14-ch47, adc1

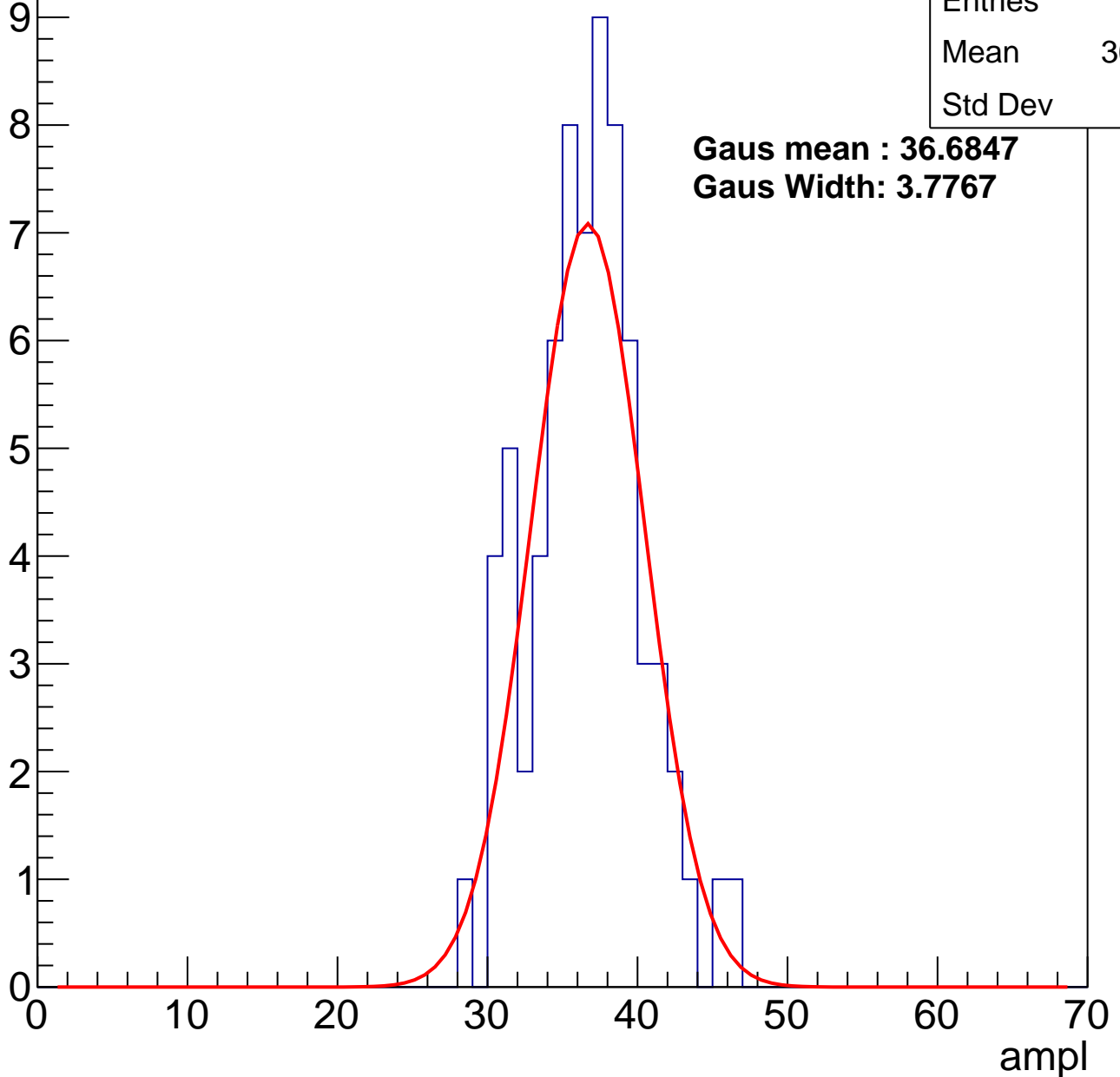
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.15
Std Dev	3.66

Gaus mean : 36.6847

Gaus Width: 3.7767



B1L101S, U14-ch47, adc2

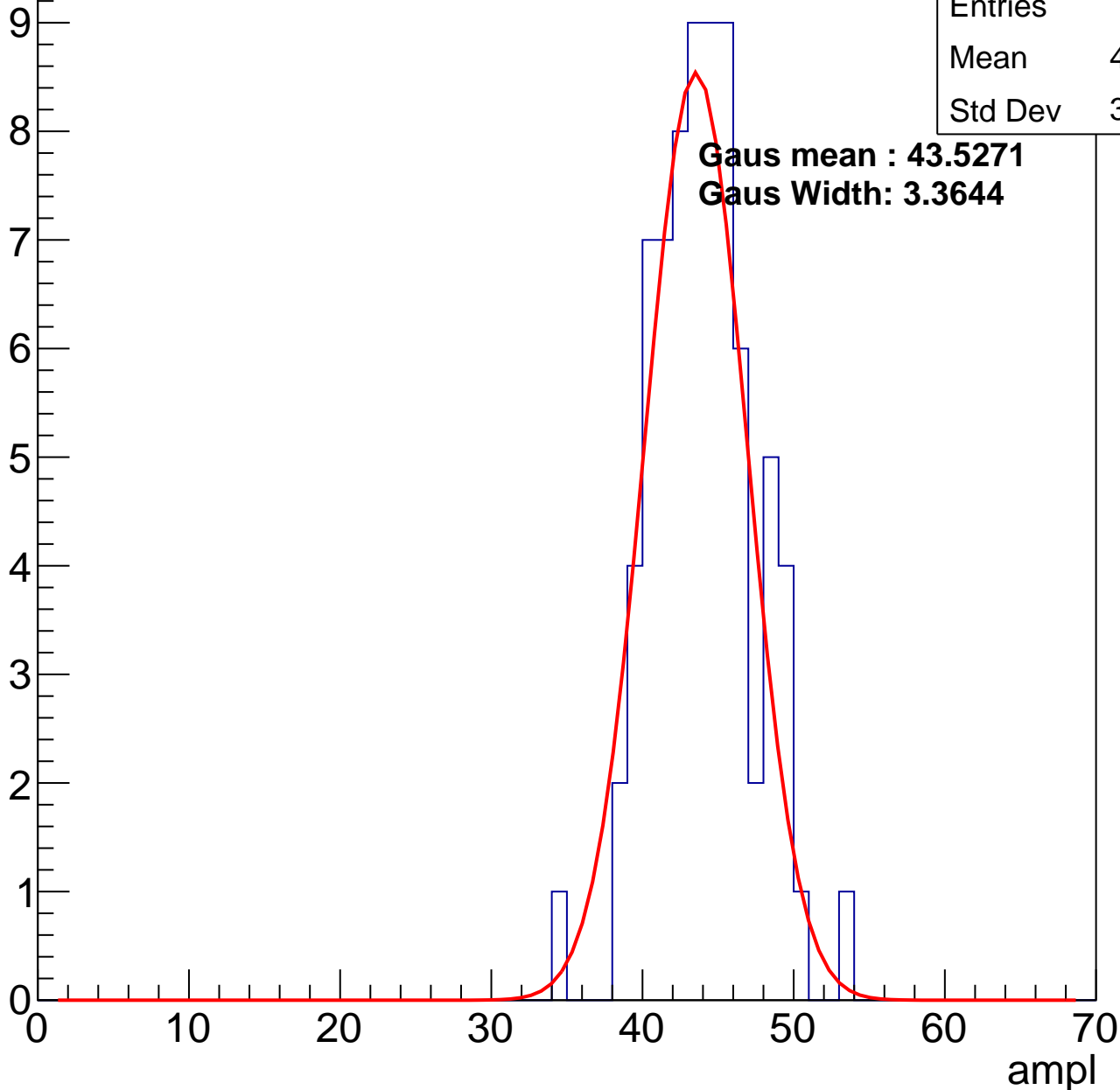
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.55
Std Dev	3.308

Gaus mean : 43.5271

Gaus Width: 3.3644

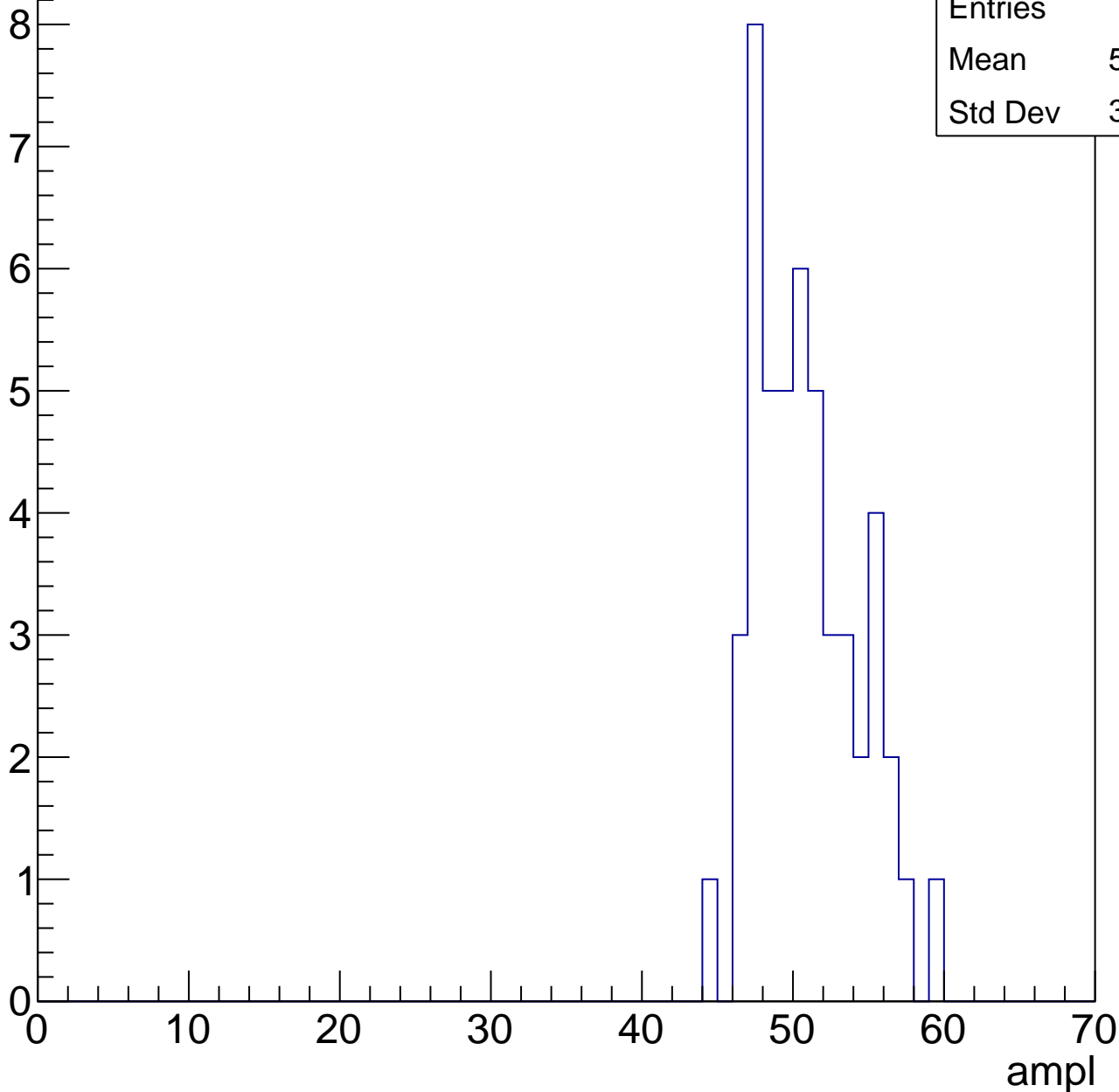


B1L101S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

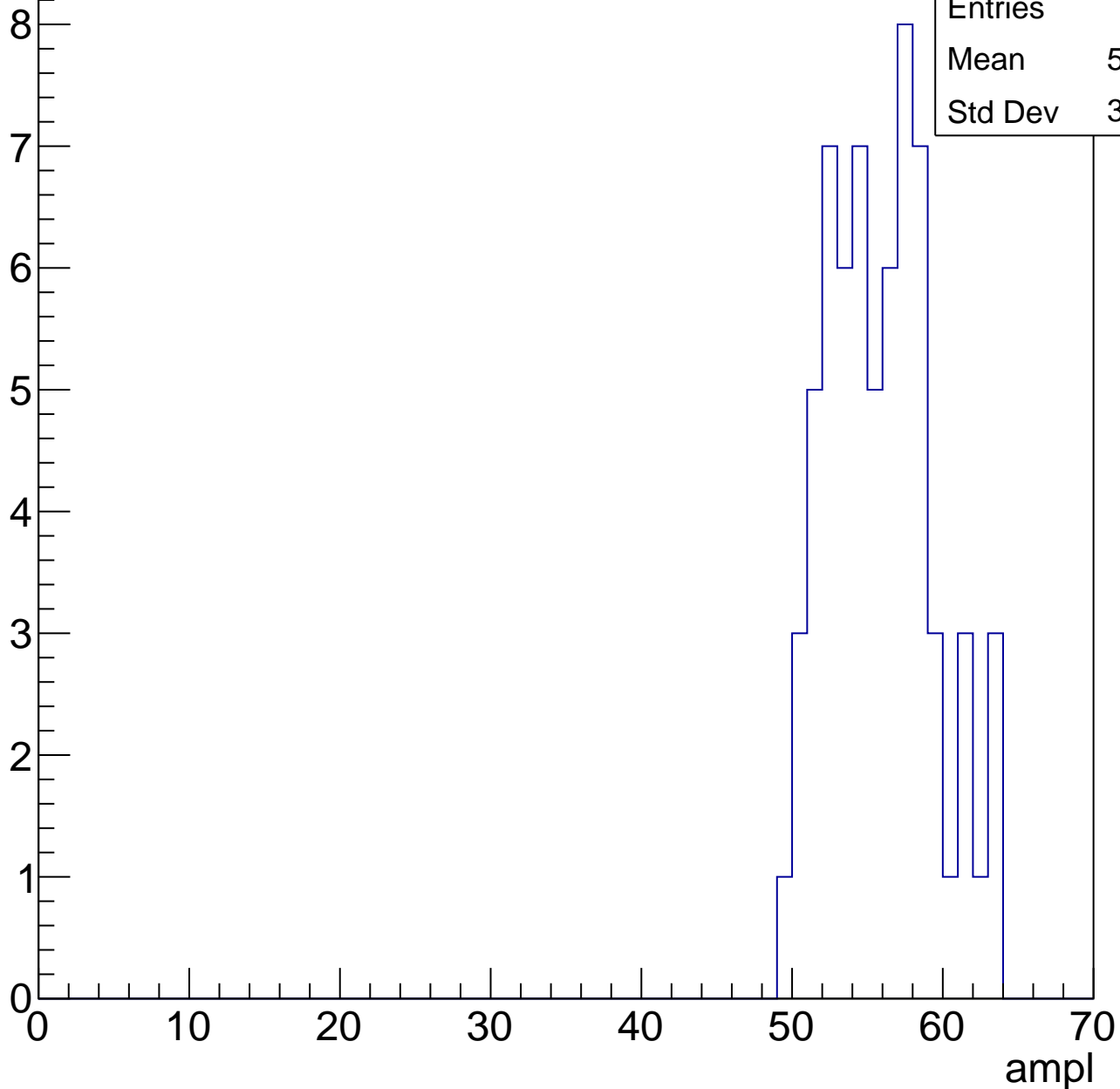
Entries	49
Mean	50.39
Std Dev	3.355



B1L101S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

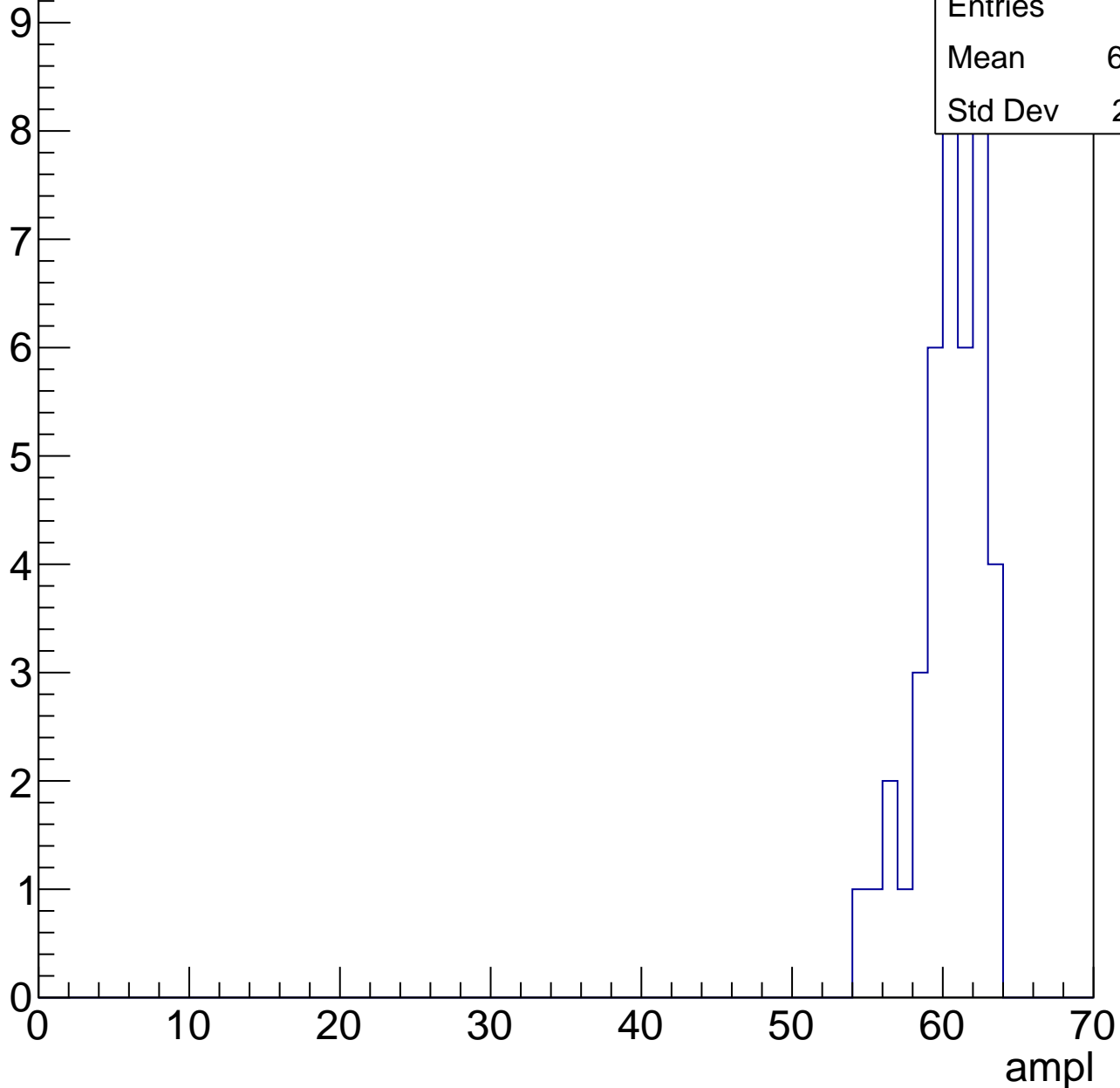


Entries	66
Mean	55.42
Std Dev	3.473

B1L101S, U14-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

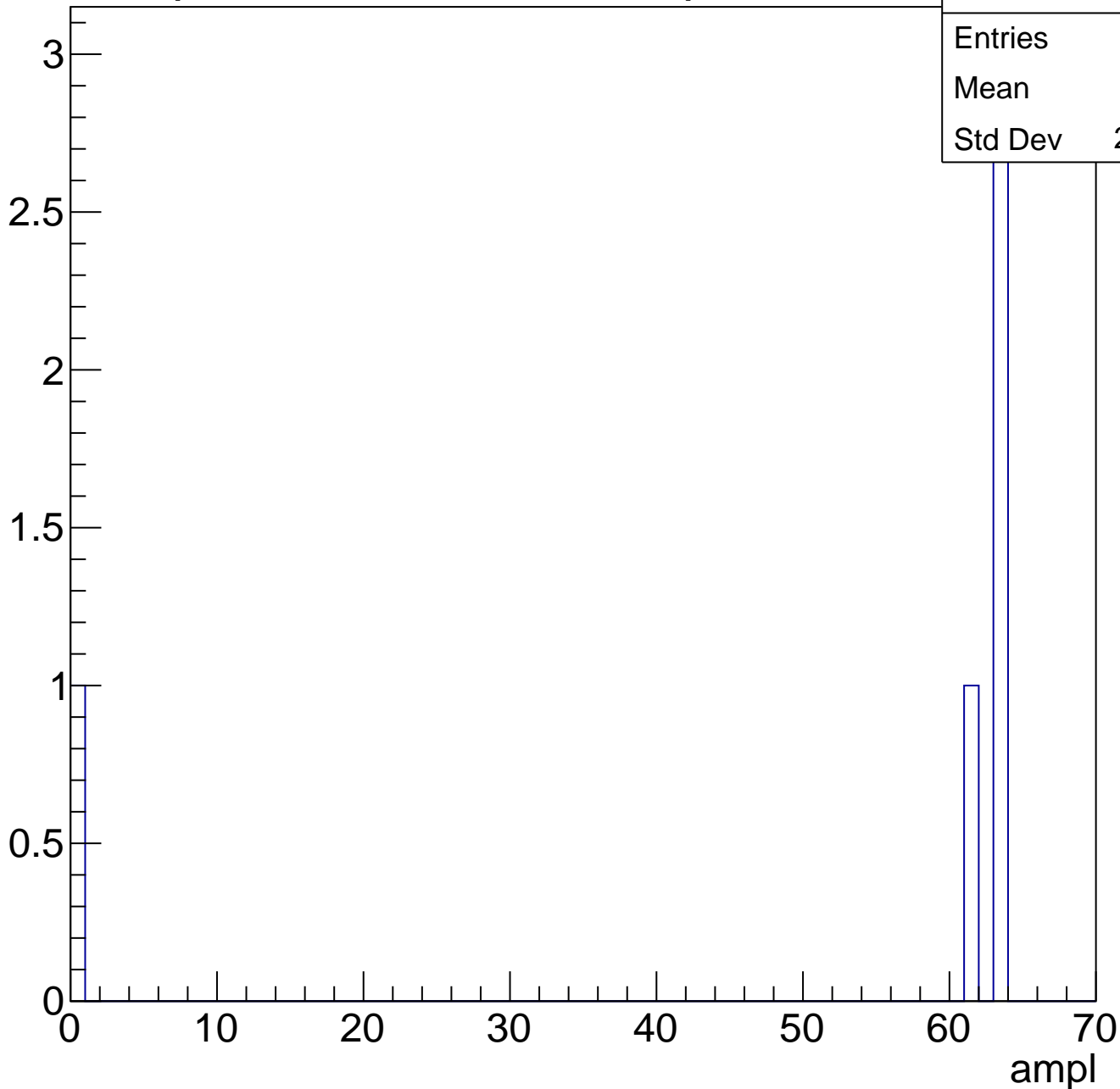
Entry



B1L101S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch48, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	27.21
Std Dev	5.836

Gaus mean : 28.0965

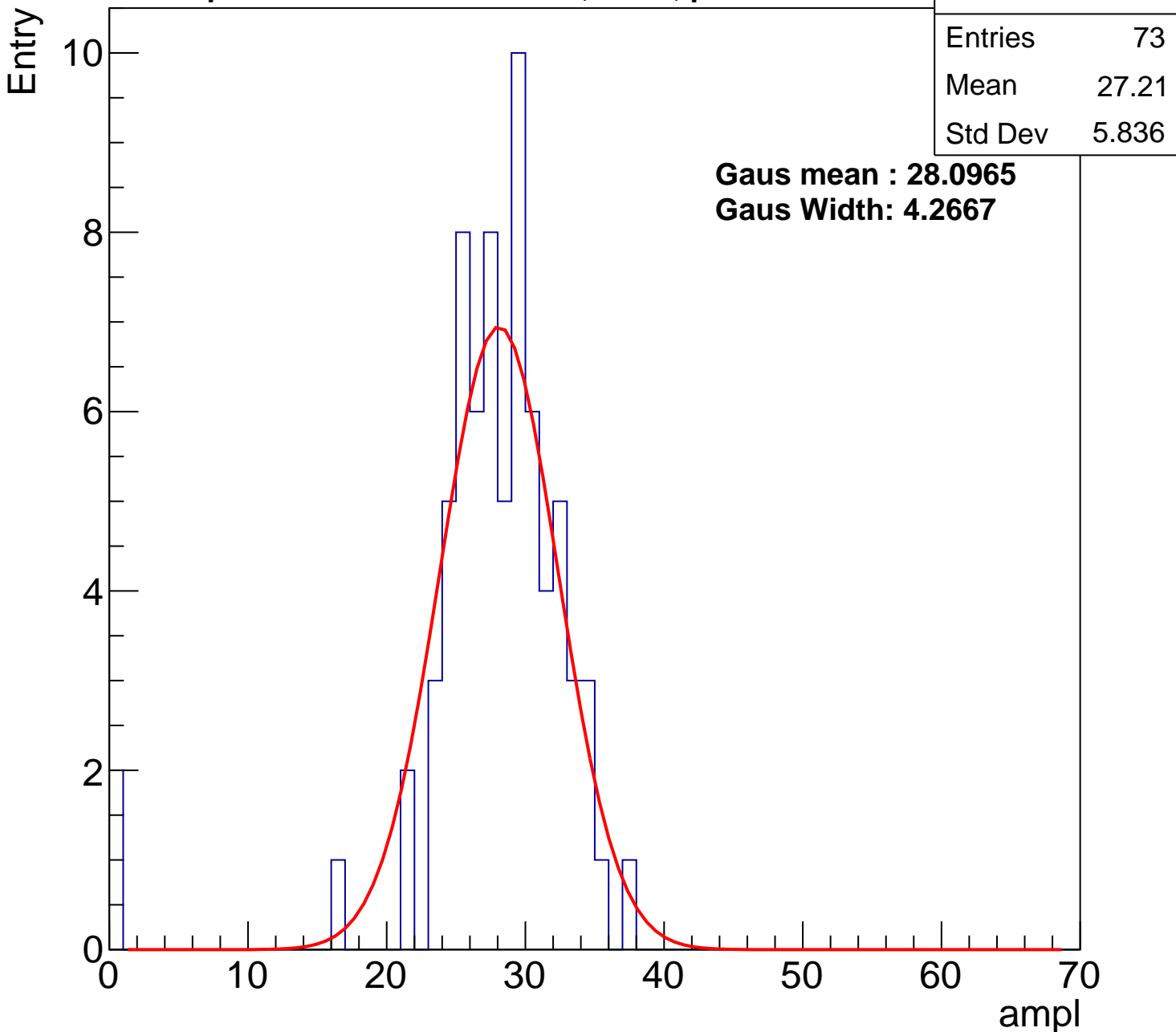
Gaus Width: 4.2667

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



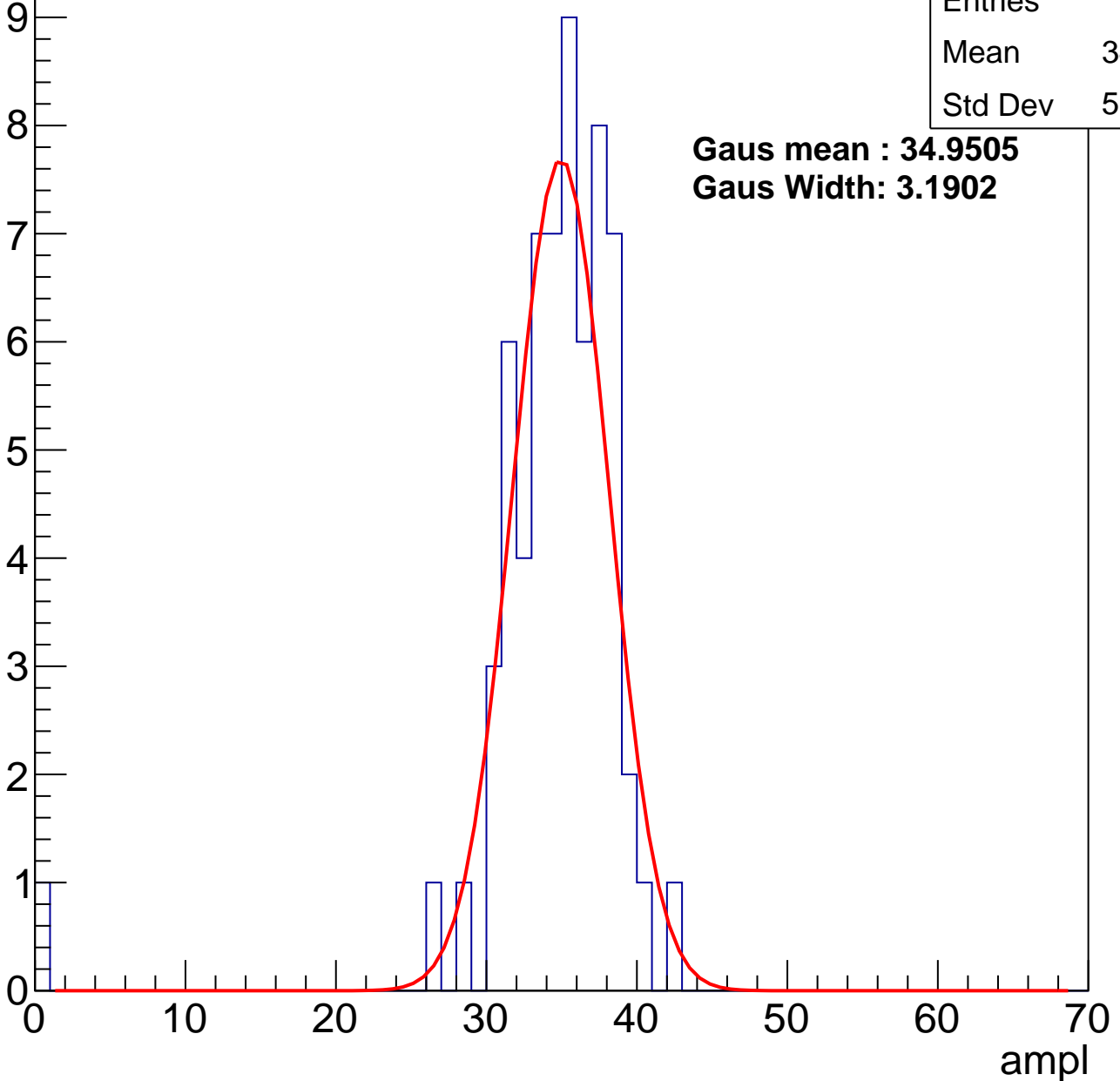
B1L101S, U14-ch48, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	34.06
Std Dev	5.226

Gaus mean : 34.9505
Gaus Width: 3.1902



B1L101S, U14-ch48, adc2

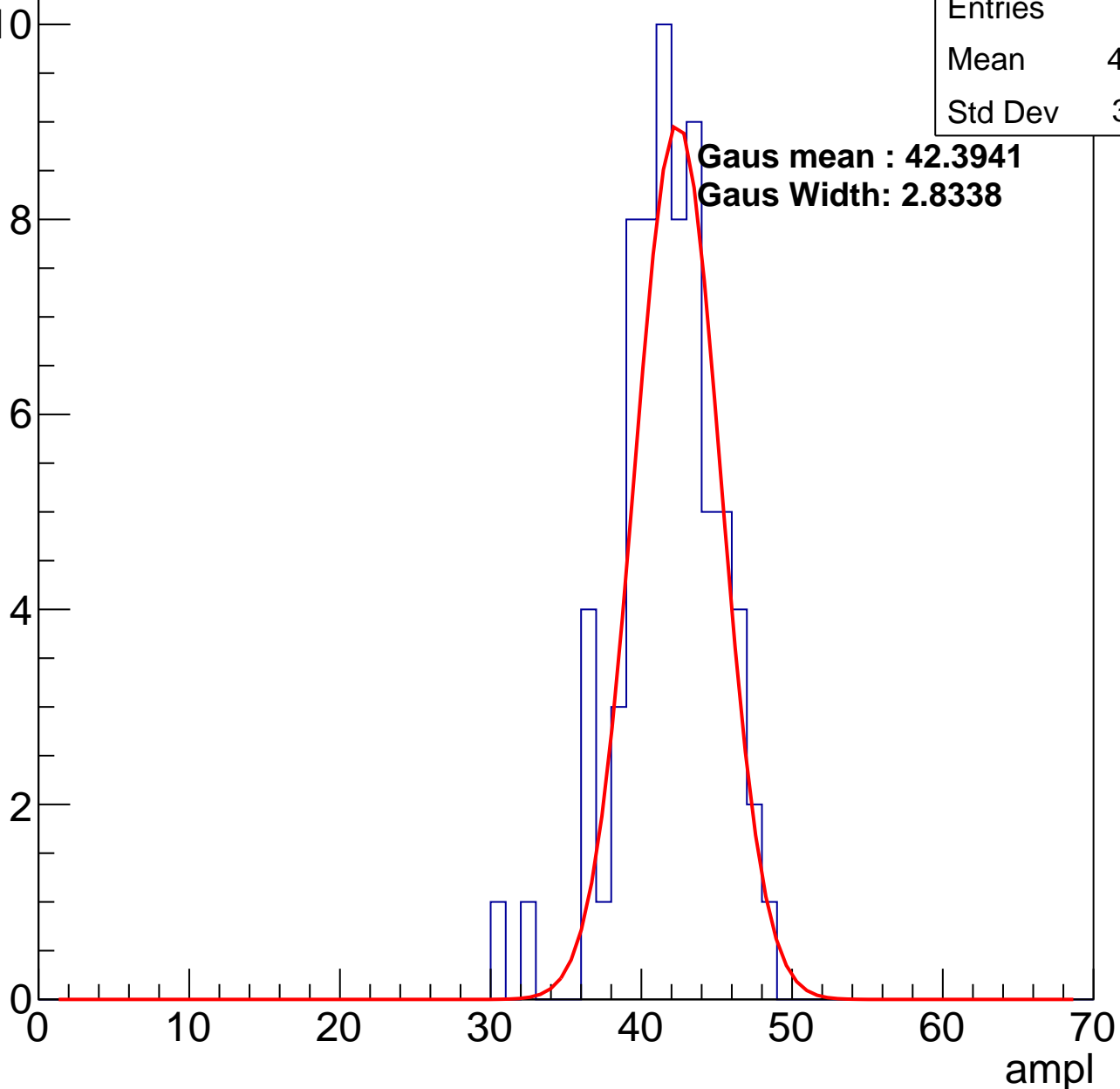
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	41.33
Std Dev	3.311

Gaus mean : 42.3941

Gaus Width: 2.8338

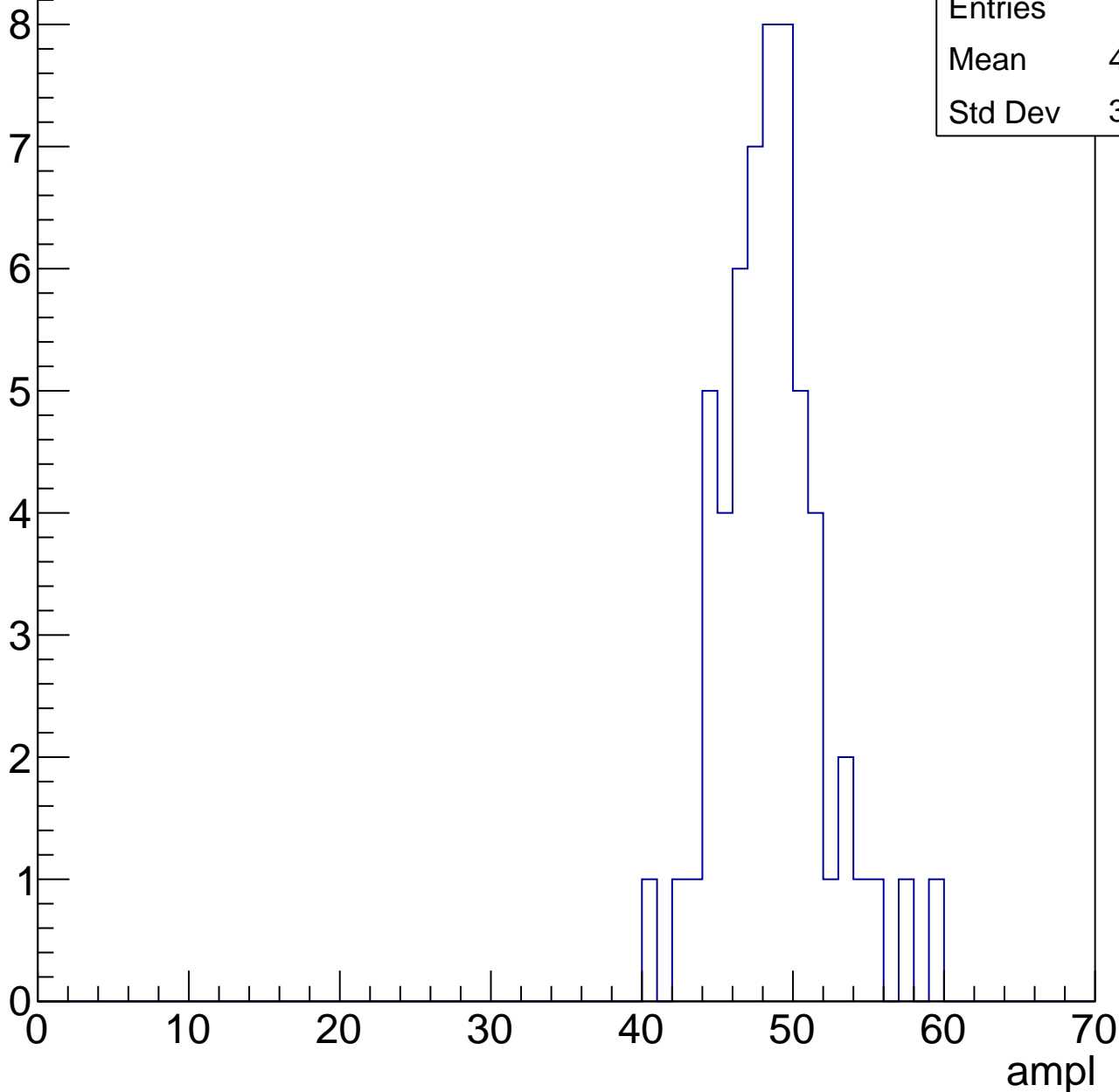


B1L101S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.12
Std Dev	3.475

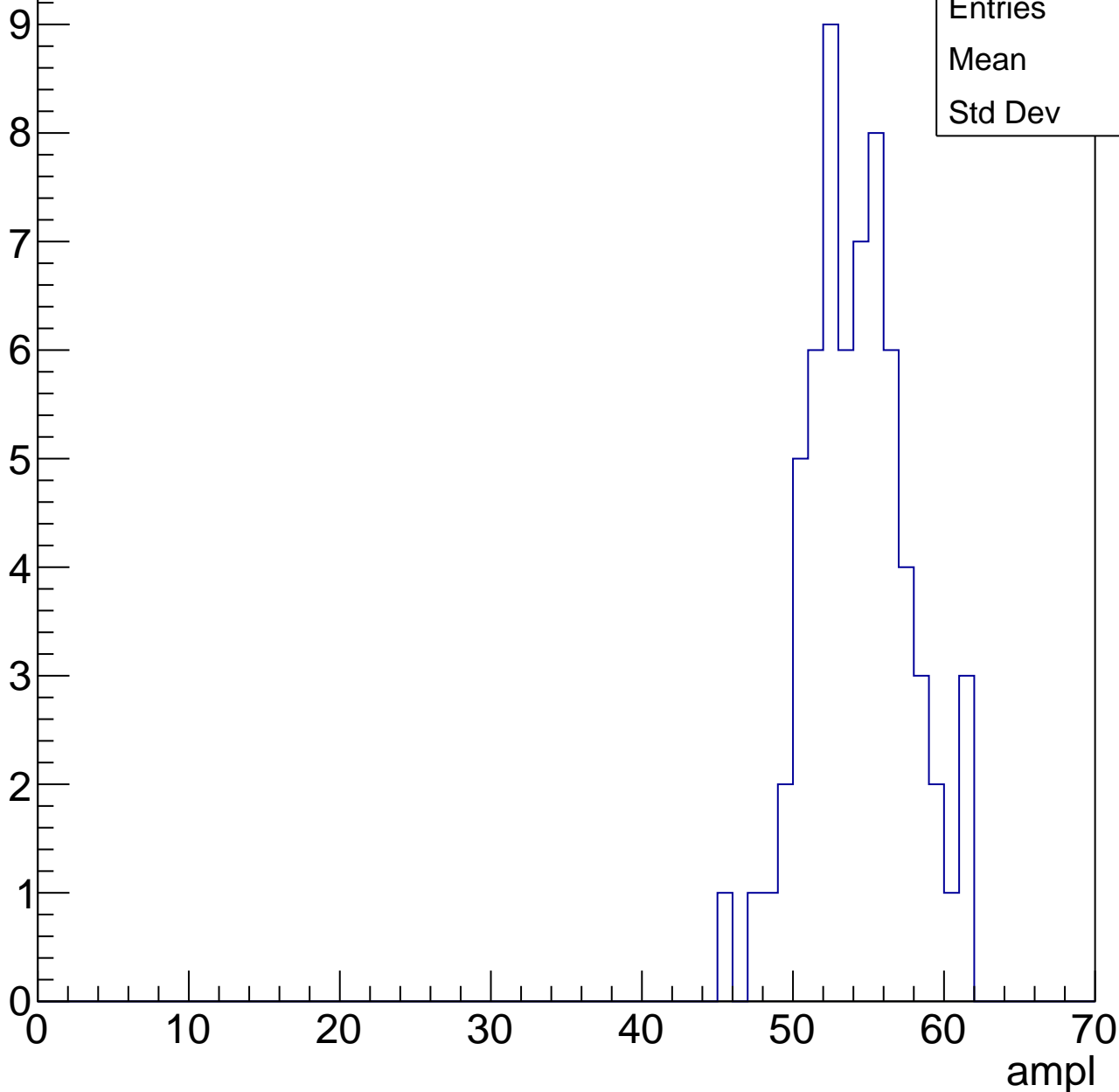


B1L101S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	53.8
Std Dev	3.37



B1L101S, U14-ch48, adc5

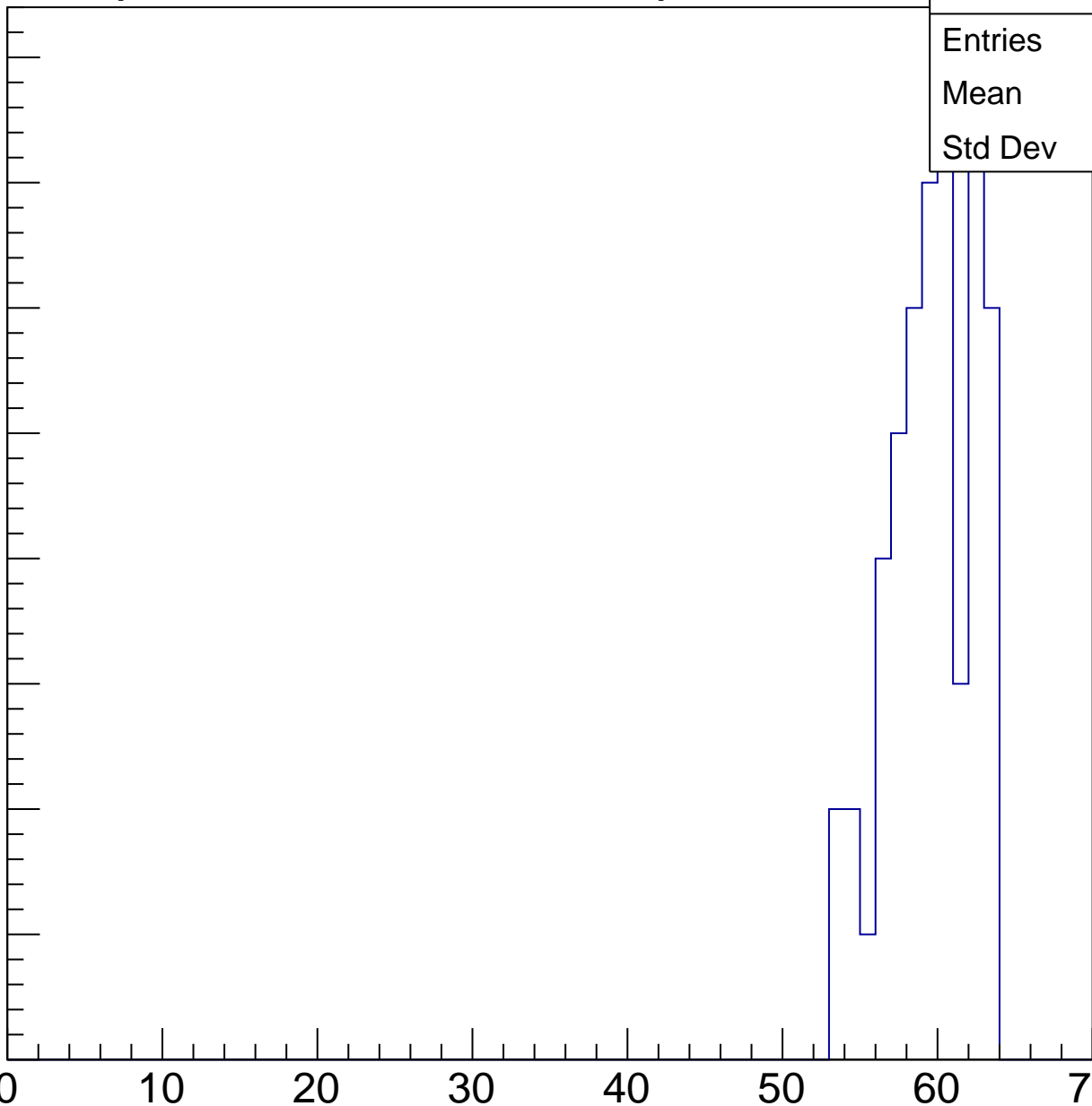
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.15
Std Dev	2.727

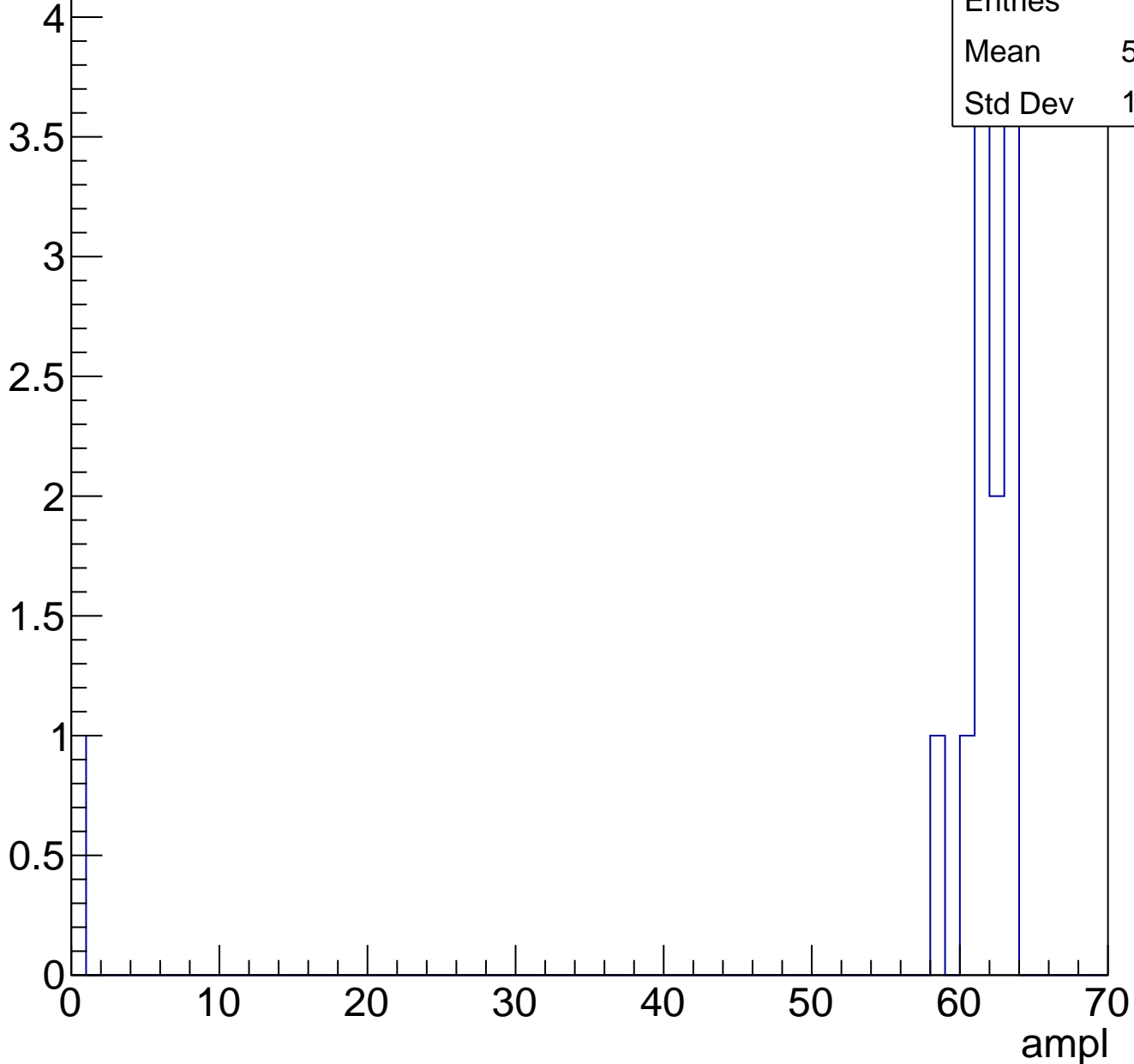
ampl



B1L101S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch49, adc0

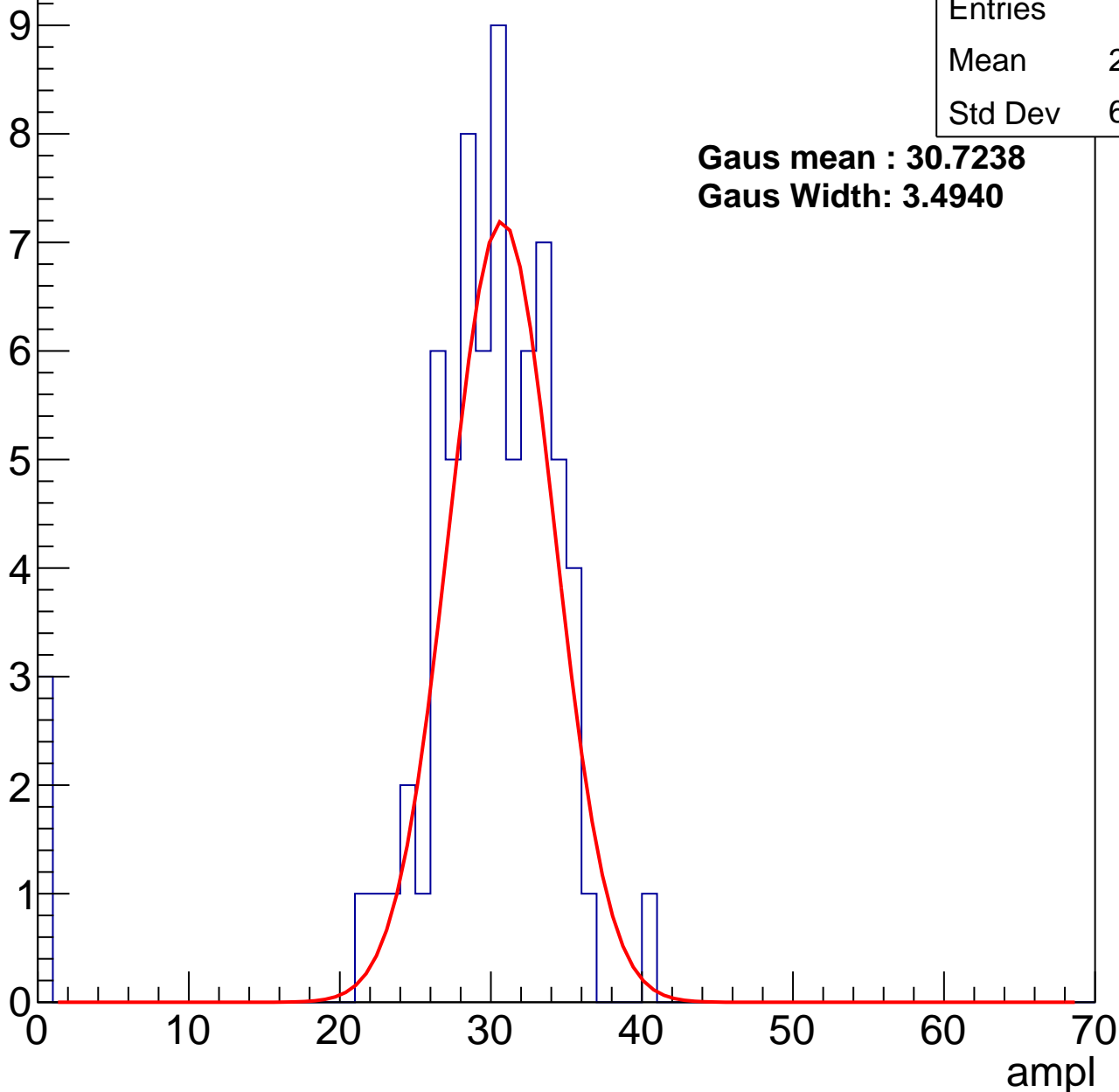
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.64
Std Dev	6.917

Gaus mean : 30.7238

Gaus Width: 3.4940



B1L101S, U14-ch49, adc1

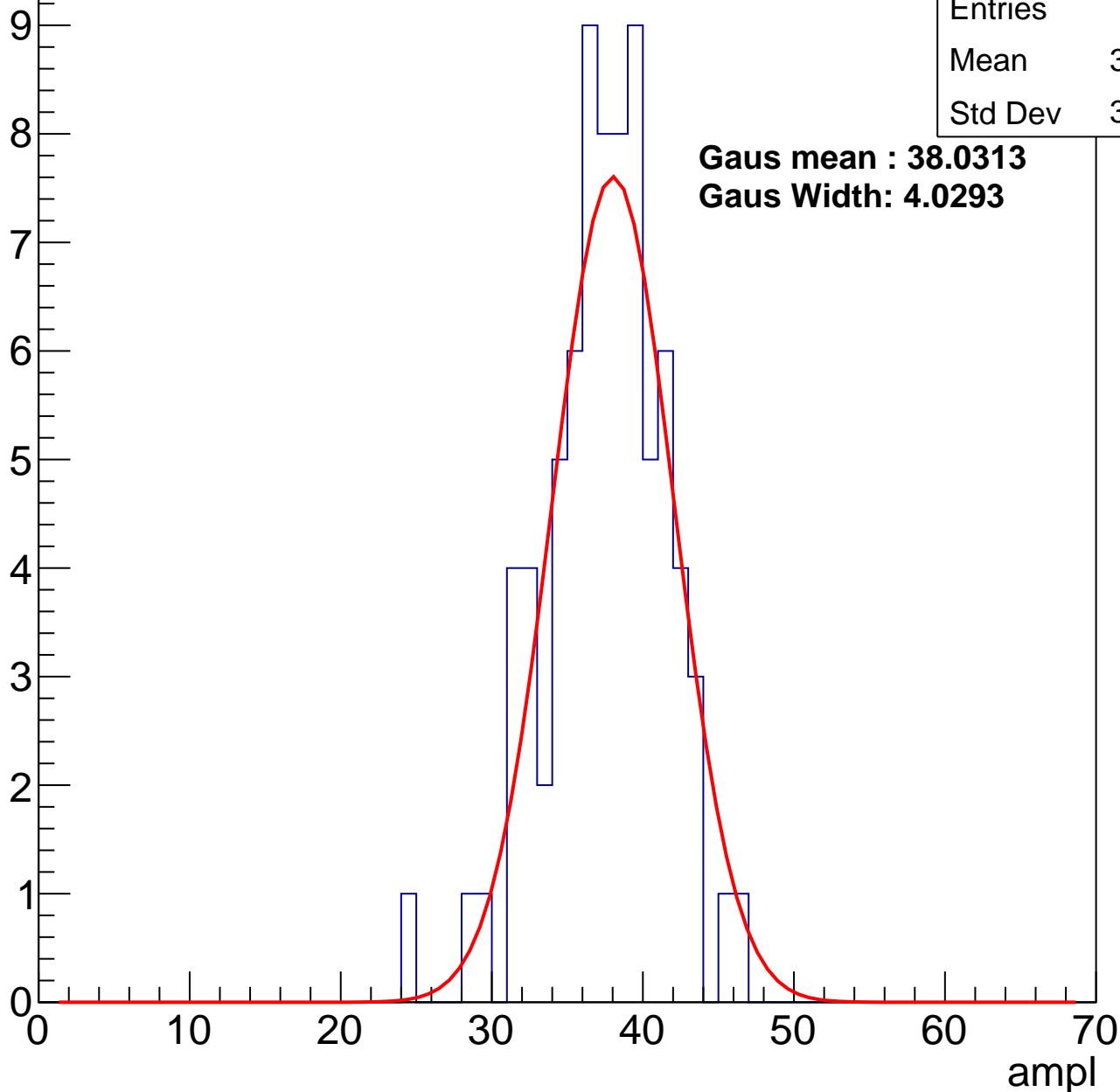
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.03
Std Dev	3.932

Gaus mean : 38.0313

Gaus Width: 4.0293



B1L101S, U14-ch49, adc2

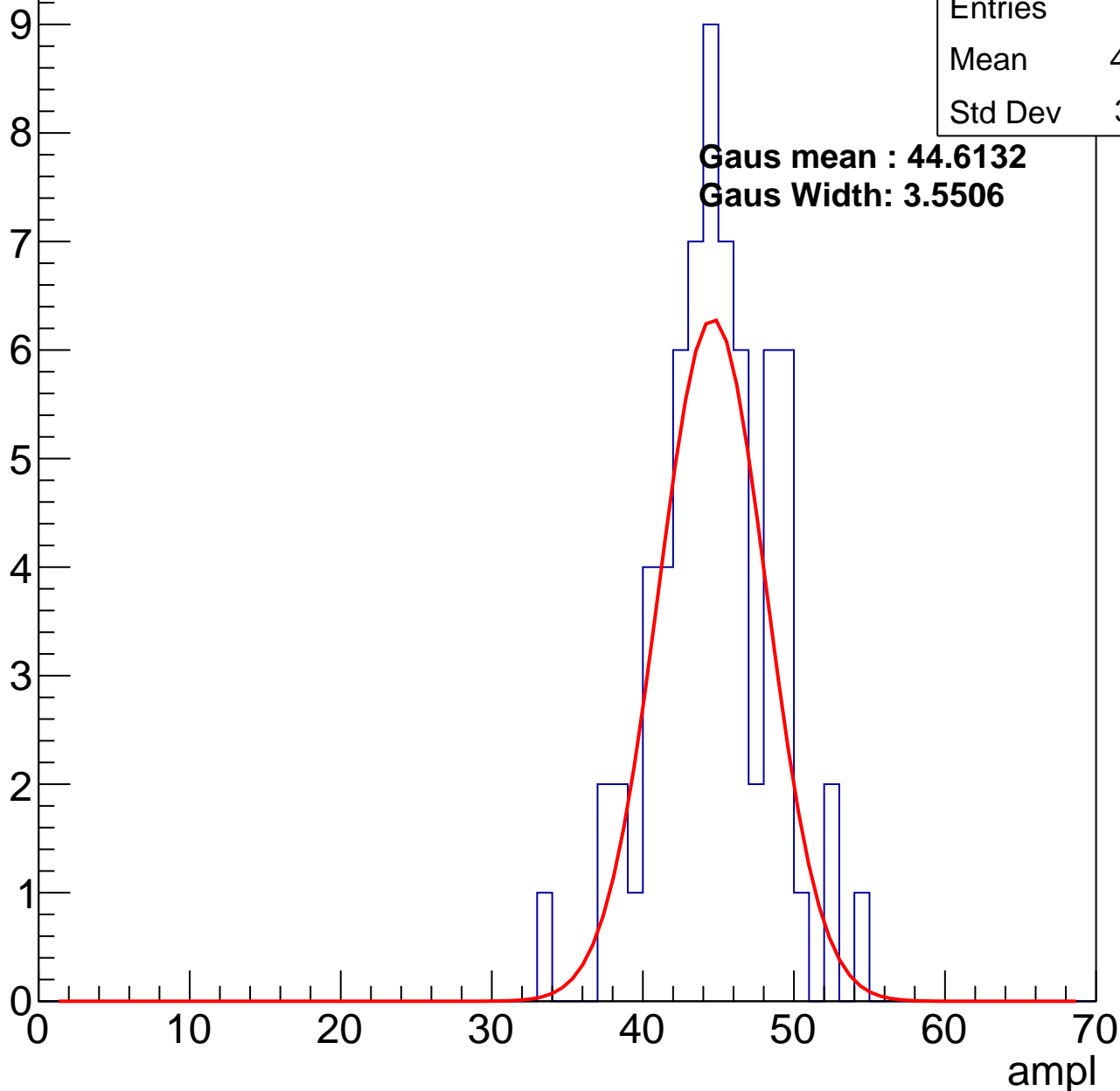
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.33
Std Dev	3.861

Gaus mean : 44.6132

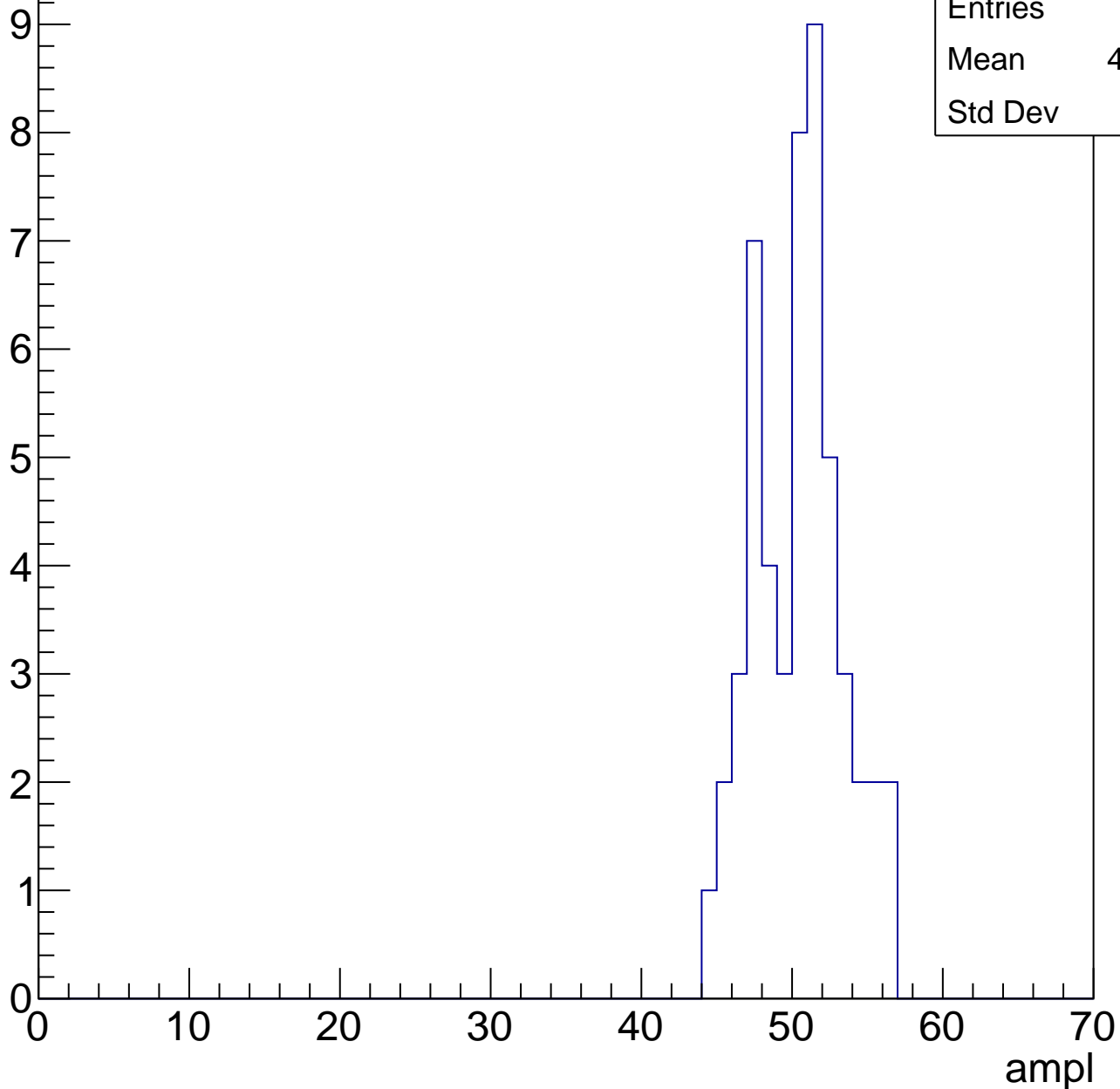
Gaus Width: 3.5506



B1L101S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



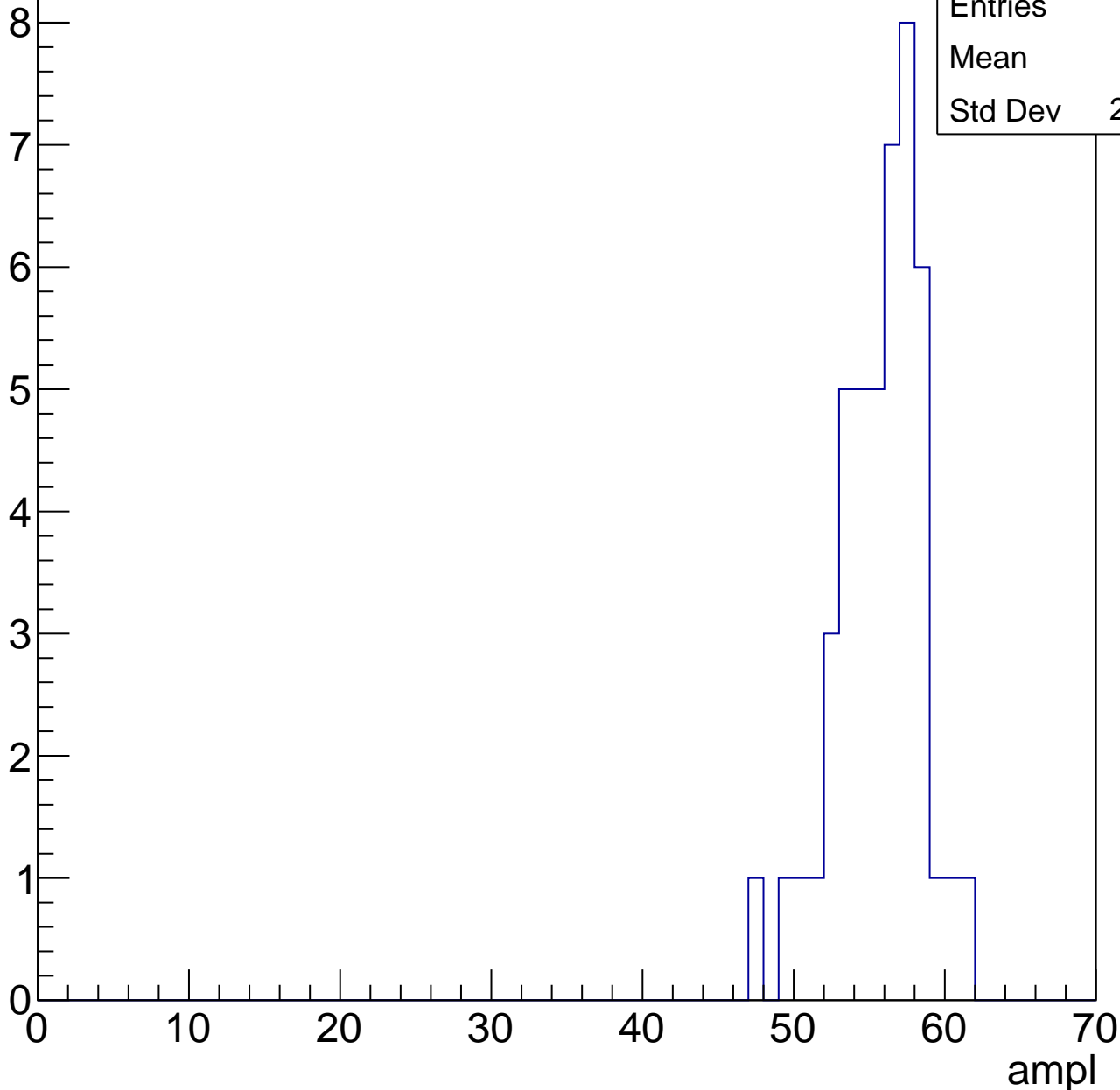
Entries	51
Mean	49.96
Std Dev	2.89

B1L101S, U14-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

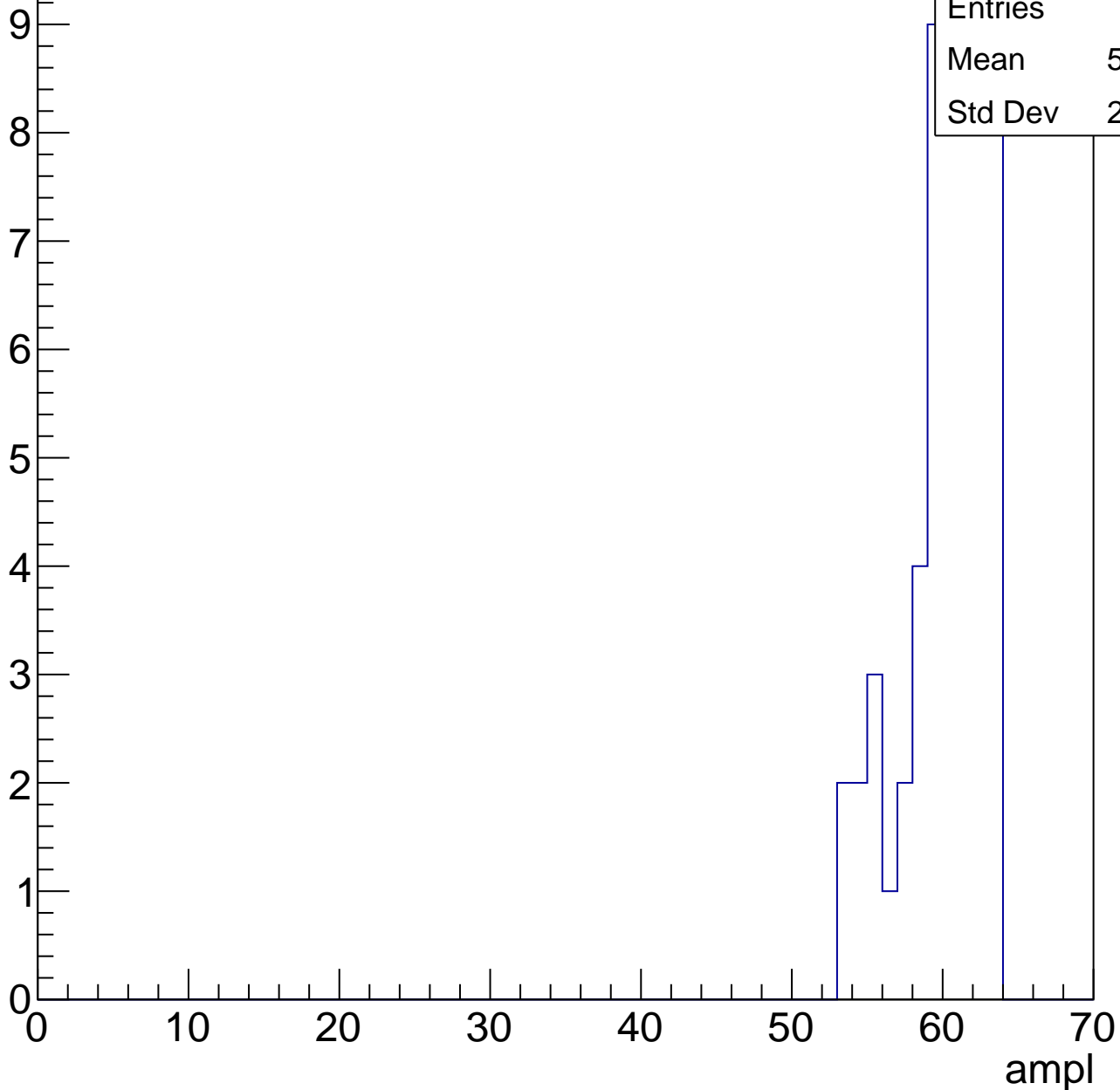
Entries	46
Mean	55.2
Std Dev	2.802



B1L101S, U14-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

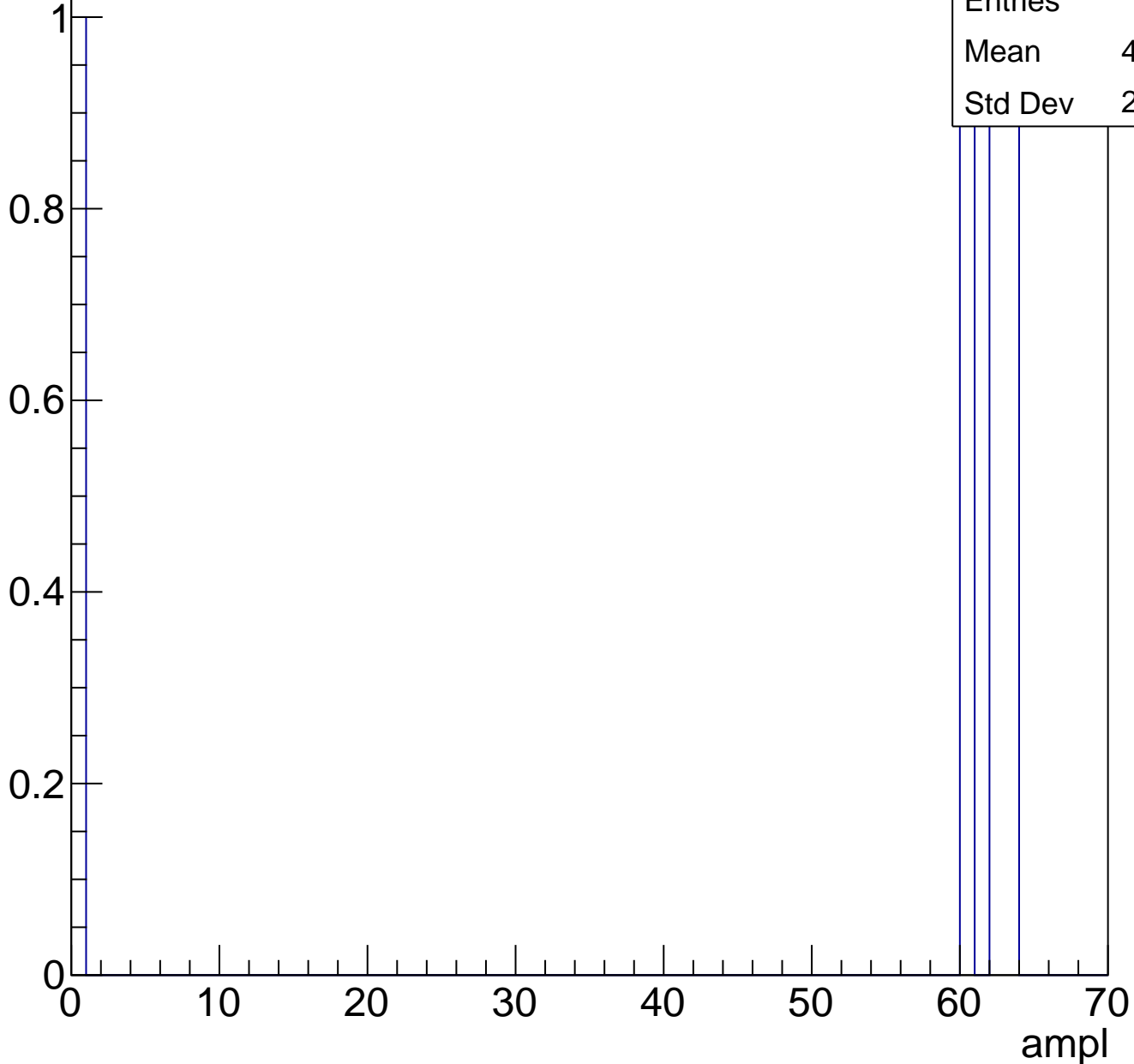
Entry



B1L101S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch50, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	28.86
Std Dev	5.984

Gaus mean : 30.6800

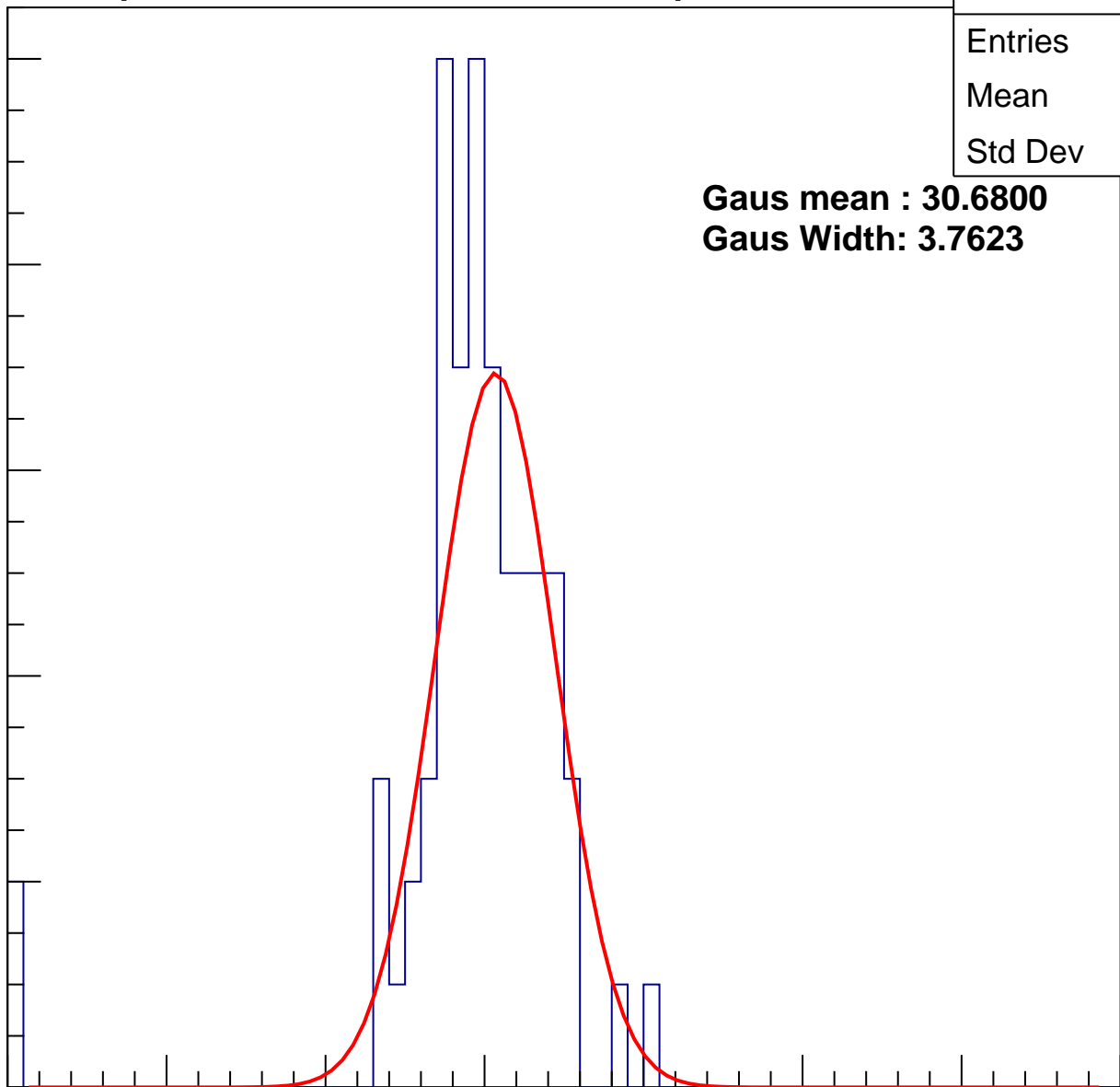
Gaus Width: 3.7623

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch50, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	35.03
Std Dev	5.5

Gaus mean : 34.7932

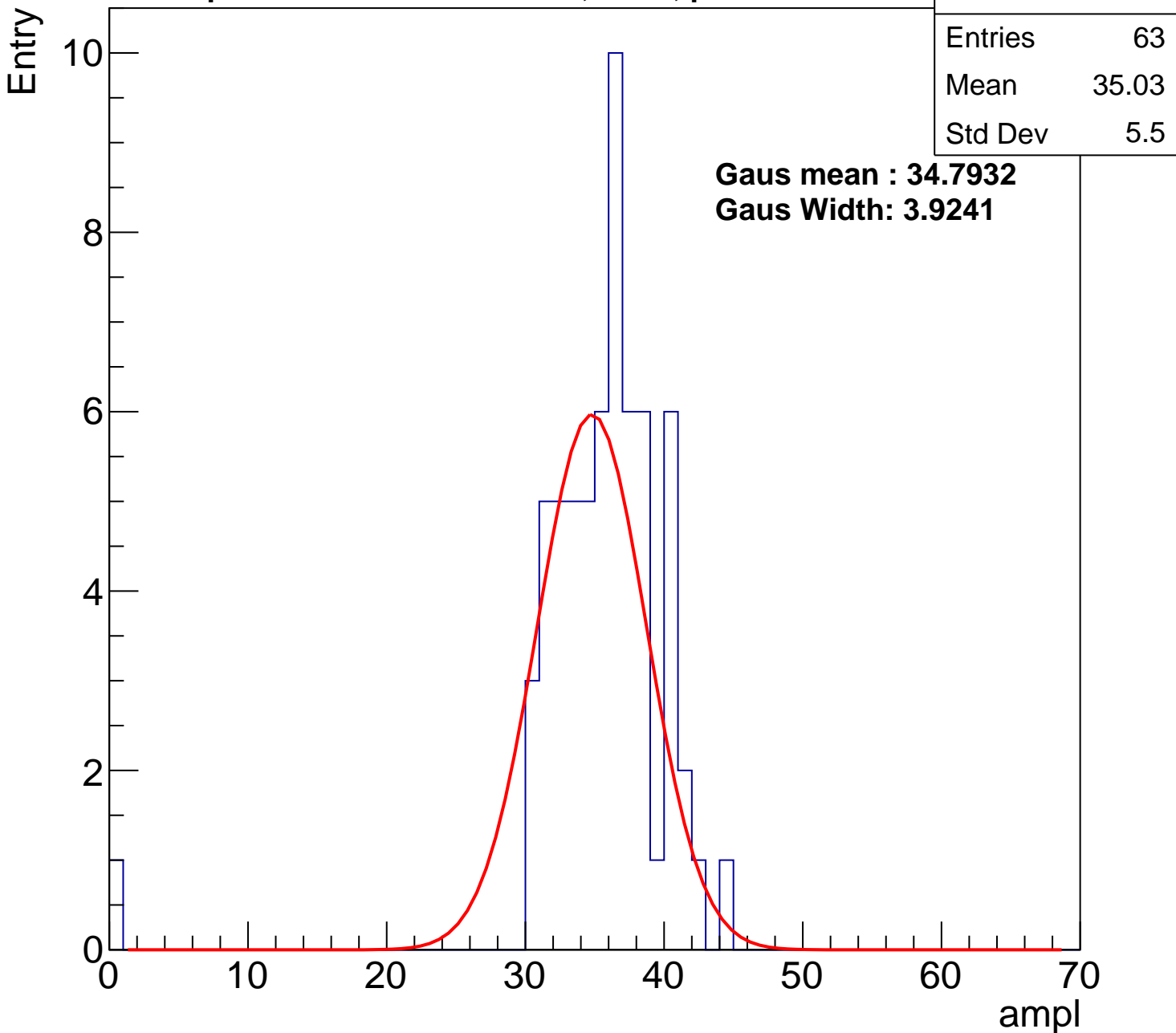
Gaus Width: 3.9241

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch50, adc2

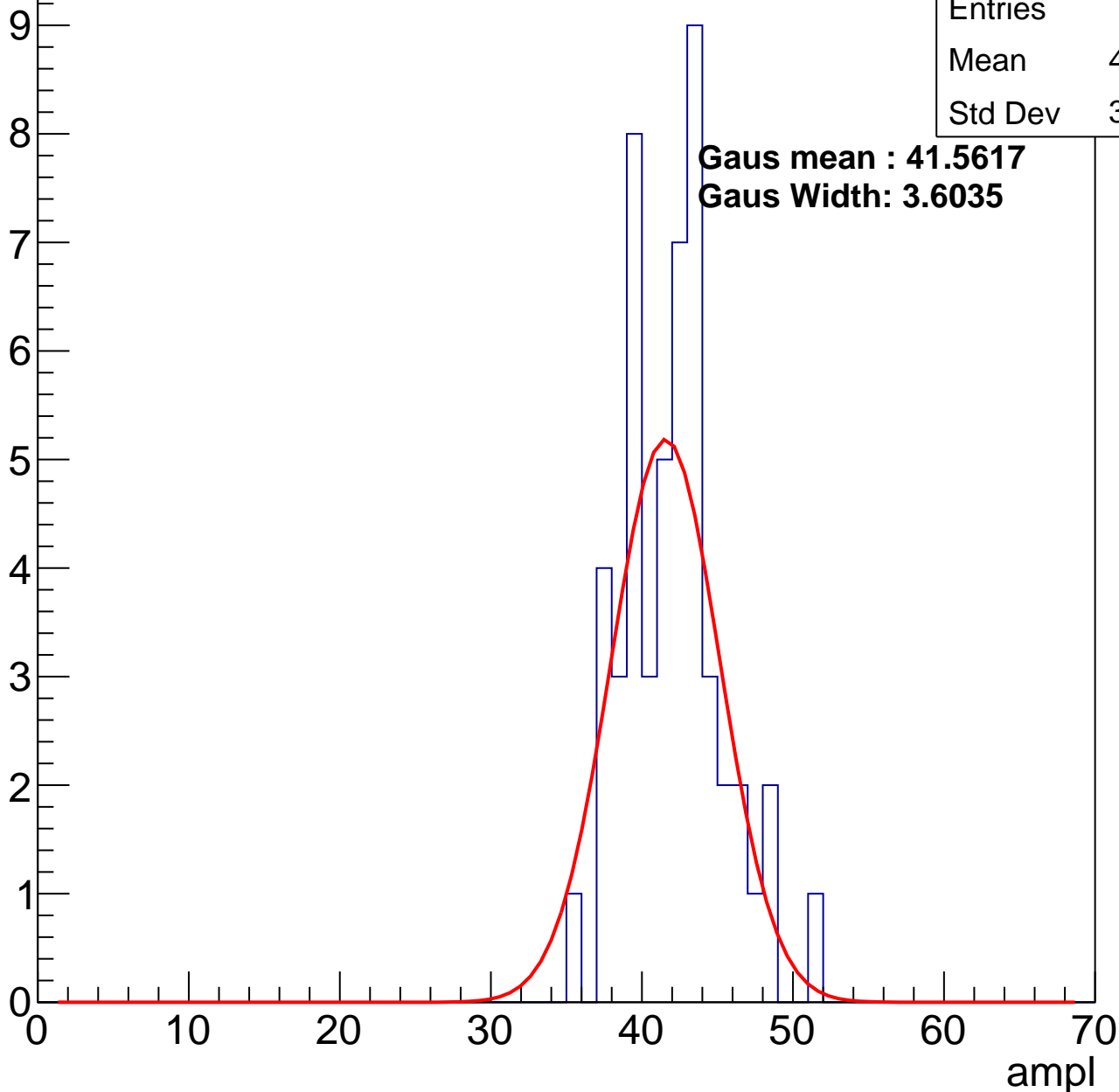
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	41.63
Std Dev	3.217

Gaus mean : 41.5617

Gaus Width: 3.6035

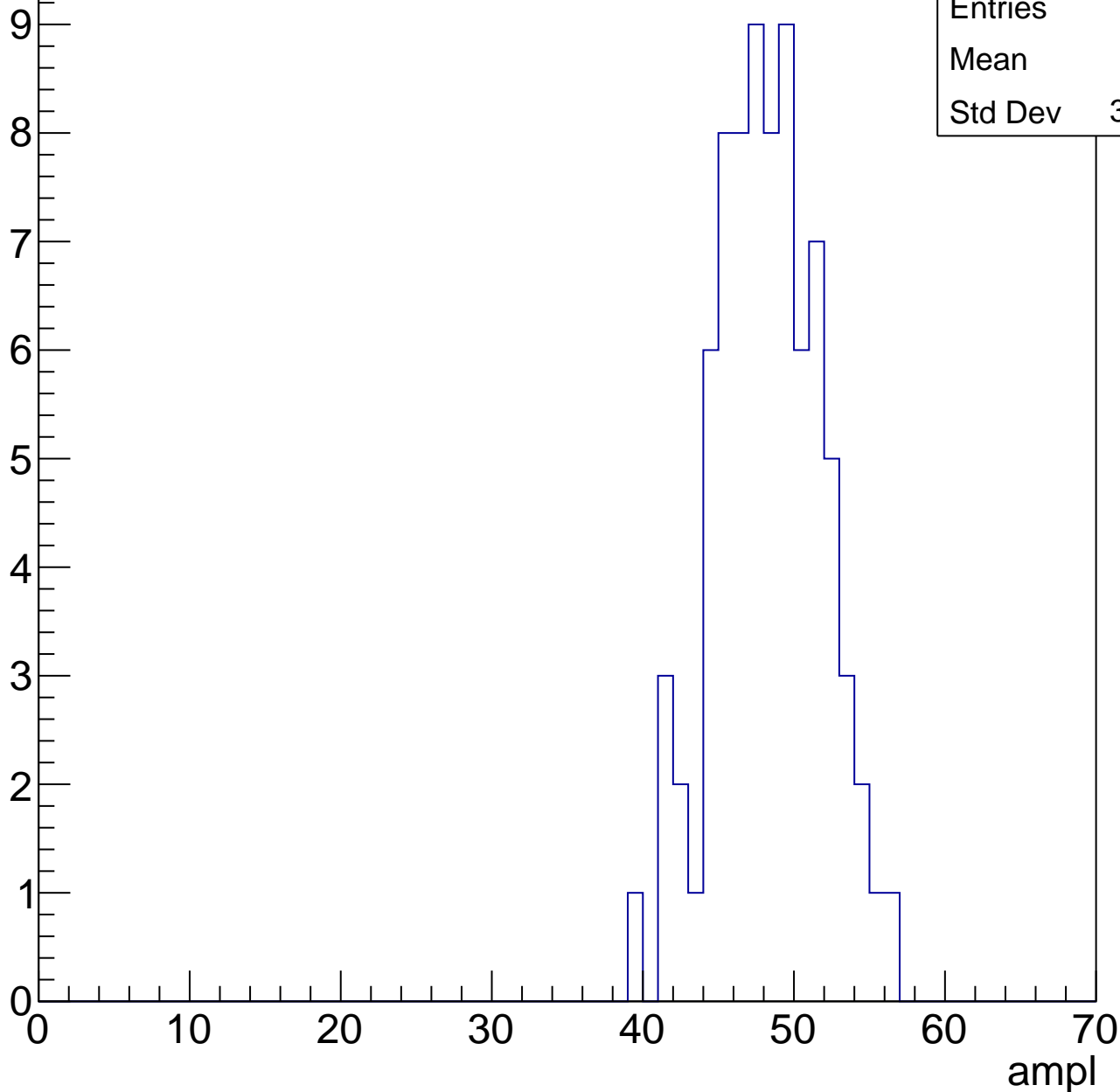


B1L101S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

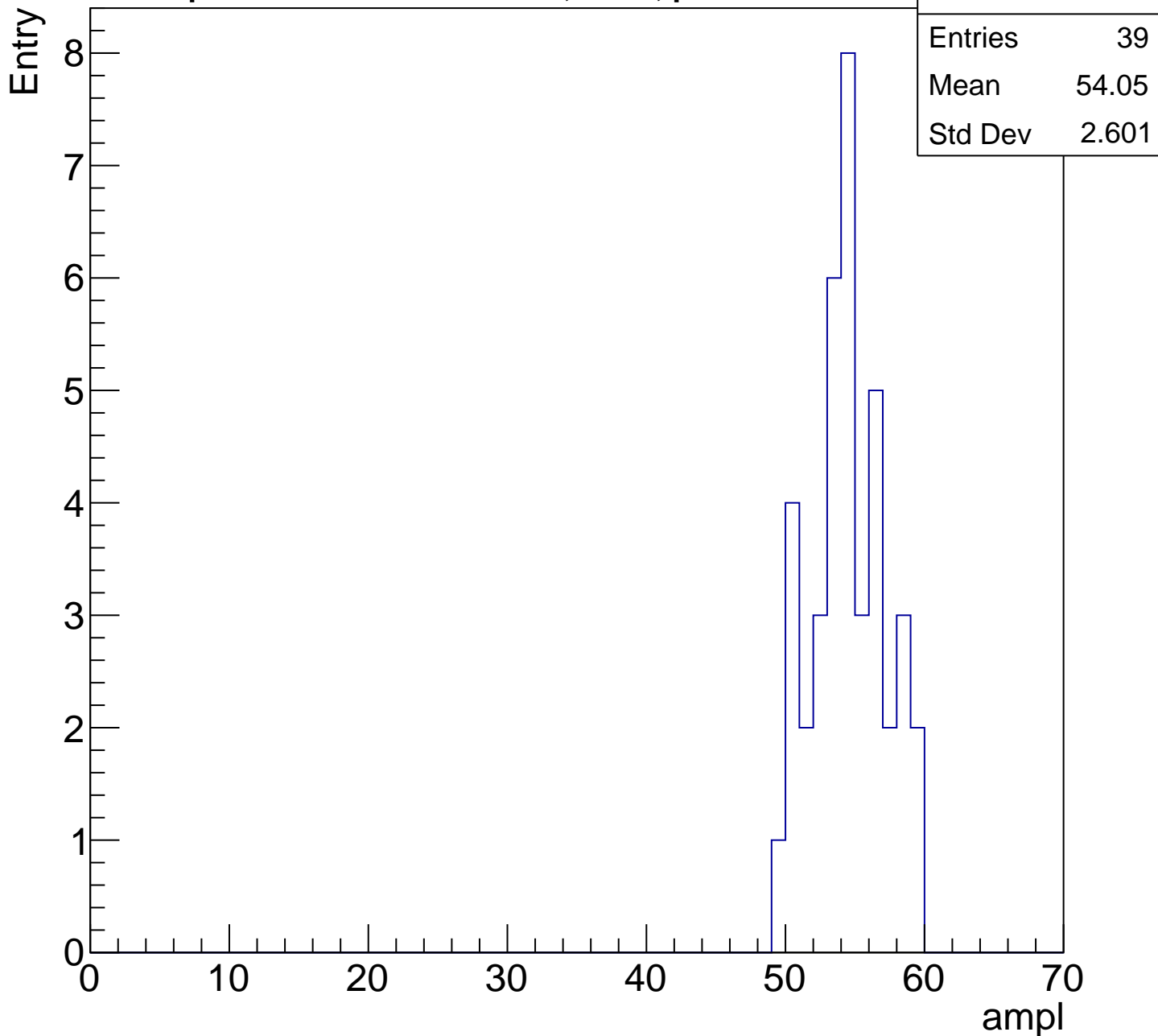
Entry

Entries	80
Mean	47.8
Std Dev	3.476



B1L101S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch50, adc5

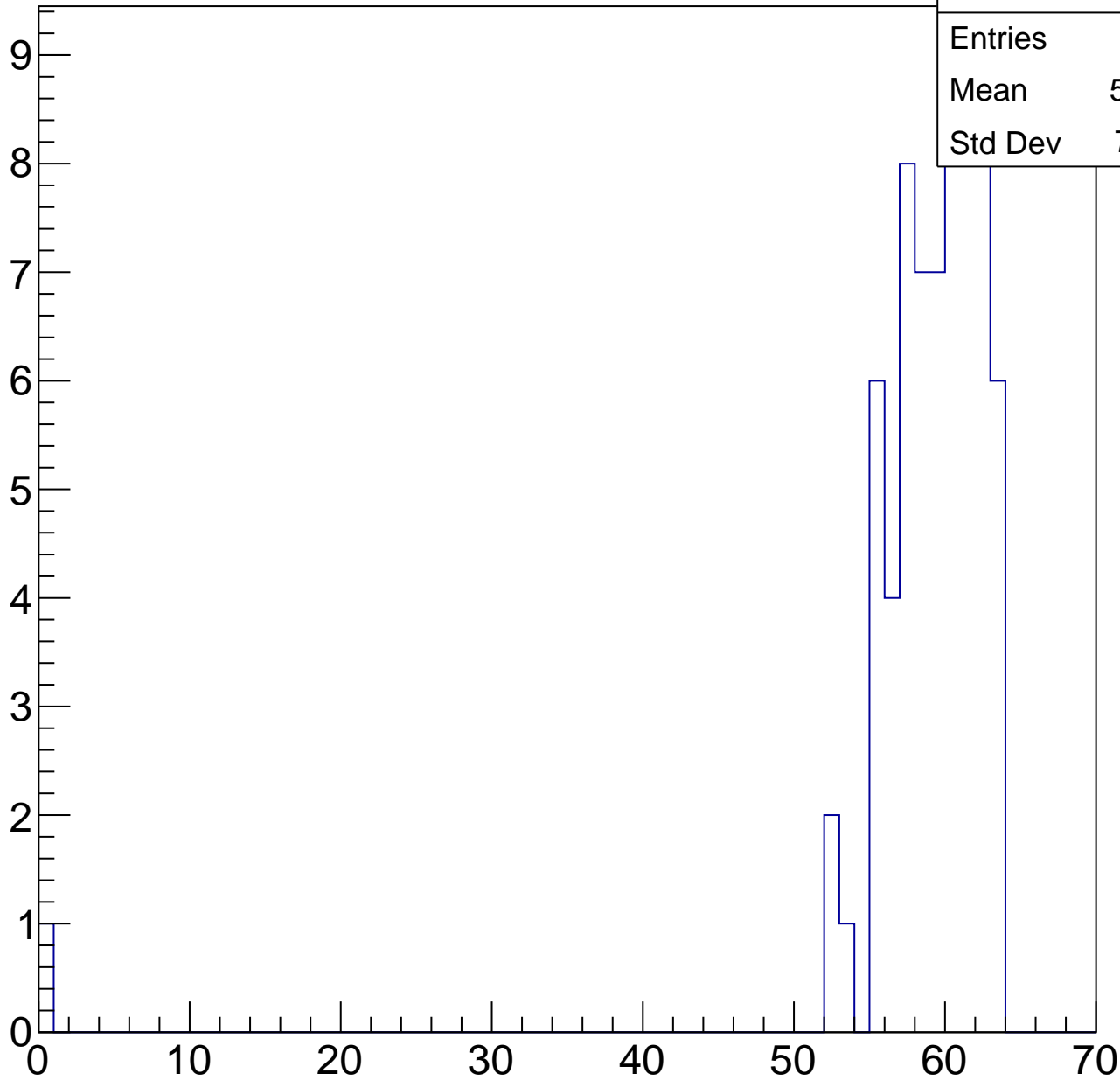
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	58.03
Std Dev	7.661

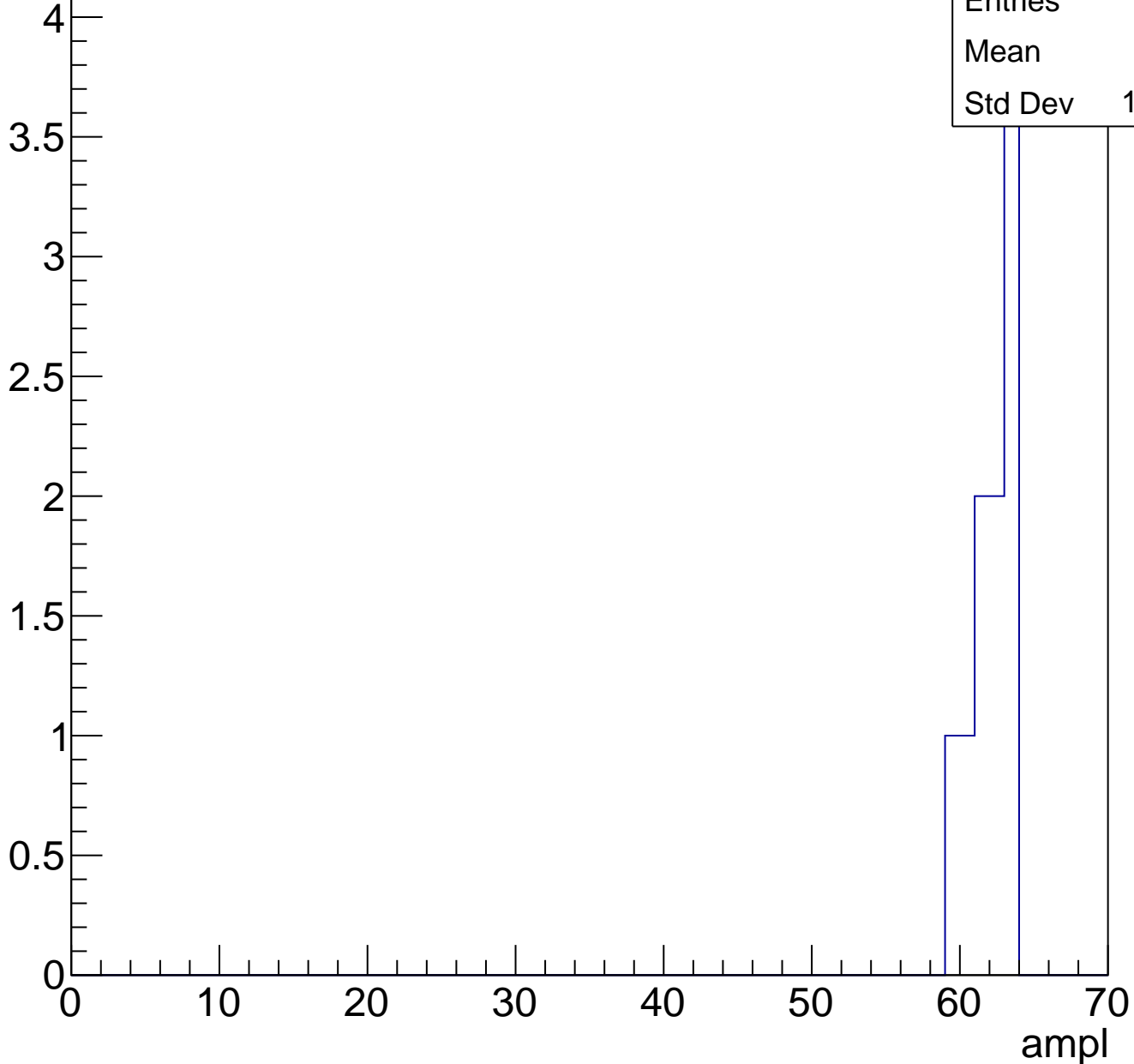
ampl



B1L101S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch51, adc0

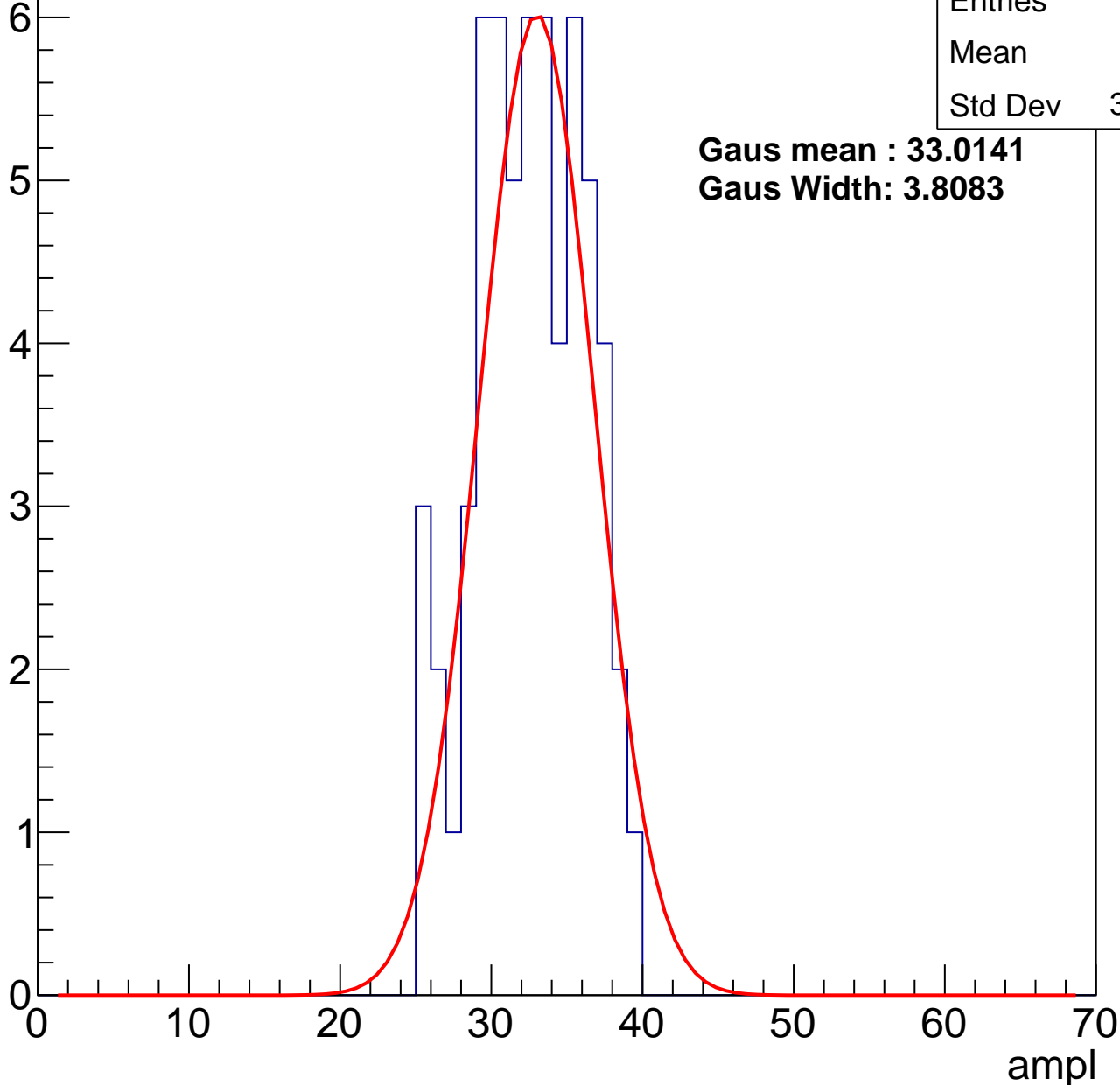
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	32.1
Std Dev	3.539

Gaus mean : 33.0141

Gaus Width: 3.8083



B1L101S, U14-ch51, adc1

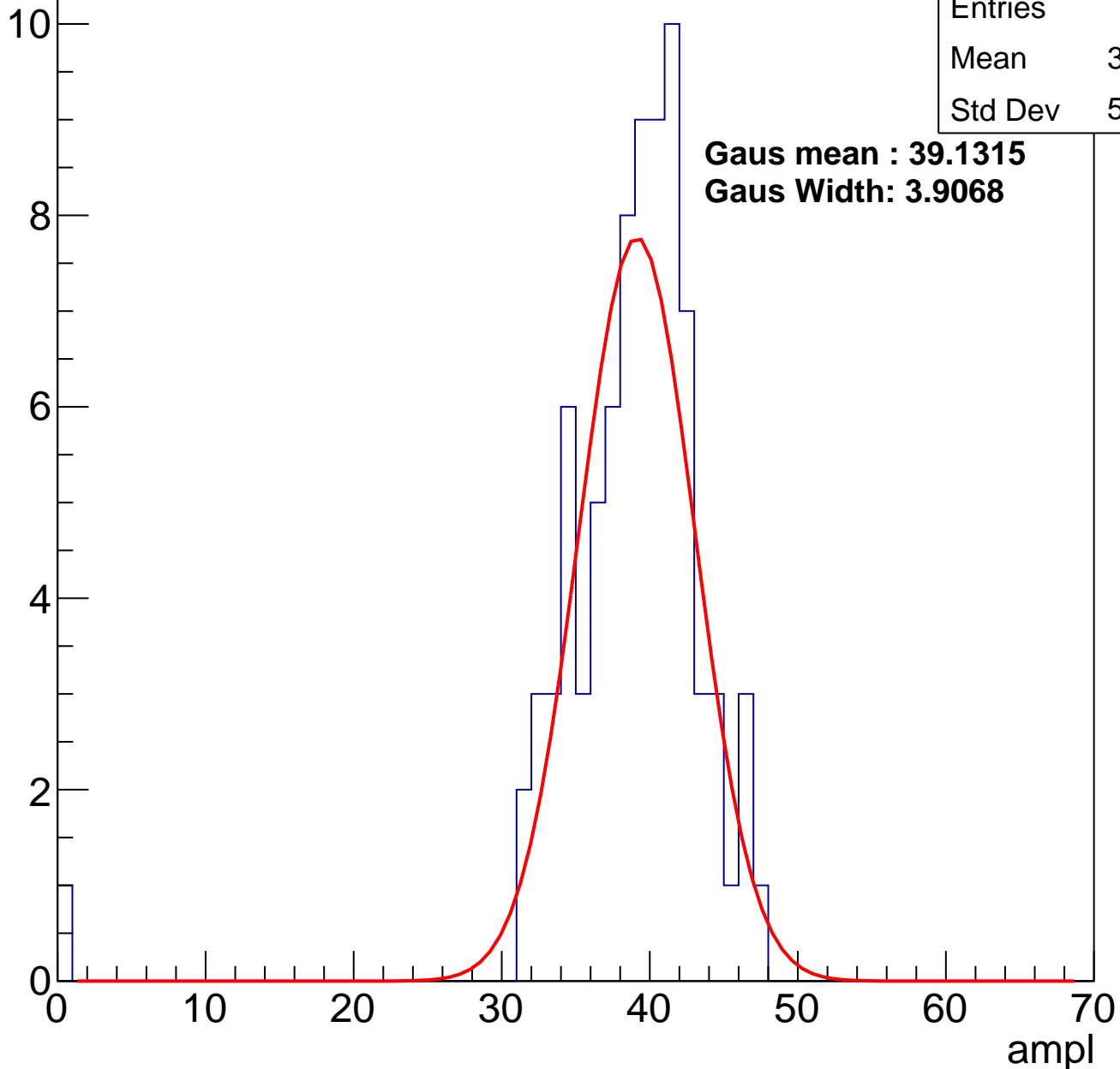
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	38.29
Std Dev	5.617

Gaus mean : 39.1315

Gaus Width: 3.9068

Entry



B1L101S, U14-ch51, adc2

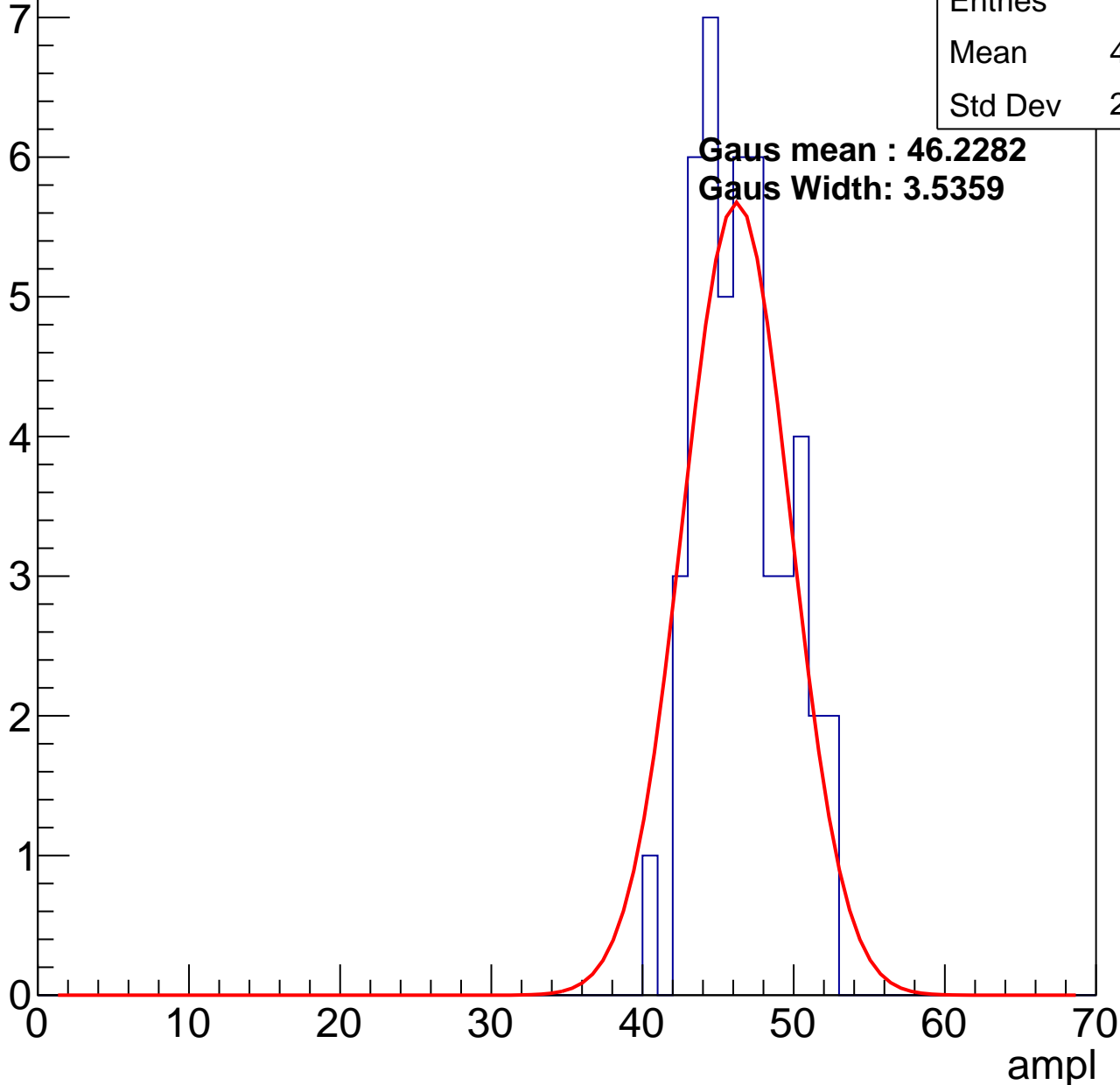
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	46.08
Std Dev	2.893

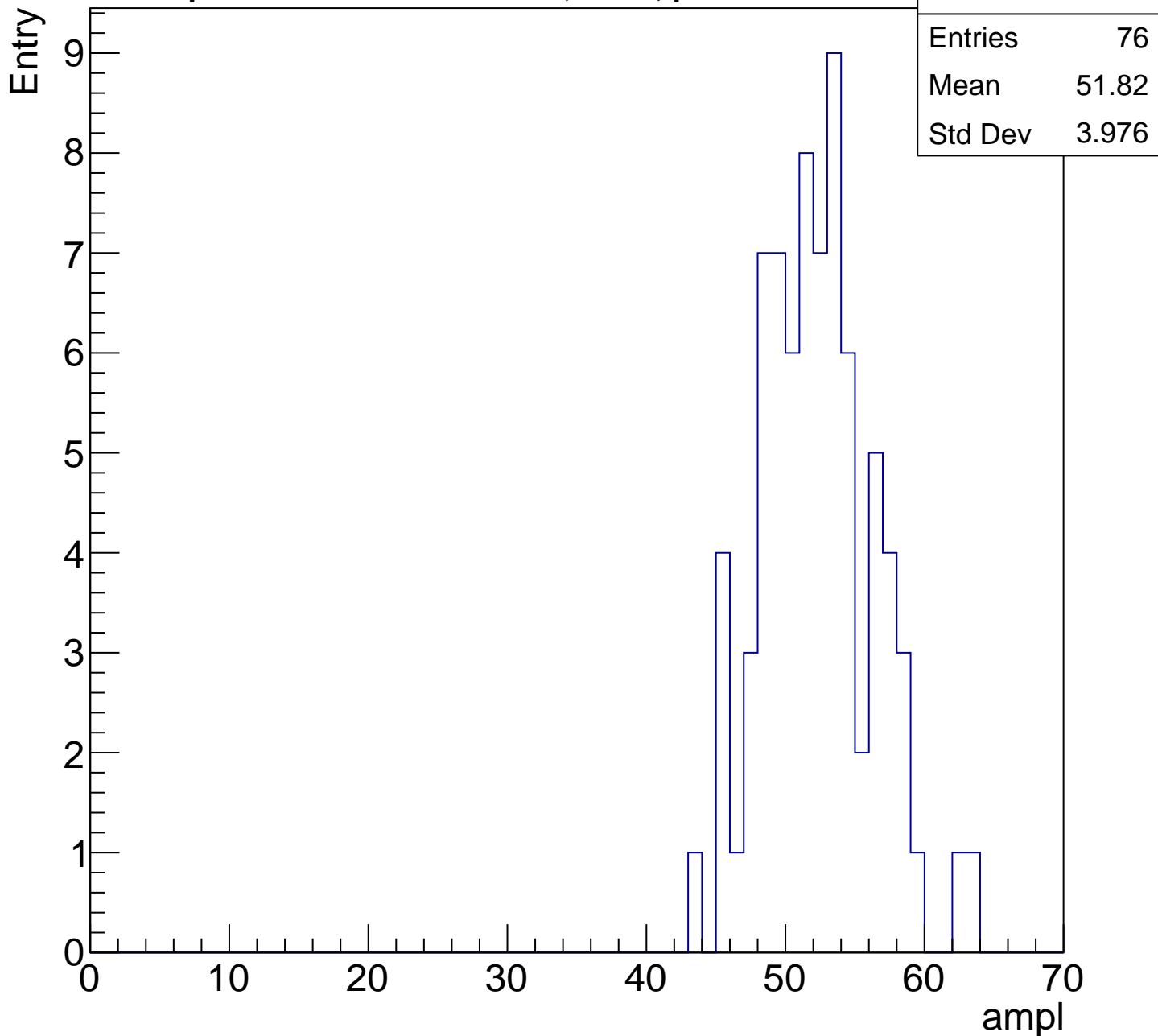
Gaus mean : 46.2282

Gaus Width: 3.5359



B1L101S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch51, adc4

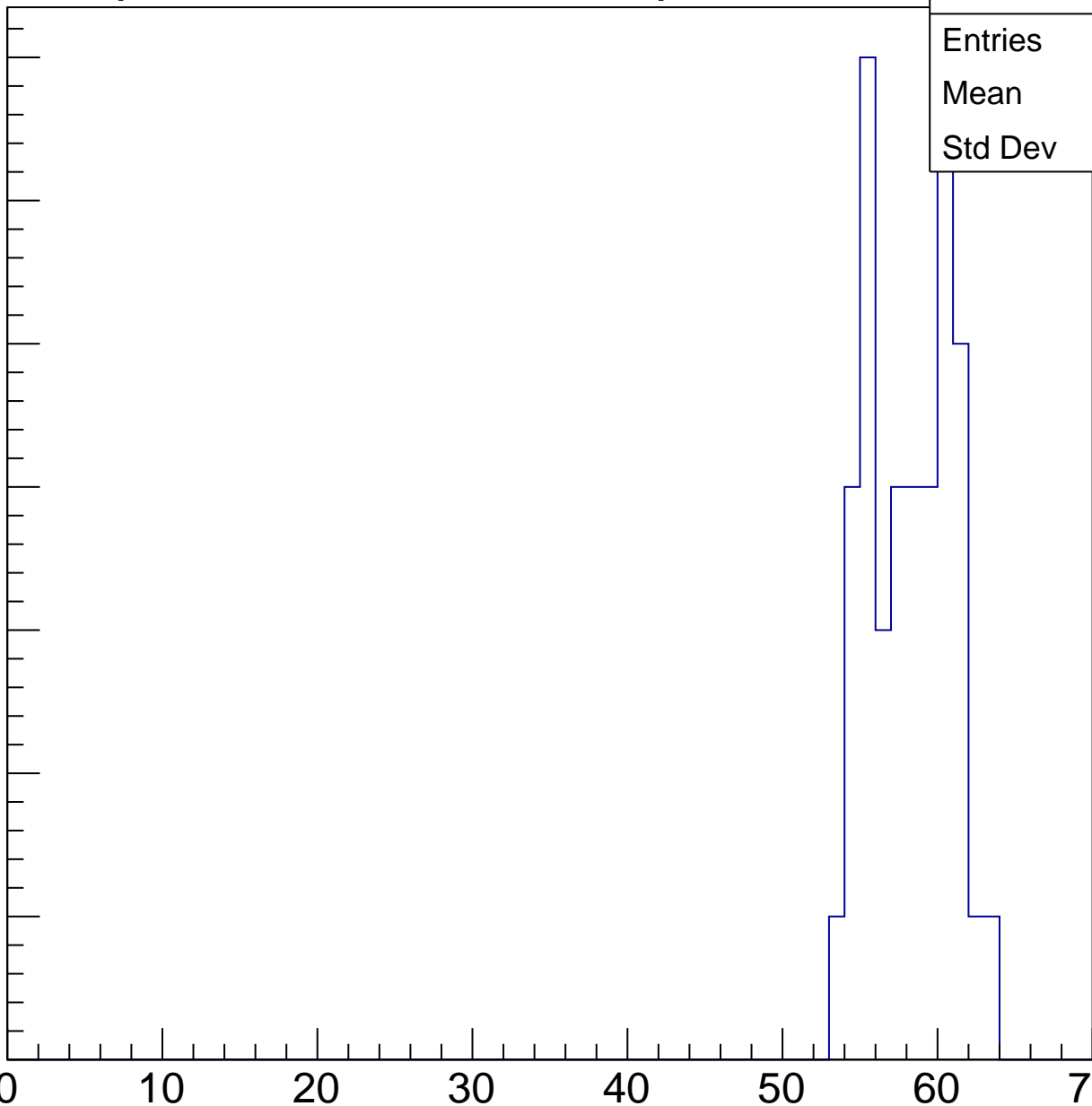
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	57.76
Std Dev	2.63

ampl

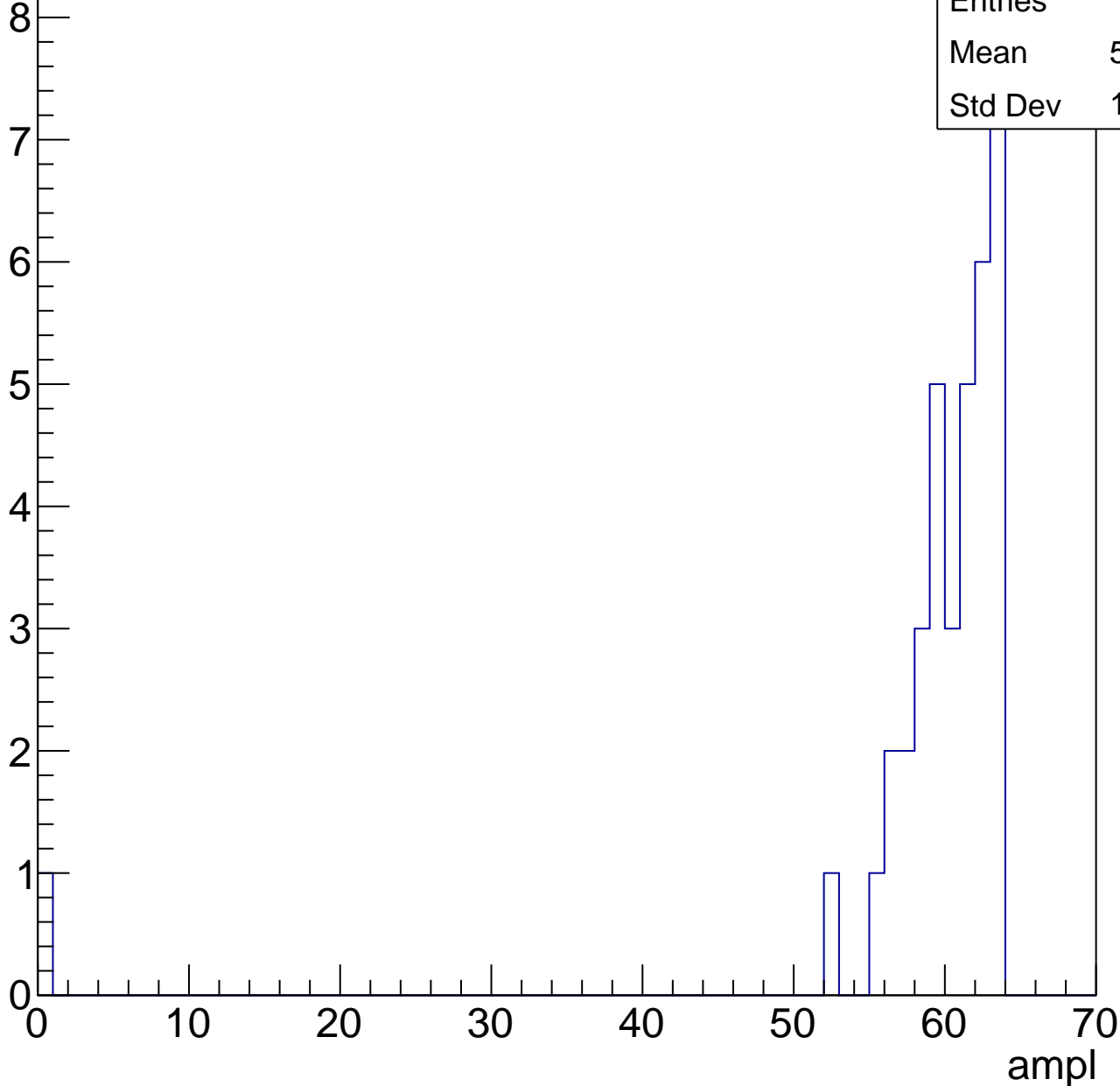


B1L101S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.46
Std Dev	10.09



B1L101S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch52, adc0

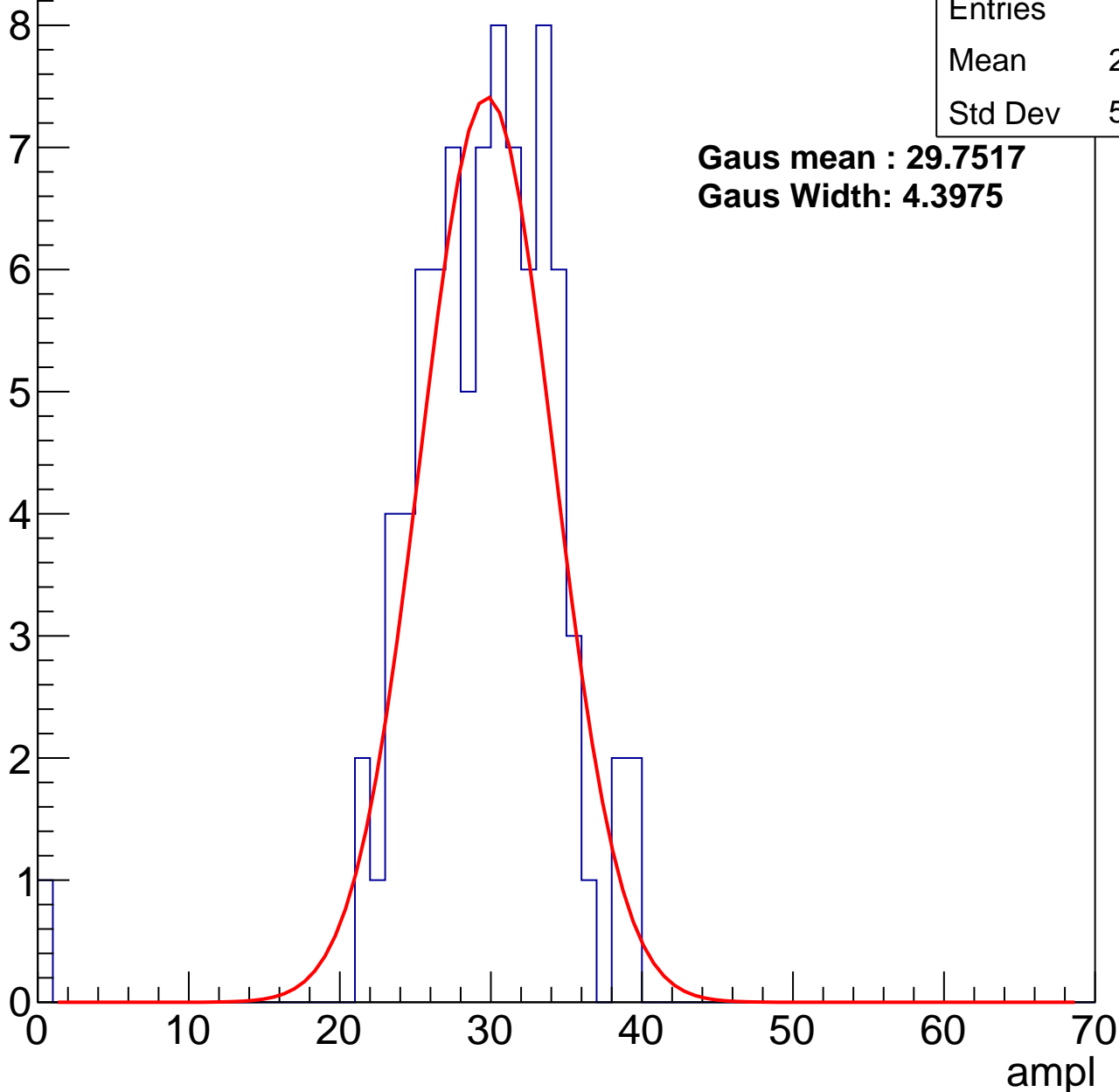
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.09
Std Dev	5.206

Gaus mean : 29.7517

Gaus Width: 4.3975



B1L101S, U14-ch52, adc1

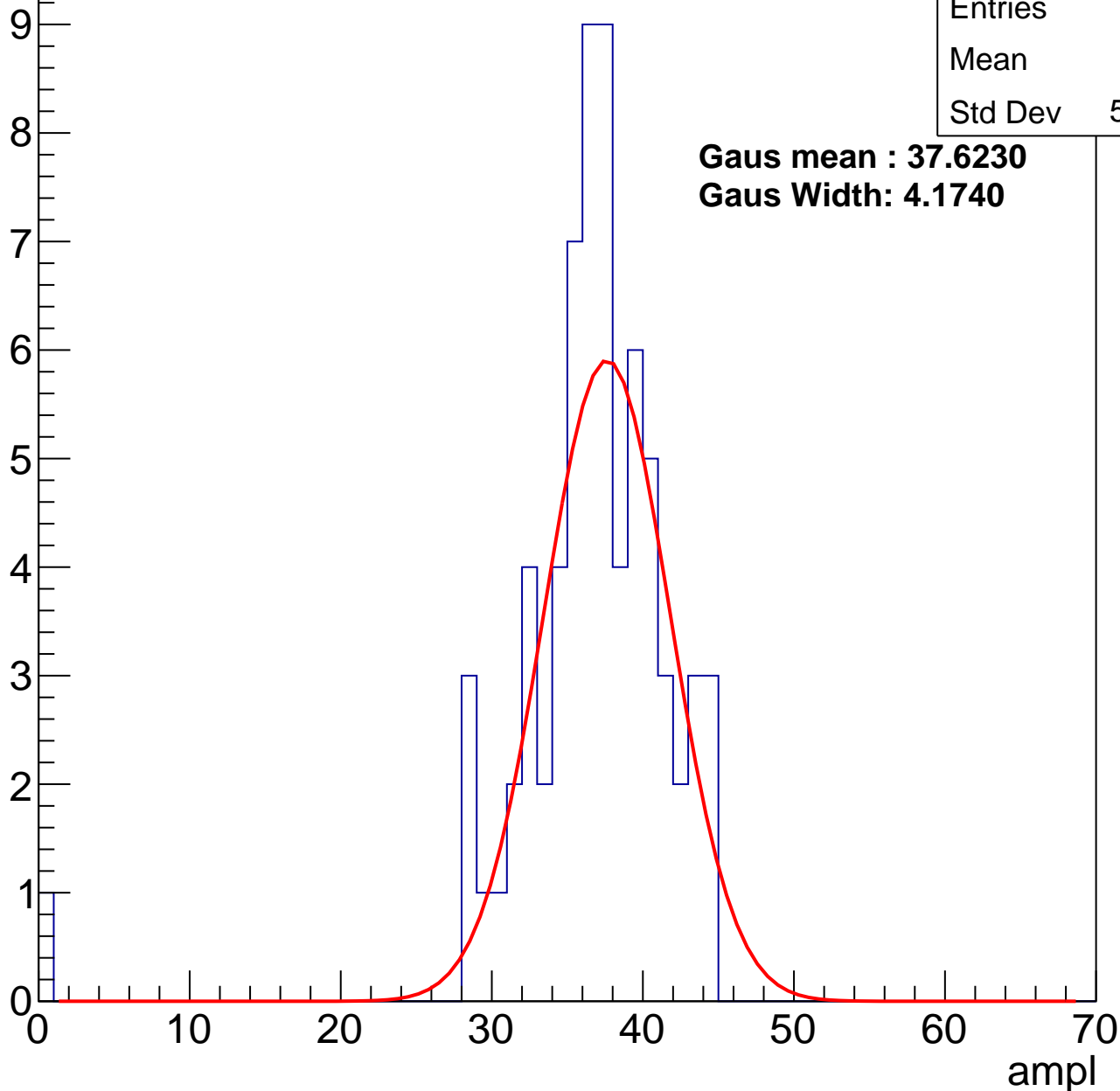
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.1
Std Dev	5.864

Gaus mean : 37.6230

Gaus Width: 4.1740



B1L101S, U14-ch52, adc2

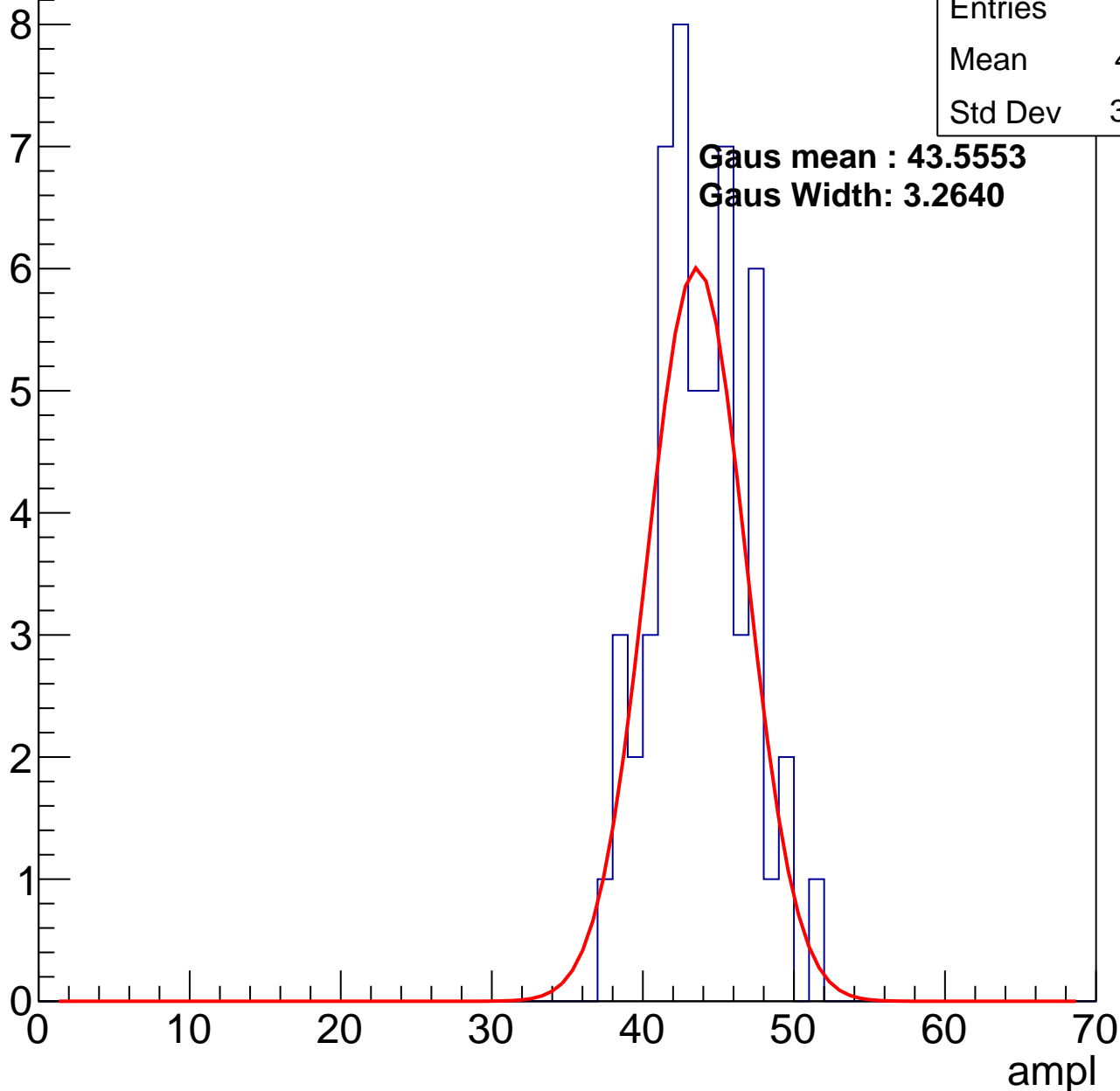
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.31
Std Dev	3.096

Gaus mean : 43.5553

Gaus Width: 3.2640

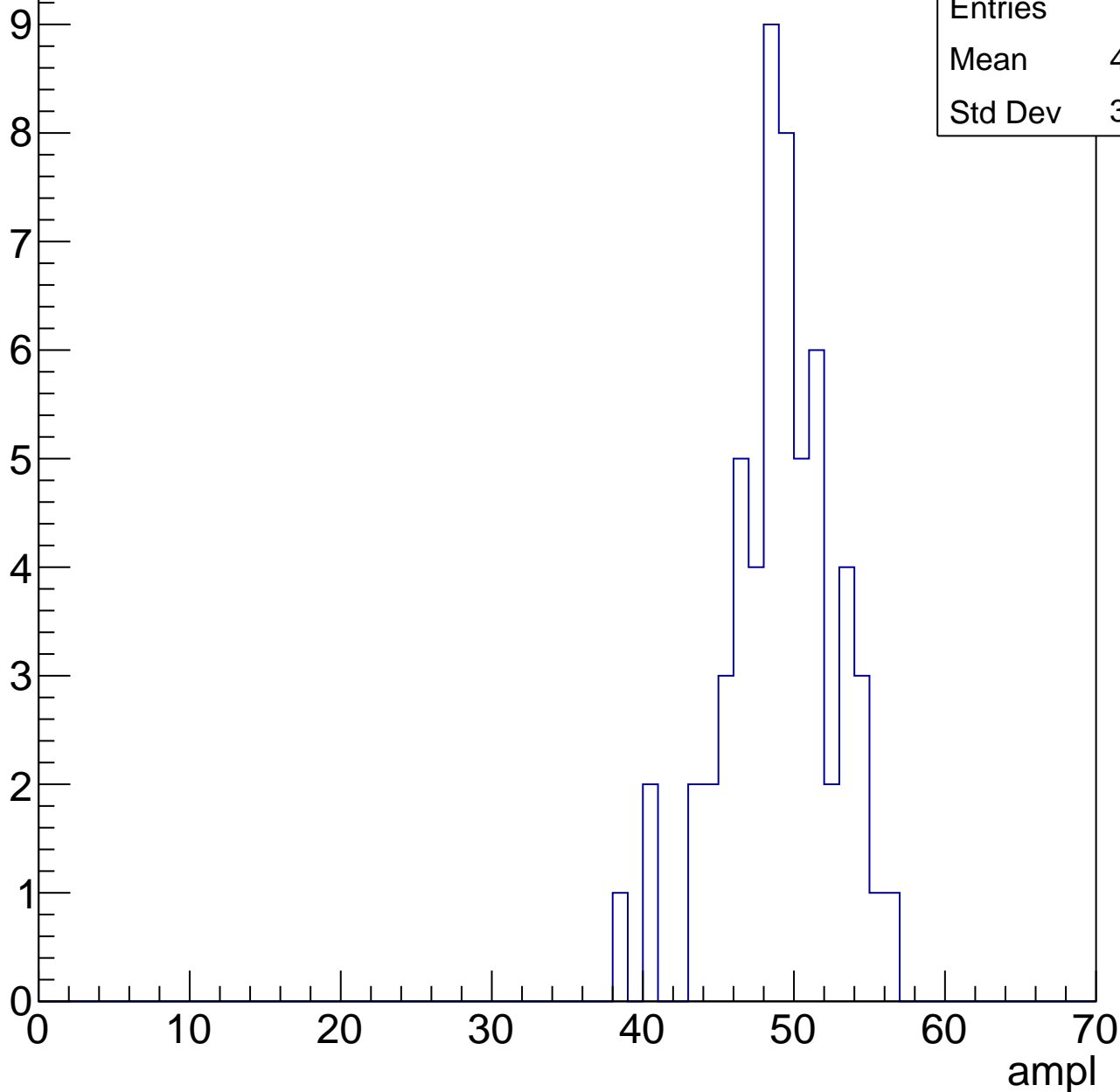


B1L101S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.52
Std Dev	3.668

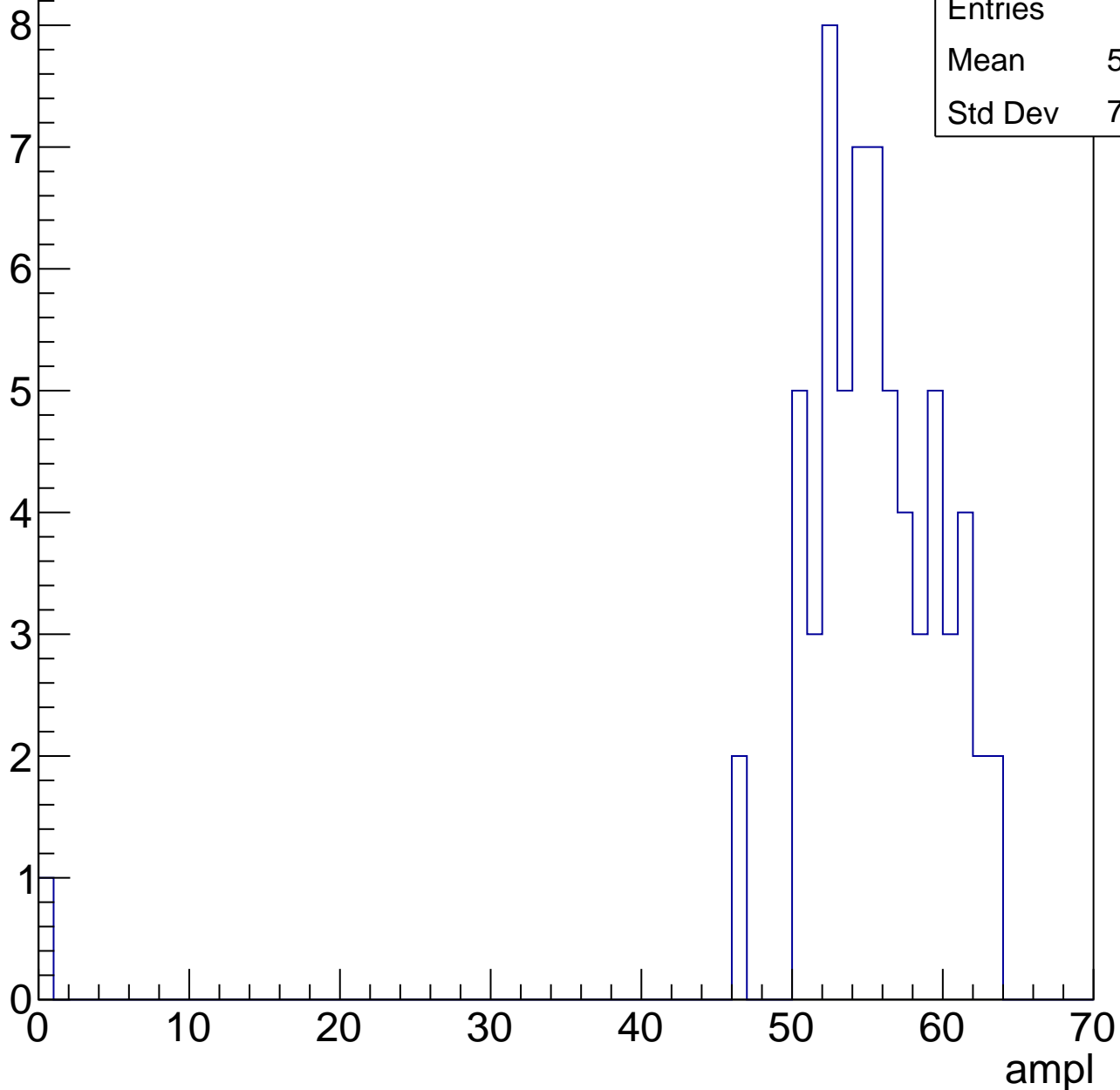


B1L101S, U14-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	54.39
Std Dev	7.793



B1L101S, U14-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 49

Mean 59.27

Std Dev 2.481

ampl

0

10

20

30

40

50

60

70

B1L101S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch53, adc0

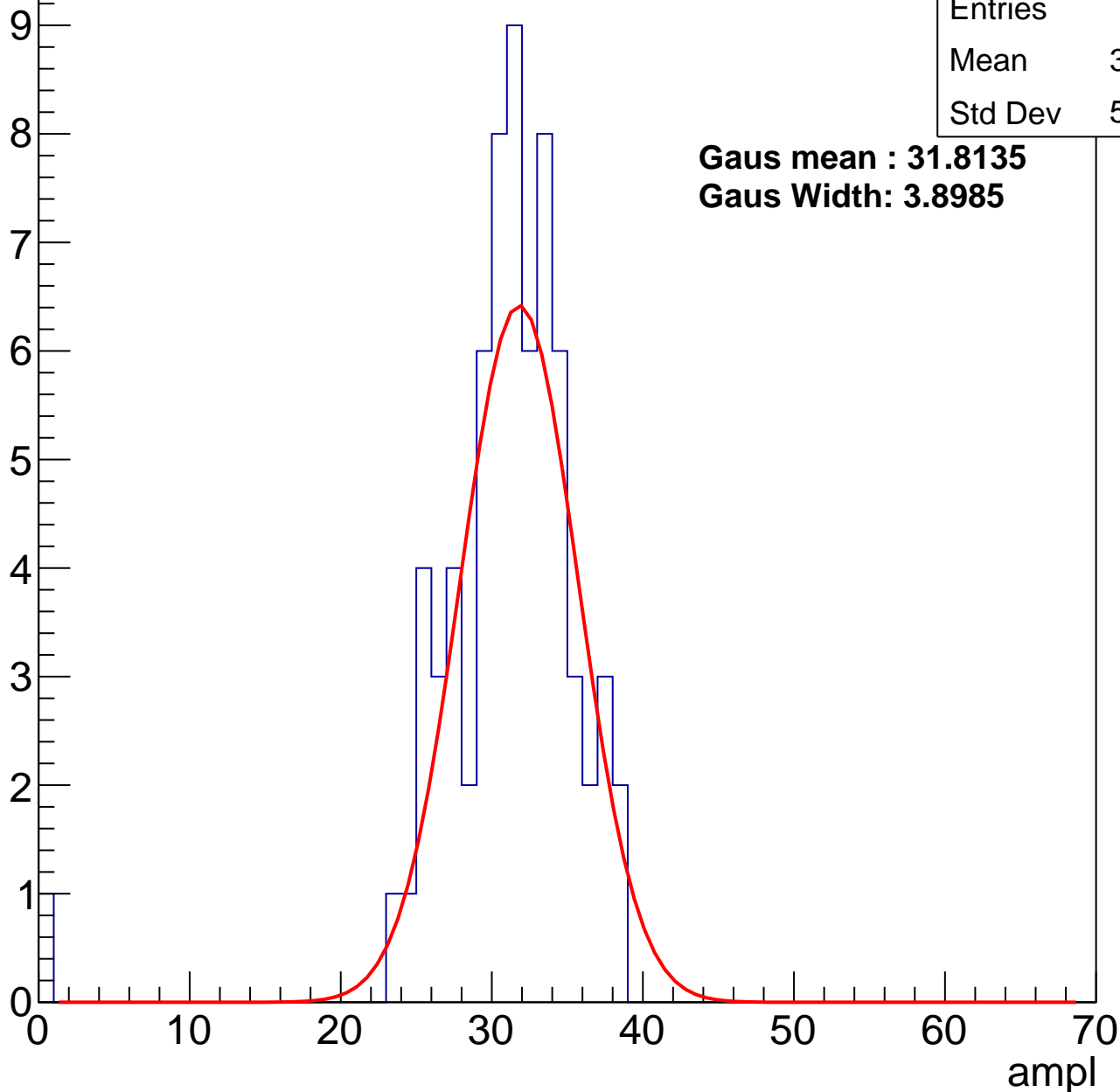
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.52
Std Dev	5.089

Gaus mean : 31.8135

Gaus Width: 3.8985



B1L101S, U14-ch53, adc1

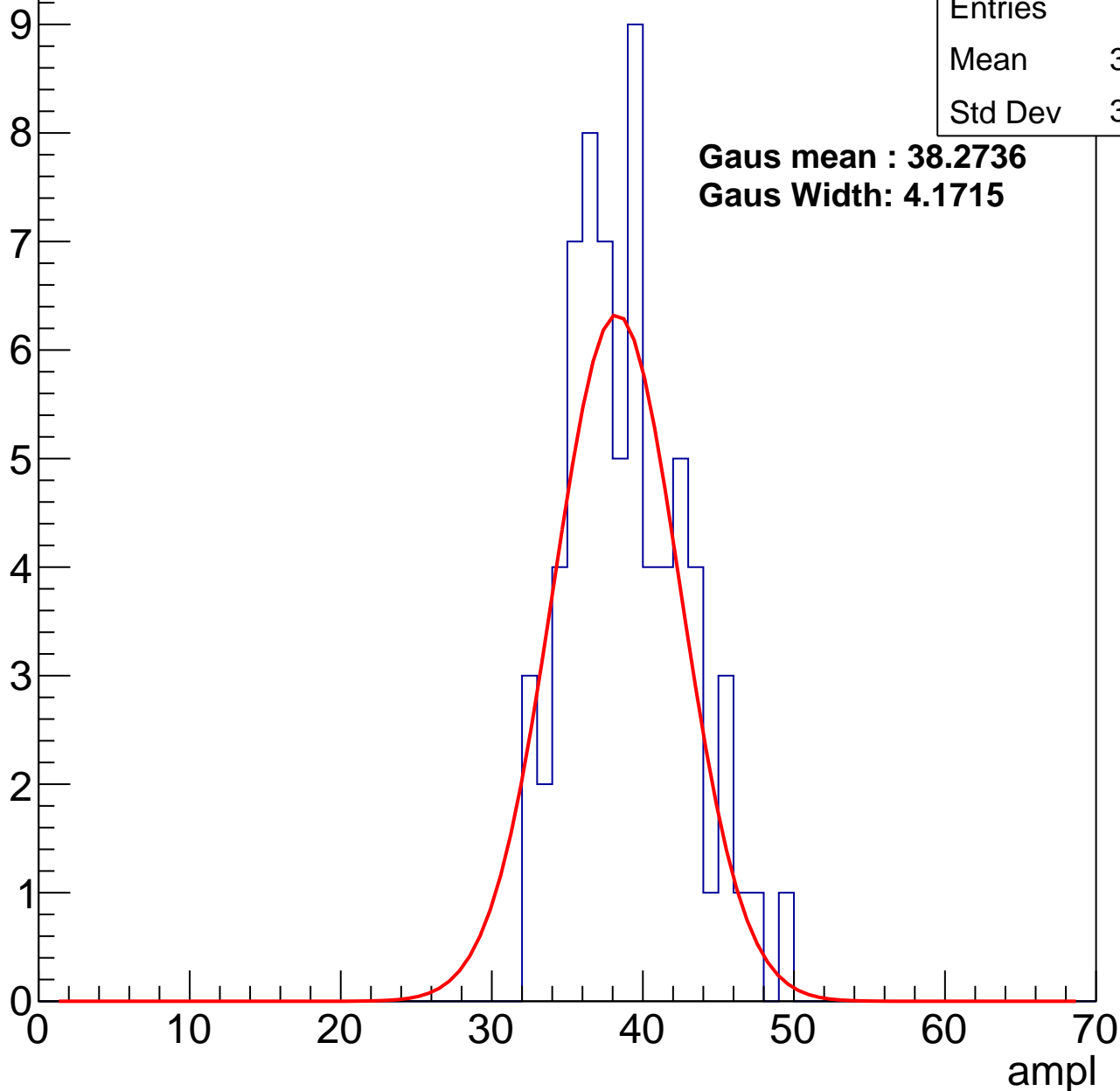
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.52
Std Dev	3.798

Gaus mean : 38.2736

Gaus Width: 4.1715



B1L101S, U14-ch53, adc2

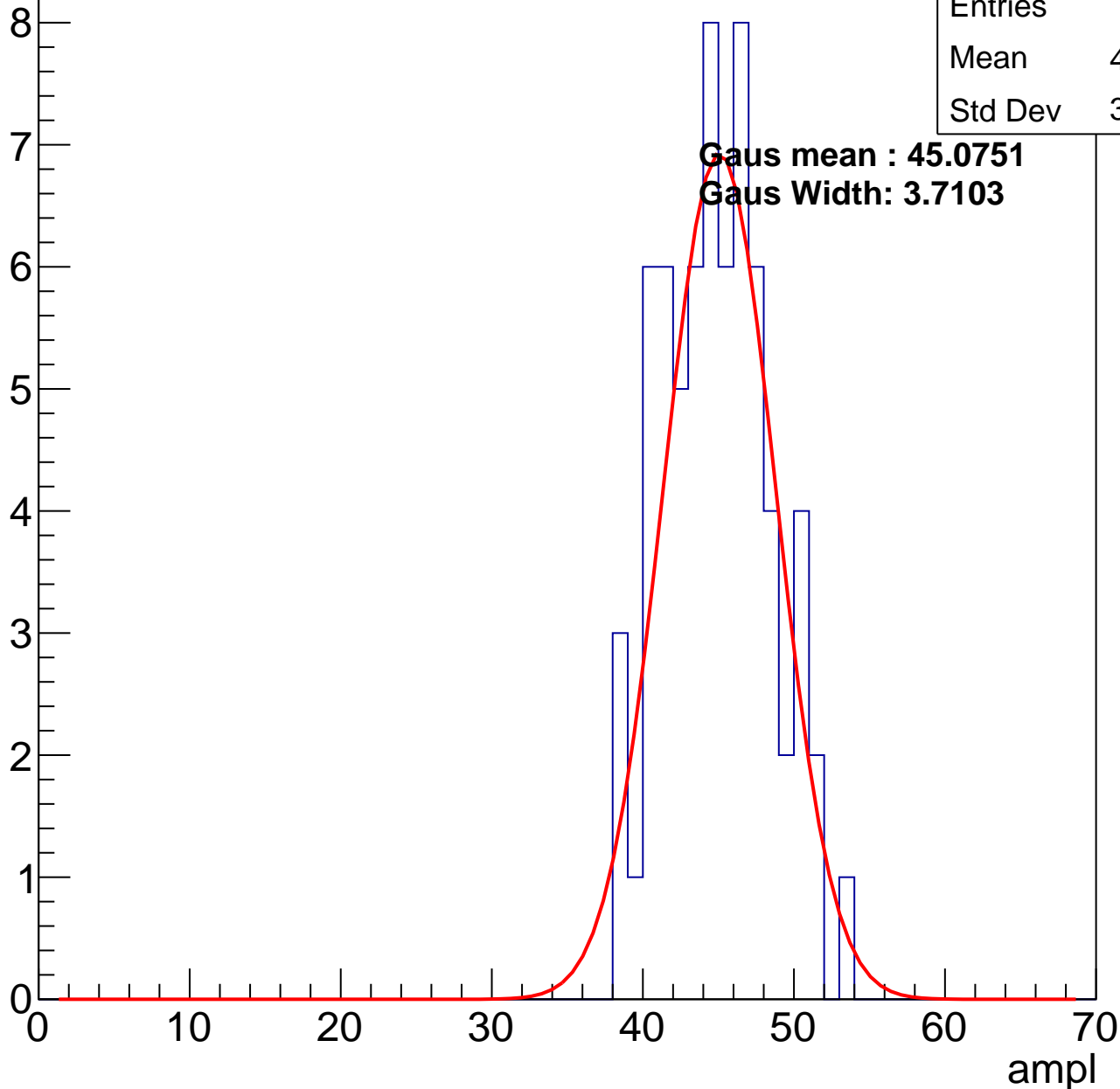
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.47
Std Dev	3.479

Gaus mean : 45.0751

Gaus Width: 3.7103

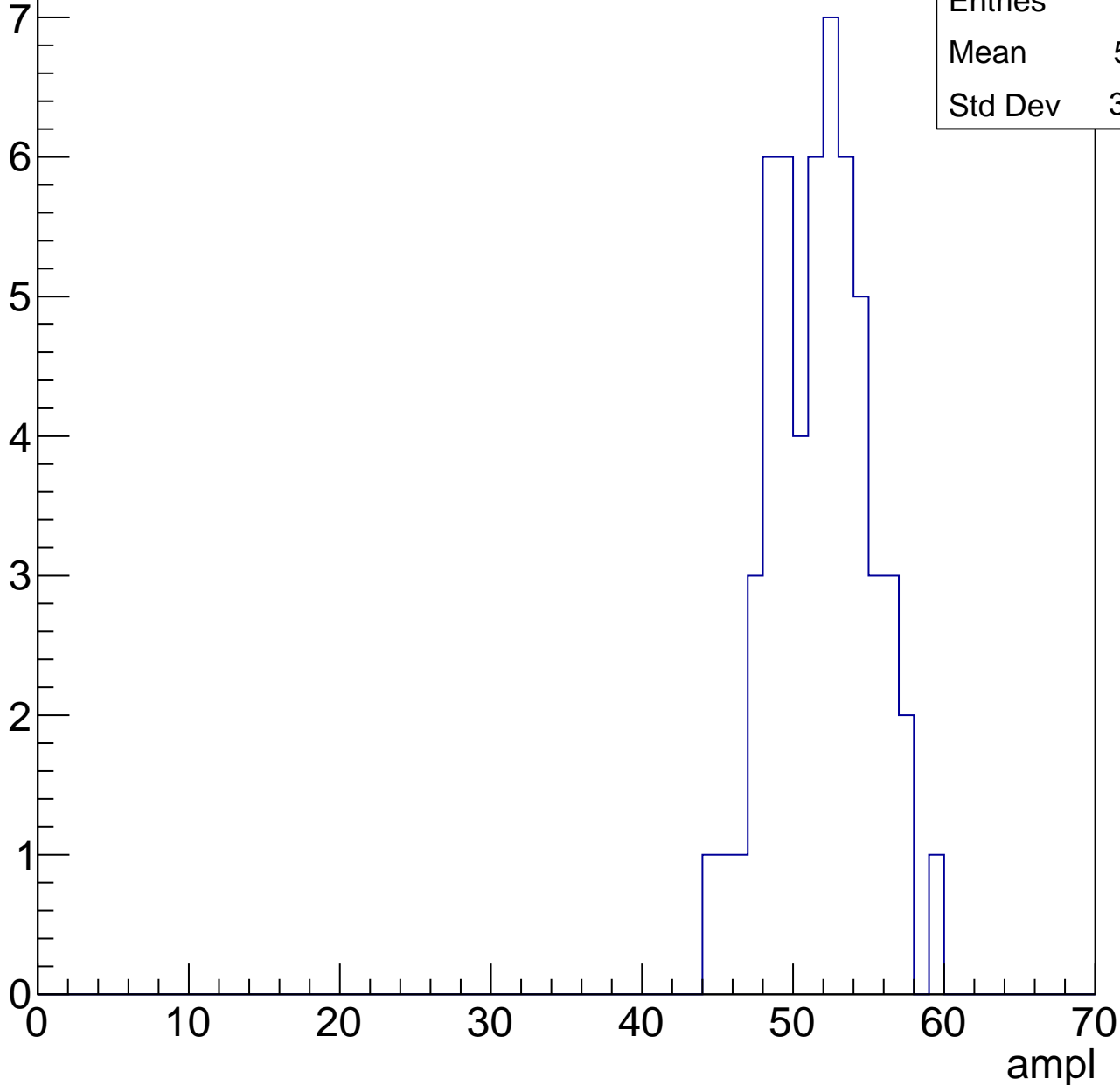


B1L101S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

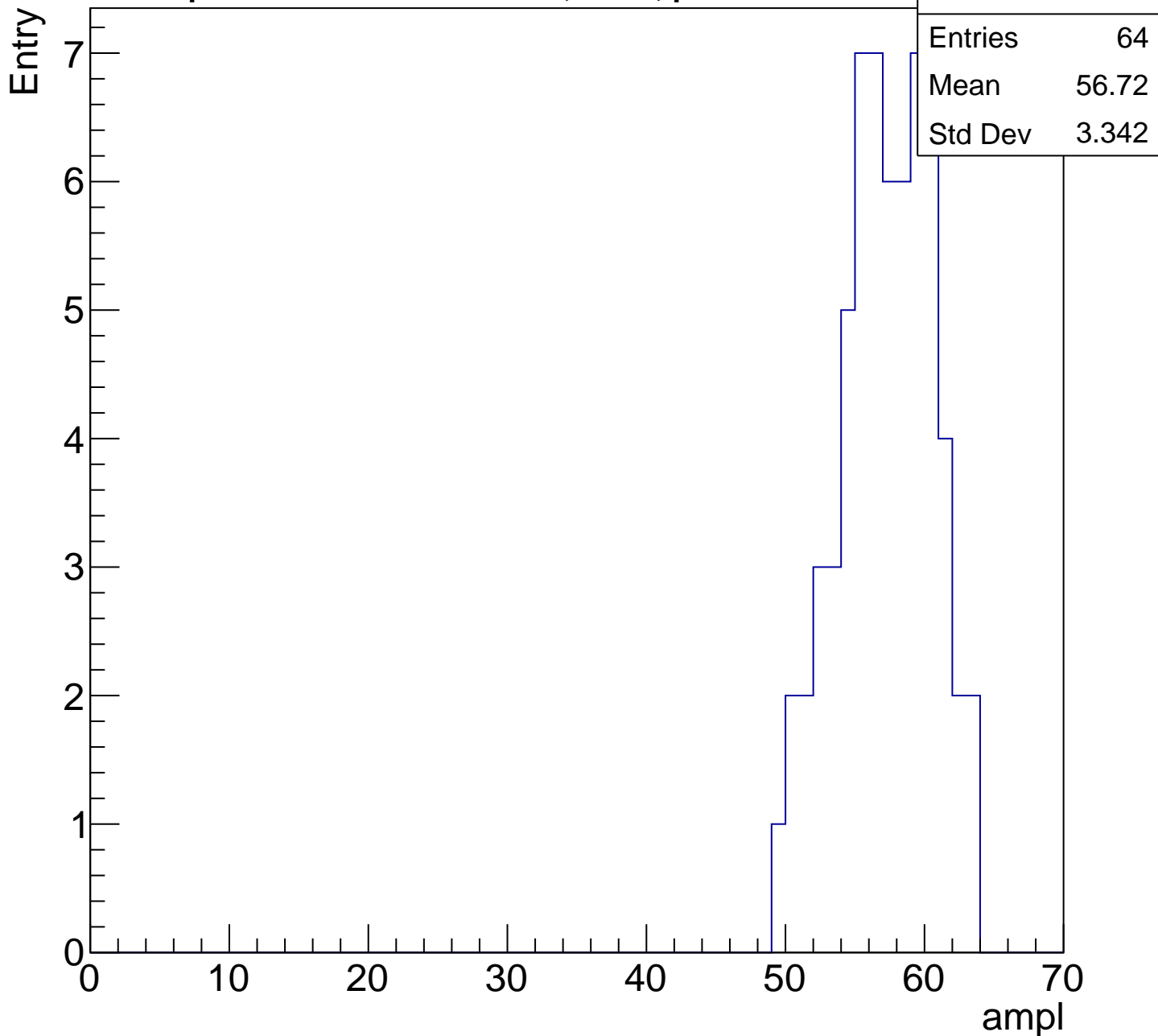
Entry

Entries	55
Mean	51.31
Std Dev	3.219



B1L101S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

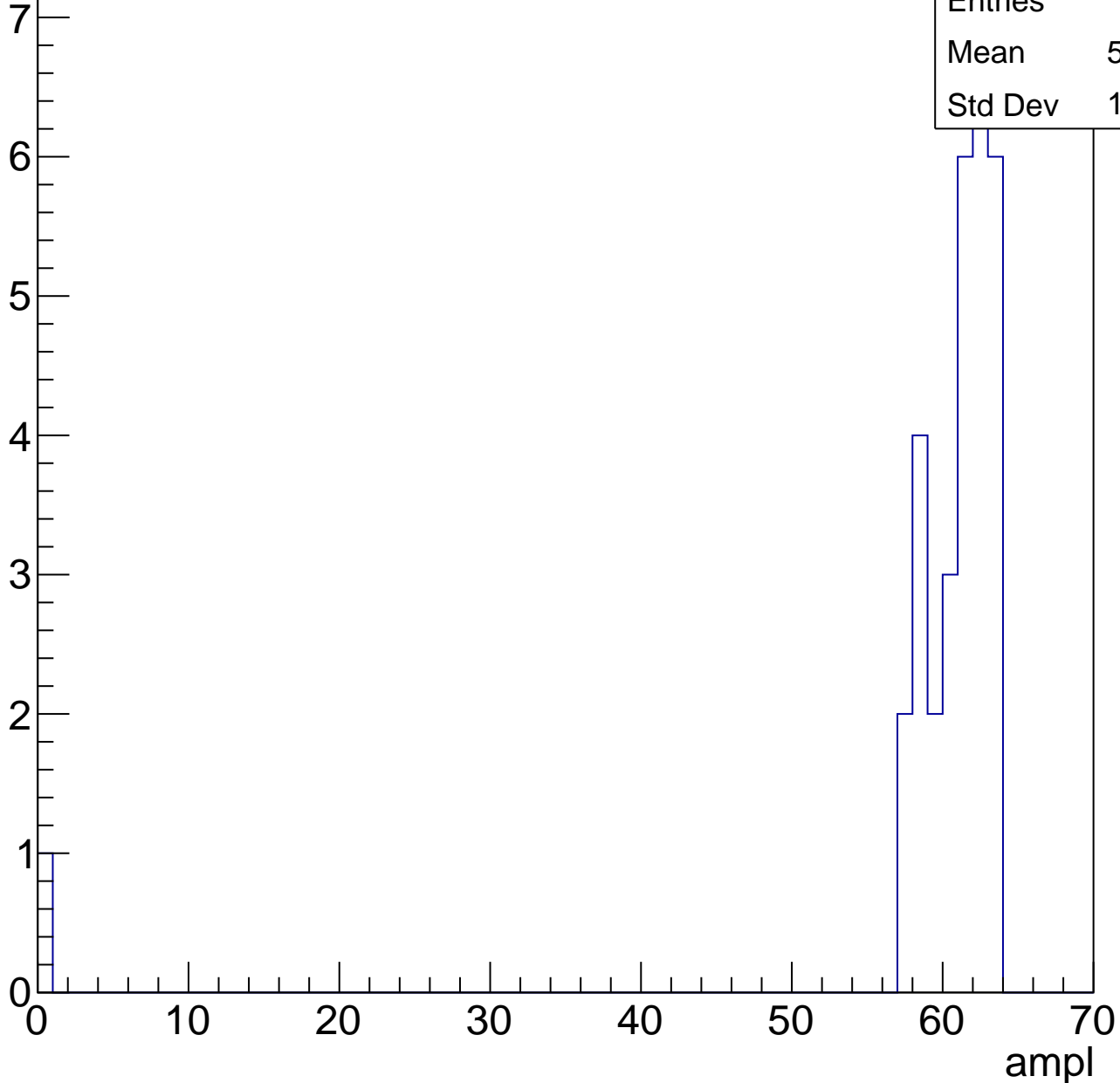


B1L101S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.77
Std Dev	10.89



B1L101S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch54, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	28.63
Std Dev	3.437

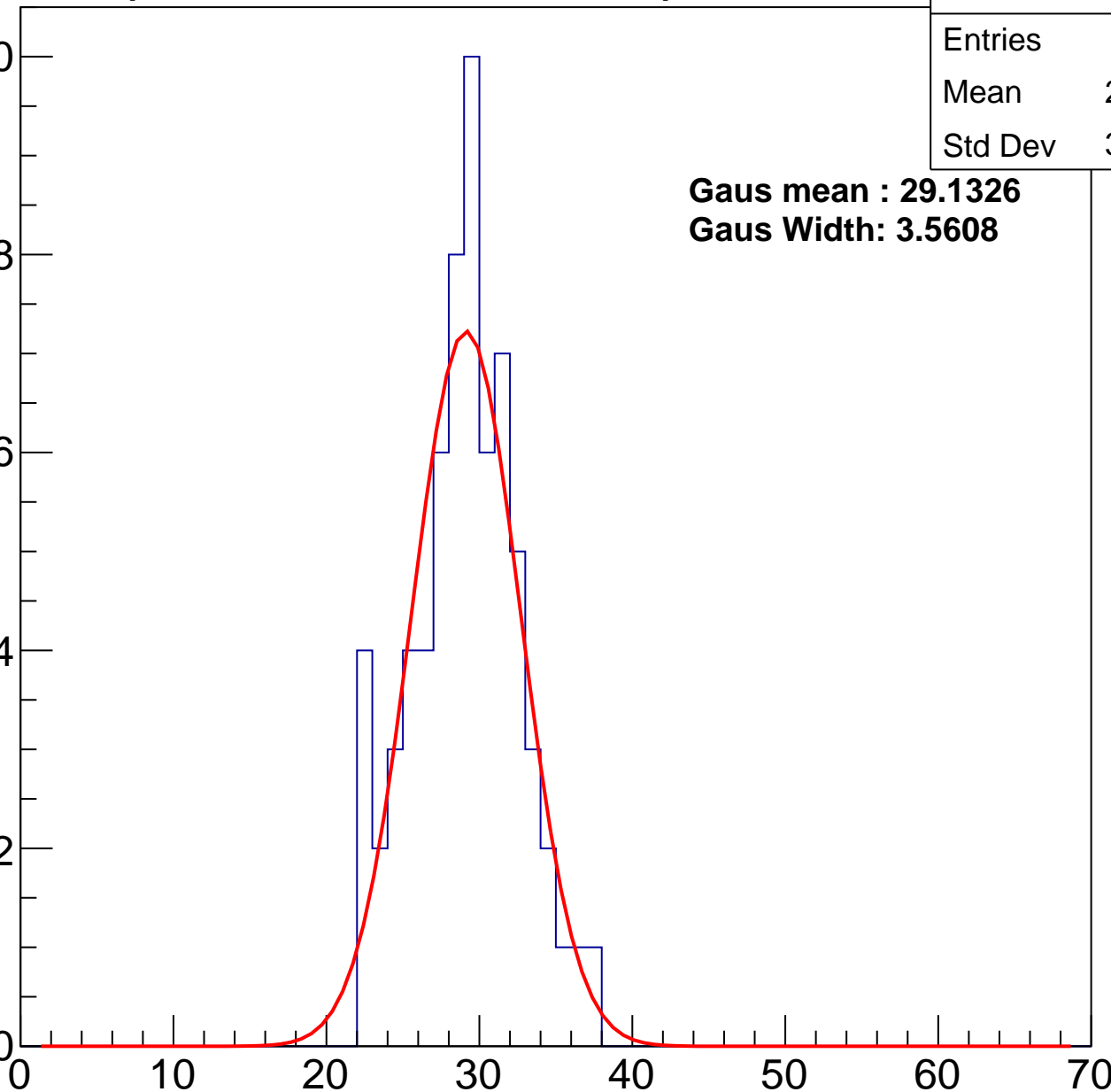
Gaus mean : 29.1326

Gaus Width: 3.5608

Entry

10
8
6
4
2
0

ampl



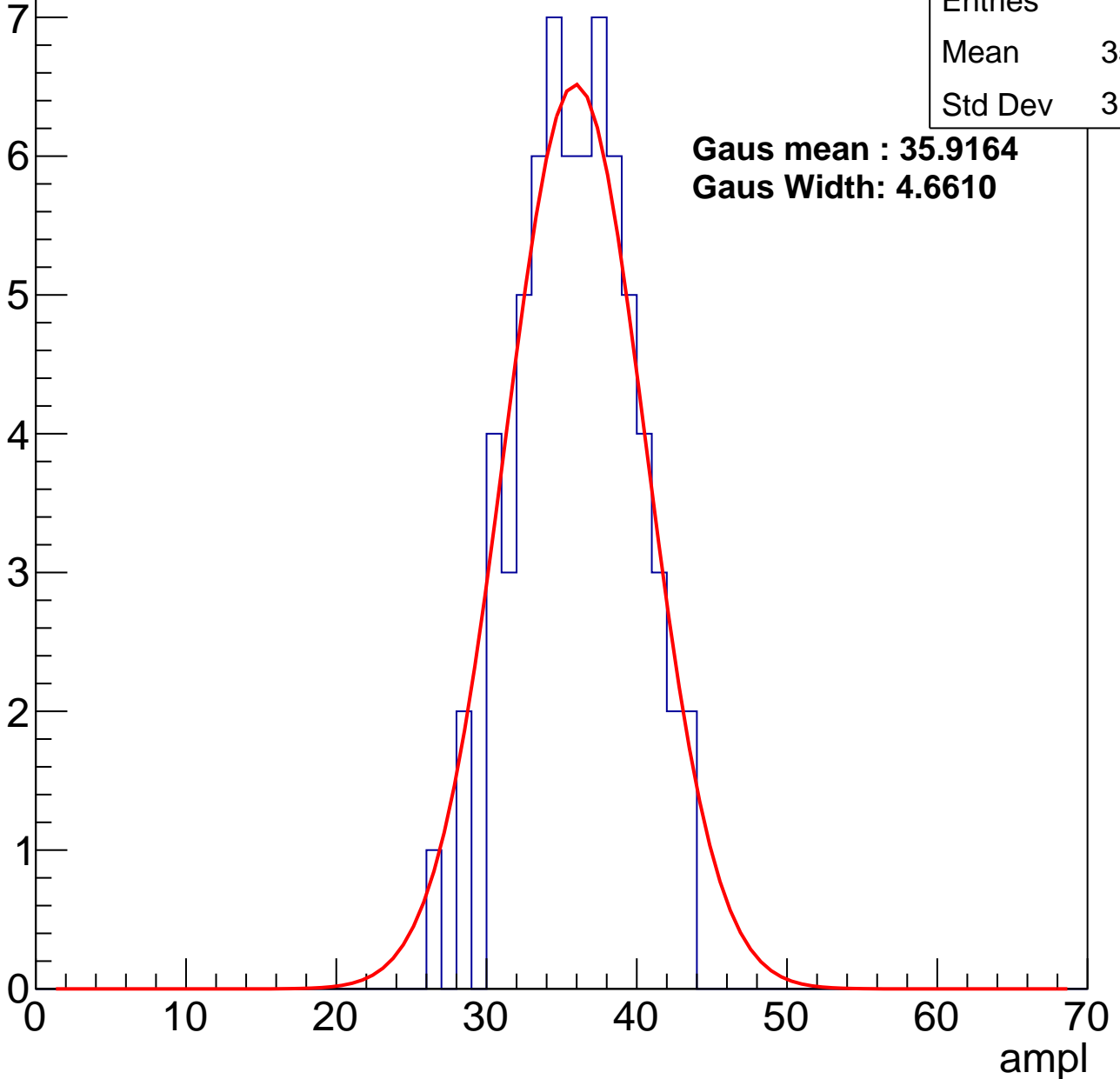
B1L101S, U14-ch54, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.54
Std Dev	3.779

Gaus mean : 35.9164
Gaus Width: 4.6610



B1L101S, U14-ch54, adc2

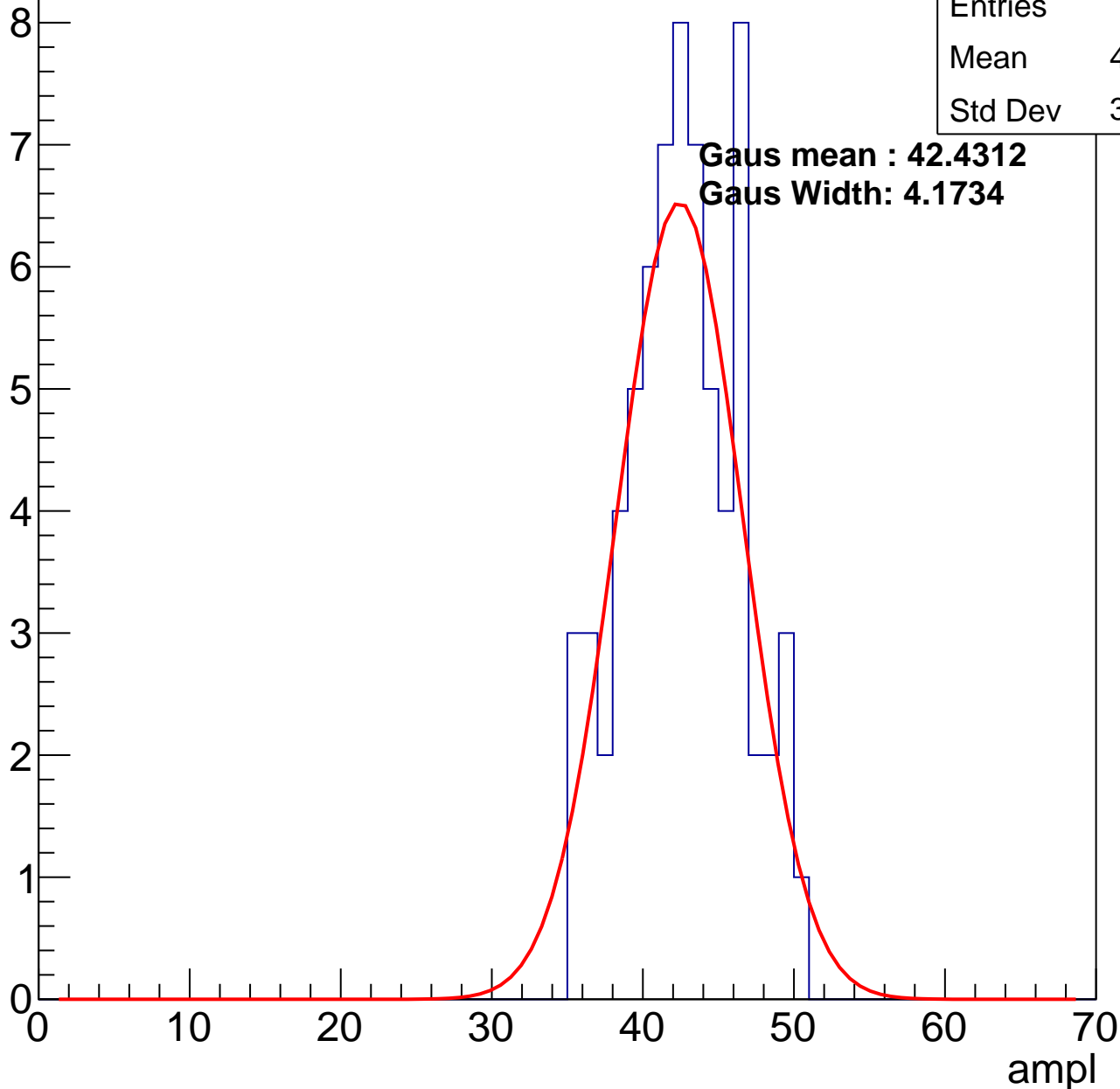
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.19
Std Dev	3.716

Gaus mean : 42.4312

Gaus Width: 4.1734

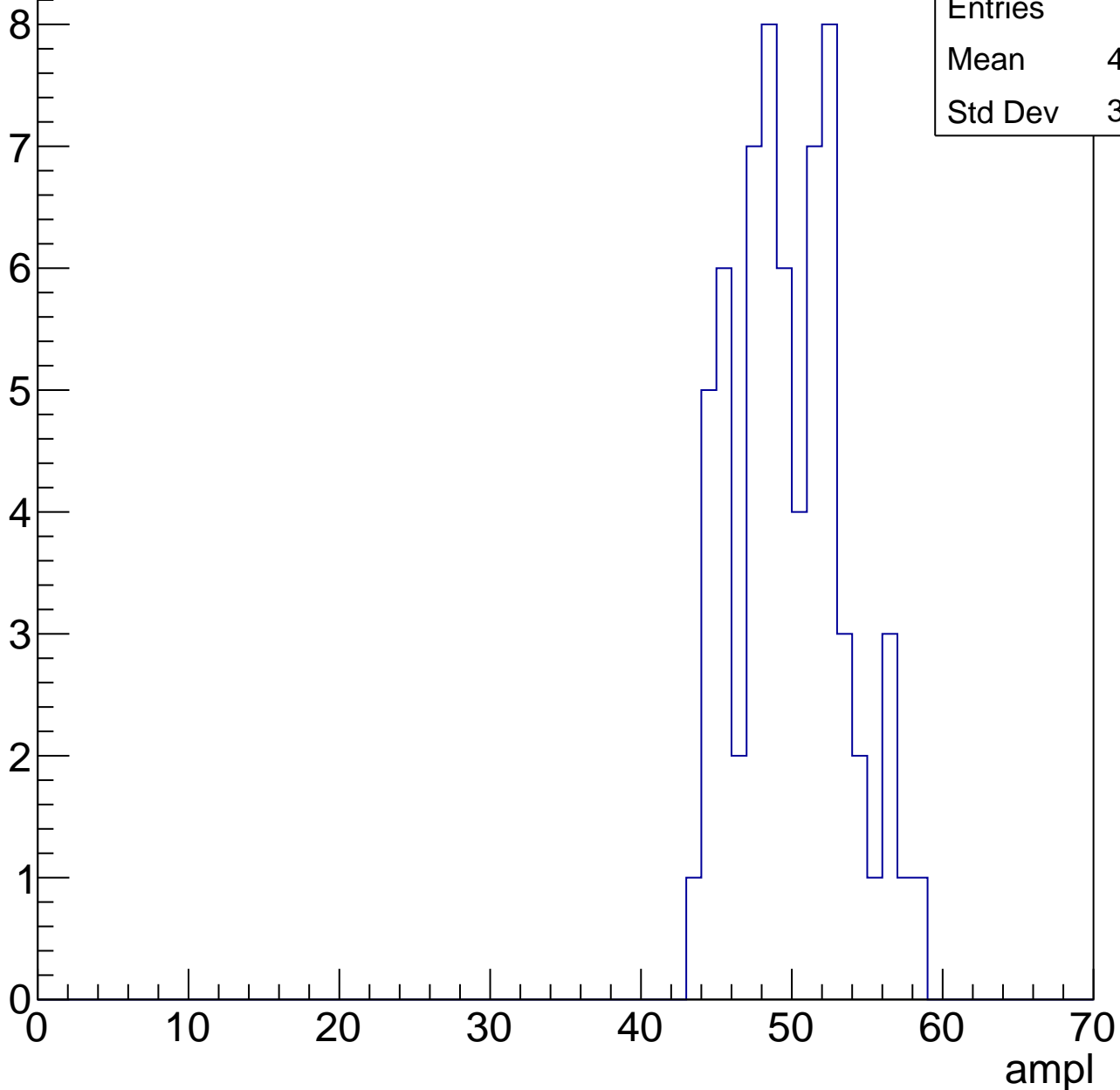


B1L101S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.38
Std Dev	3.576

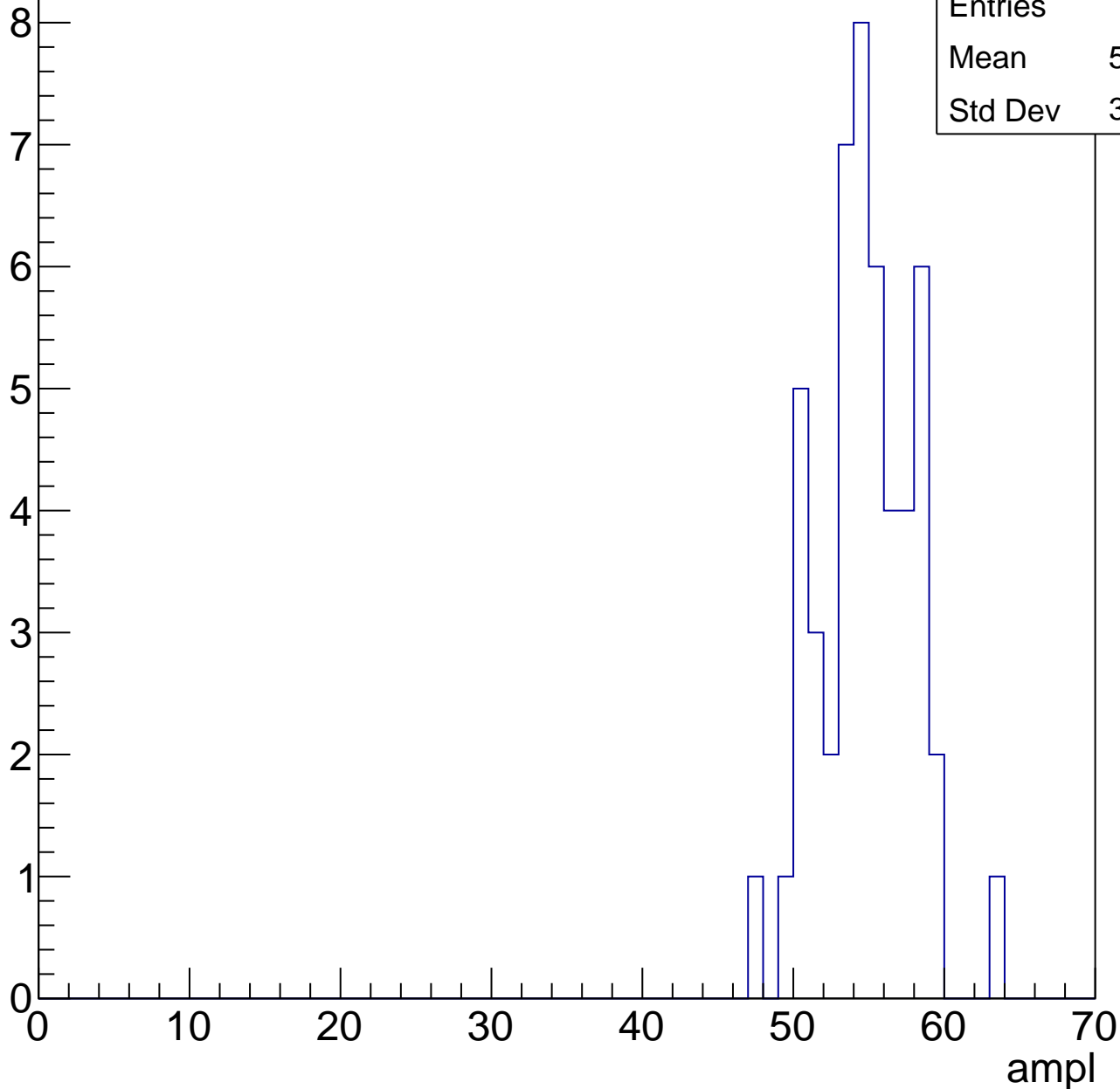


B1L101S, U14-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

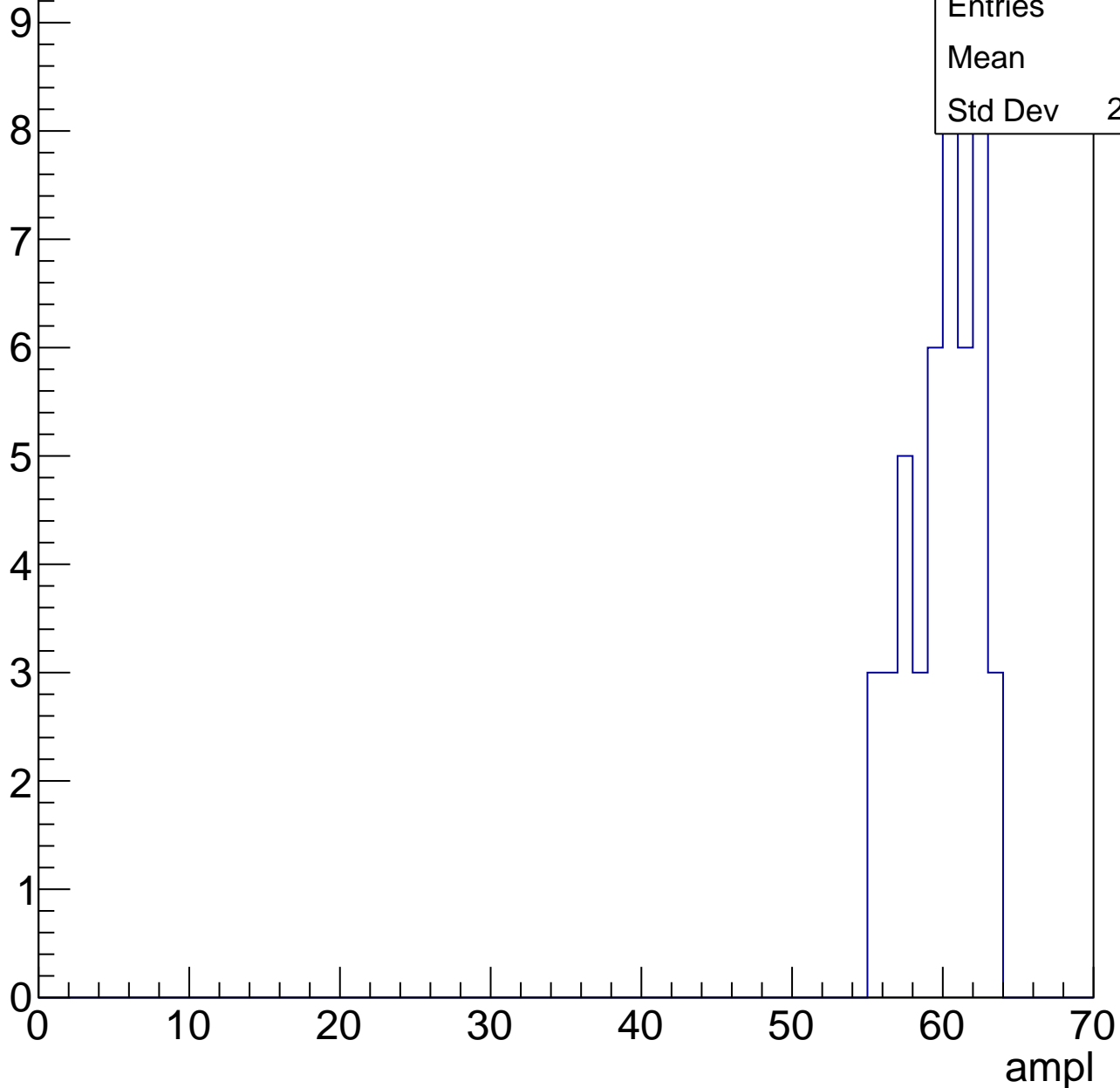
Entries	50
Mean	54.34
Std Dev	3.083



B1L101S, U14-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

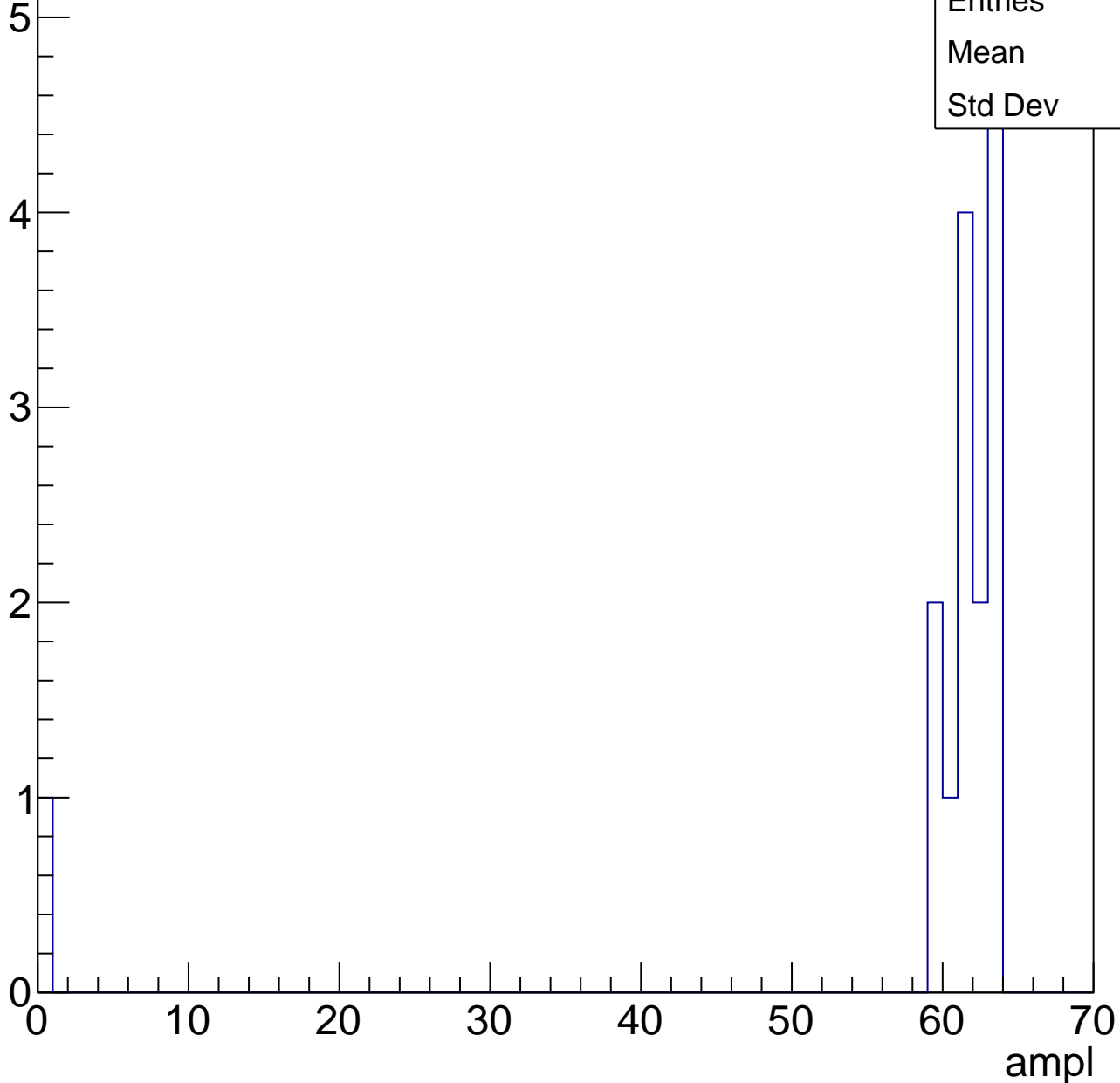


B1L101S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.4
Std Dev	15.4



B1L101S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch55, adc0

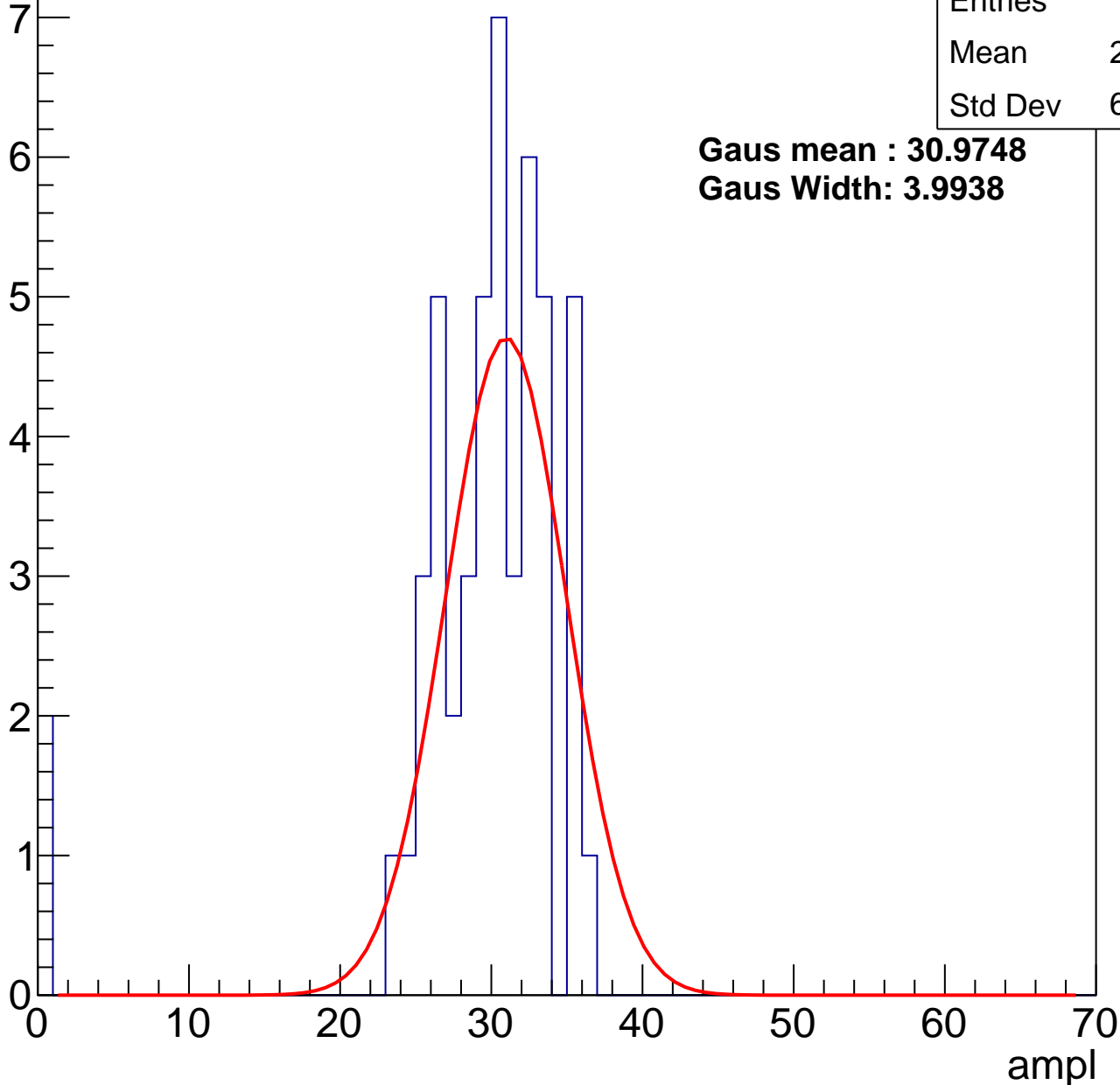
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	28.69
Std Dev	6.738

Gaus mean : 30.9748

Gaus Width: 3.9938



B1L101S, U14-ch55, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	36.04
Std Dev	3.801

Gaus mean : 37.3055

Gaus Width: 4.2457

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L101S, U14-ch55, adc2

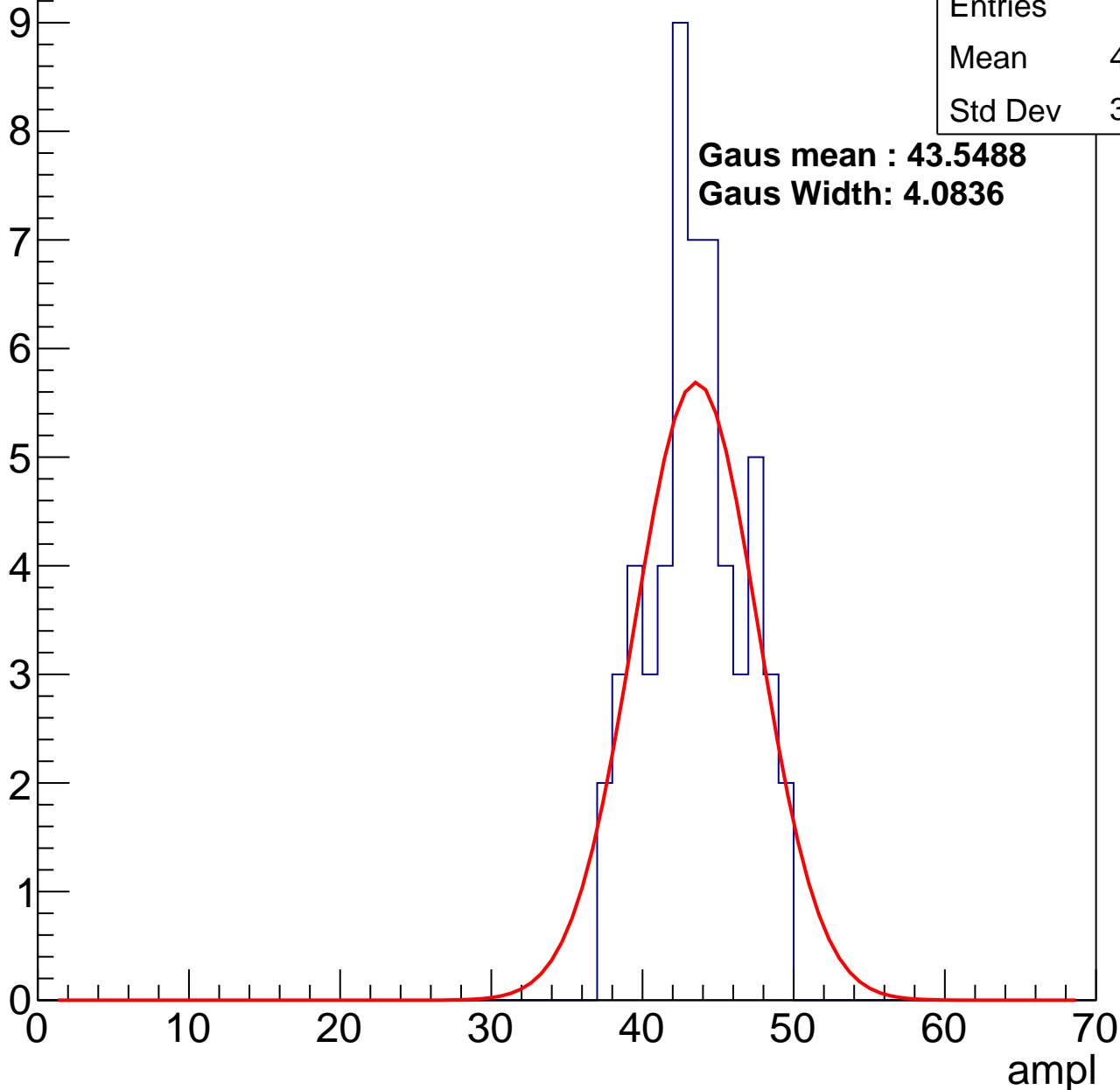
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.04
Std Dev	3.105

Gaus mean : 43.5488

Gaus Width: 4.0836

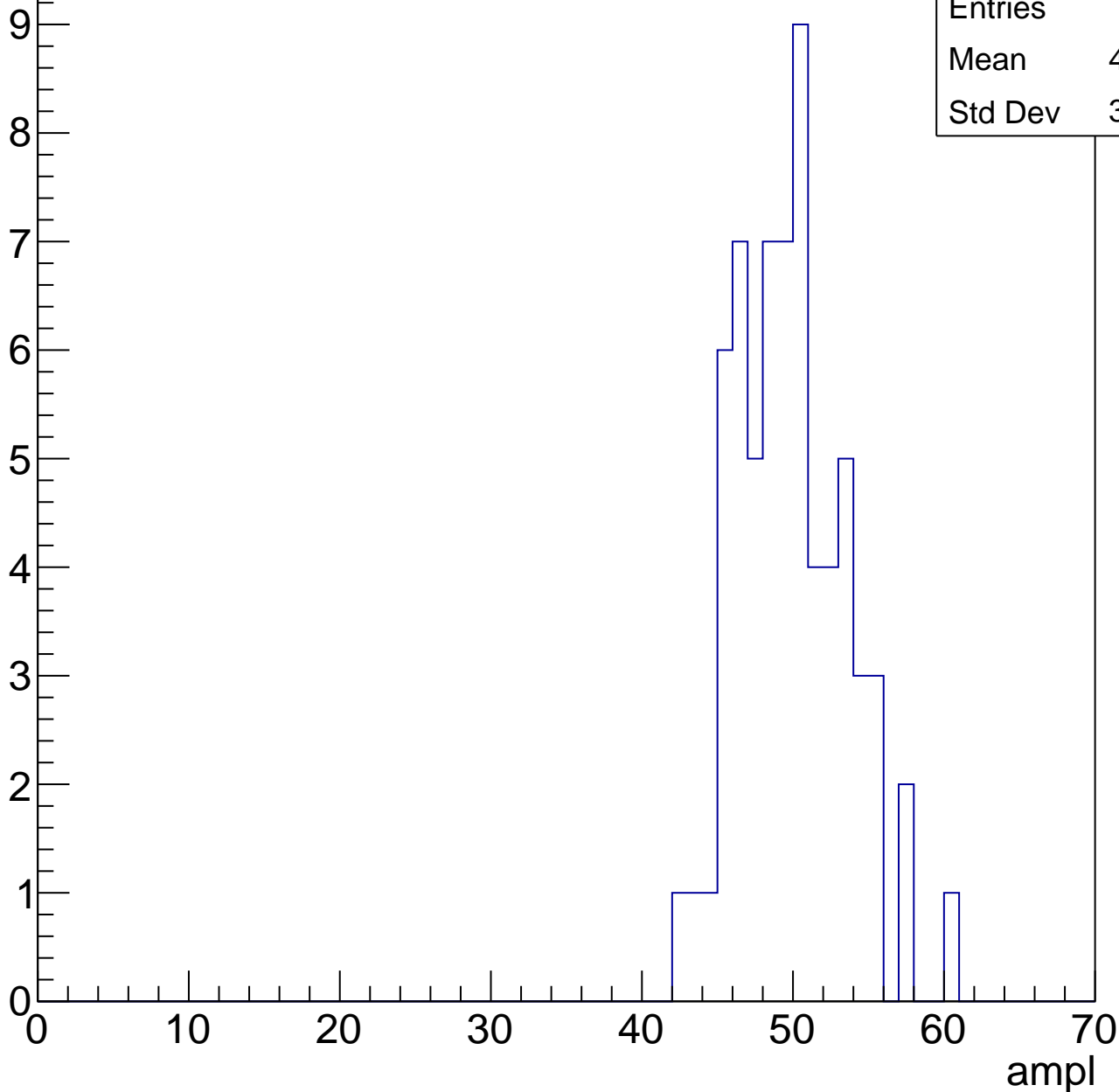


B1L101S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

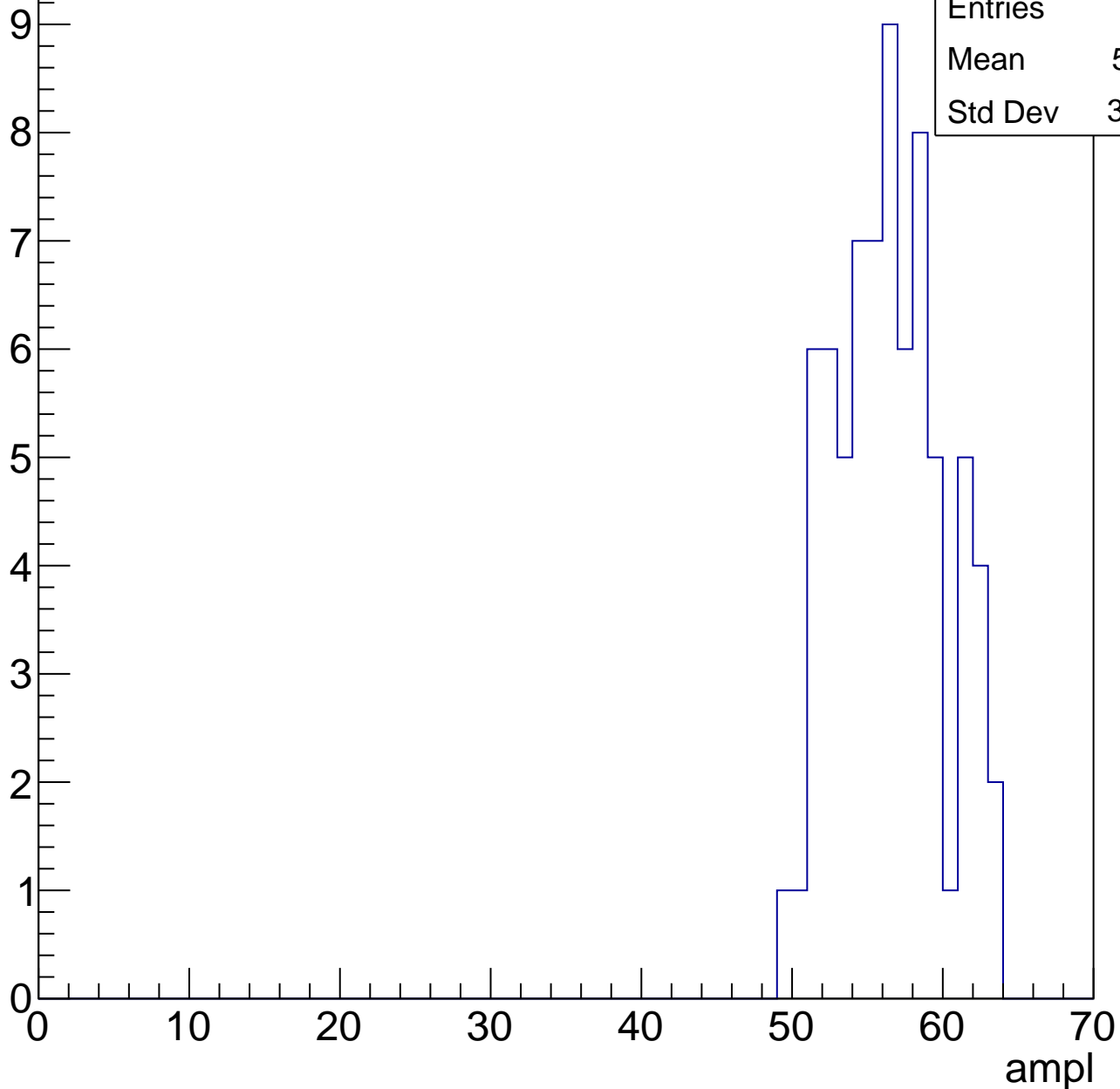
Entries	66
Mean	49.44
Std Dev	3.602



B1L101S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	73
Mean	56.01
Std Dev	3.466

B1L101S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	31
Mean	58.45
Std Dev	10.84

ampl

0

10

20

30

40

50

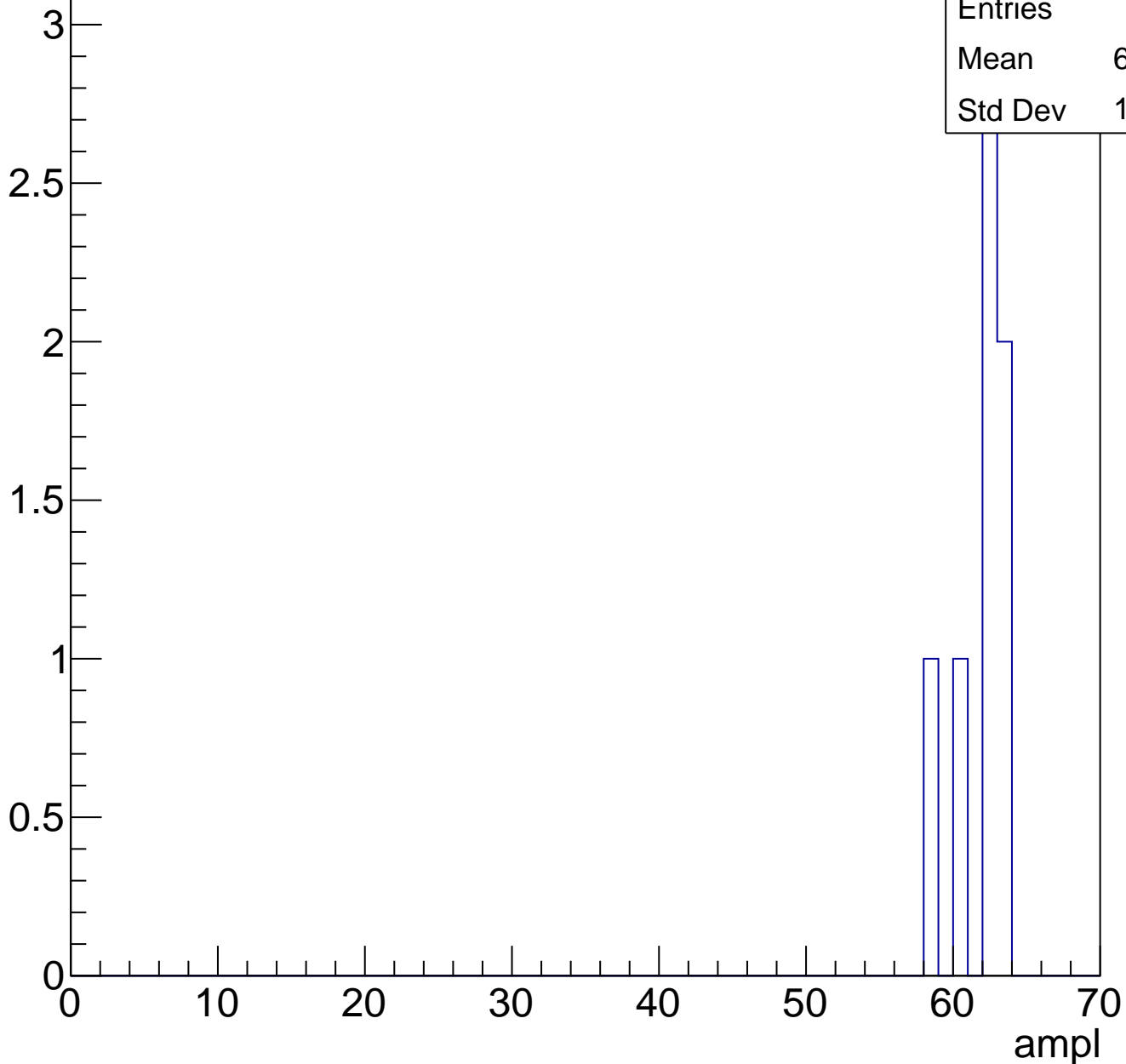
60

70

B1L101S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch56, adc0

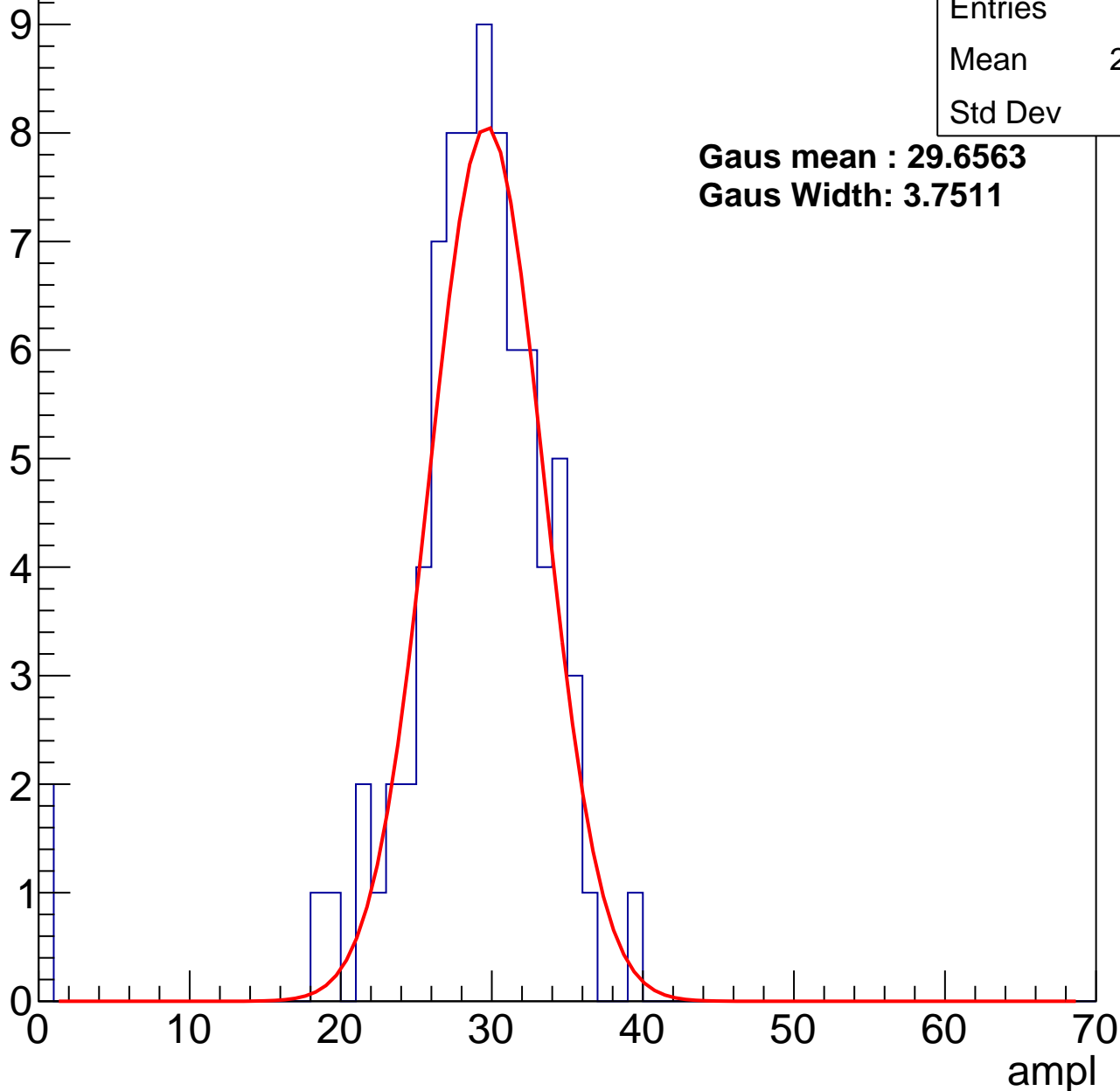
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.12
Std Dev	5.92

Gaus mean : 29.6563

Gaus Width: 3.7511



B1L101S, U14-ch56, adc1

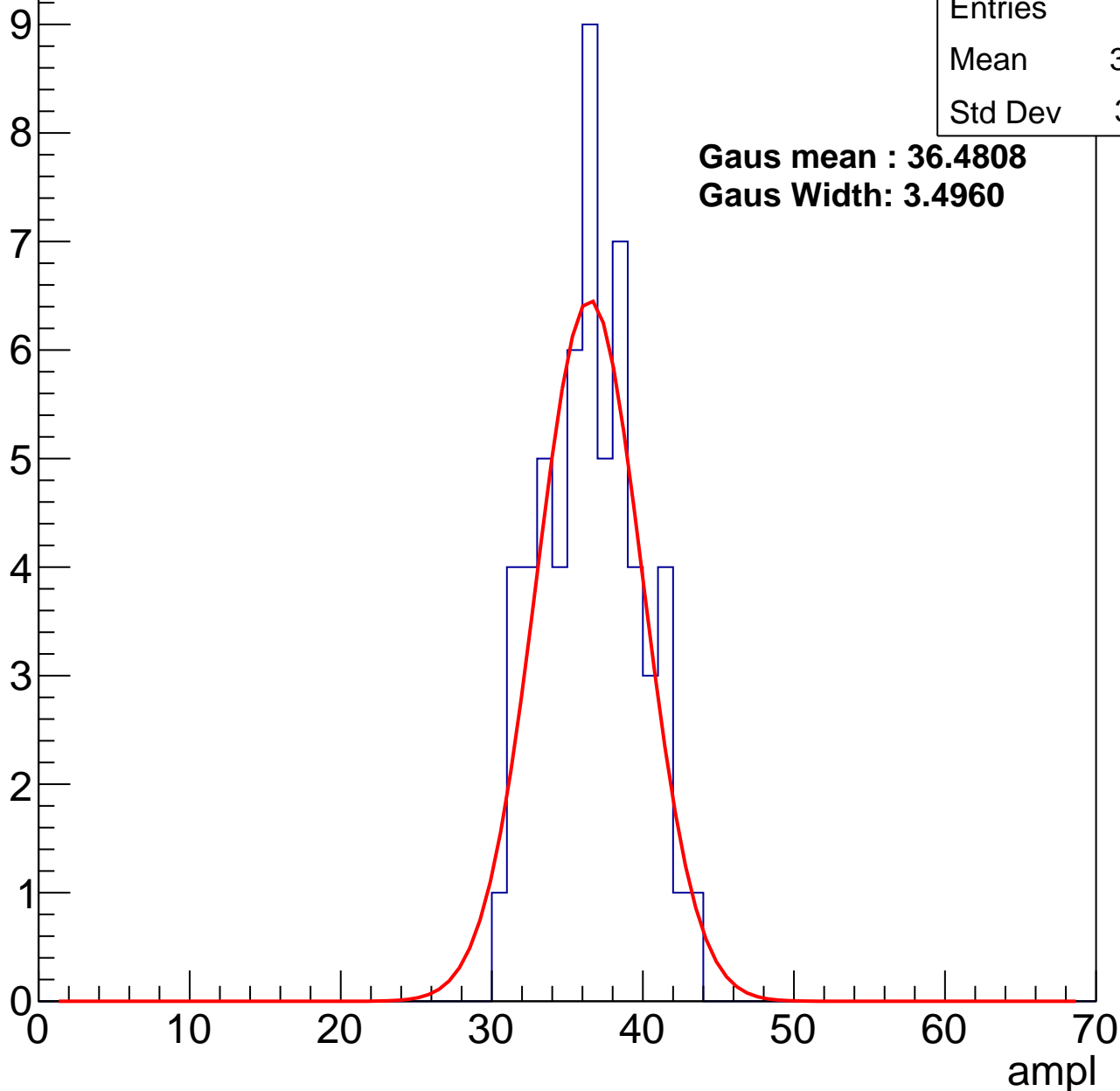
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.09
Std Dev	3.131

Gaus mean : 36.4808

Gaus Width: 3.4960



B1L101S, U14-ch56, adc2

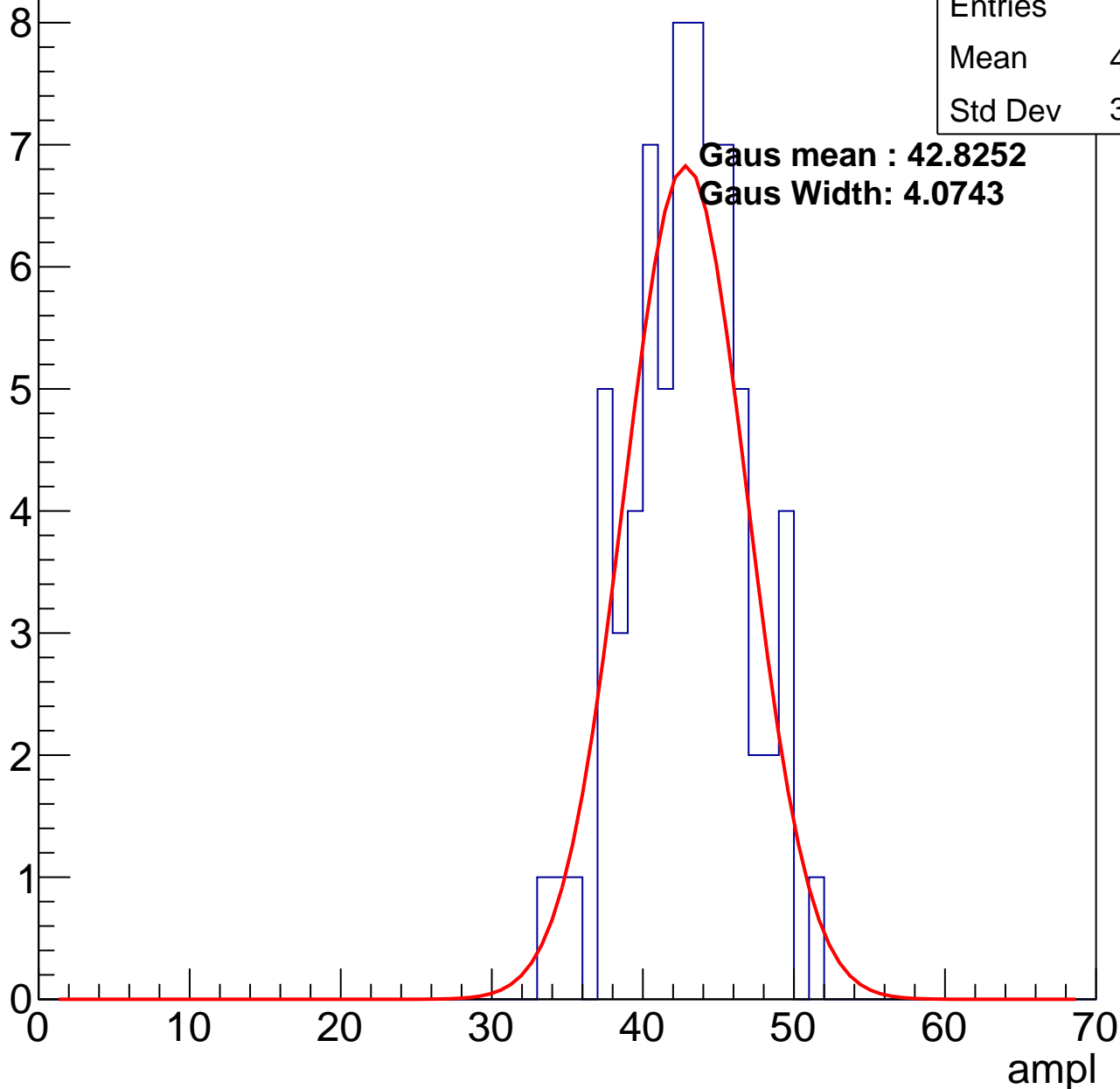
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.42
Std Dev	3.755

Gaus mean : 42.8252

Gaus Width: 4.0743

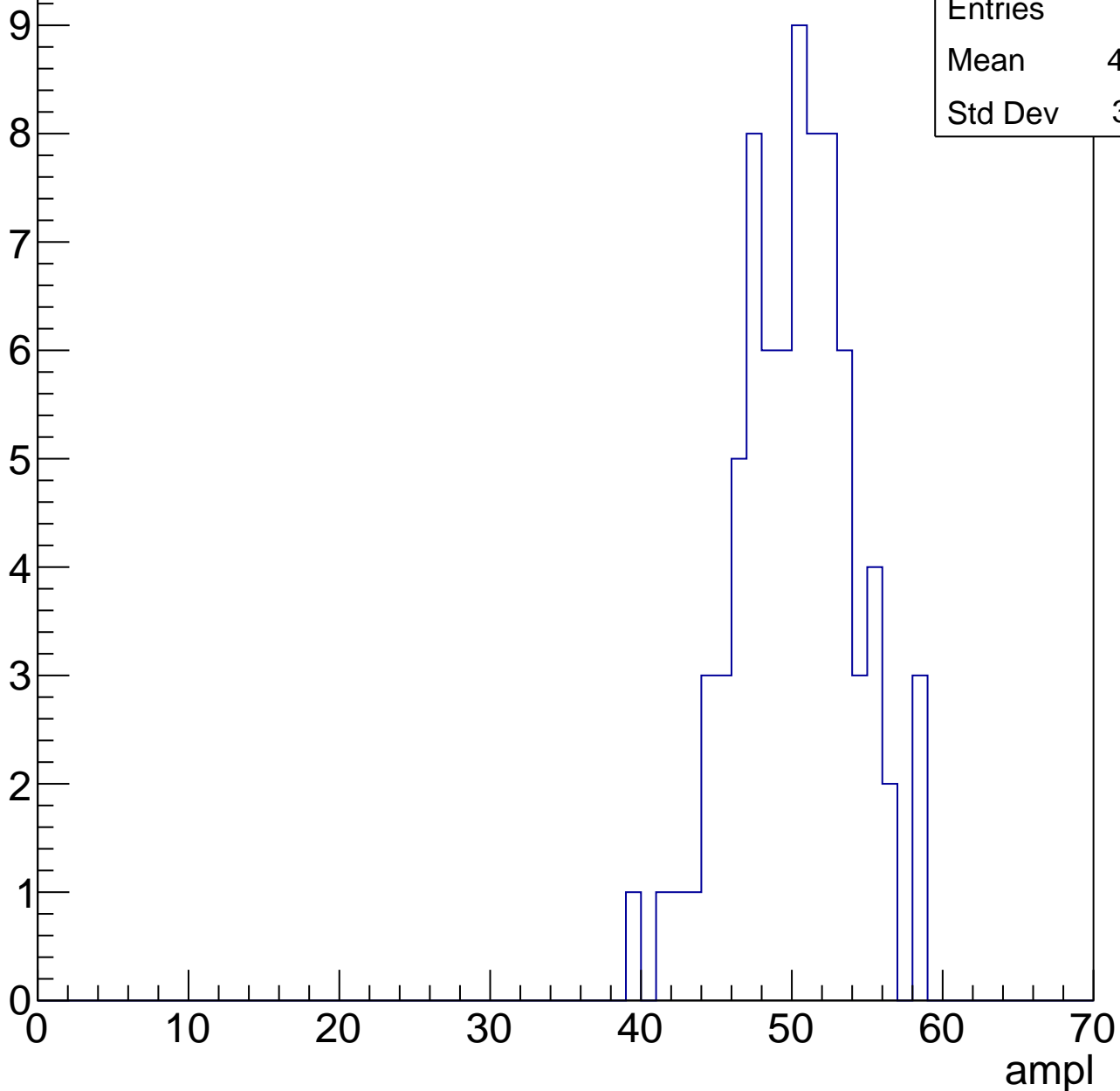


B1L101S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	49.74
Std Dev	3.891

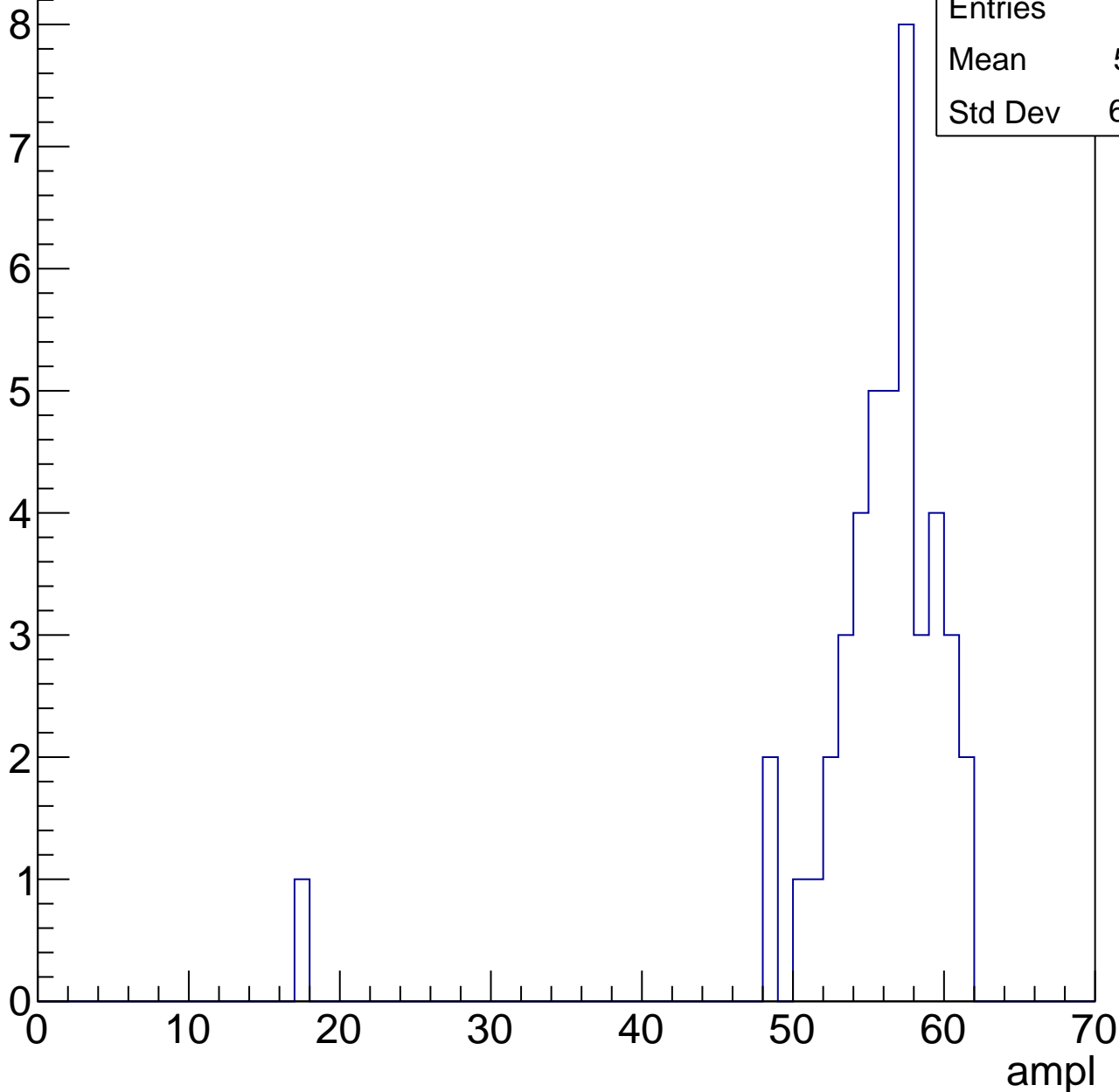


B1L101S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	54.91
Std Dev	6.557

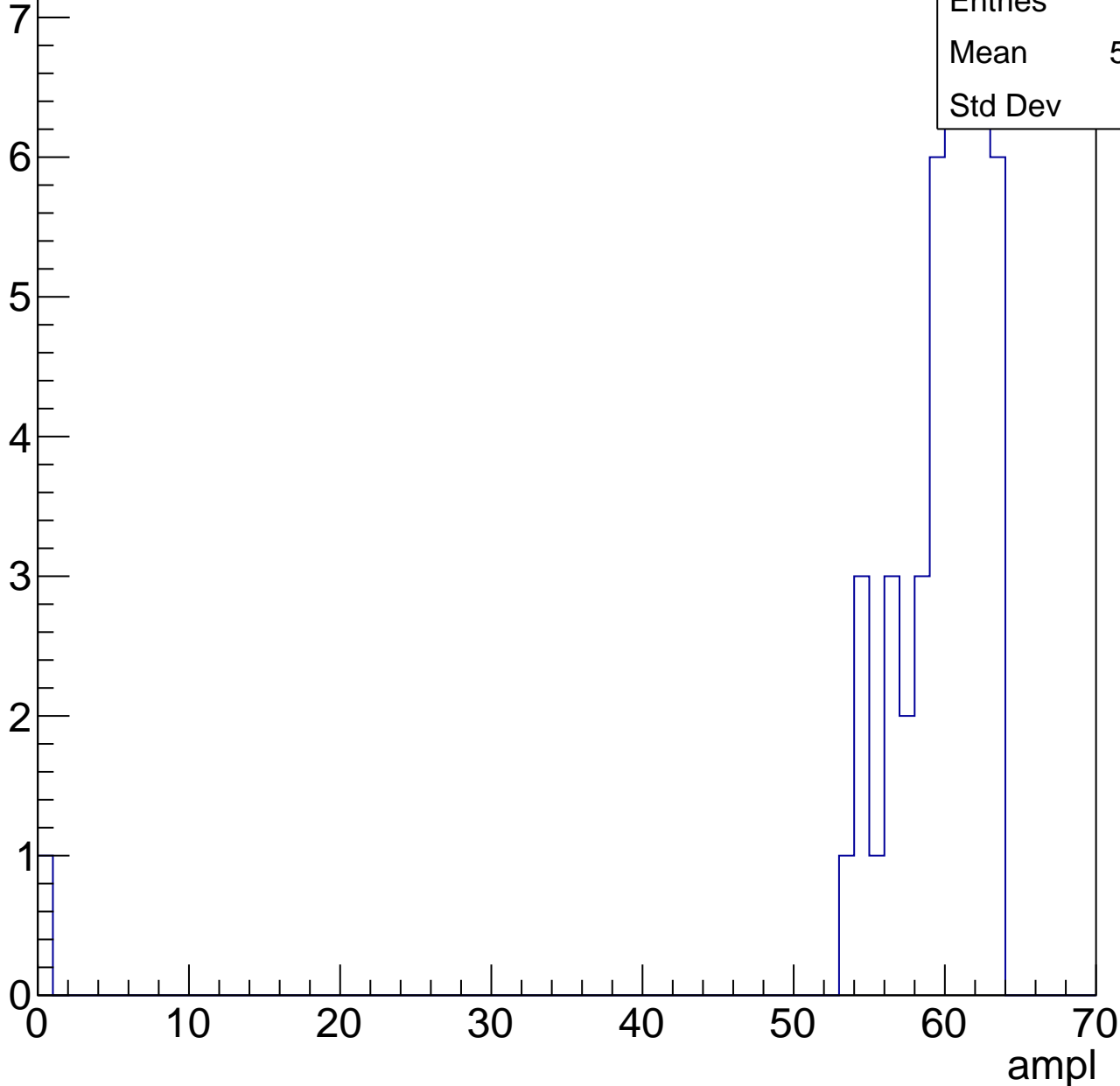


B1L101S, U14-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

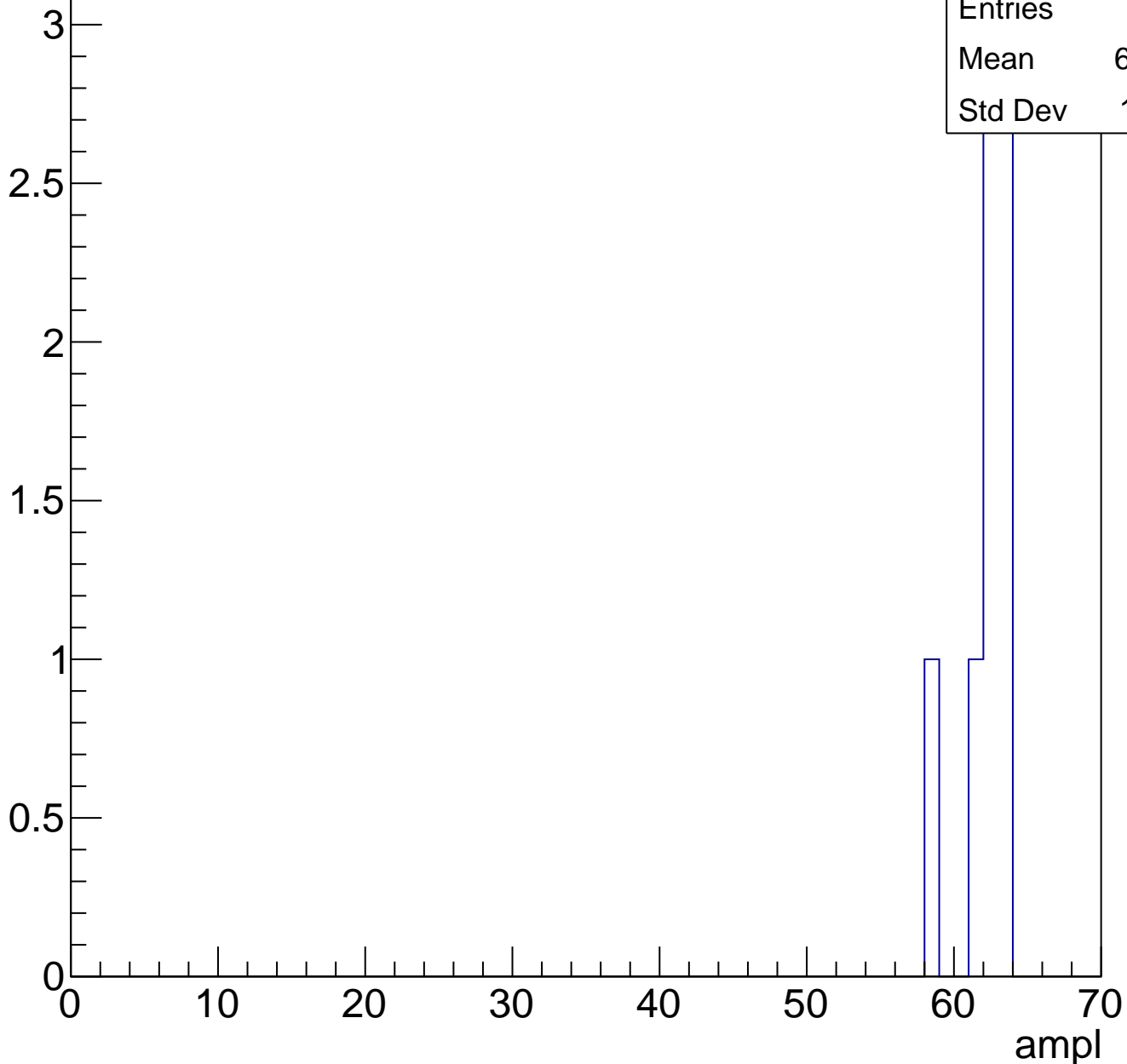
Entries	47
Mean	58.28
Std Dev	9.01



B1L101S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch57, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	30.25
Std Dev	3.293

Gaus mean : 30.6169

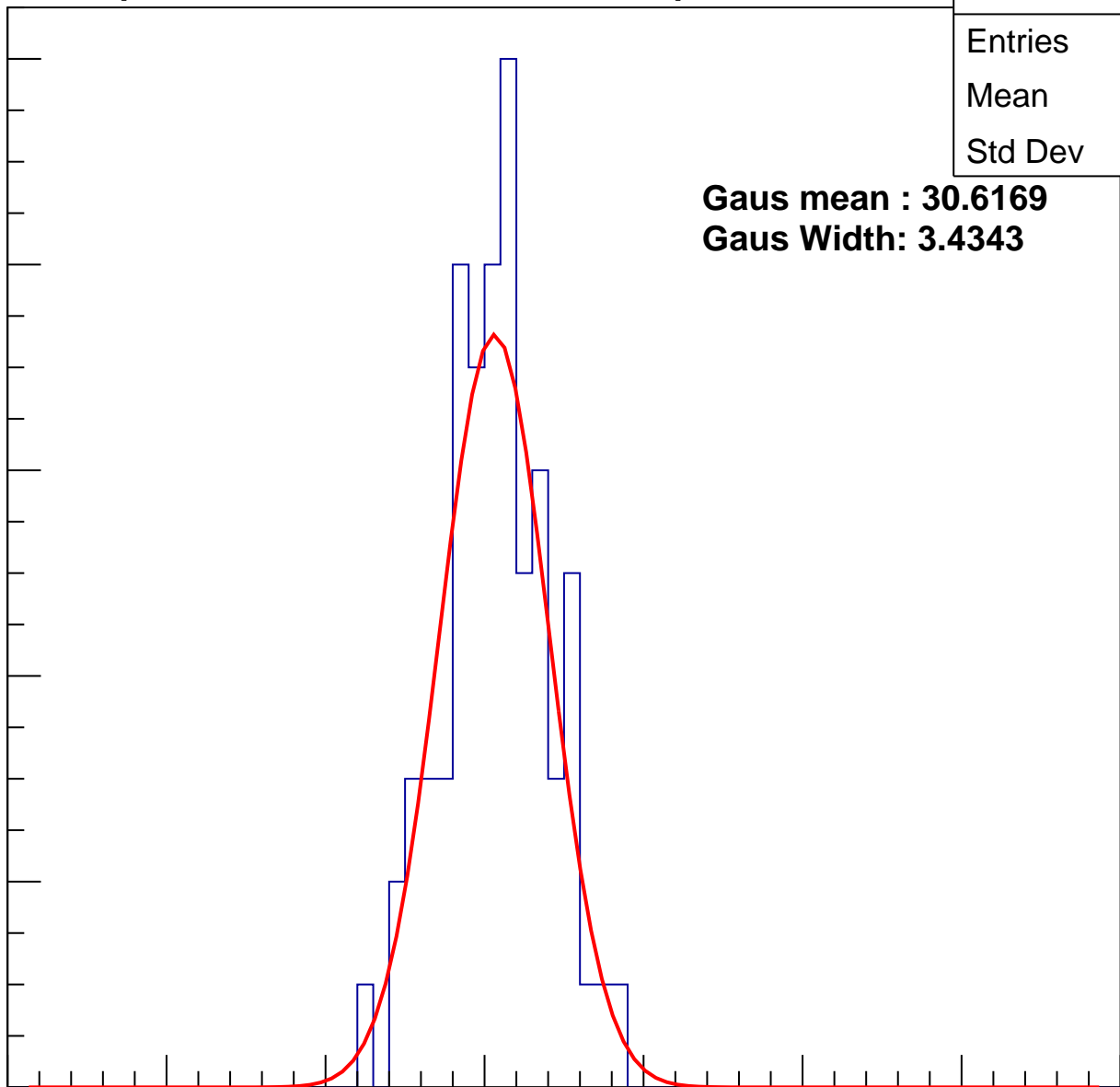
Gaus Width: 3.4343

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch57, adc1

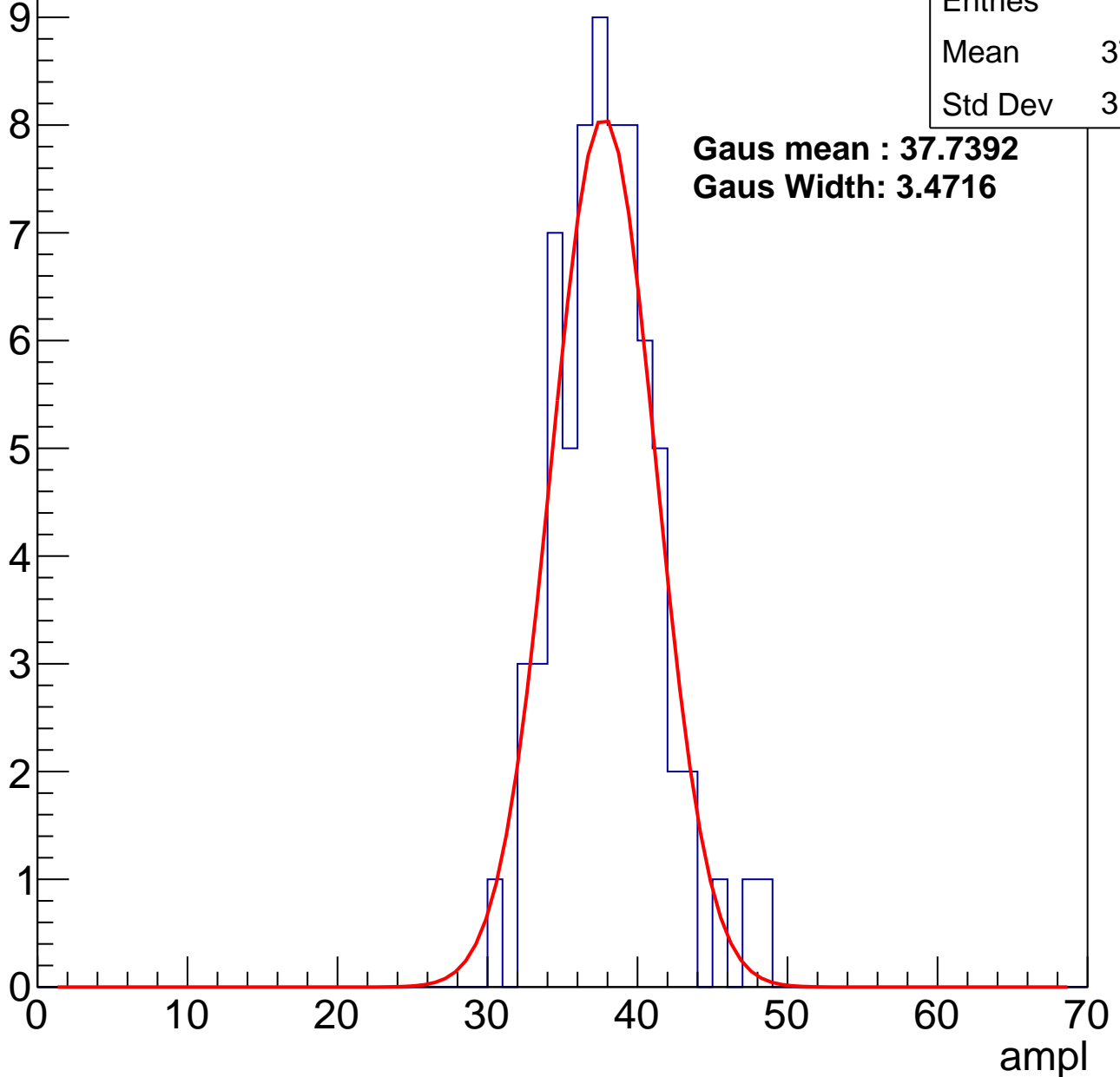
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.57
Std Dev	3.425

Gaus mean : 37.7392

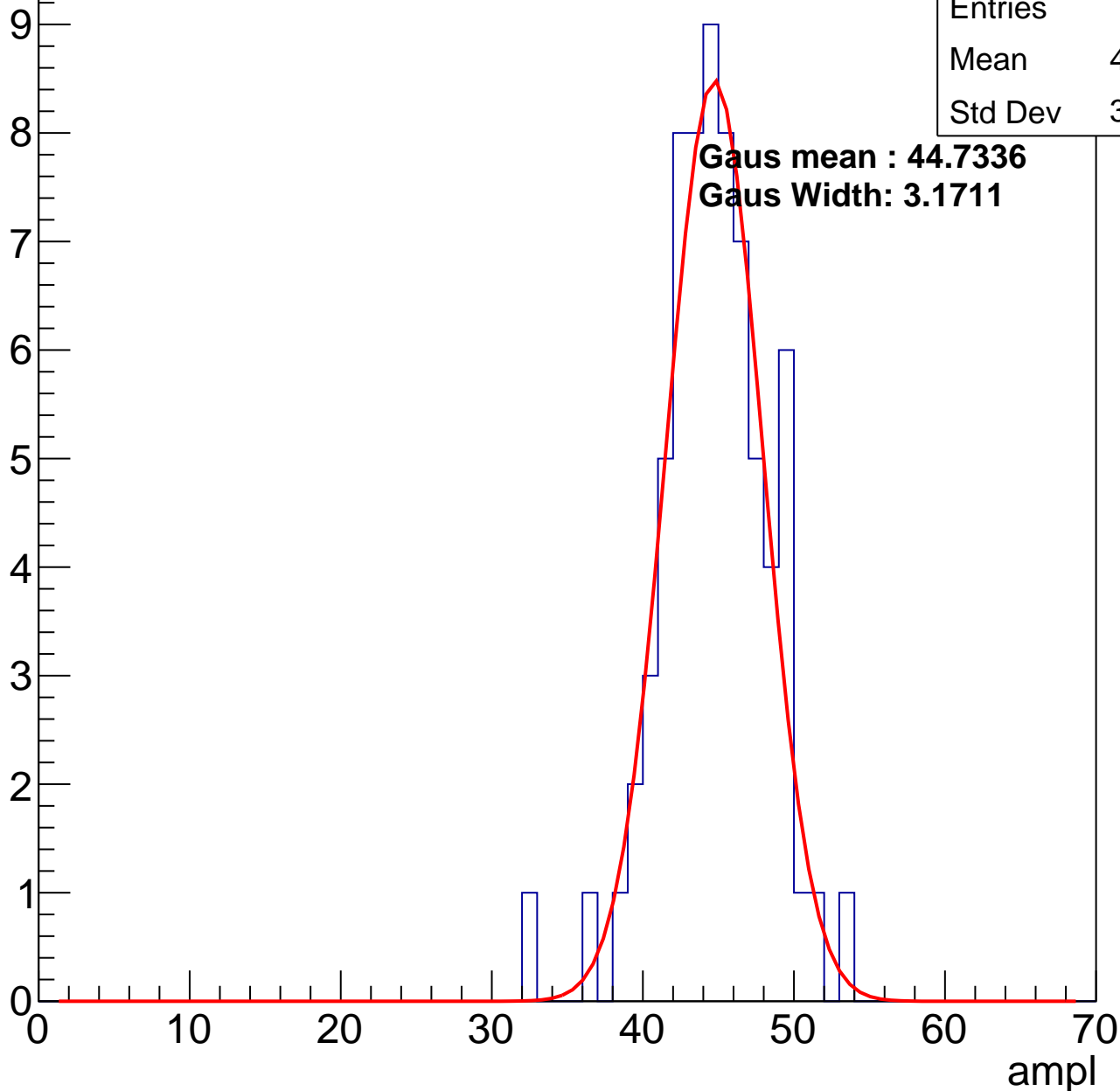
Gaus Width: 3.4716



B1L101S, U14-ch57, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

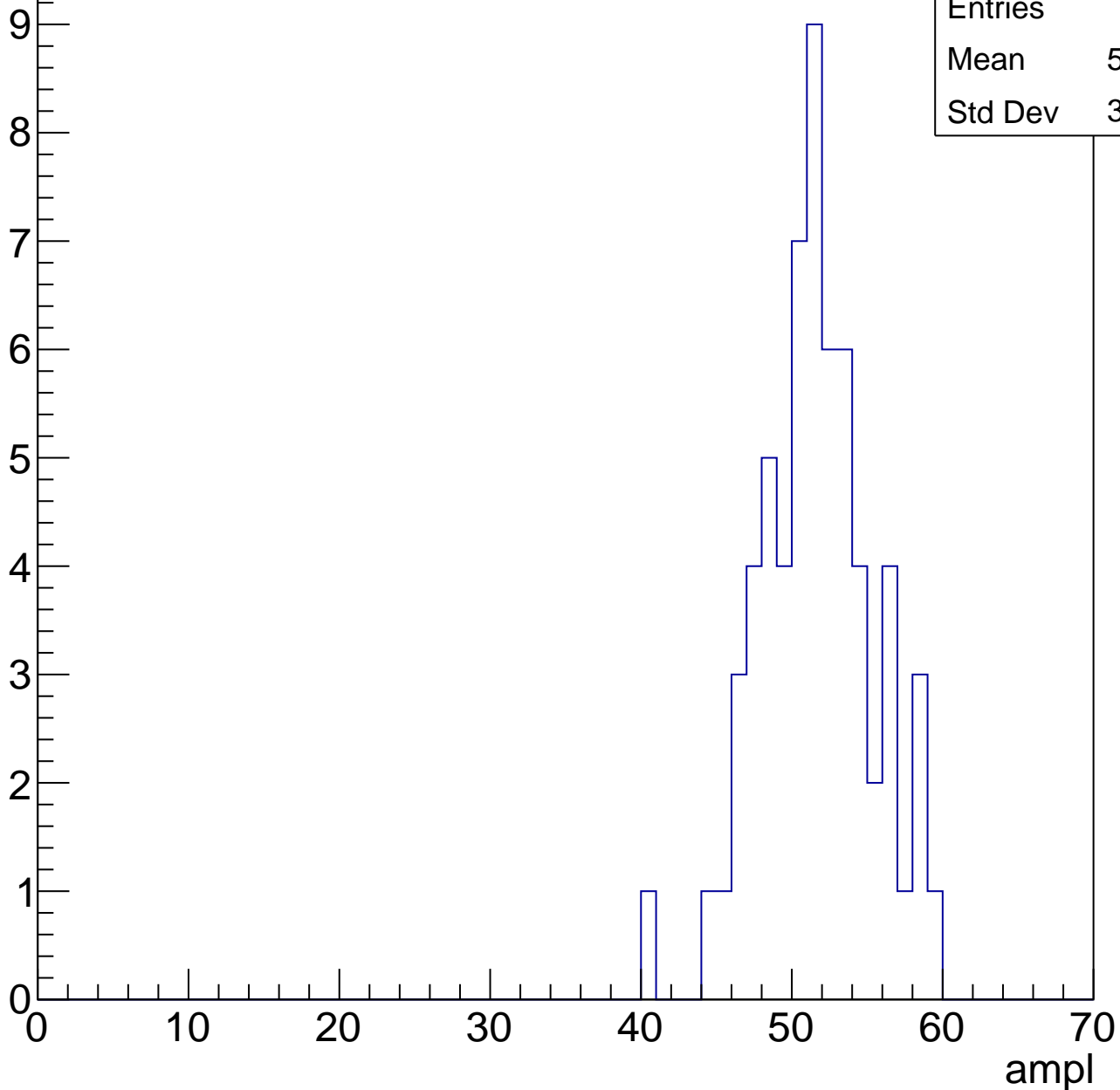


B1L101S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	51.13
Std Dev	3.718

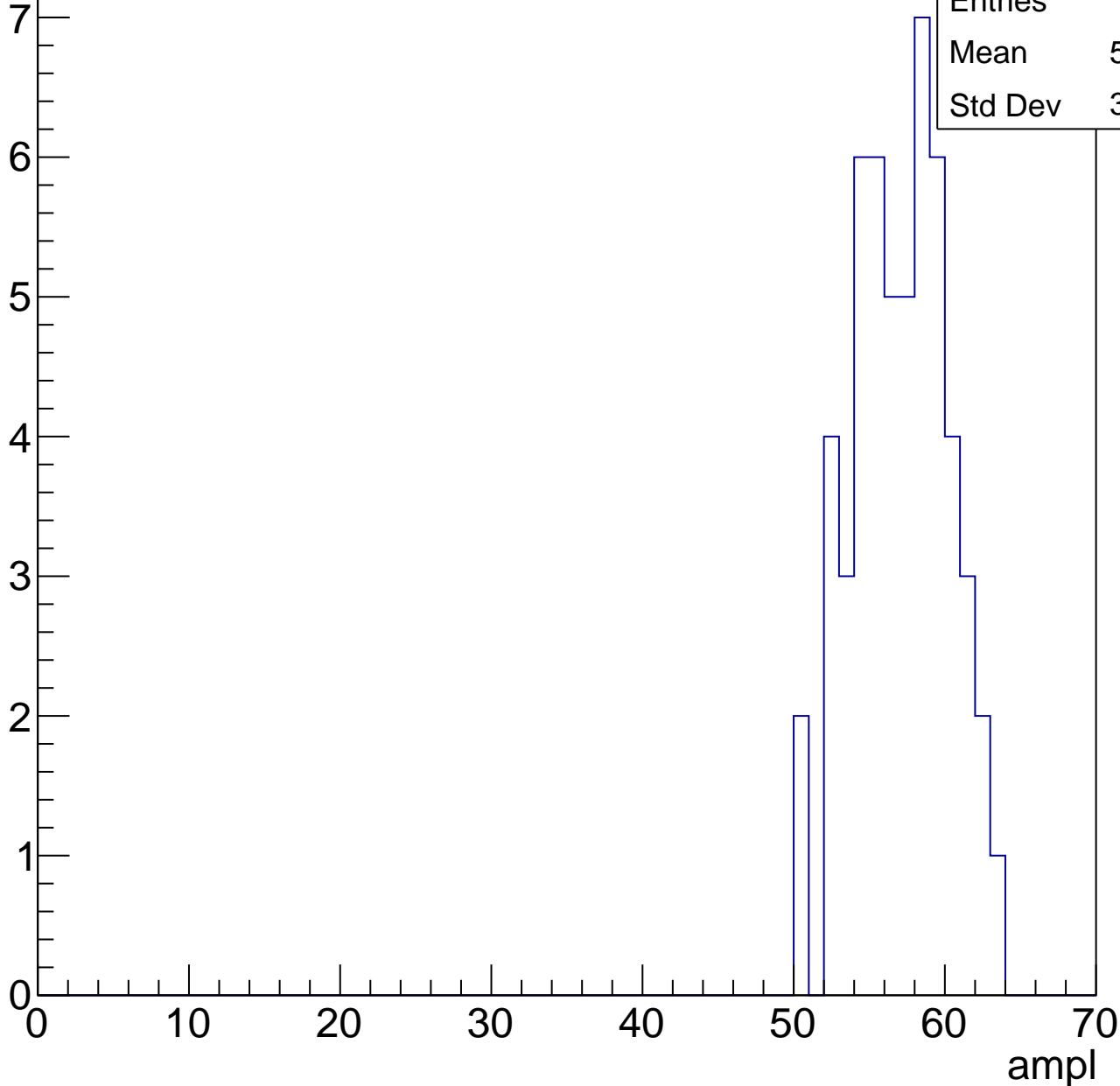


B1L101S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

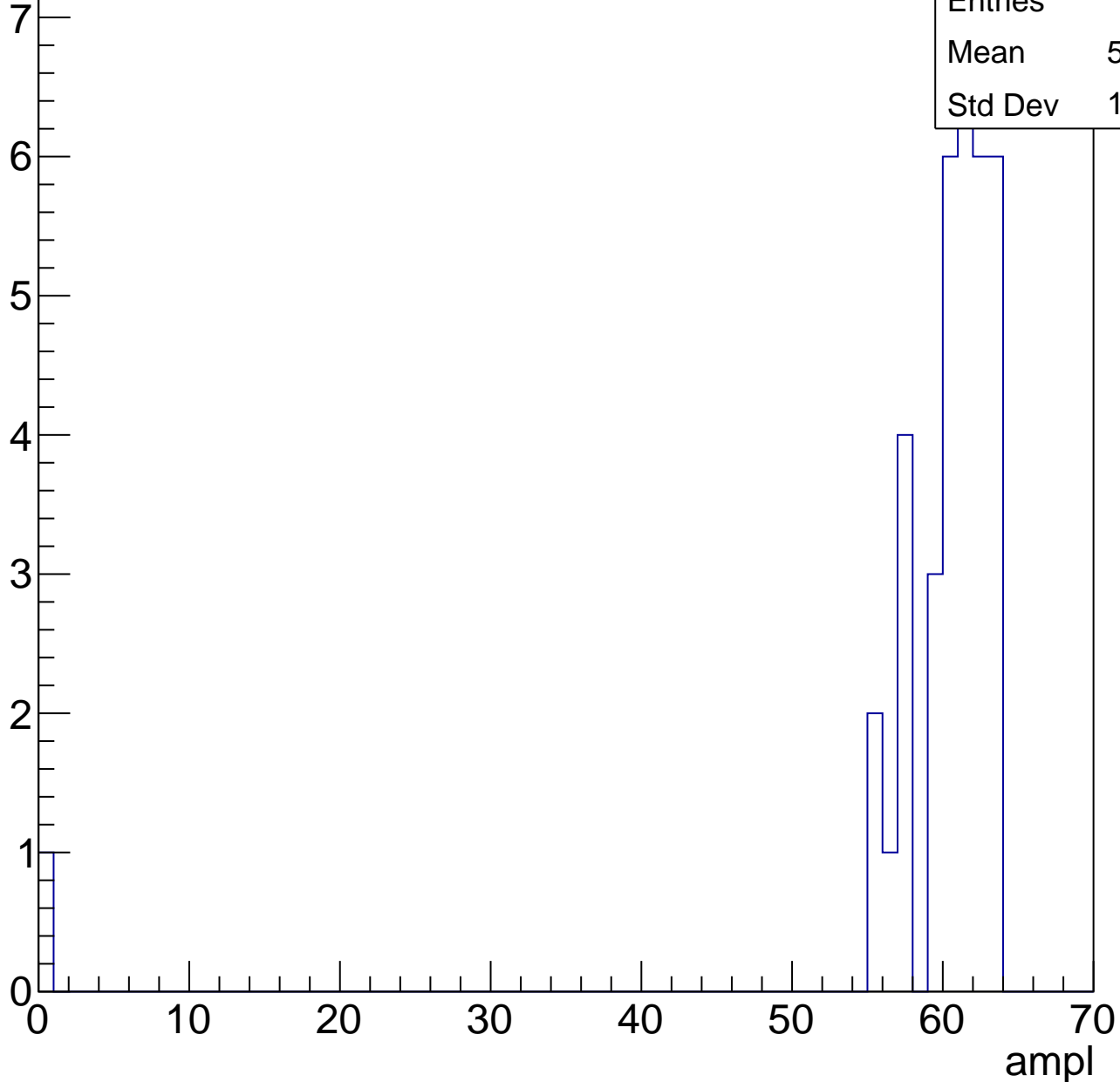
Entries	54
Mean	56.59
Std Dev	3.106



B1L101S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch58, adc0

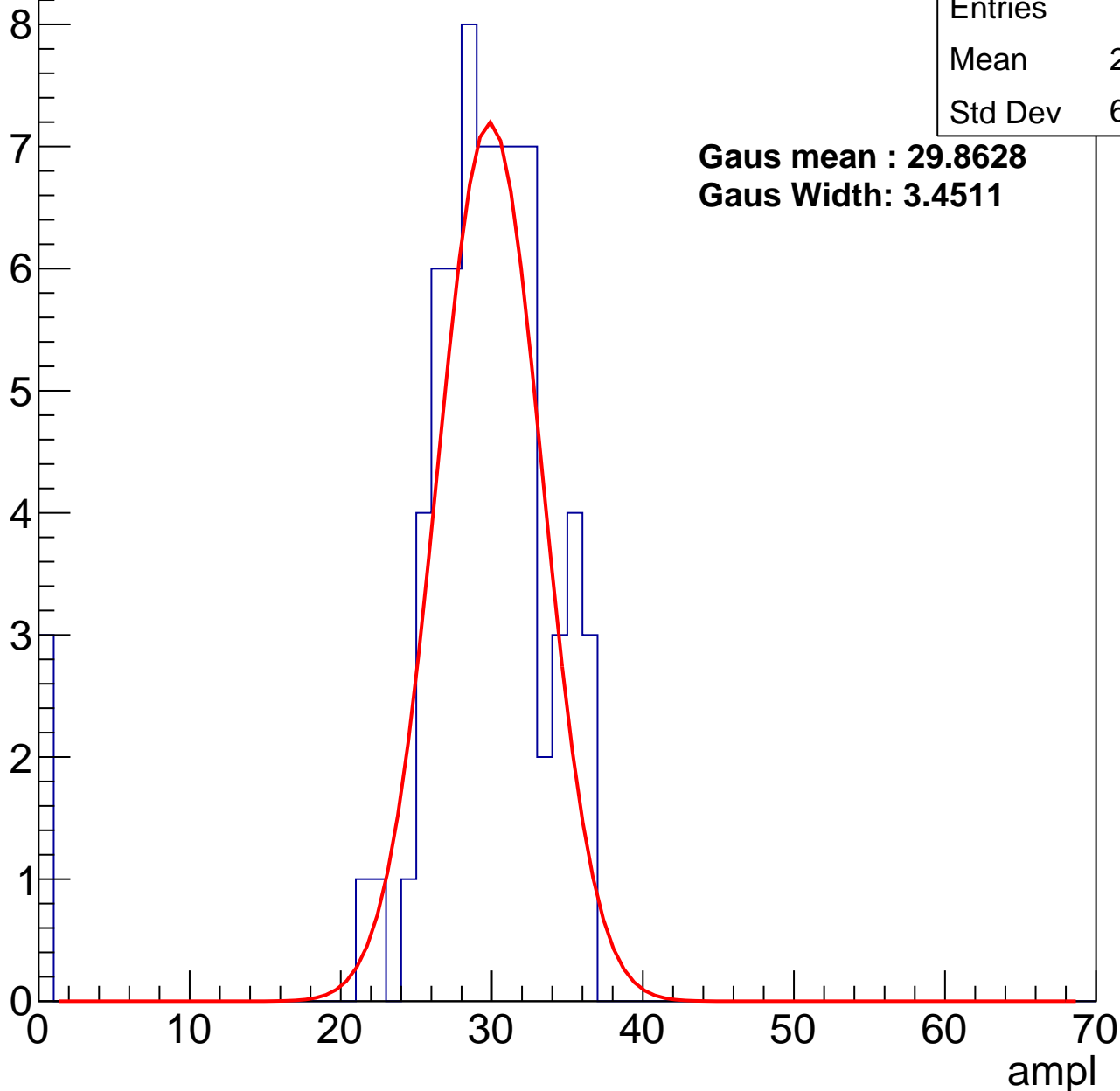
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.27
Std Dev	6.835

Gaus mean : 29.8628

Gaus Width: 3.4511



B1L101S, U14-ch58, adc1

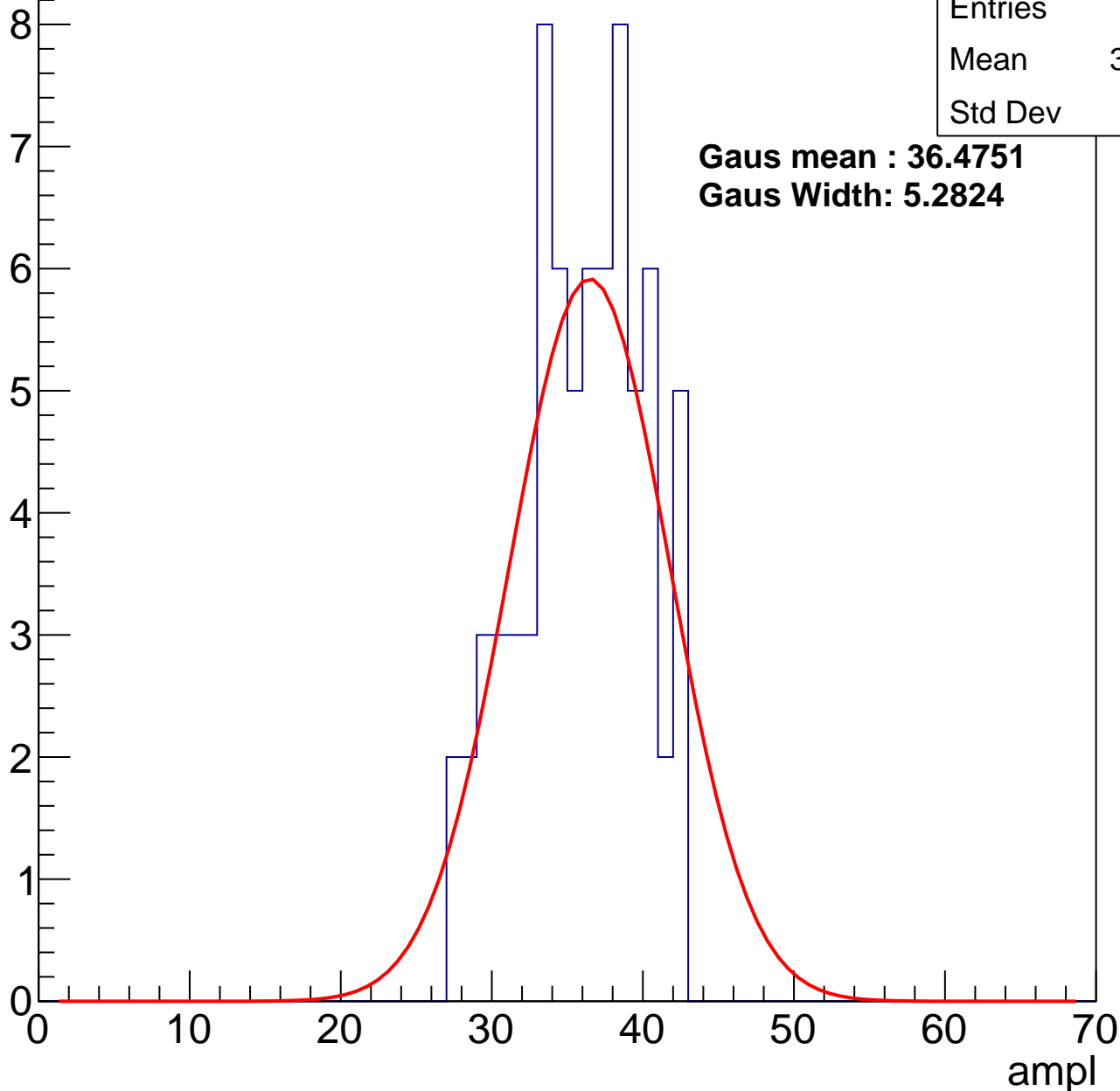
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.45
Std Dev	3.98

Gaus mean : 36.4751

Gaus Width: 5.2824



B1L101S, U14-ch58, adc2

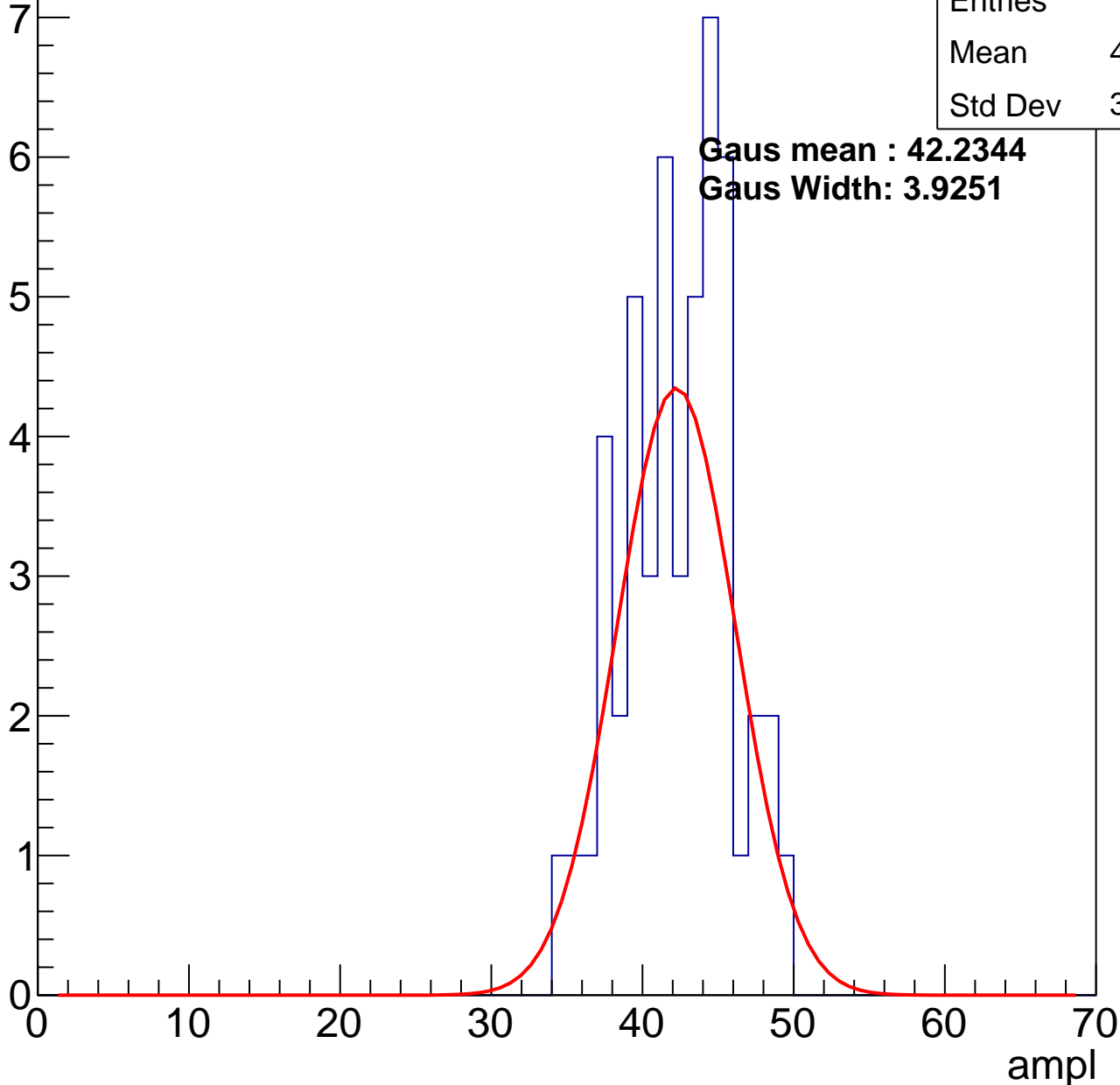
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	41.88
Std Dev	3.514

Gaus mean : 42.2344

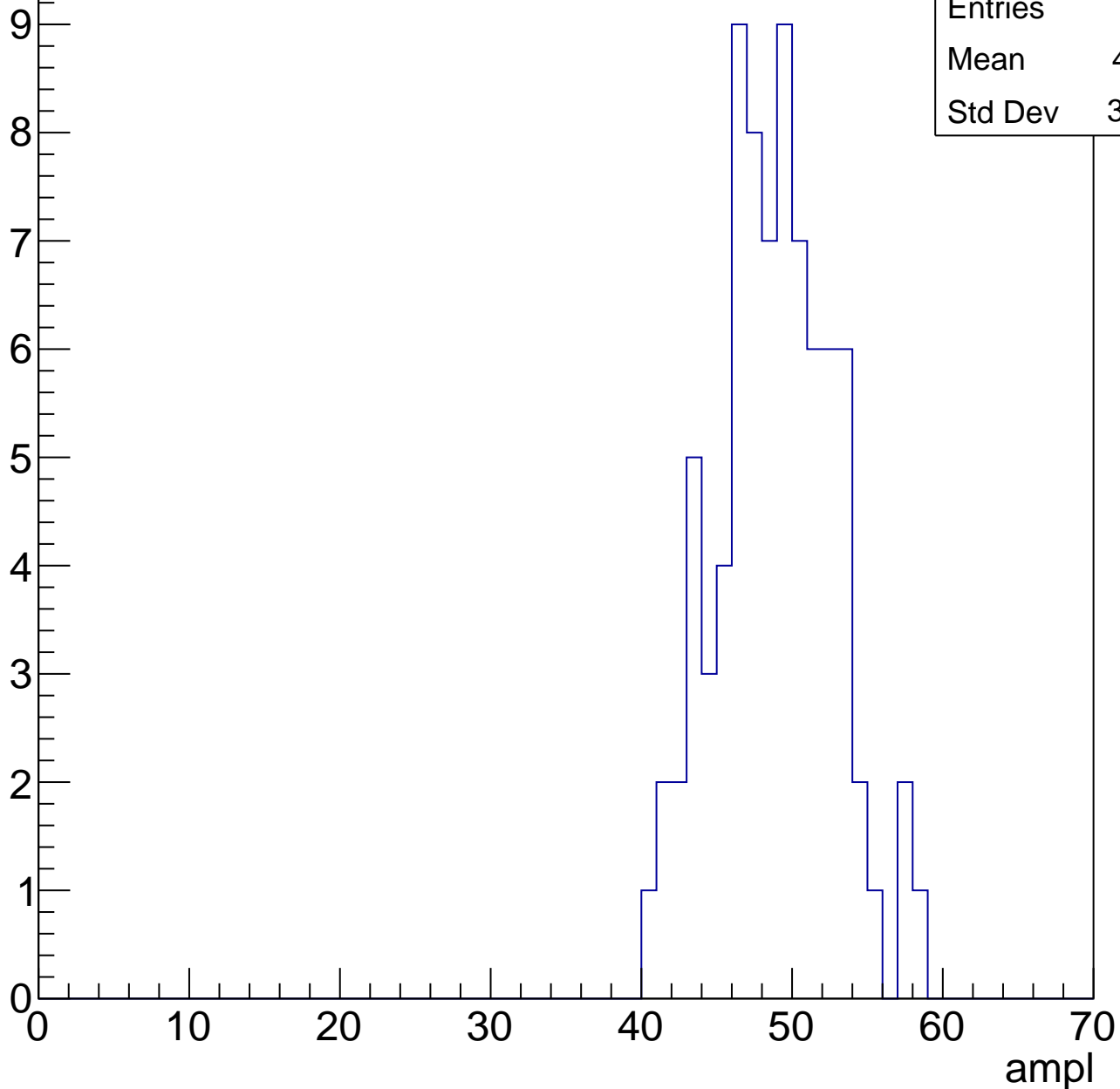
Gaus Width: 3.9251



B1L101S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

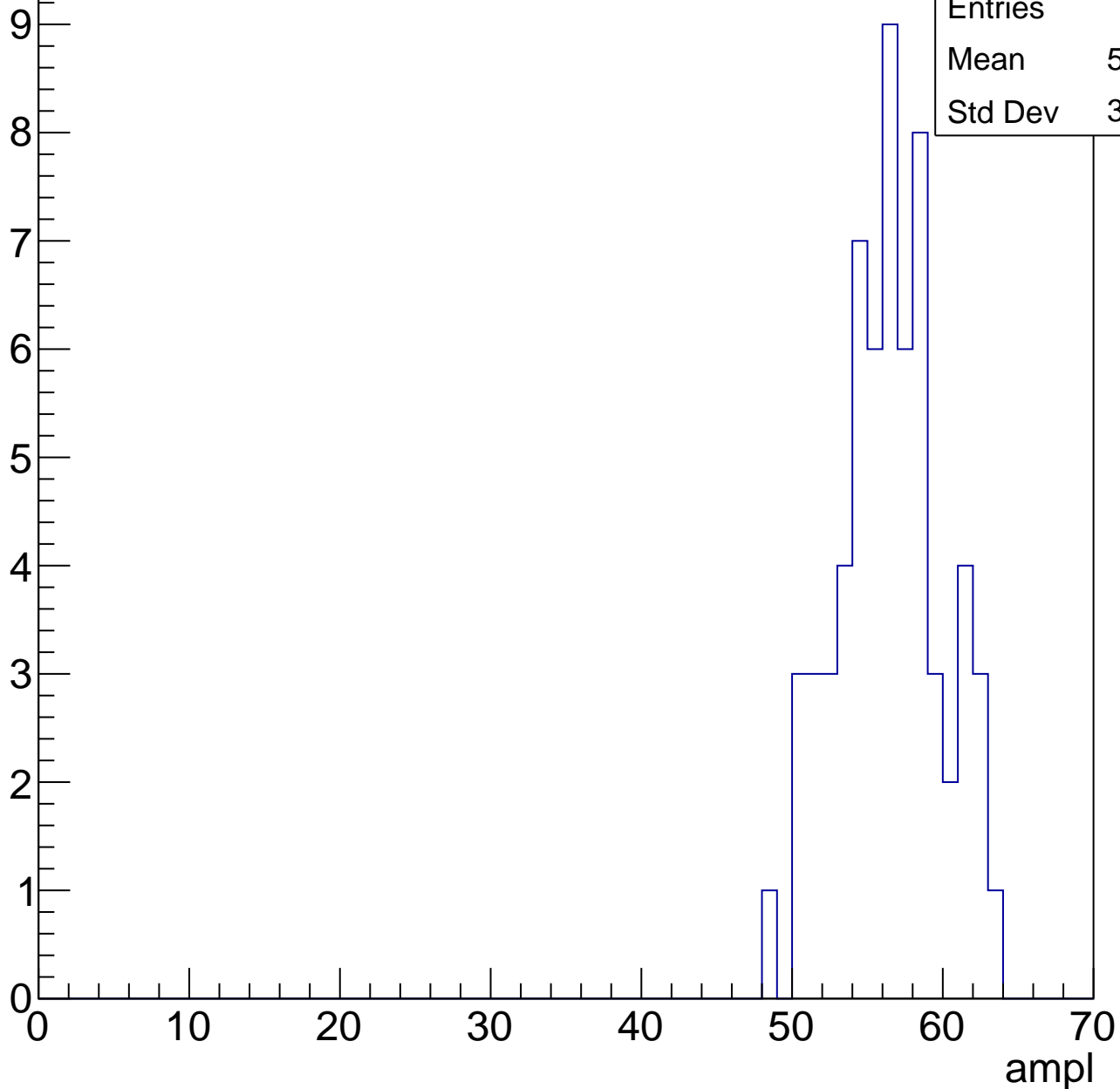
Entry



B1L101S, U14-ch58, adc4

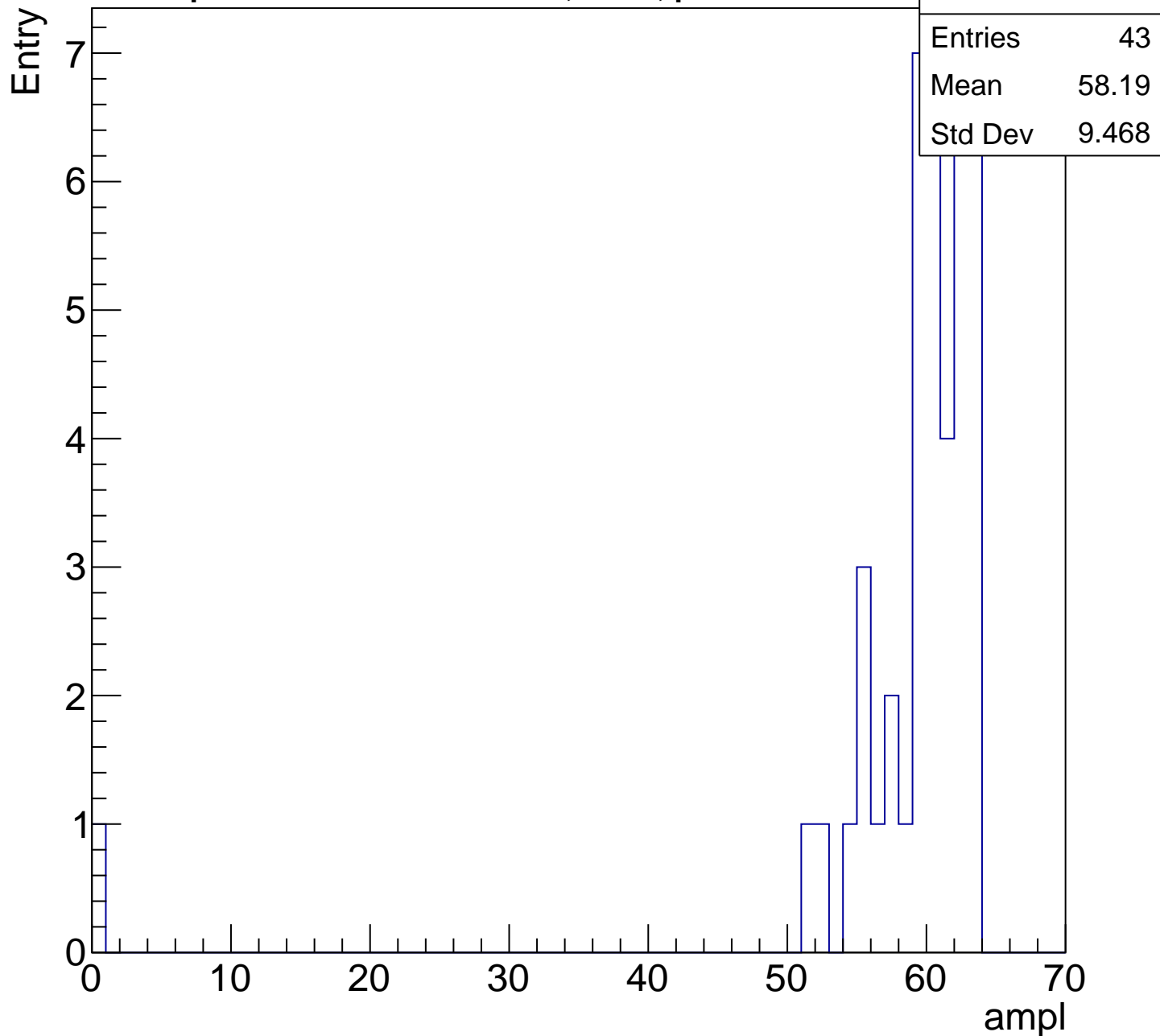
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch58, adc5

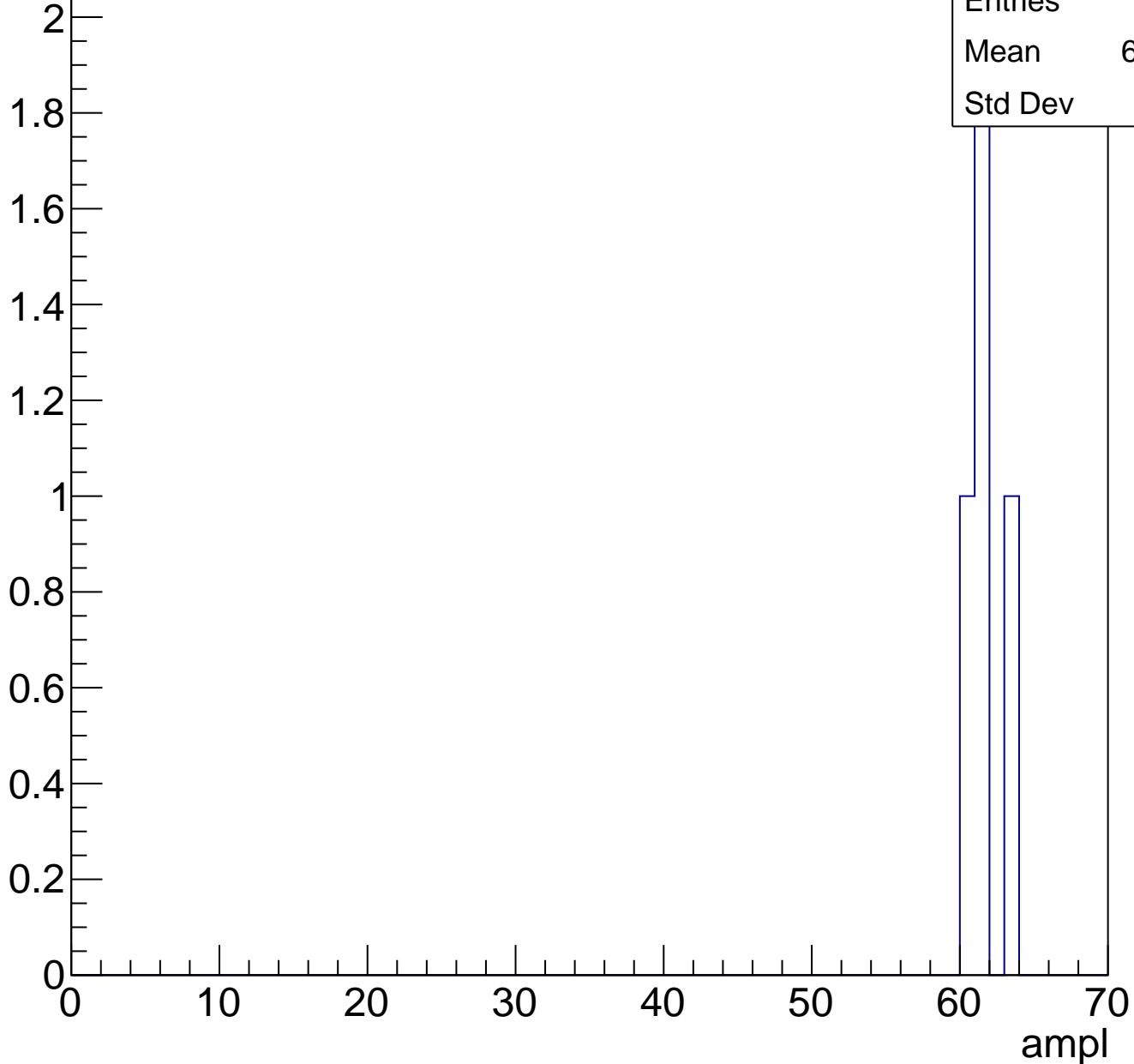
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

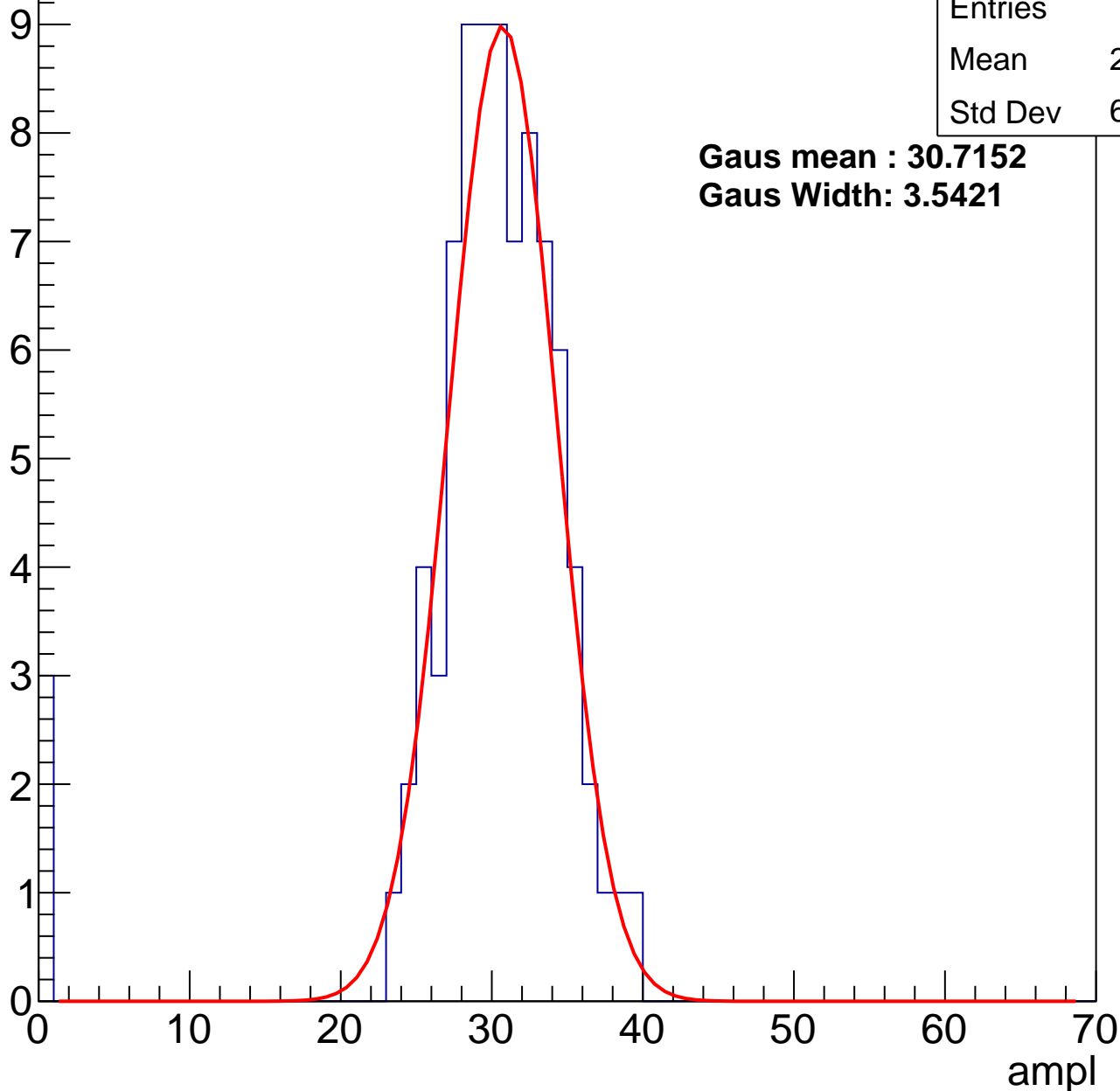
Entry



B1L101S, U14-ch59, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch59, adc1

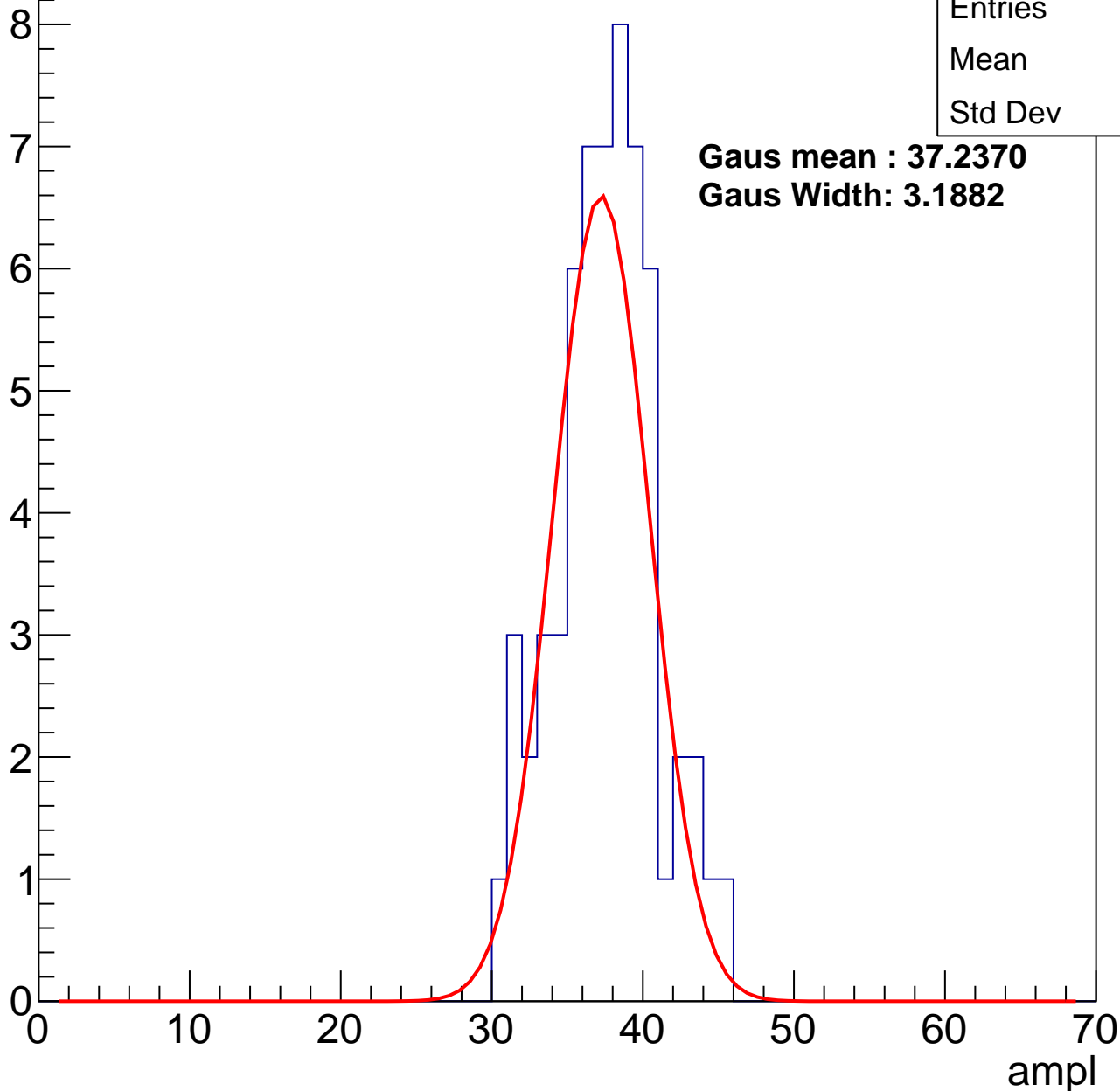
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.1
Std Dev	3.3

Gaus mean : 37.2370

Gaus Width: 3.1882



B1L101S, U14-ch59, adc2

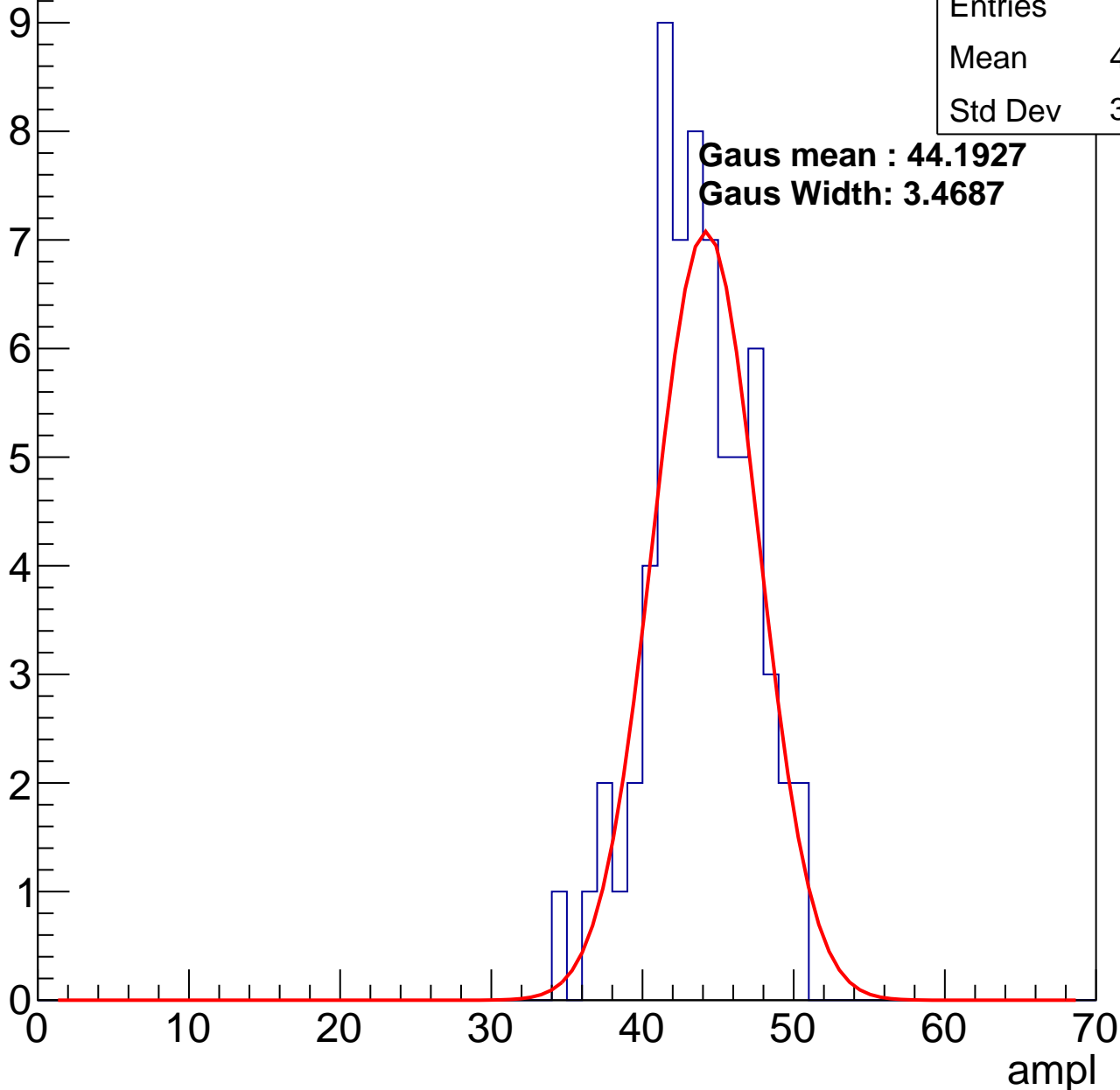
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.29
Std Dev	3.387

Gaus mean : 44.1927

Gaus Width: 3.4687

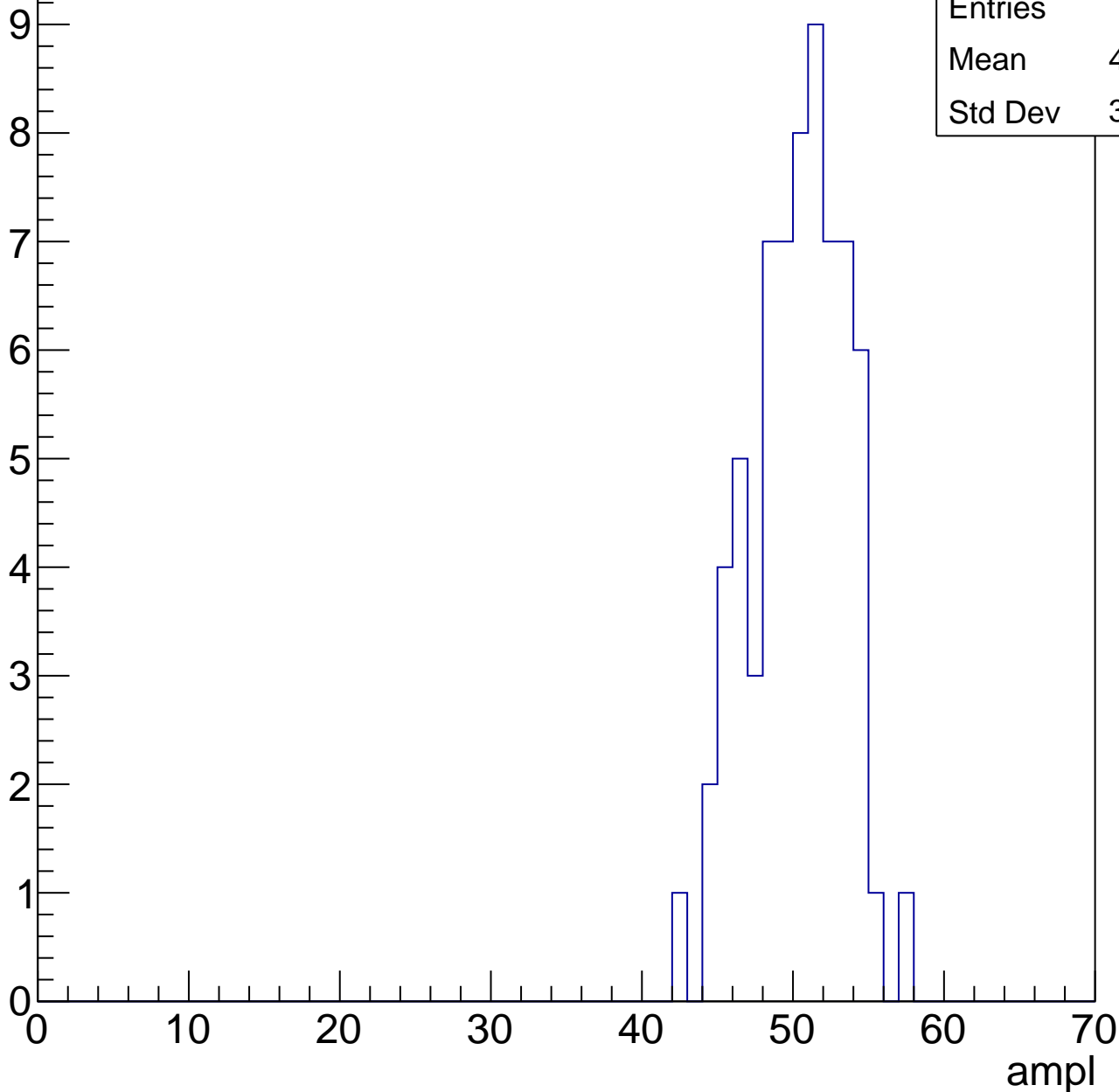


B1L101S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

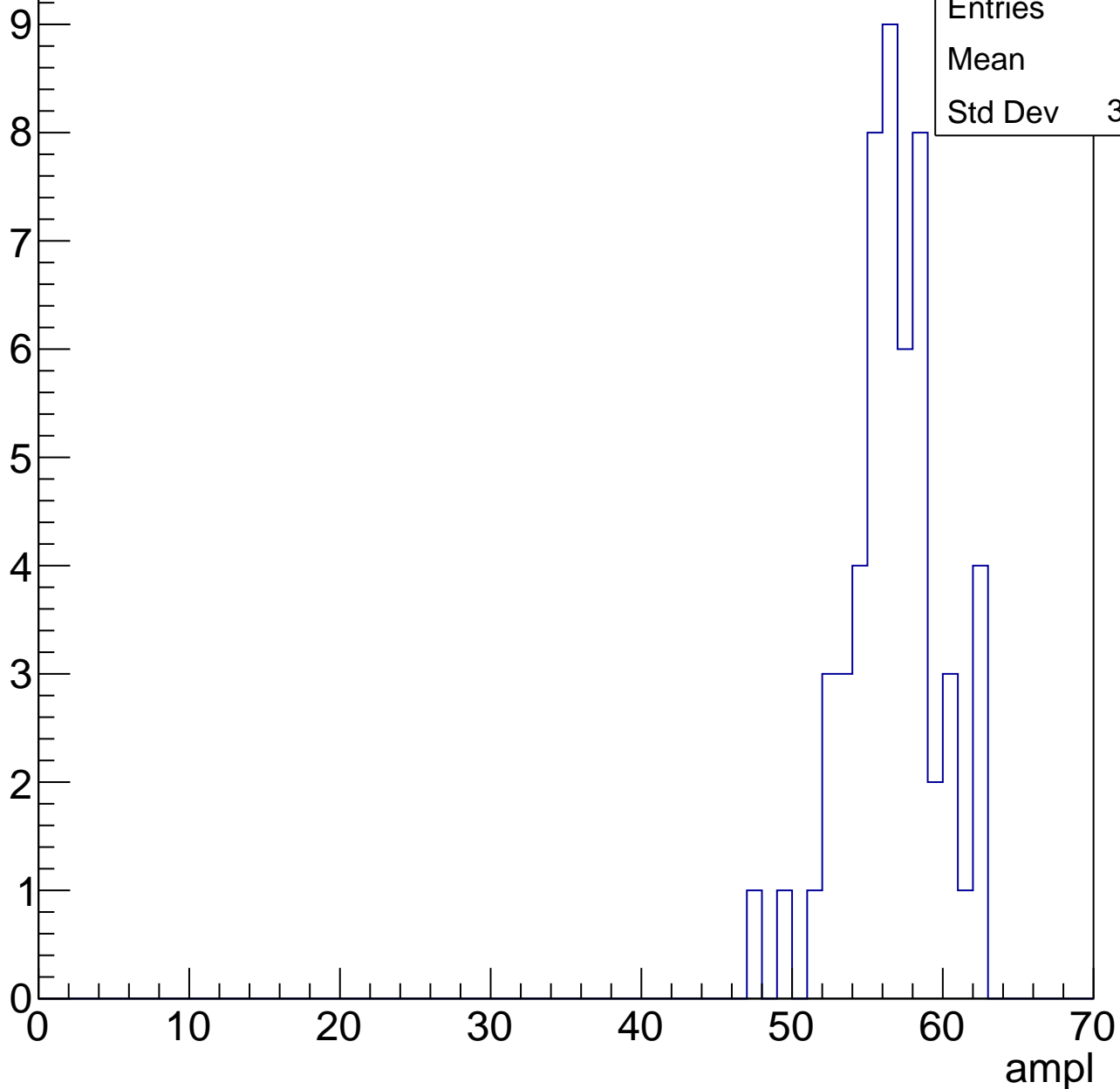
Entries	68
Mean	49.85
Std Dev	3.083



B1L101S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

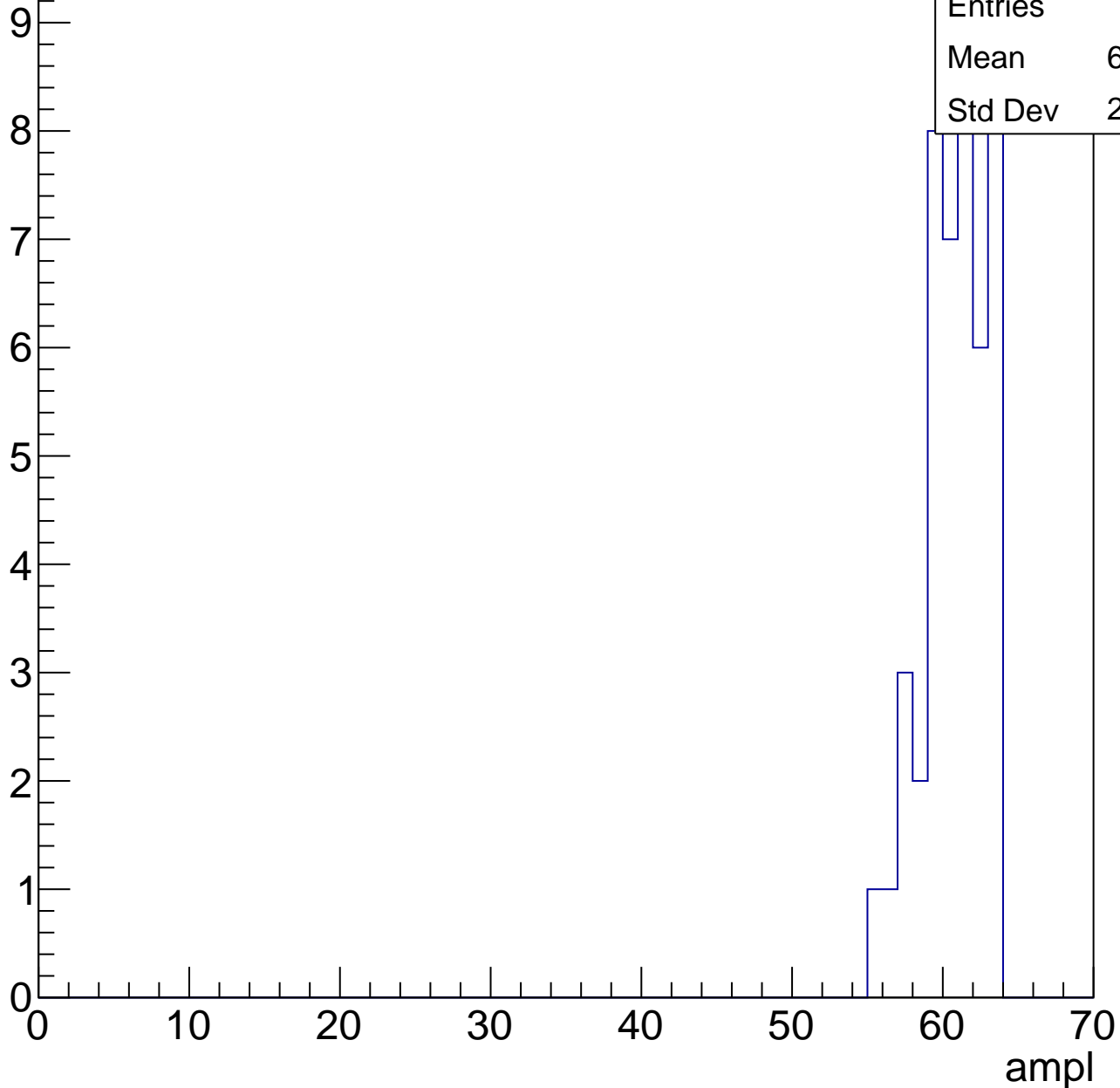


Entries	54
Mean	56.2
Std Dev	3.117

B1L101S, U14-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch60, adc0

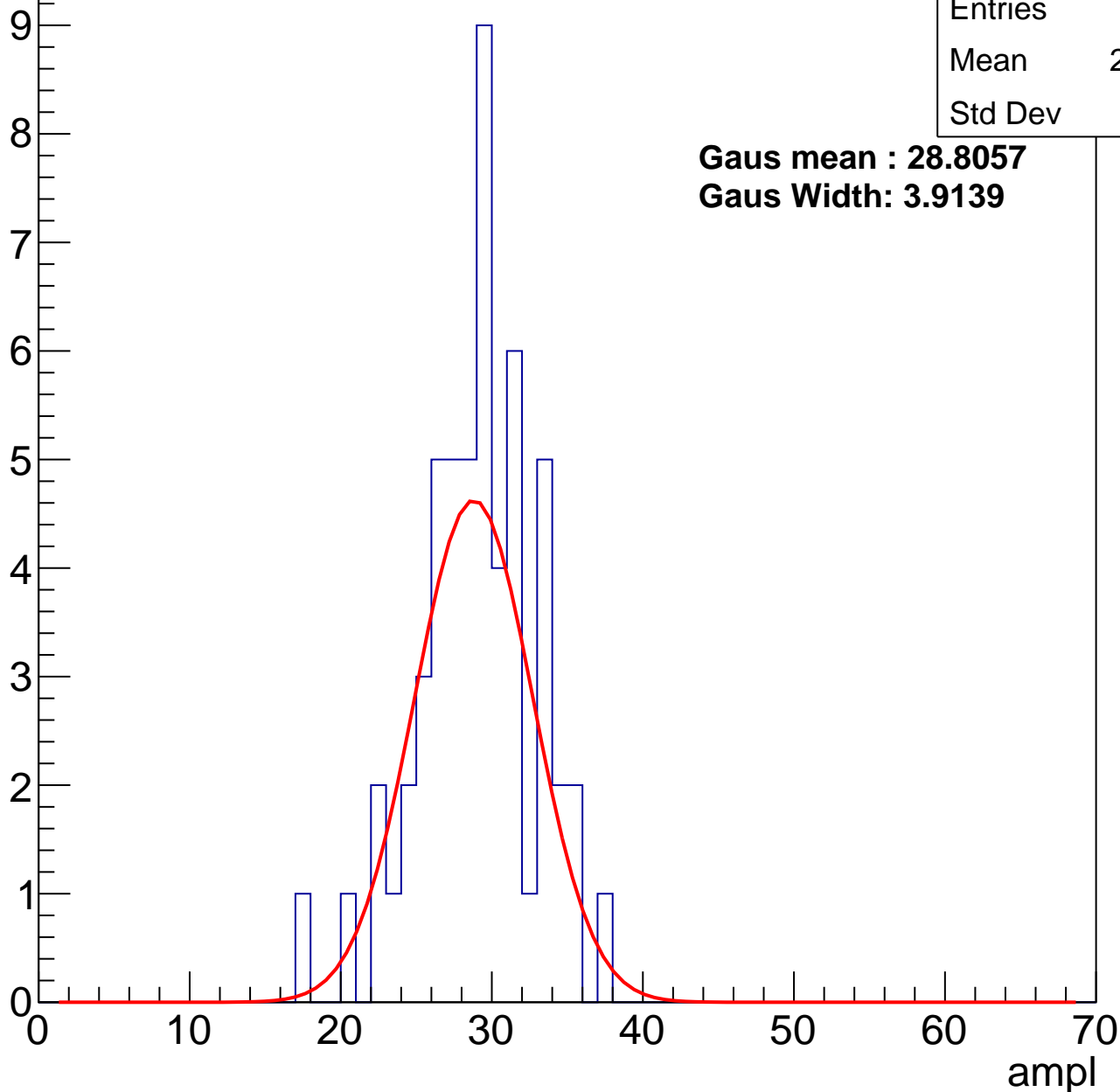
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.56
Std Dev	3.86

Gaus mean : 28.8057

Gaus Width: 3.9139



B1L101S, U14-ch60, adc1

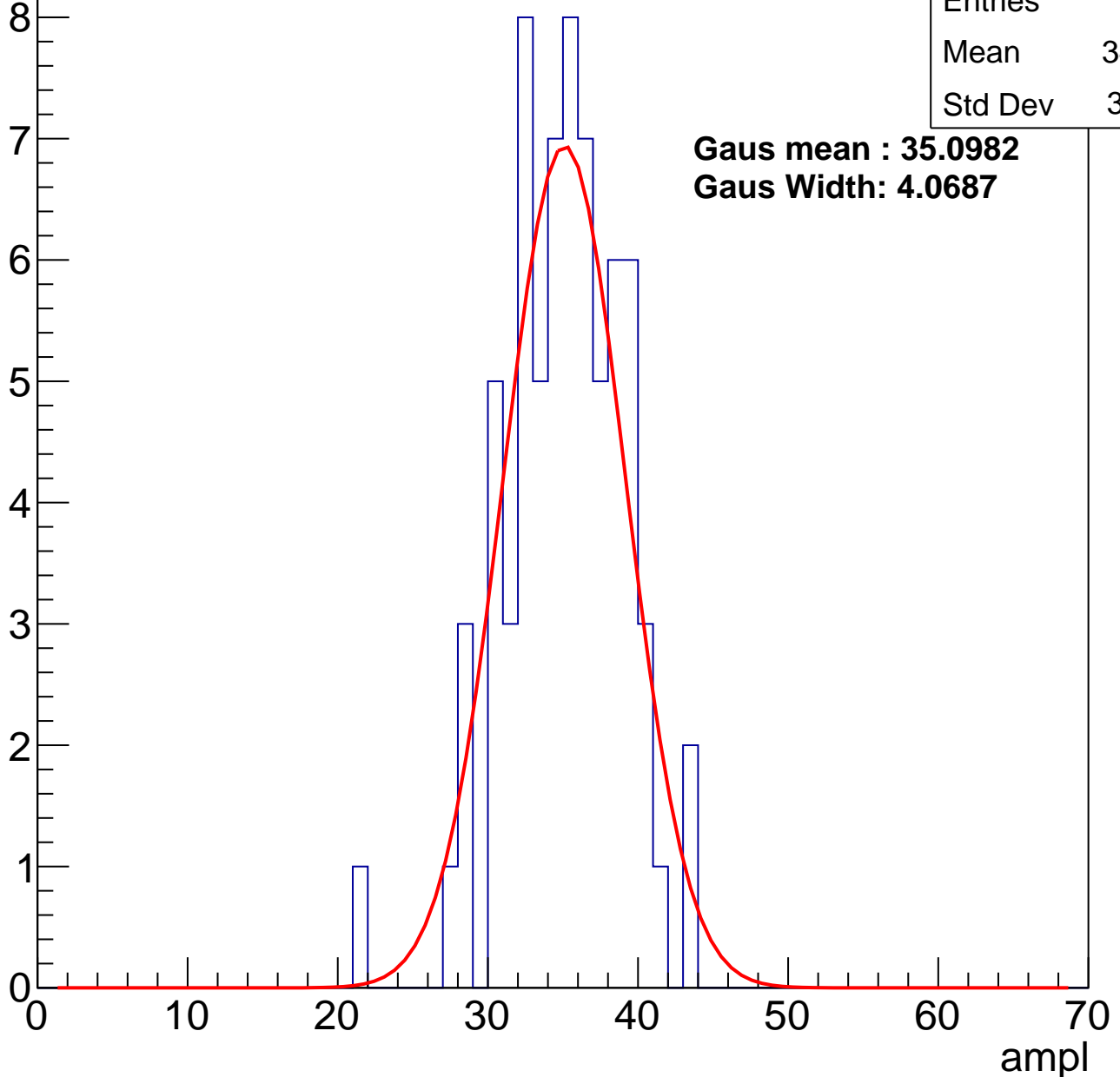
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	34.65
Std Dev	3.911

Gaus mean : 35.0982

Gaus Width: 4.0687



B1L101S, U14-ch60, adc2

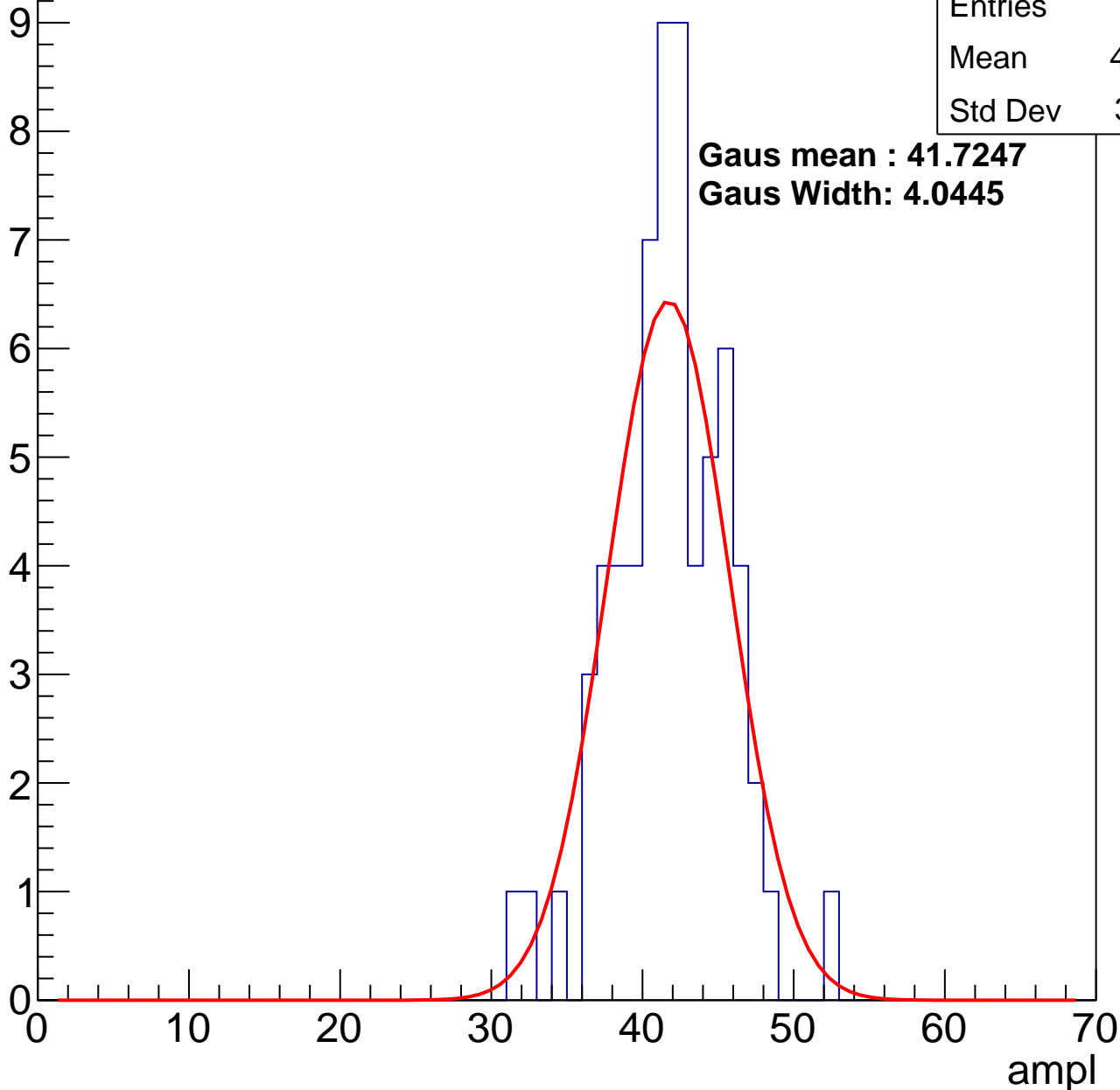
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.33
Std Dev	3.751

Gaus mean : 41.7247

Gaus Width: 4.0445

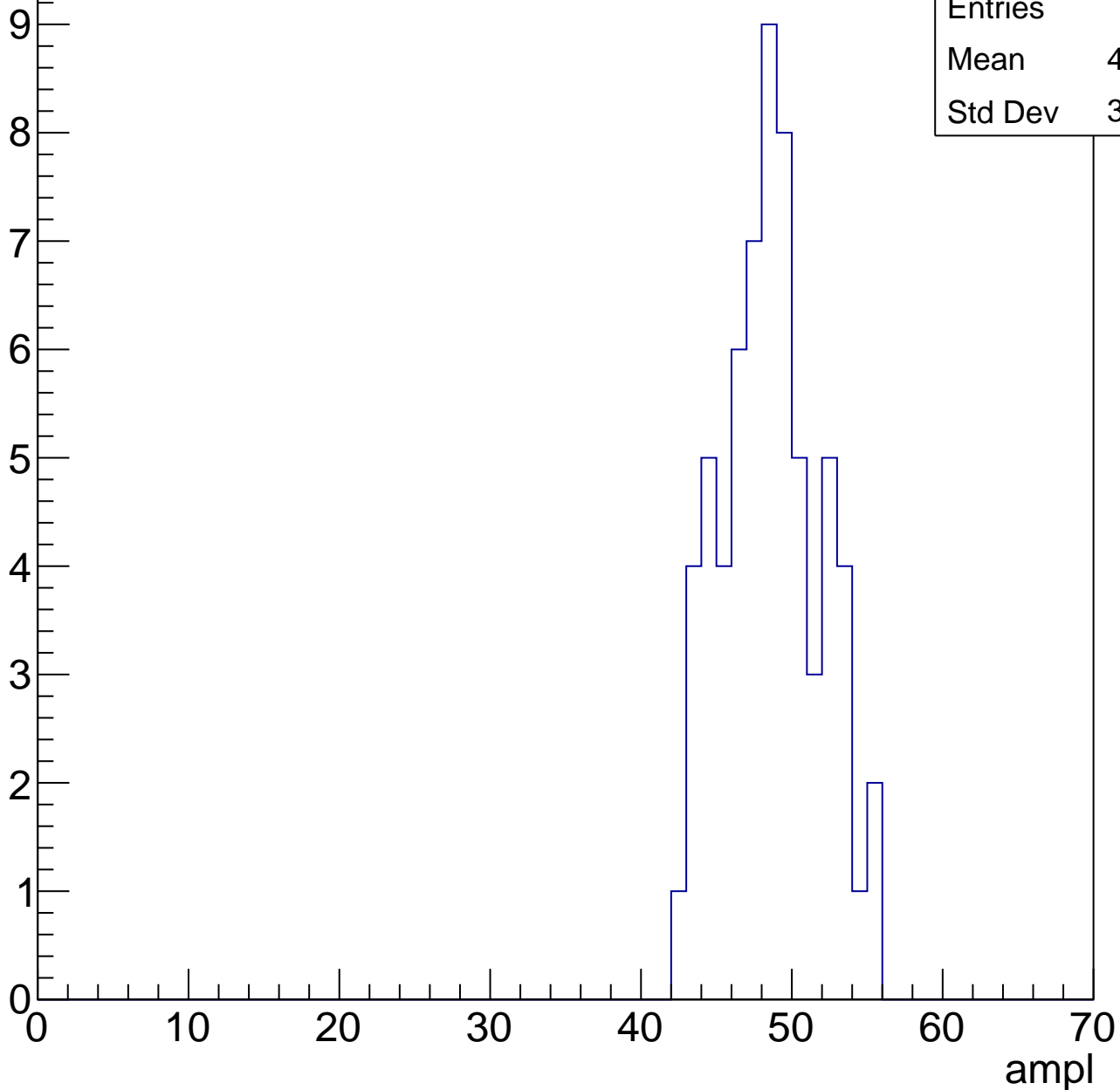


B1L101S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.16
Std Dev	3.188

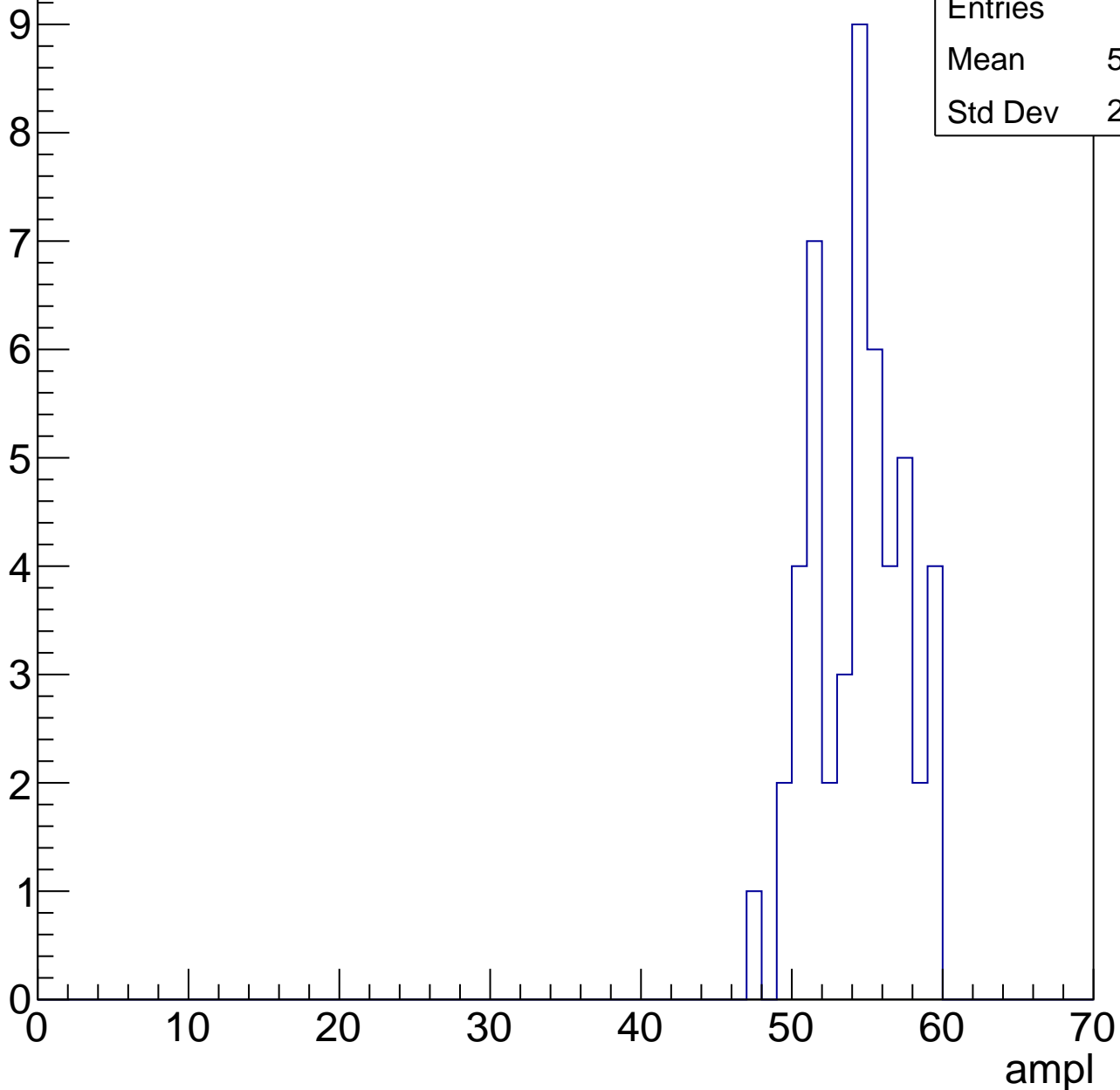


B1L101S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	53.92
Std Dev	2.982

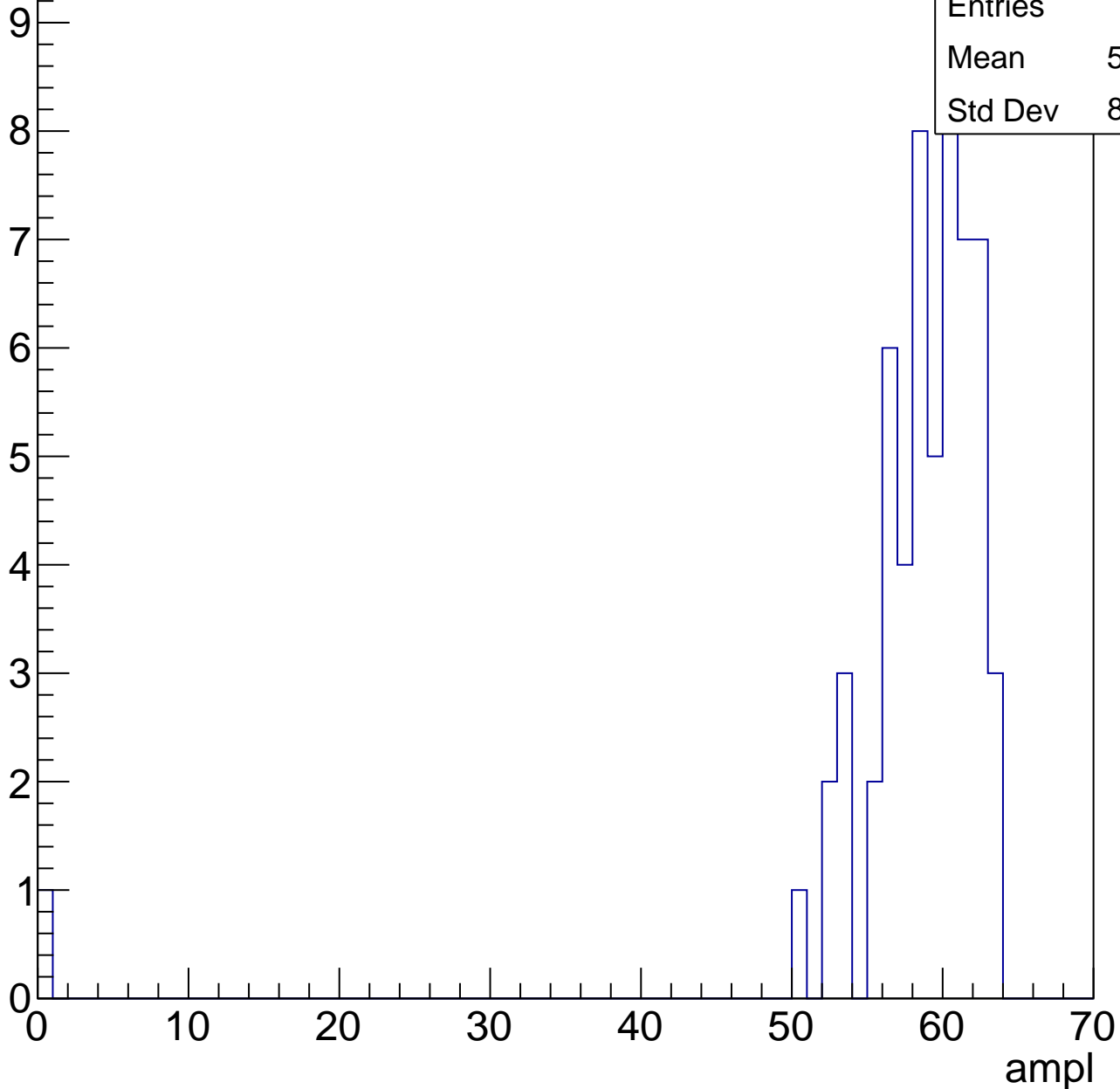


B1L101S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	57.52
Std Dev	8.198

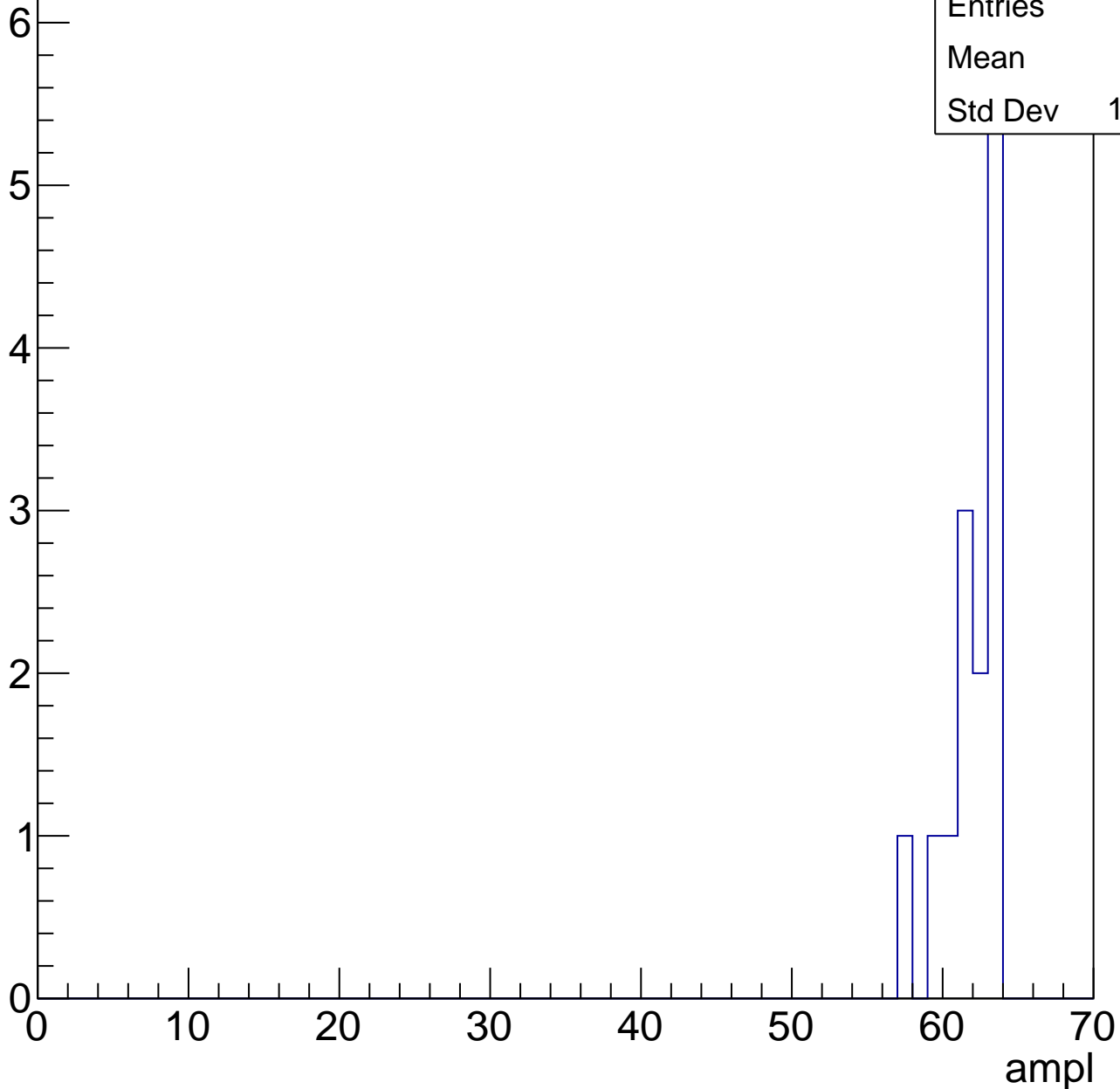


B1L101S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.5
Std Dev	1.763



B1L101S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch61, adc0

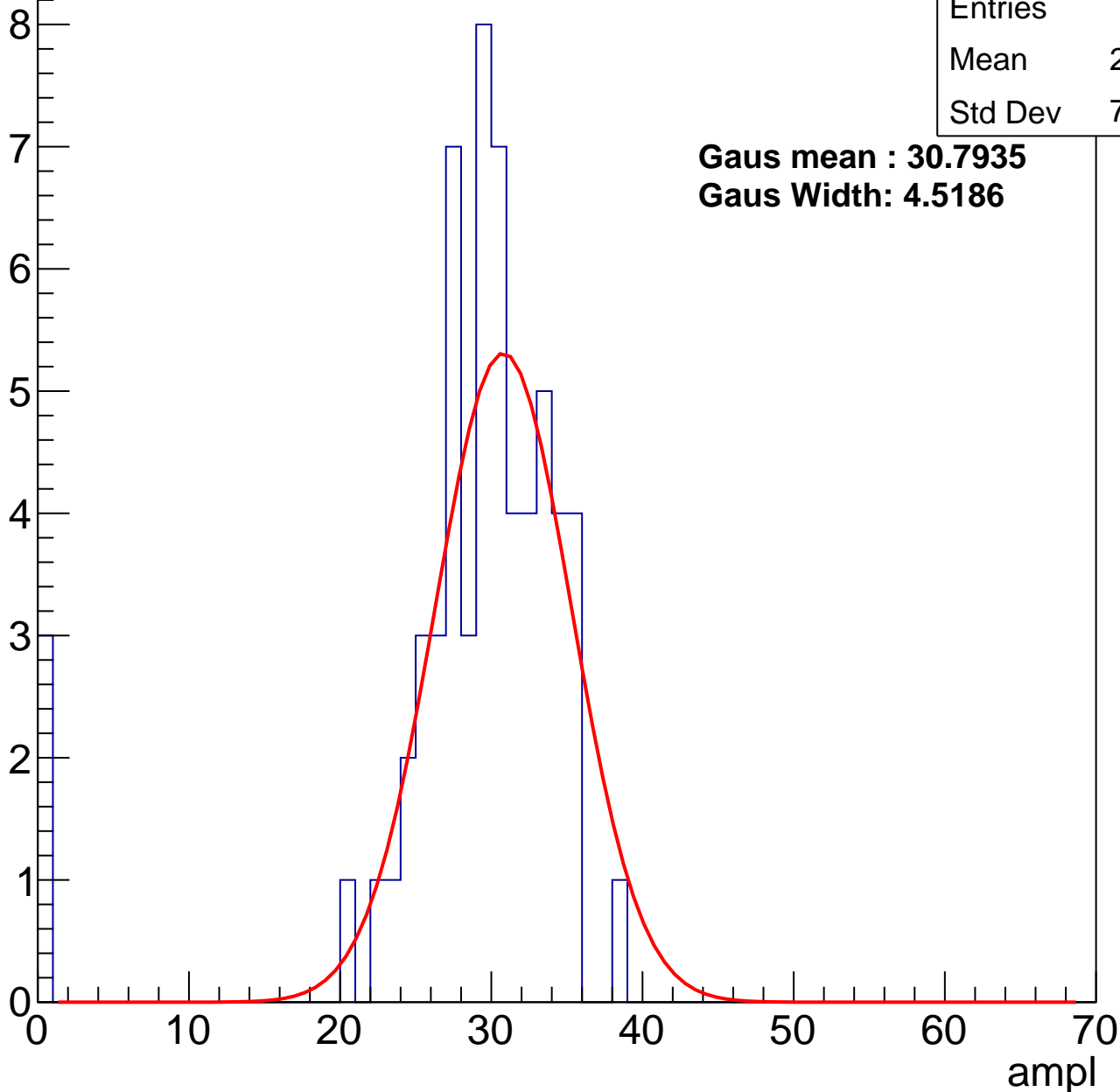
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	28.07
Std Dev	7.306

Gaus mean : 30.7935

Gaus Width: 4.5186



B1L101S, U14-ch61, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	35.59
Std Dev	3.444

Gaus mean : 35.5669

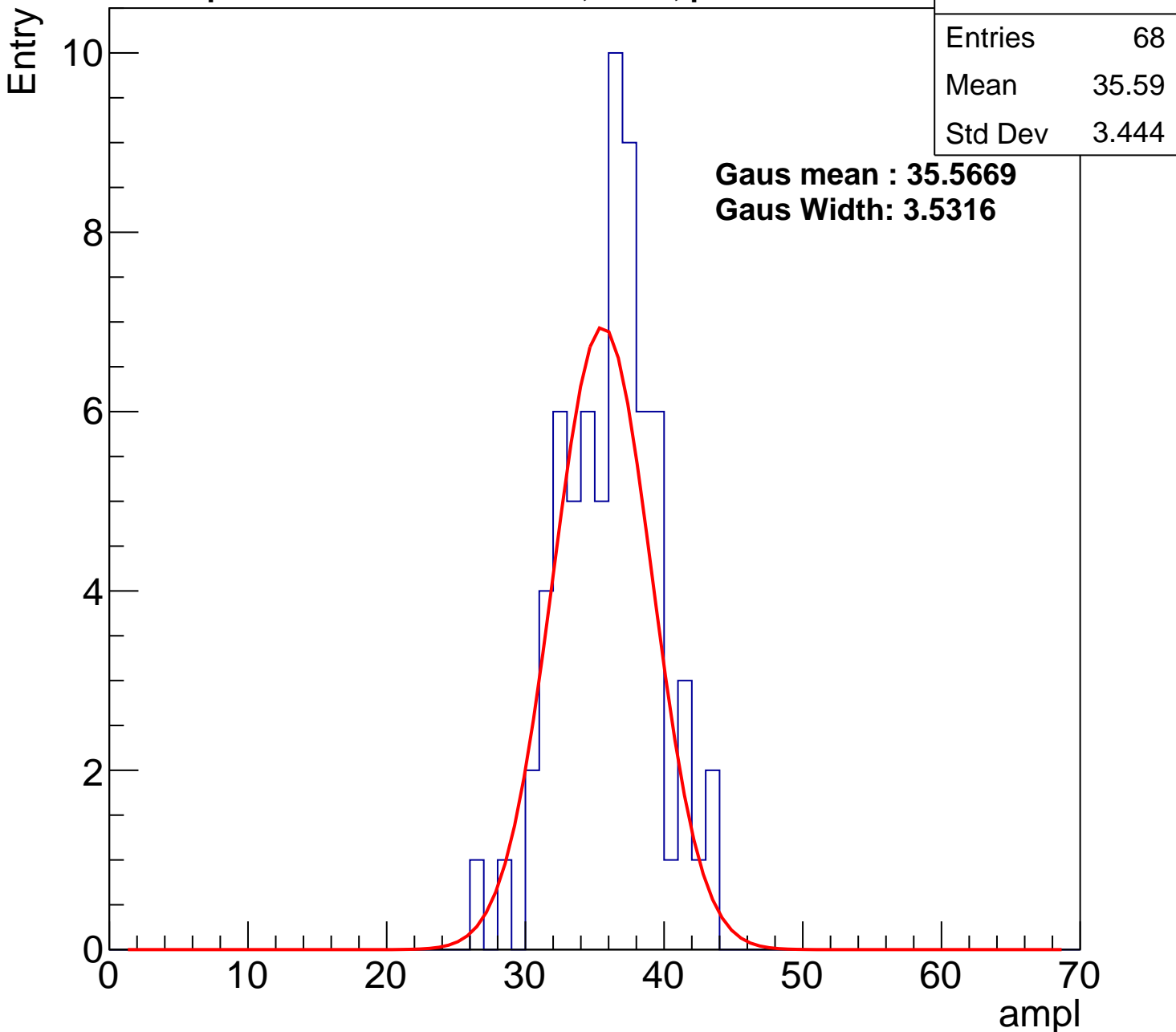
Gaus Width: 3.5316

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch61, adc2

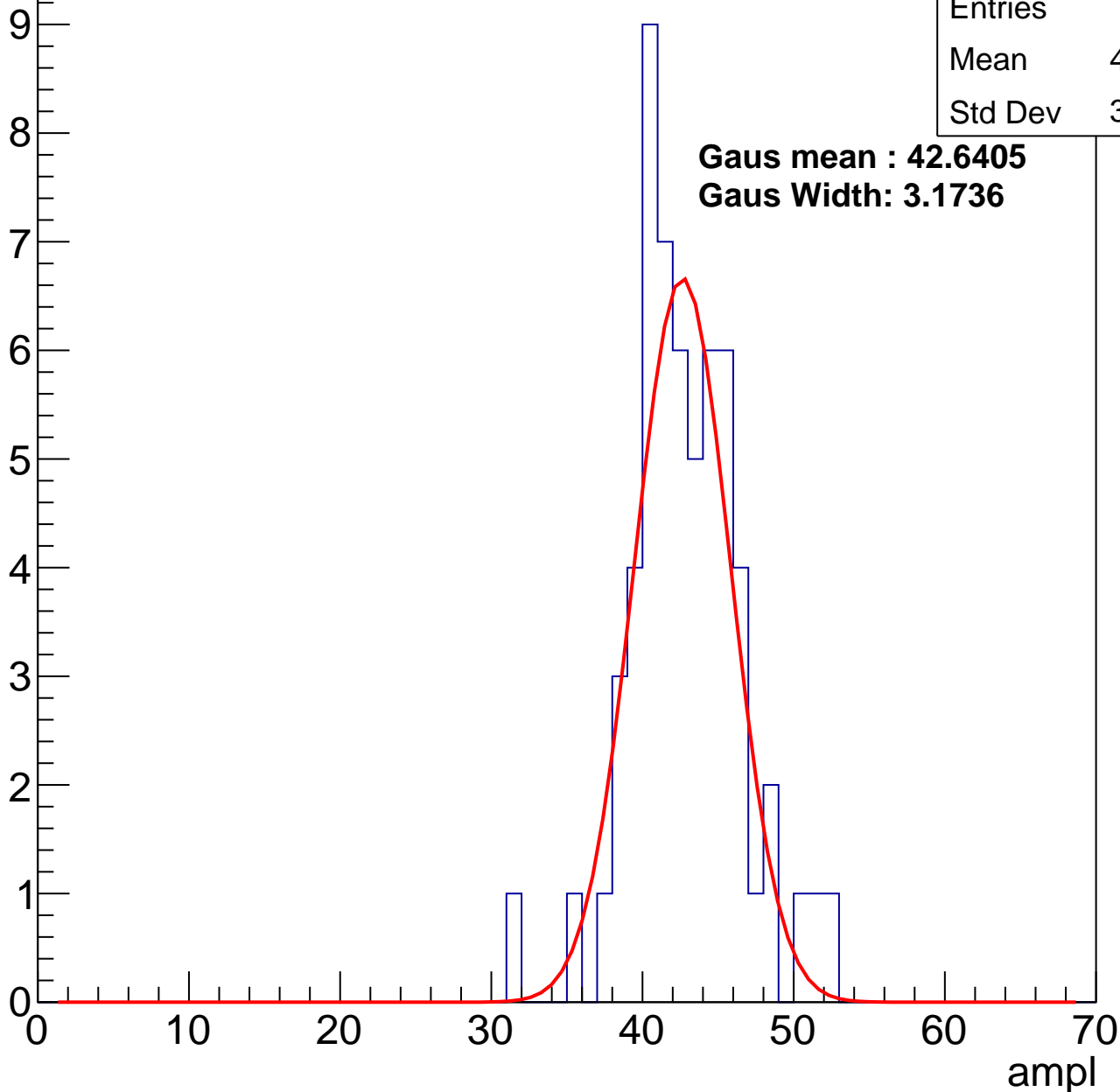
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.39
Std Dev	3.687

Gaus mean : 42.6405

Gaus Width: 3.1736



B1L101S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	48.75
Std Dev	3.375

Entry

10

8

6

4

2

0

0

10

20

30

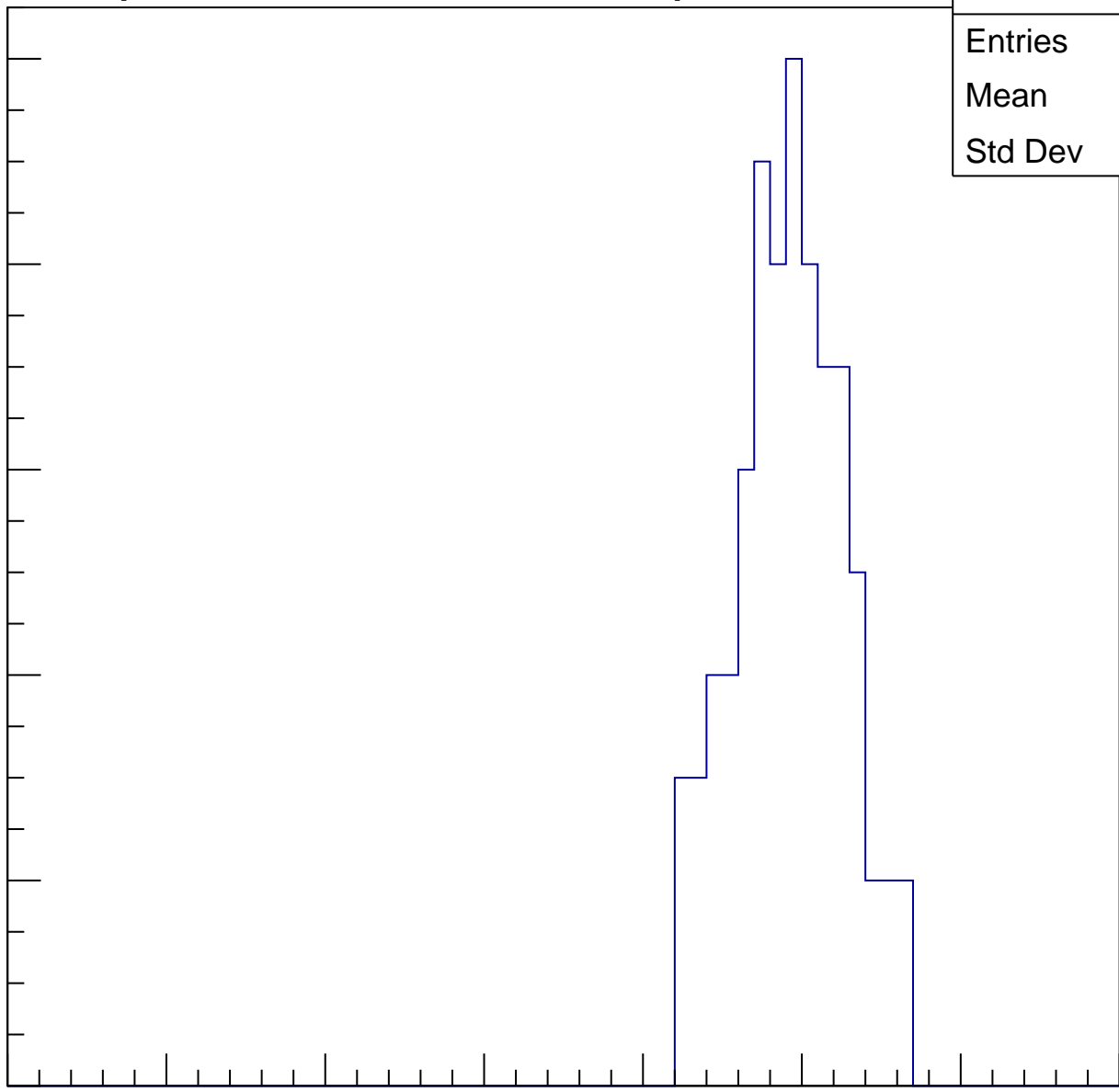
40

50

60

70

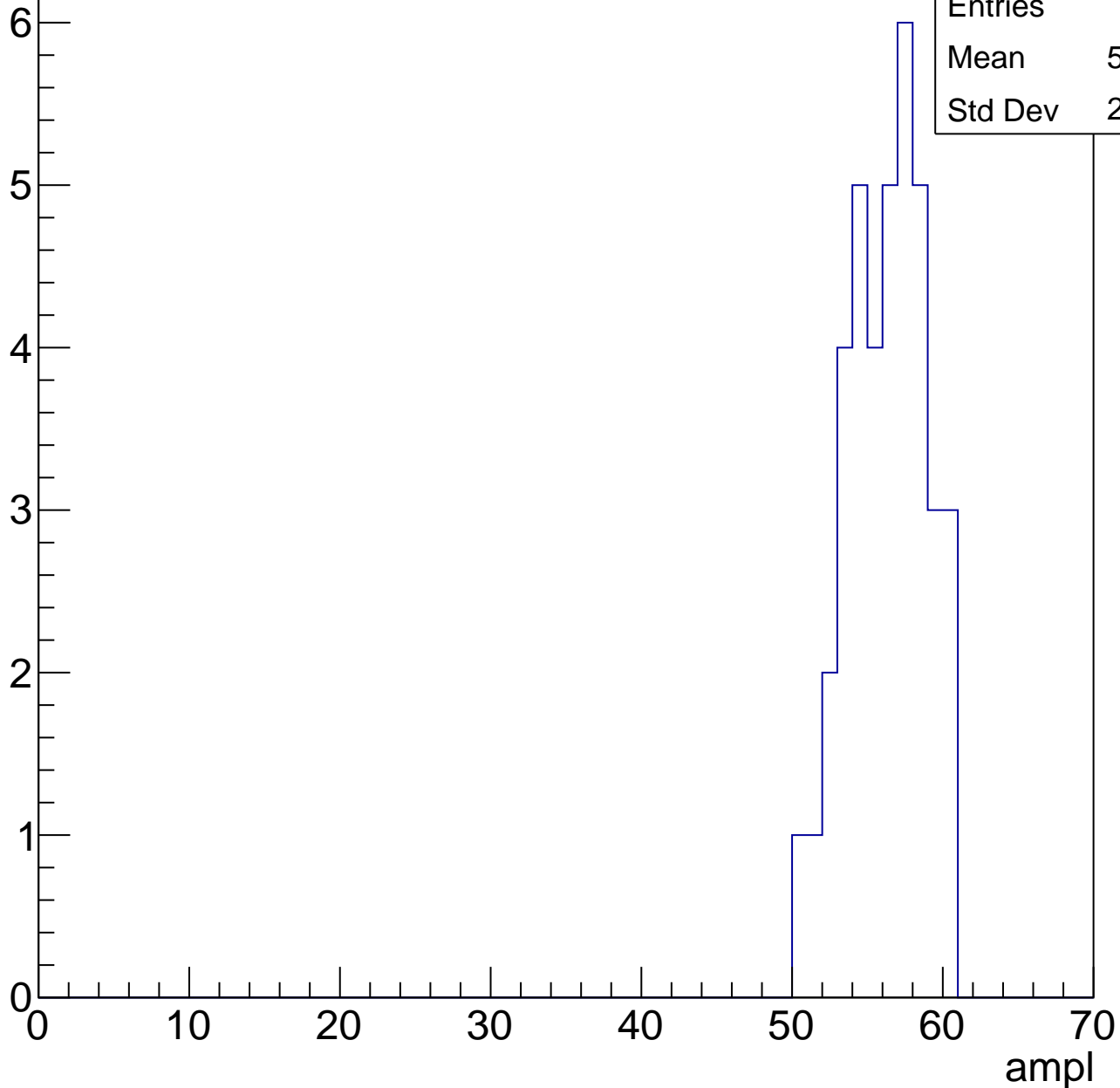
ampl



B1L101S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



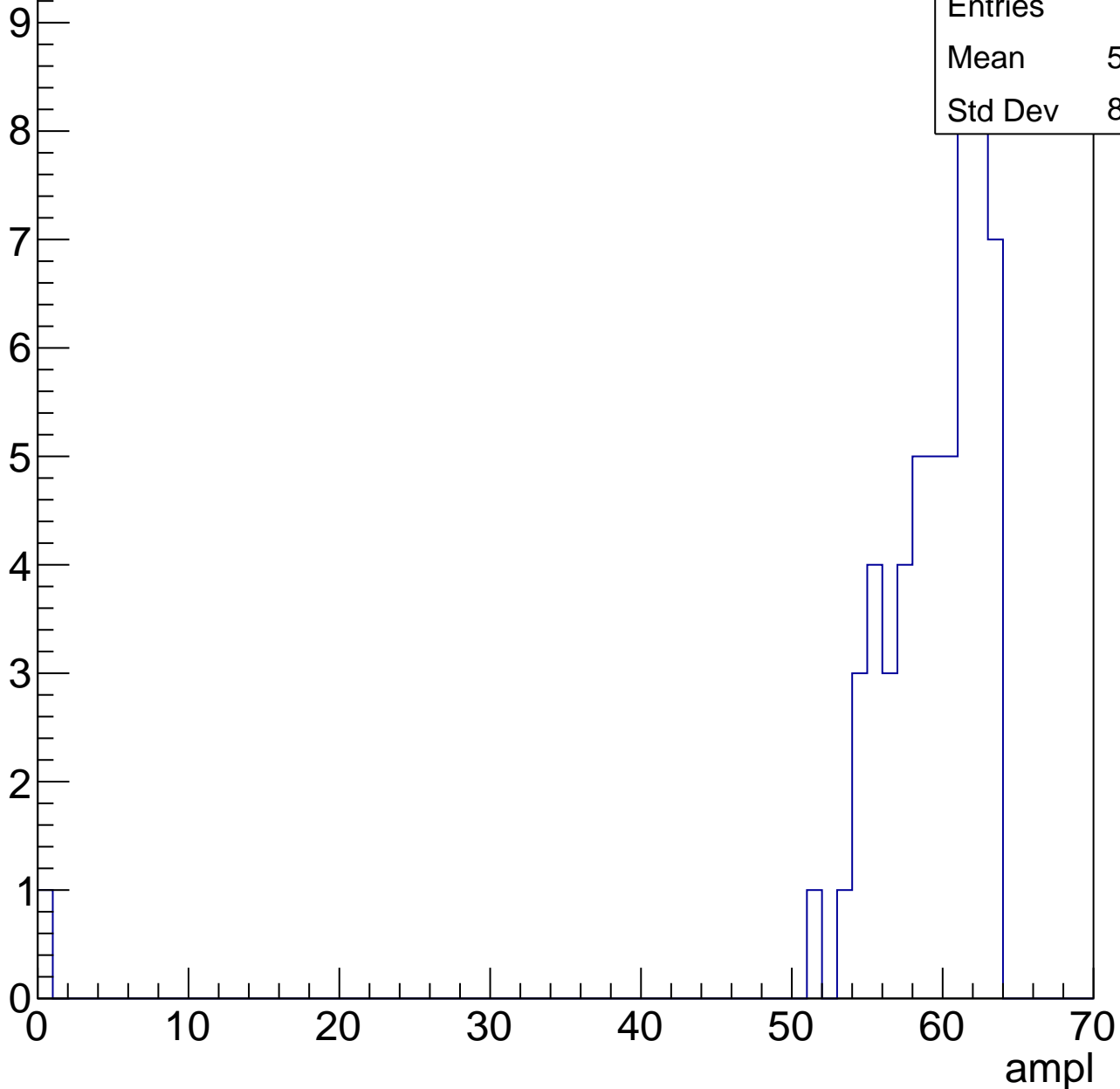
Entries	39
Mean	55.79
Std Dev	2.544

B1L101S, U14-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

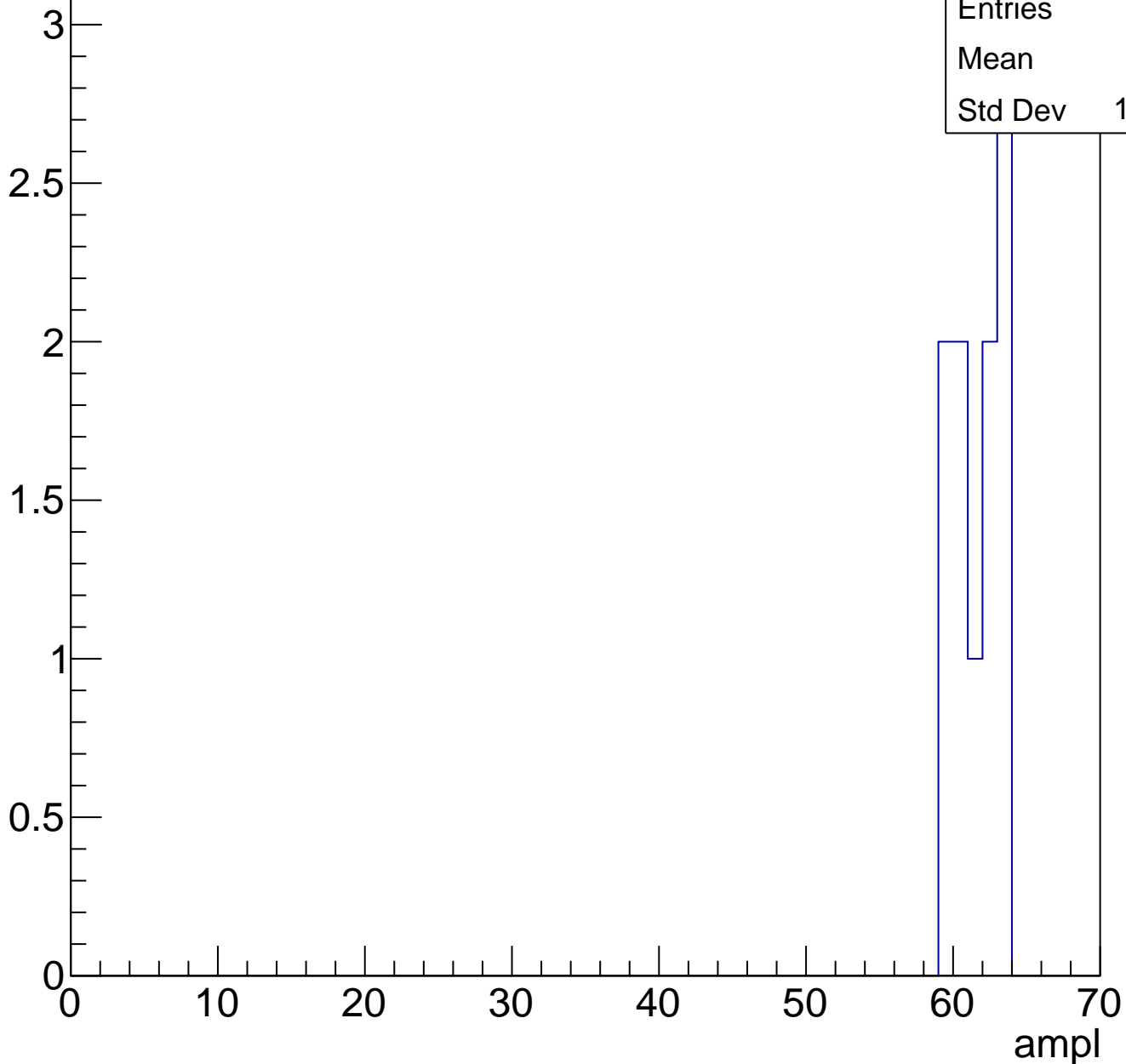
Entries	56
Mean	58.09
Std Dev	8.395



B1L101S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch62, adc0

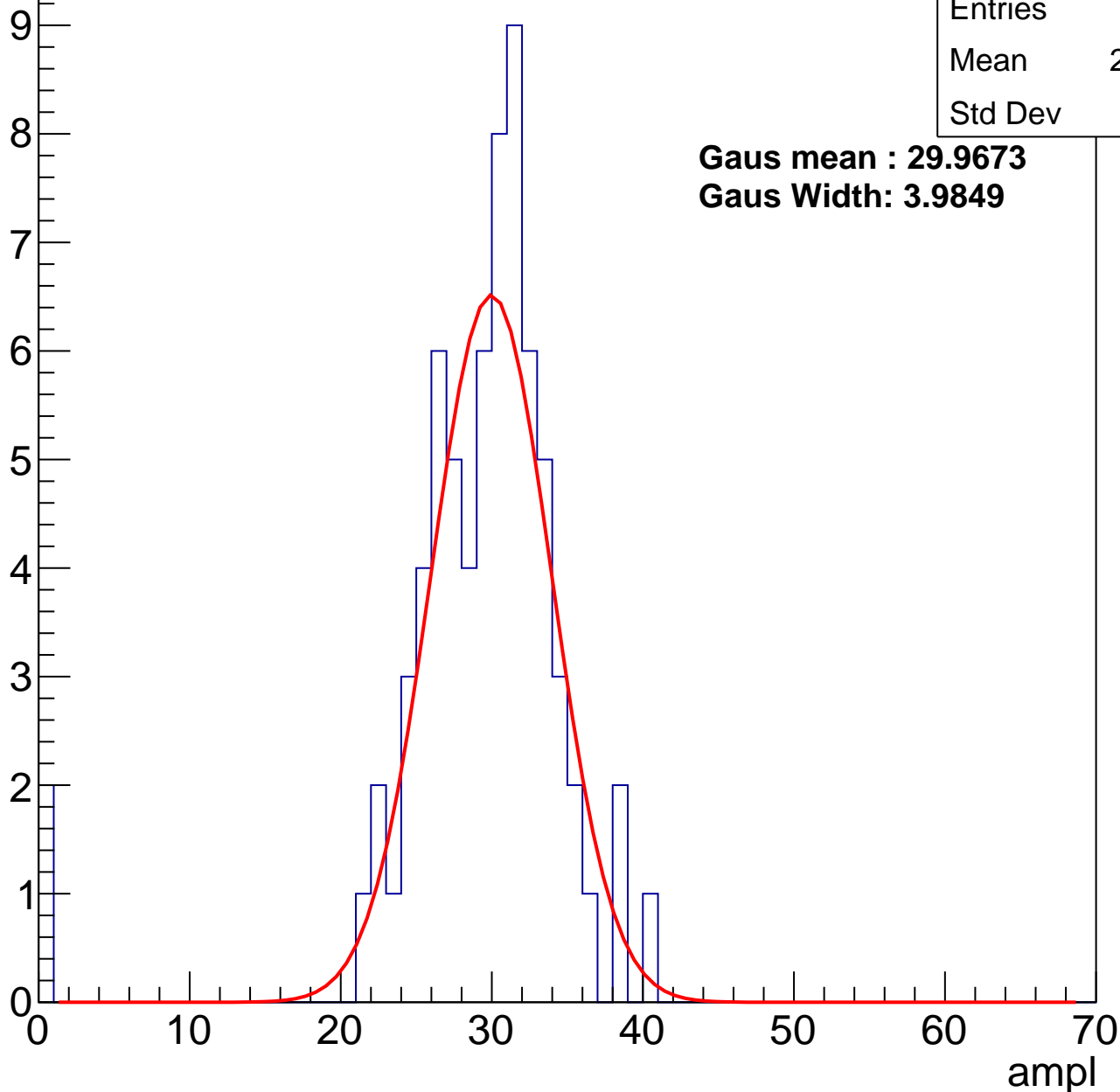
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.69
Std Dev	6.22

Gaus mean : 29.9673

Gaus Width: 3.9849



B1L101S, U14-ch62, adc1

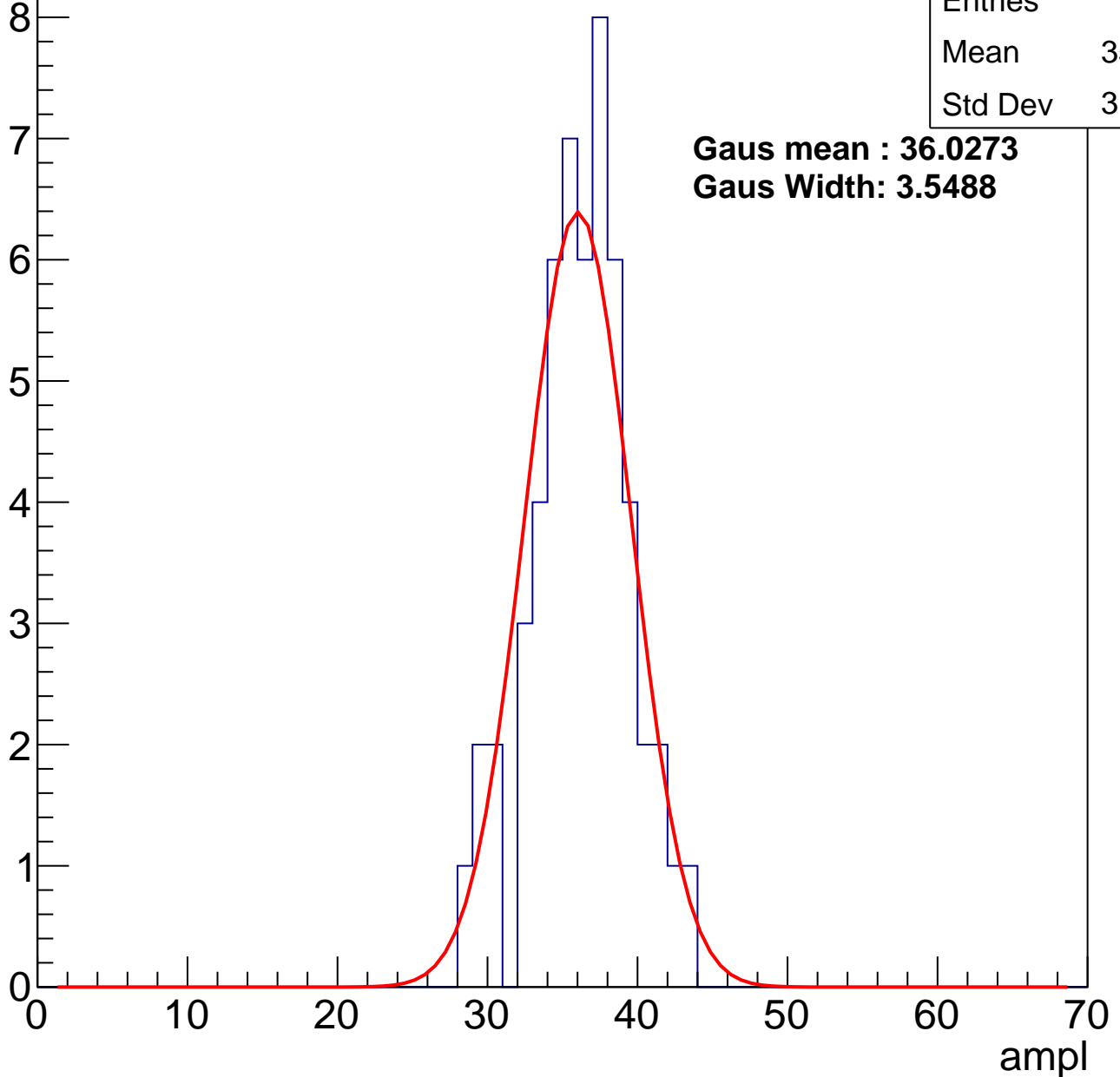
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.75
Std Dev	3.249

Gaus mean : 36.0273

Gaus Width: 3.5488



B1L101S, U14-ch62, adc2

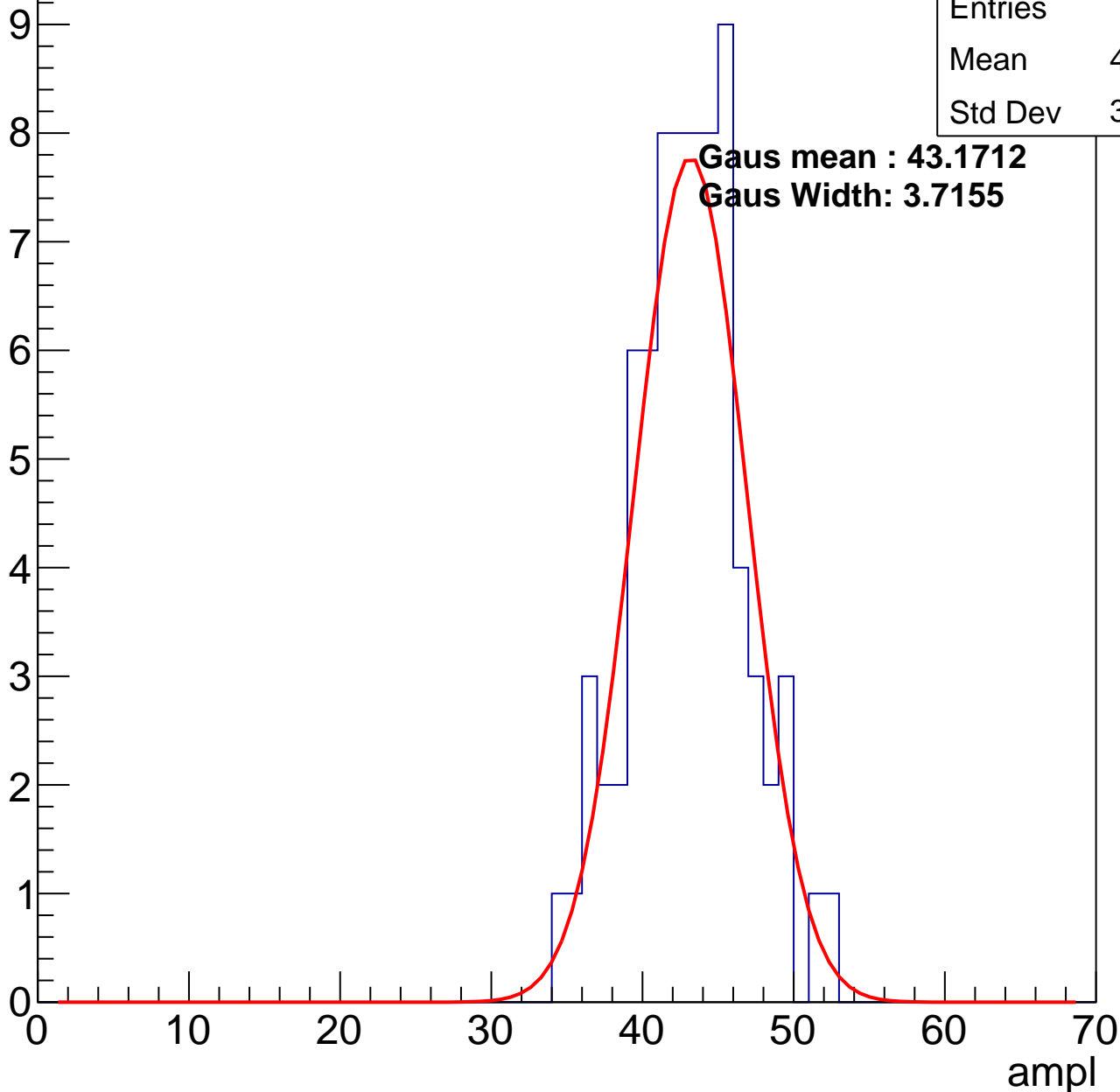
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	42.59
Std Dev	3.668

Gaus mean : 43.1712

Gaus Width: 3.7155

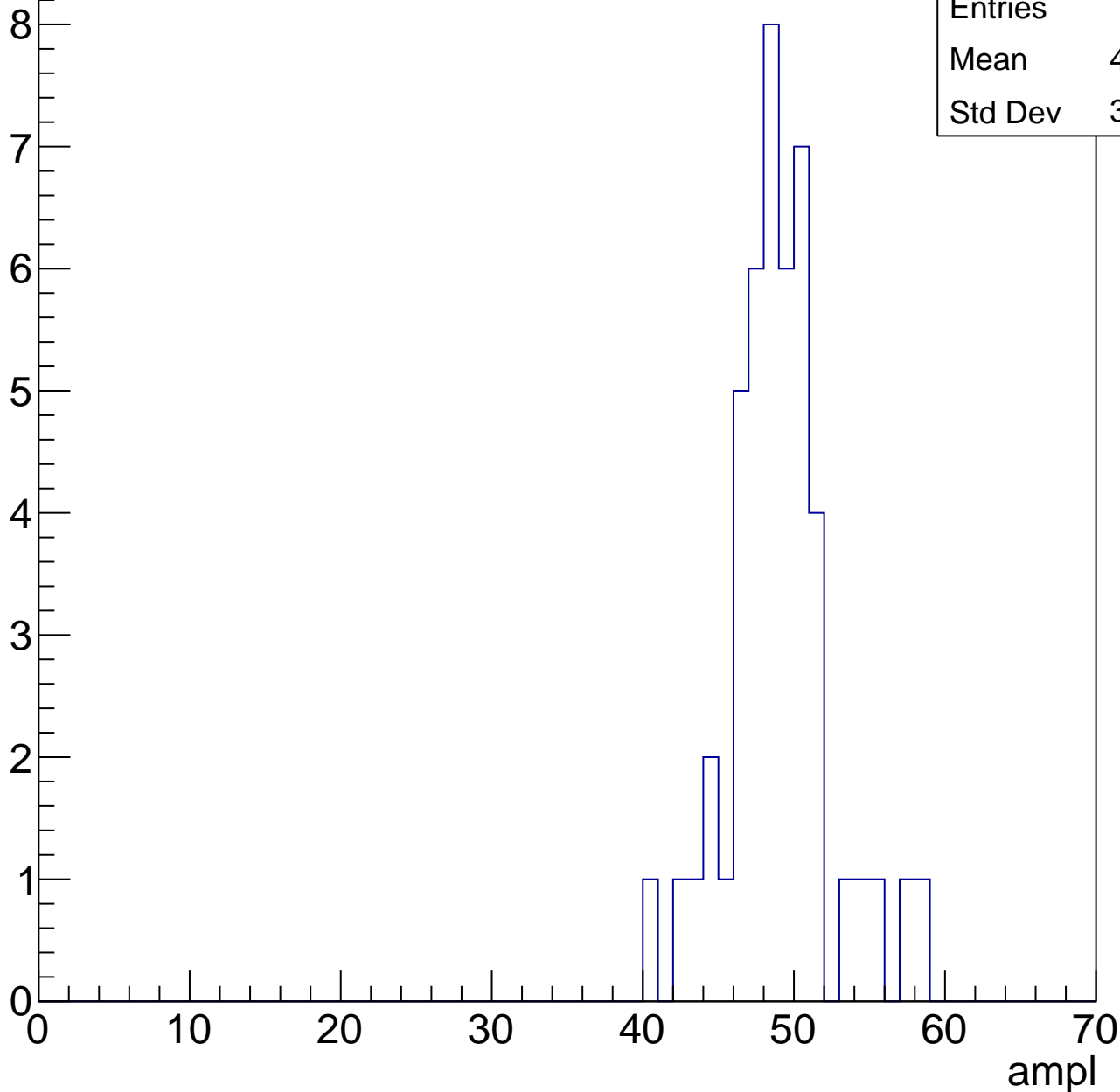


B1L101S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	48.49
Std Dev	3.395

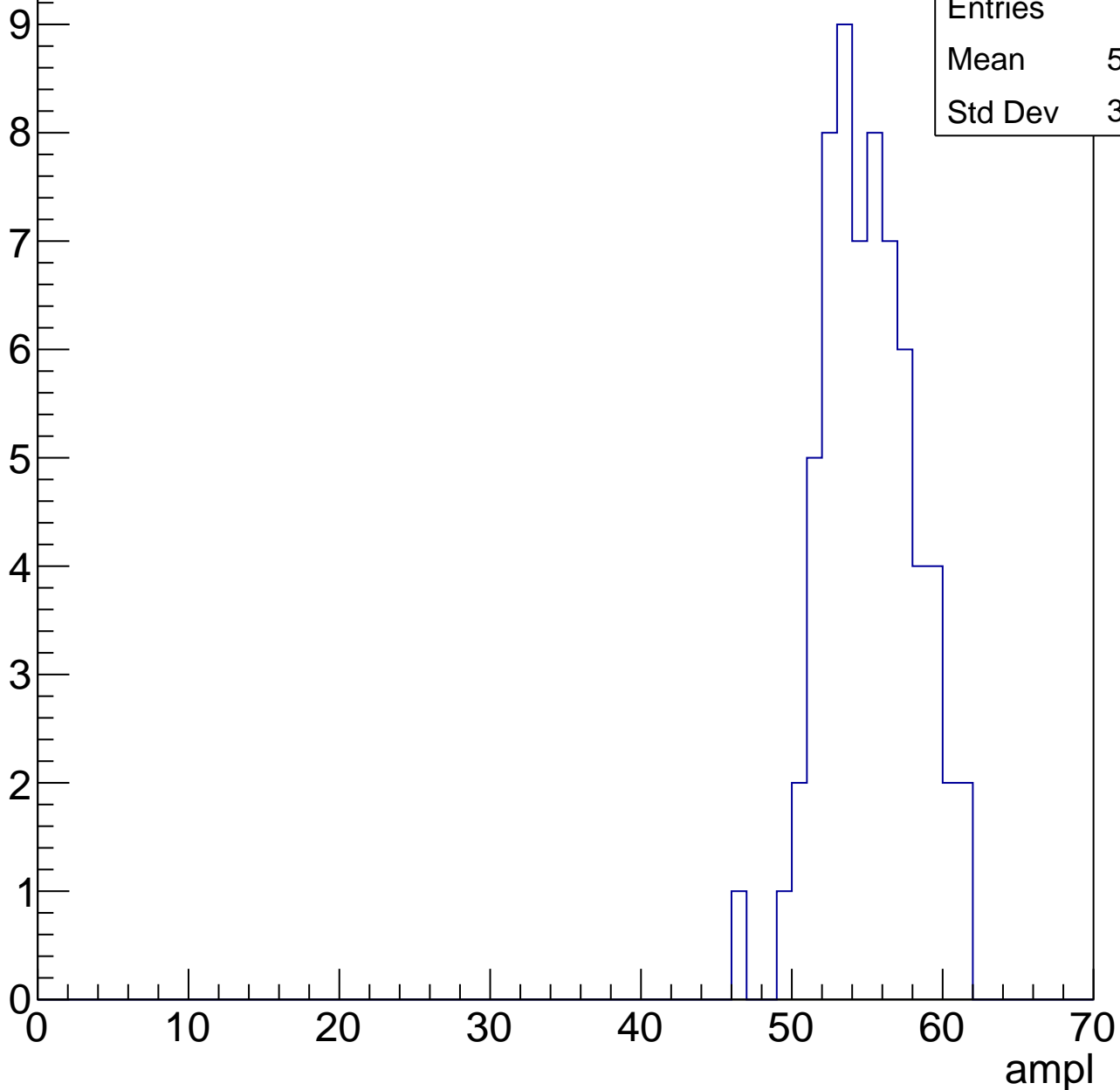


B1L101S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

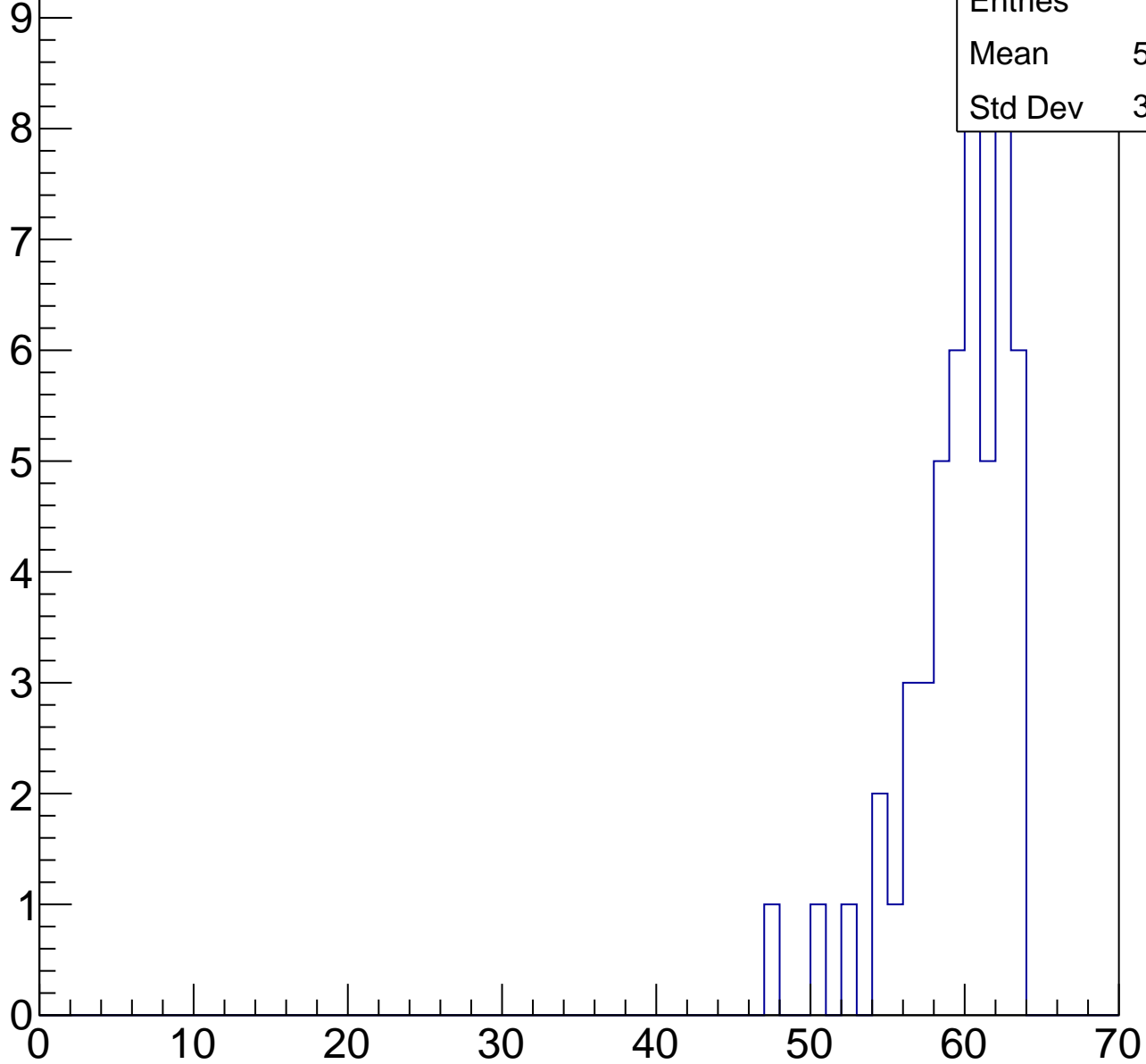
Entries	66
Mean	54.62
Std Dev	3.029



B1L101S, U14-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

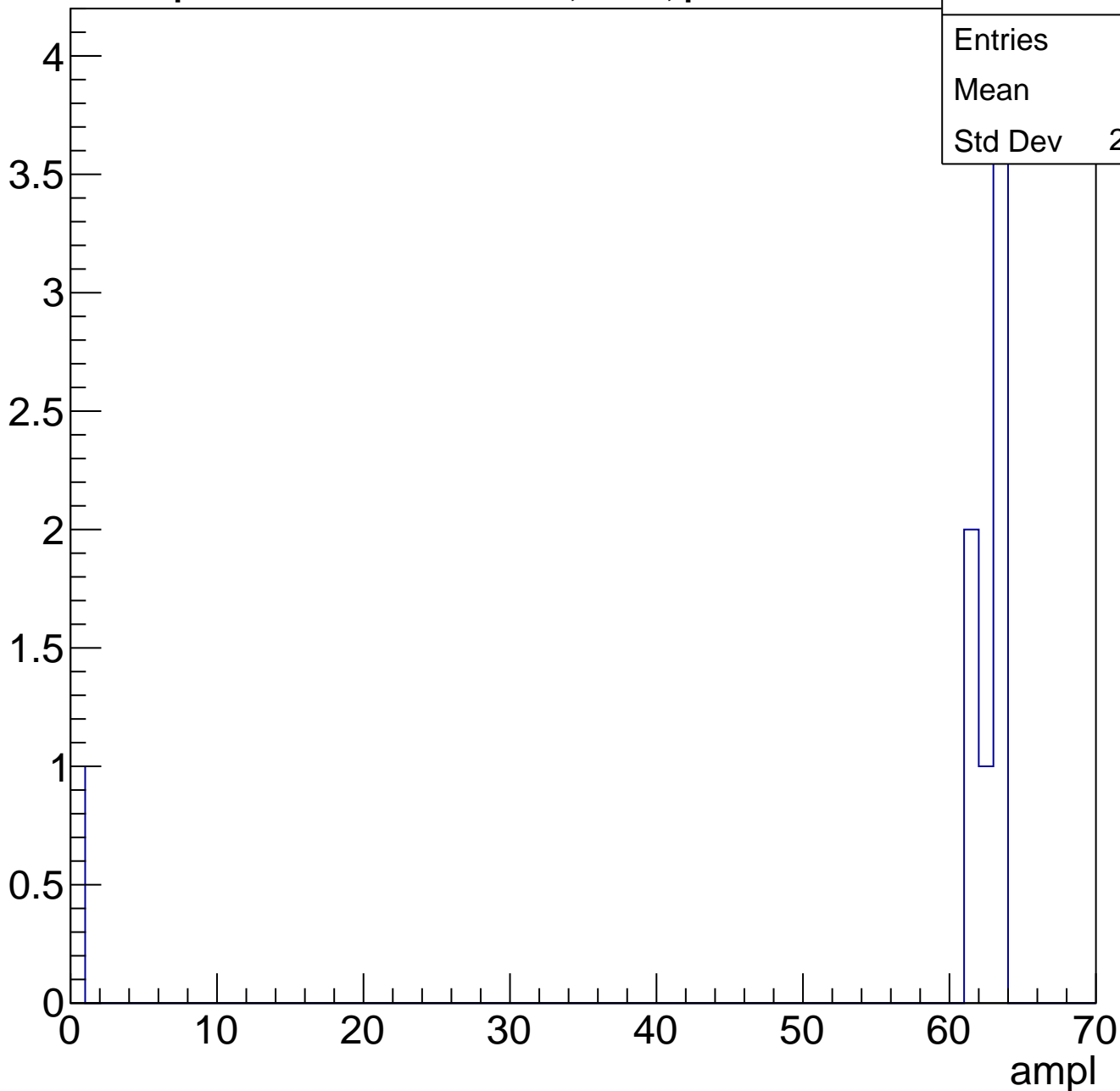


ampl

B1L101S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch63, adc0

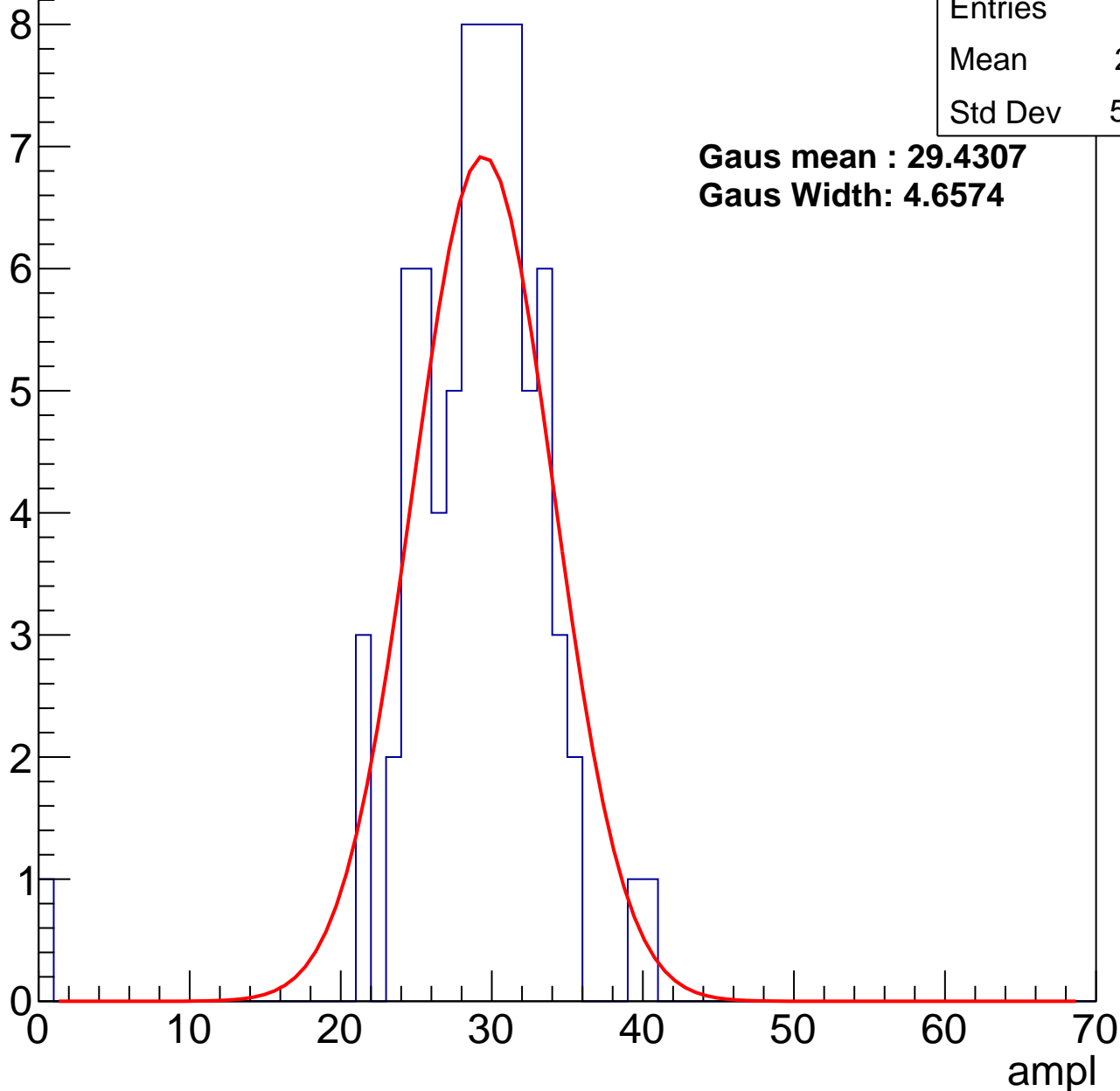
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.51
Std Dev	5.026

Gaus mean : 29.4307

Gaus Width: 4.6574



B1L101S, U14-ch63, adc1

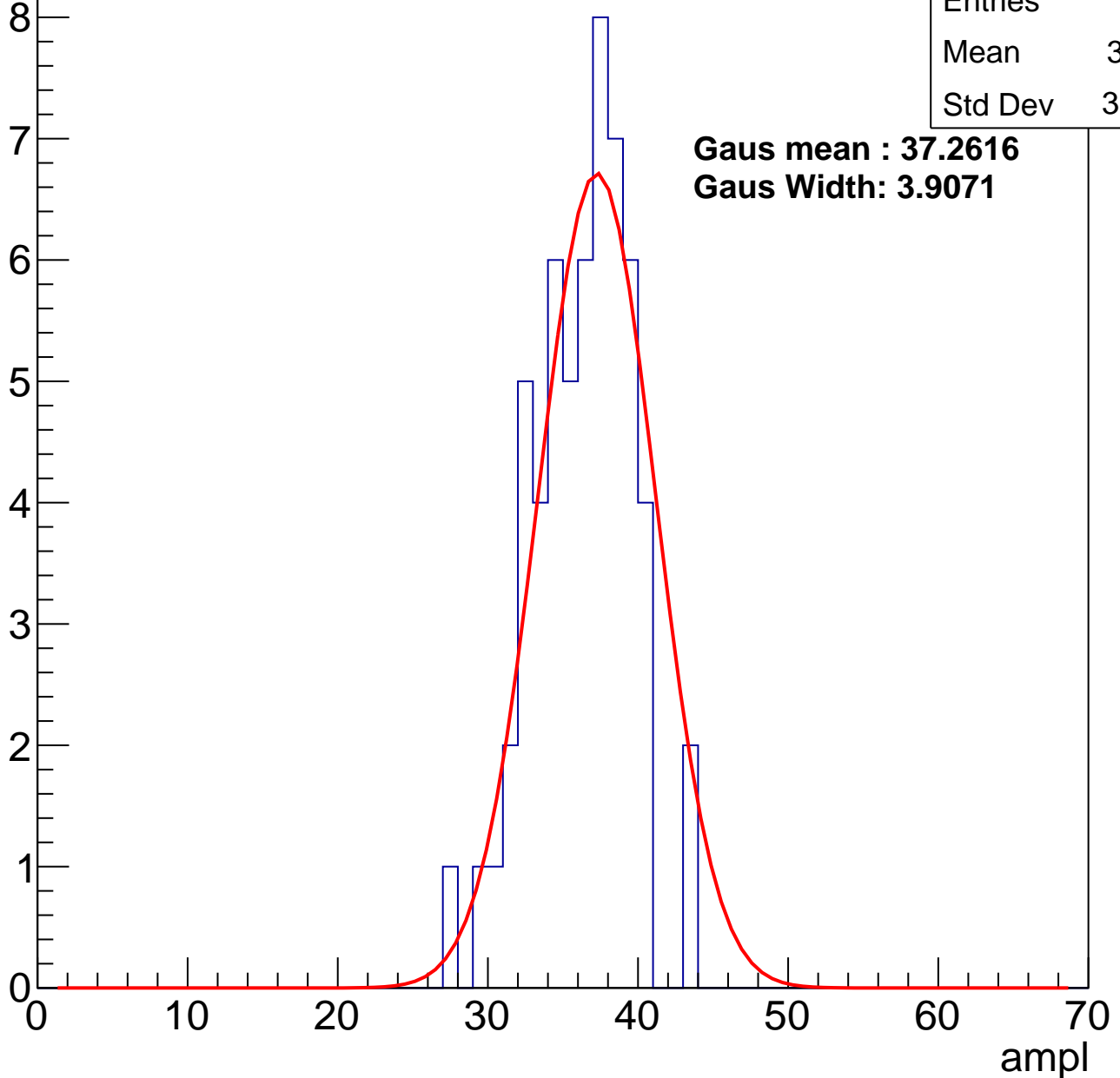
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.81
Std Dev	3.245

Gaus mean : 37.2616

Gaus Width: 3.9071



B1L101S, U14-ch63, adc2

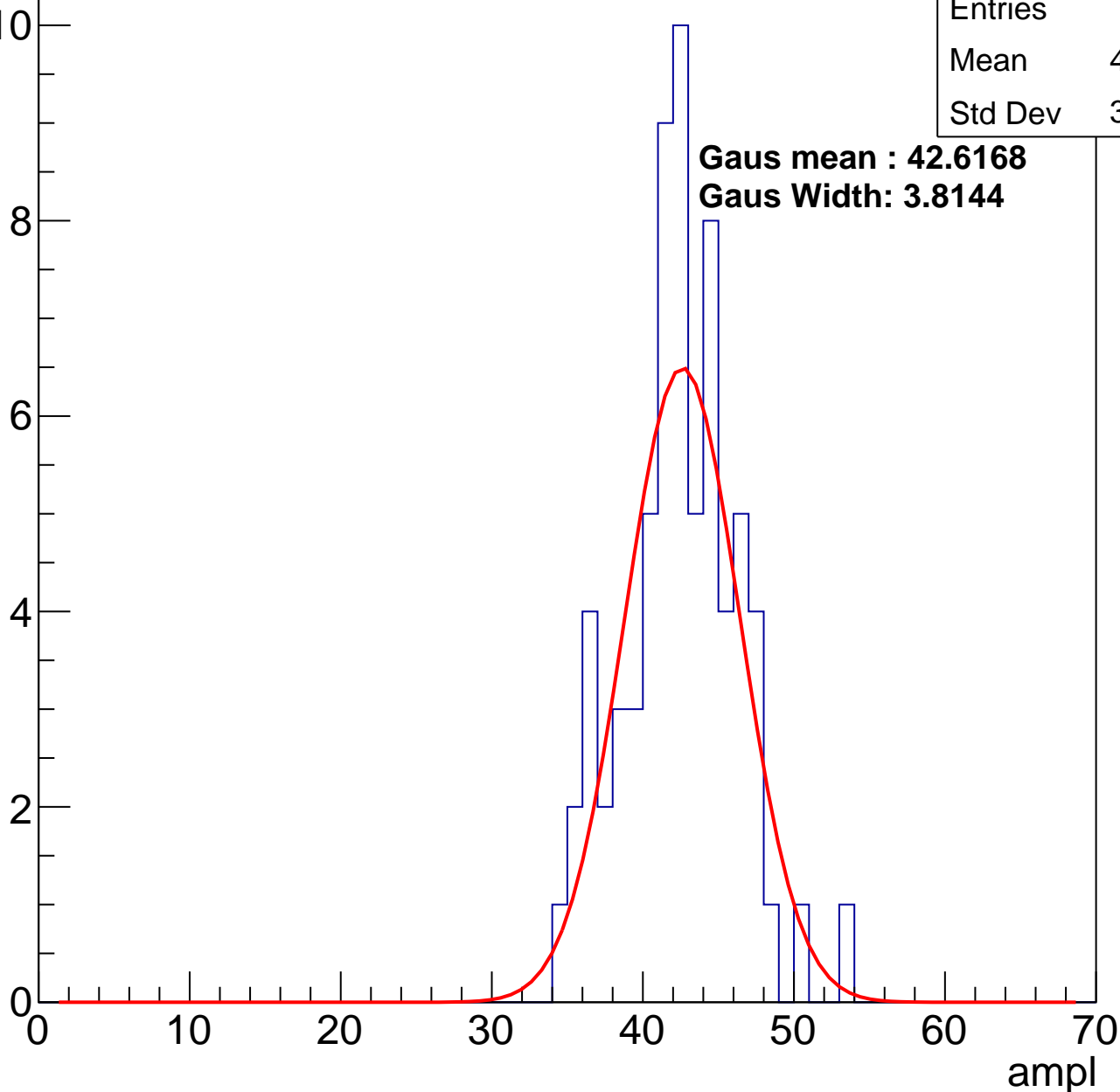
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.03
Std Dev	3.706

Gaus mean : 42.6168

Gaus Width: 3.8144

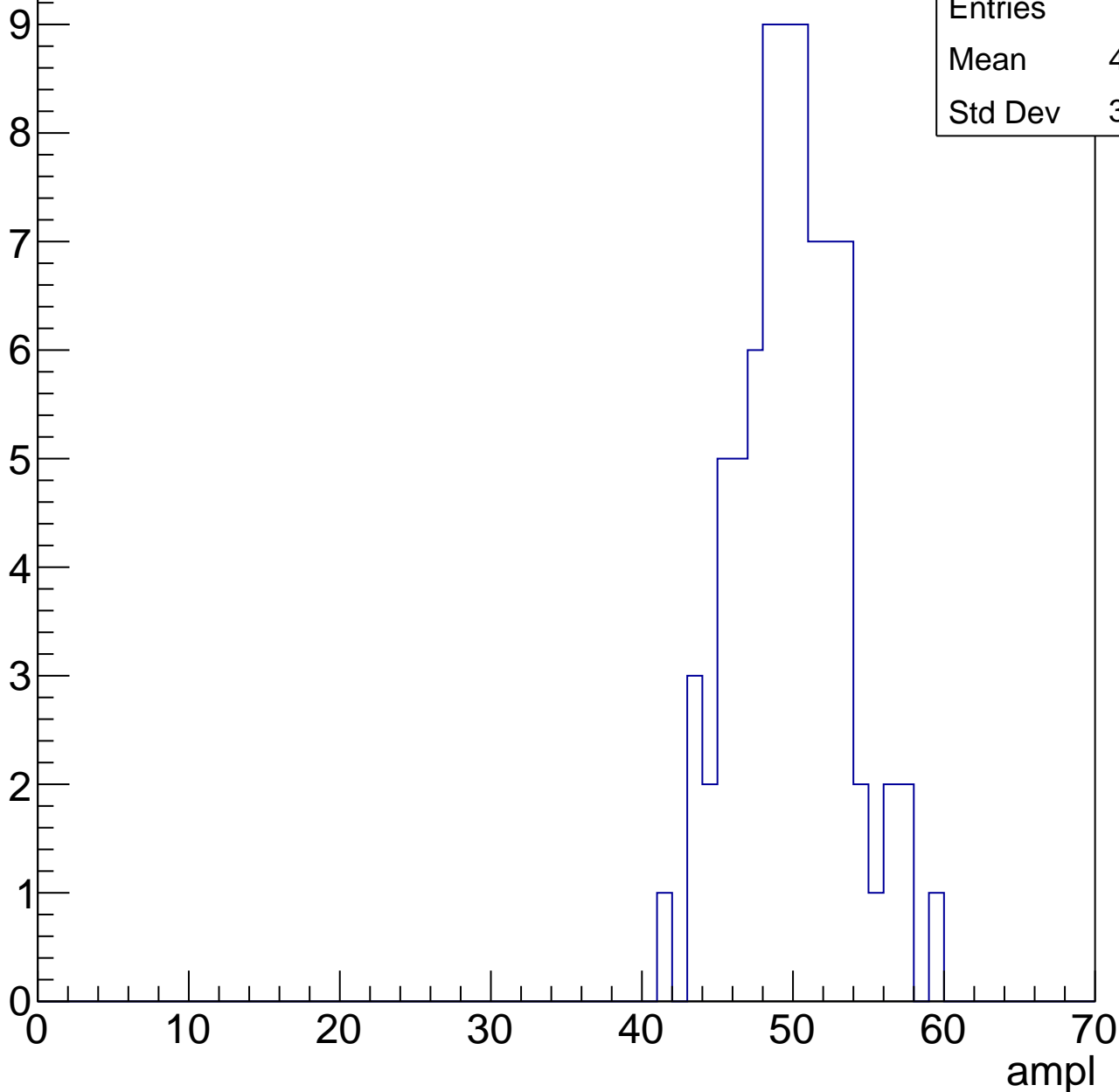


B1L101S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

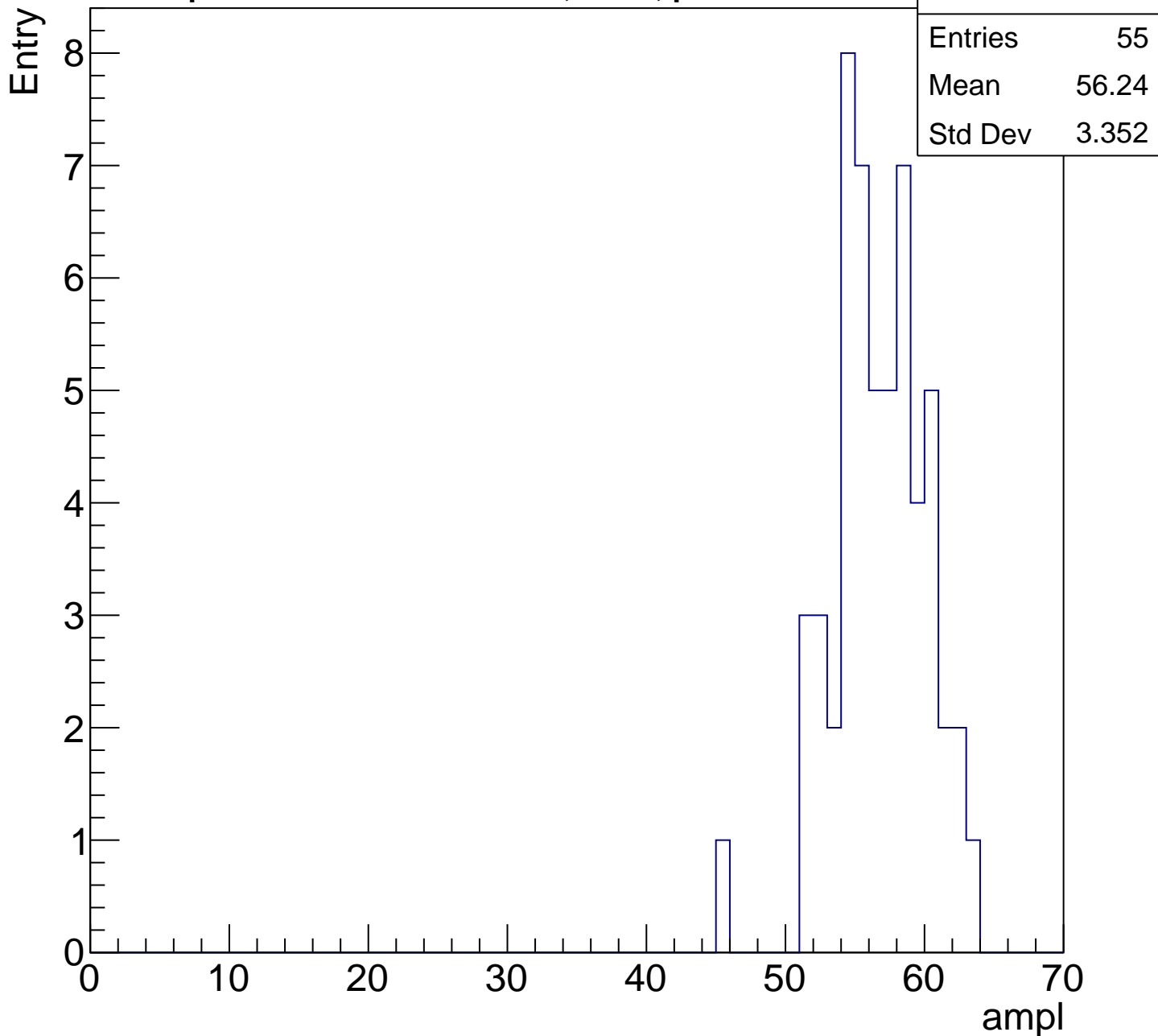
Entry

Entries	78
Mean	49.46
Std Dev	3.558



B1L101S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

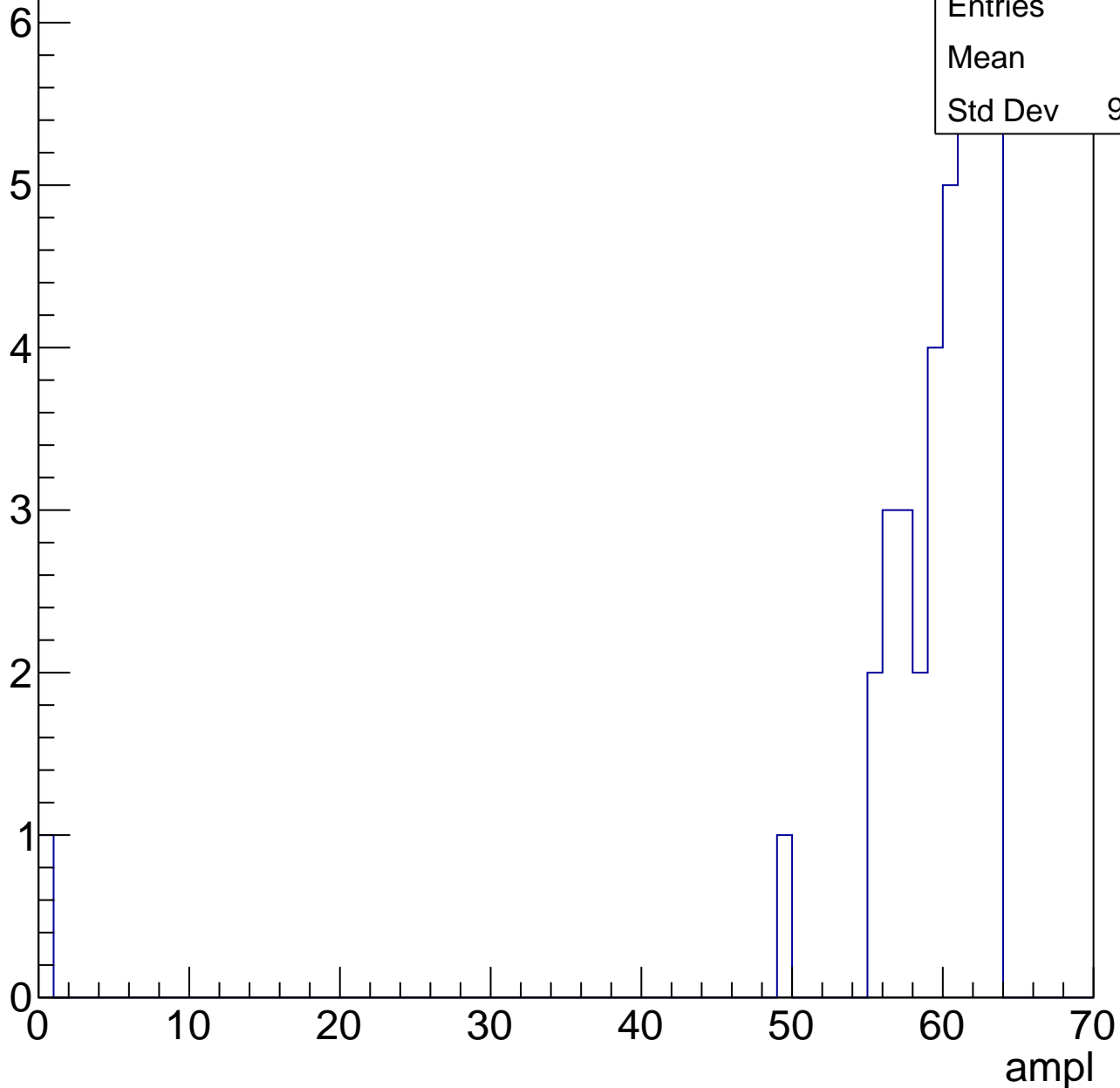


B1L101S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

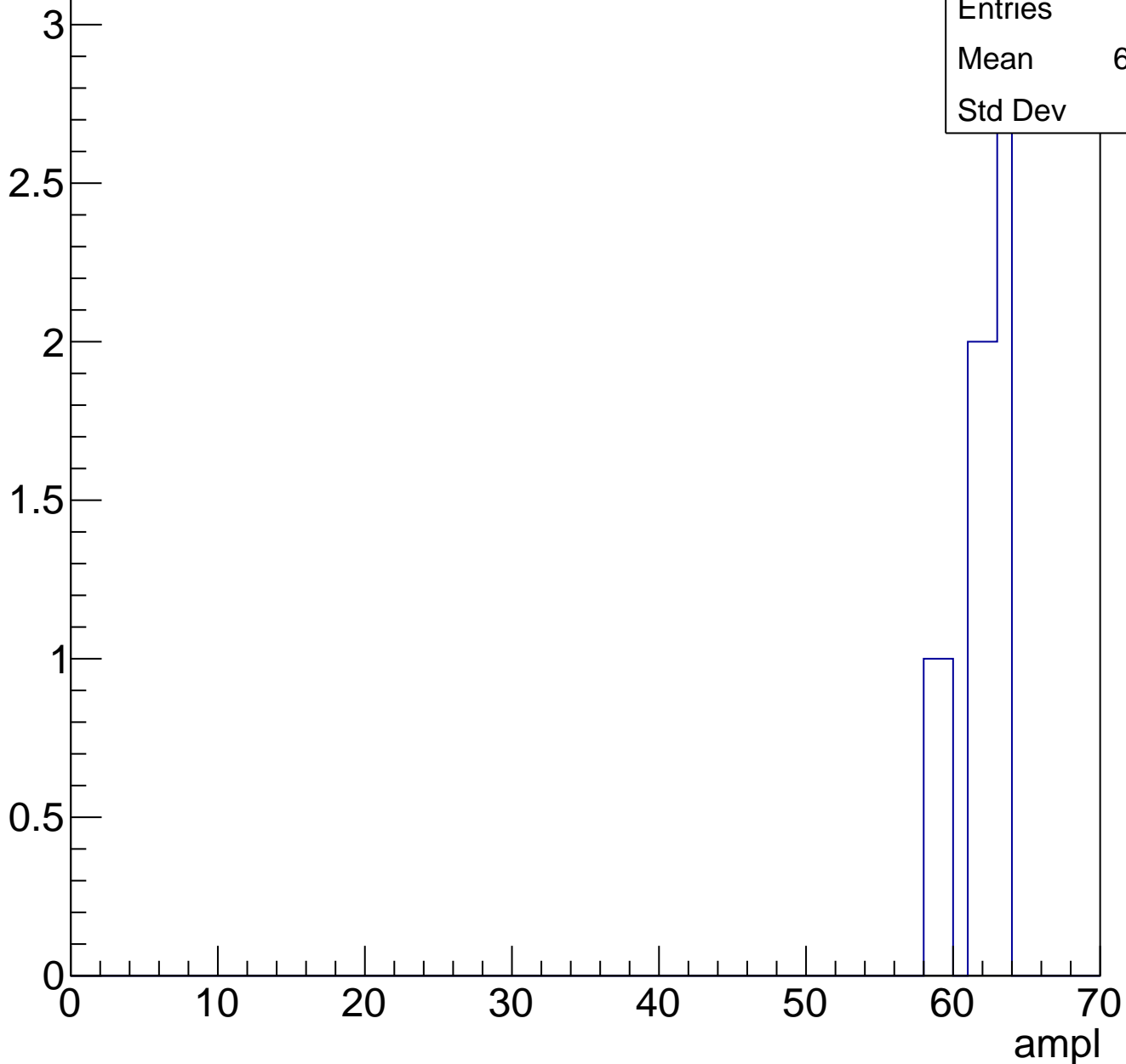
Entries	39
Mean	58.1
Std Dev	9.873



B1L101S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch64, adc0

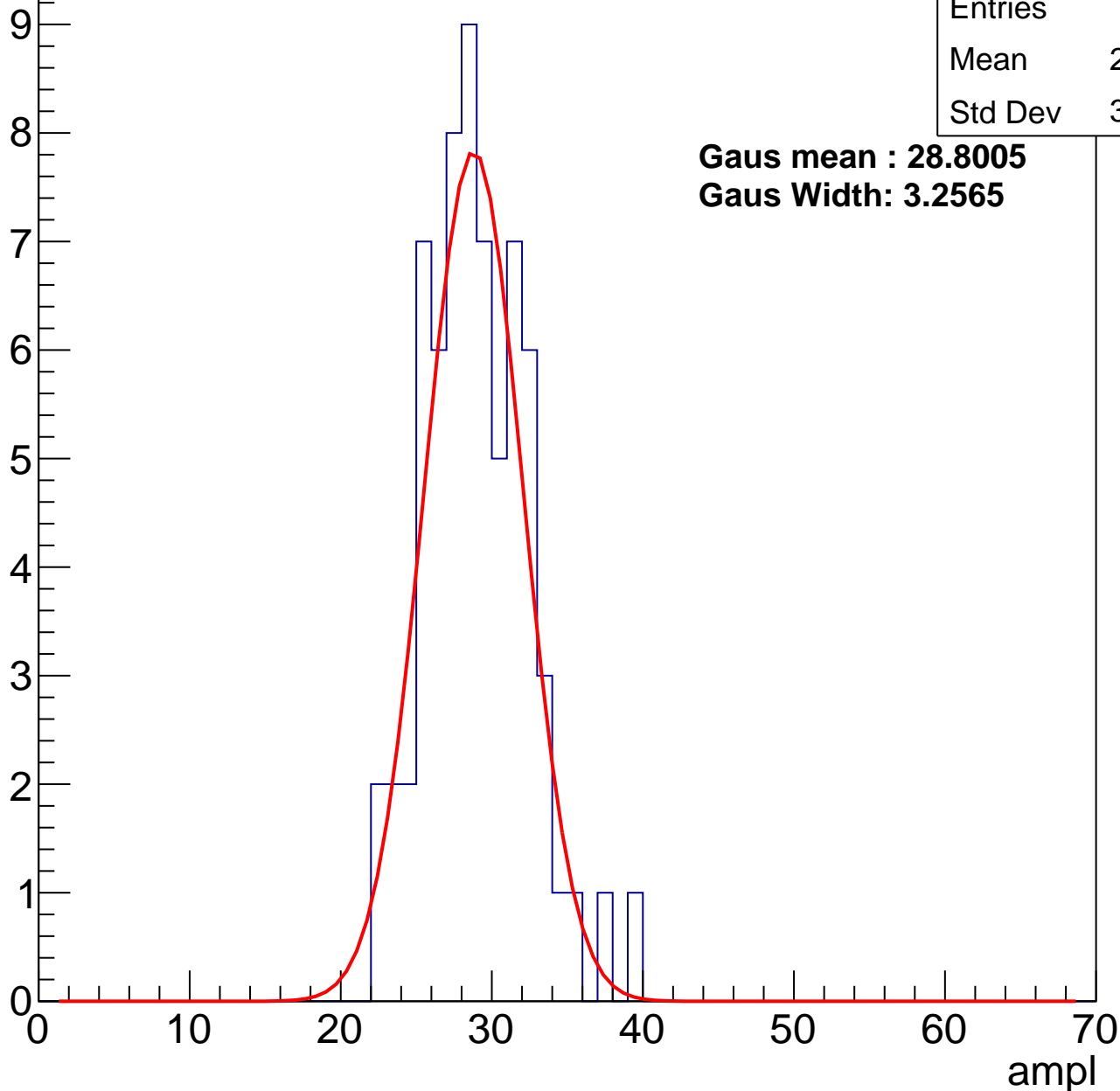
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.57
Std Dev	3.375

Gaus mean : 28.8005

Gaus Width: 3.2565



B1L101S, U14-ch64, adc1

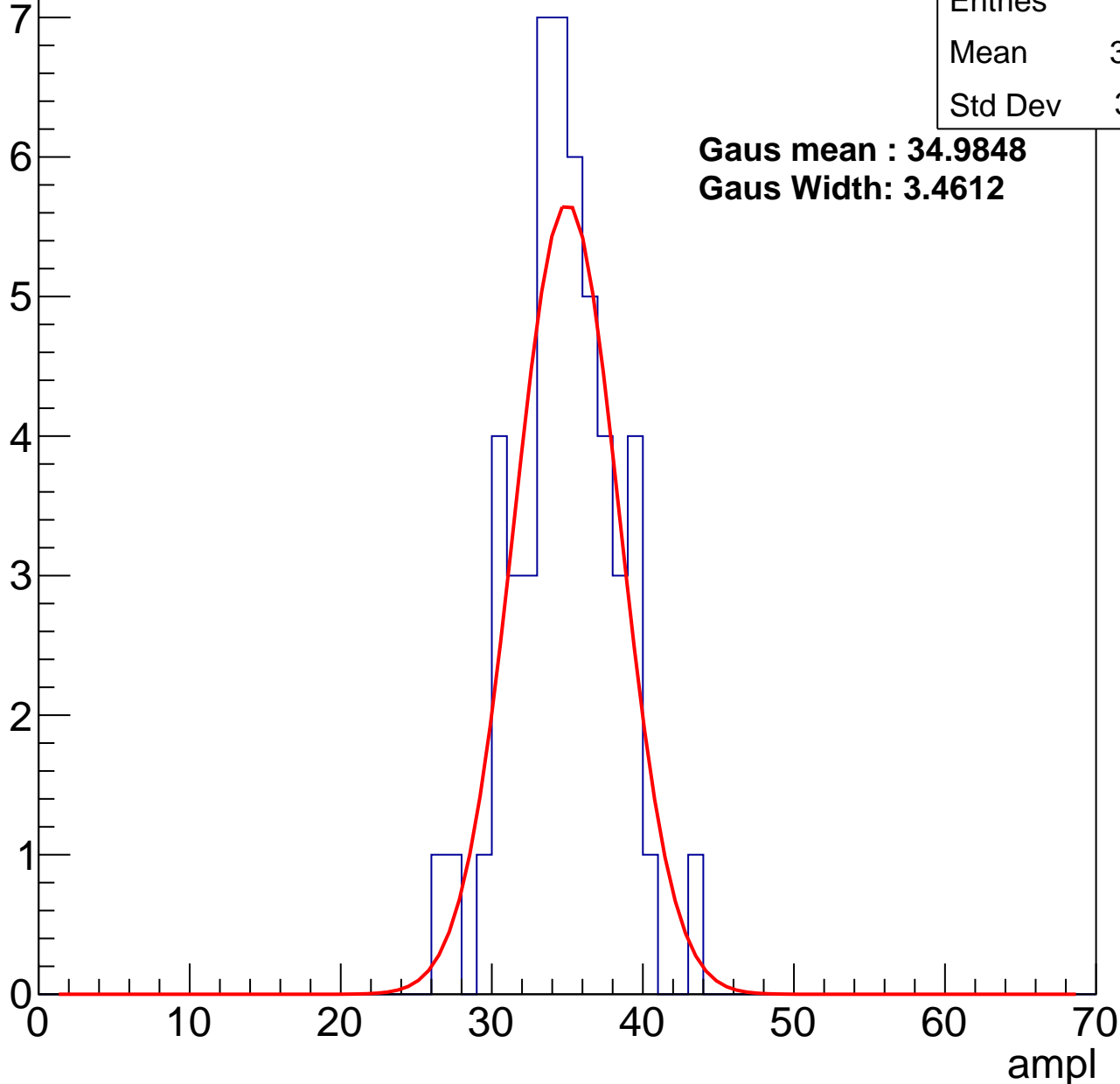
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	34.33
Std Dev	3.341

Gaus mean : 34.9848

Gaus Width: 3.4612



B1L101S, U14-ch64, adc2

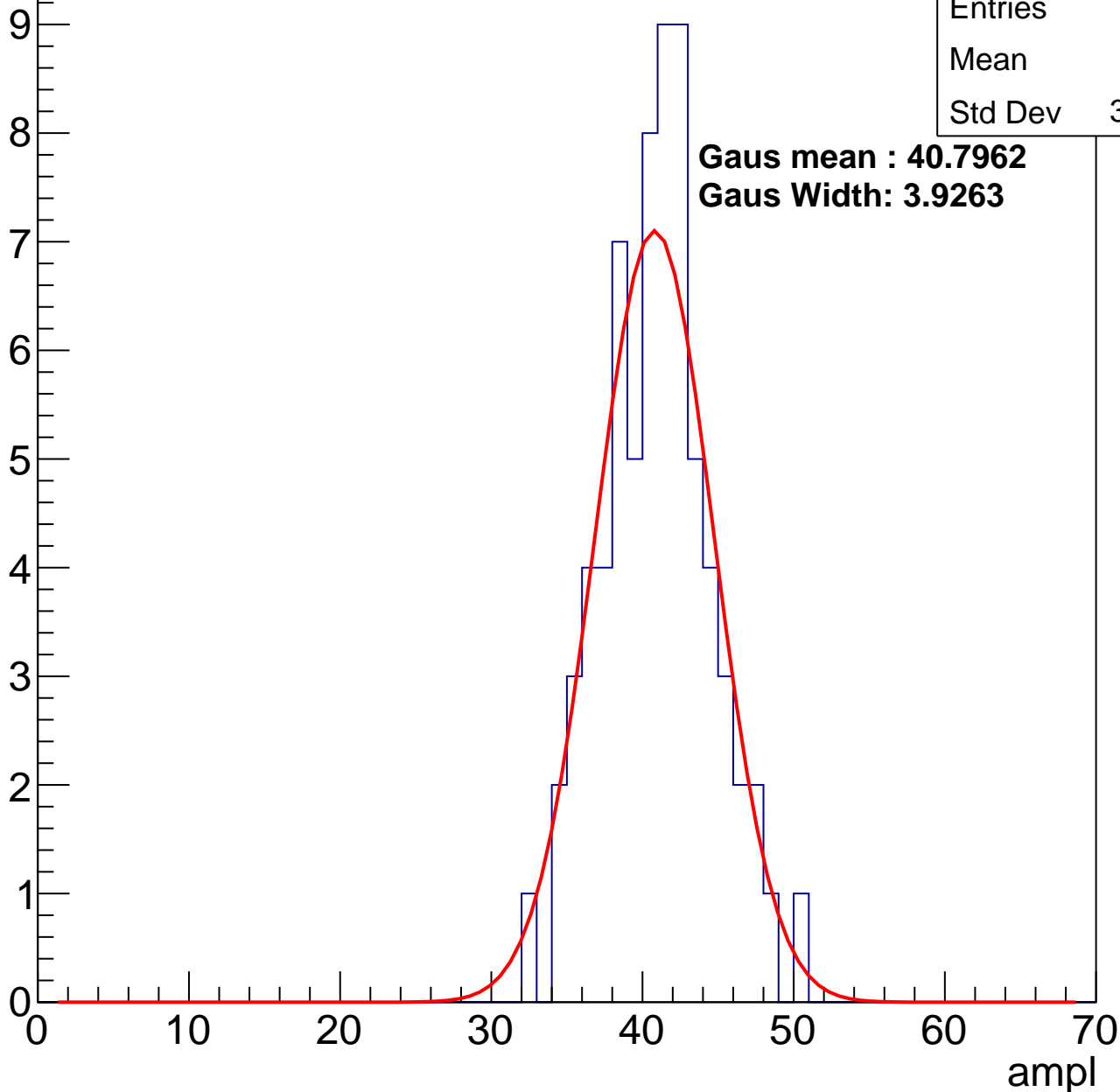
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	40.5
Std Dev	3.565

Gaus mean : 40.7962

Gaus Width: 3.9263

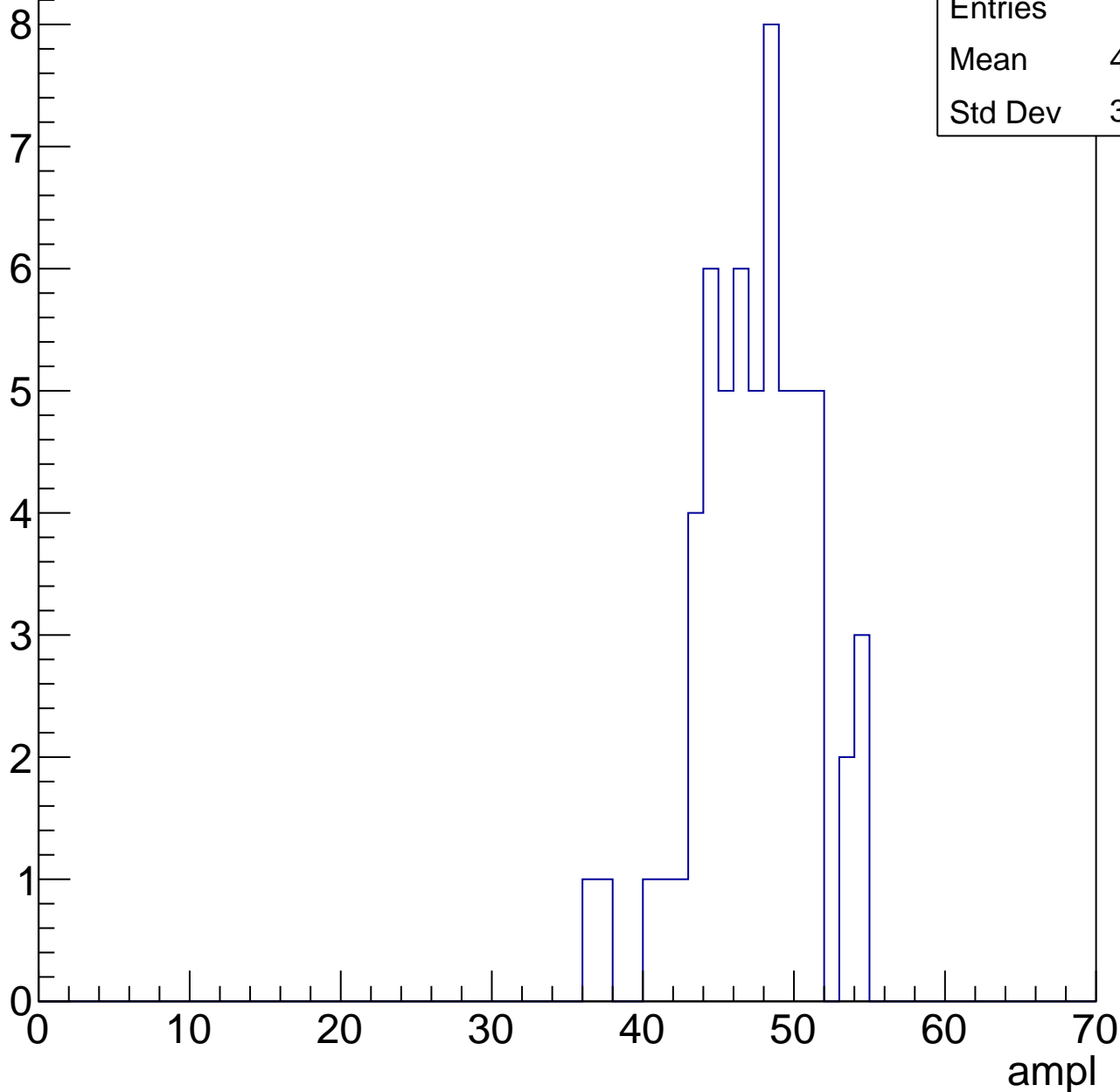


B1L101S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

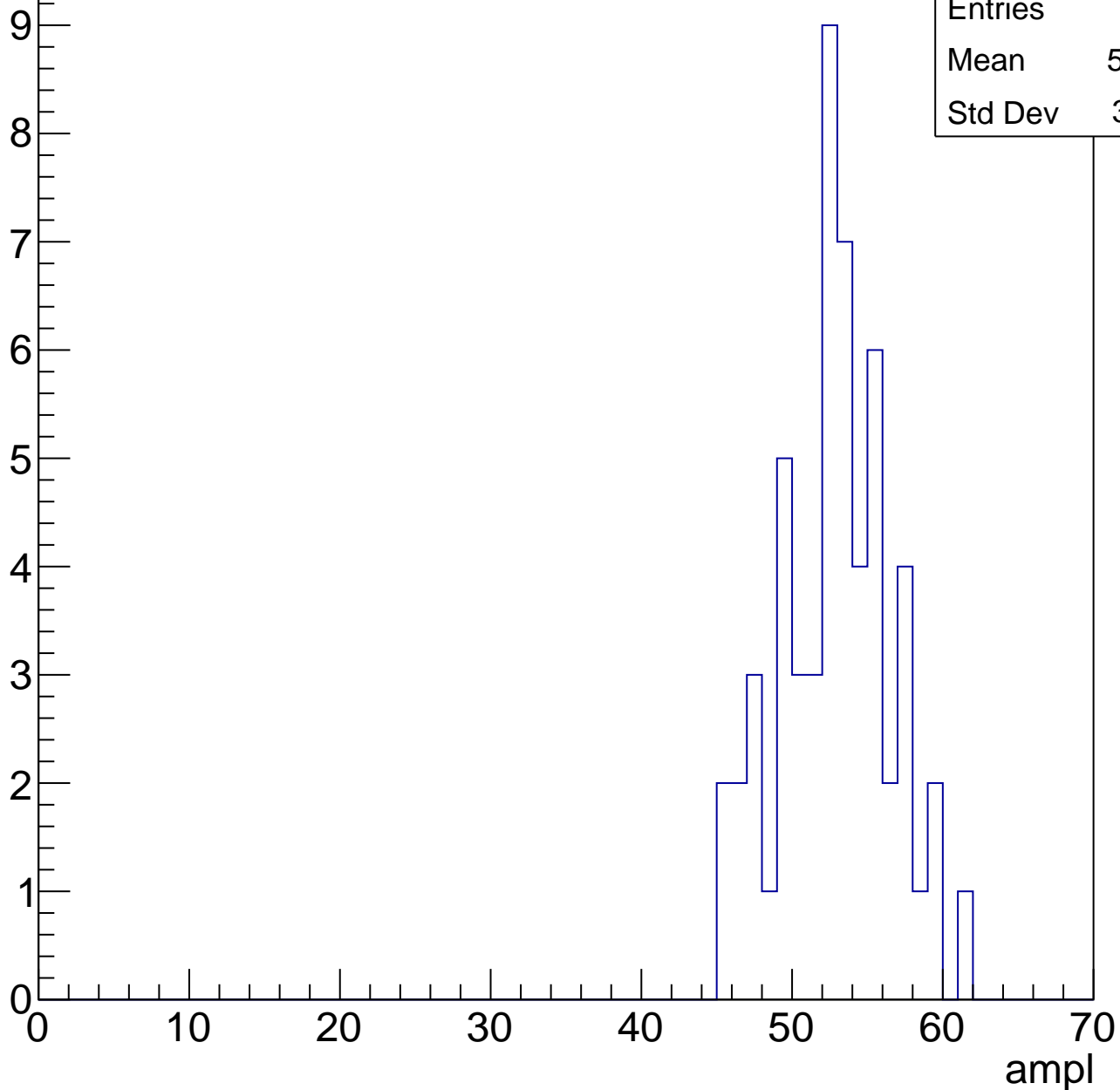
Entries	59
Mean	46.95
Std Dev	3.789



B1L101S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

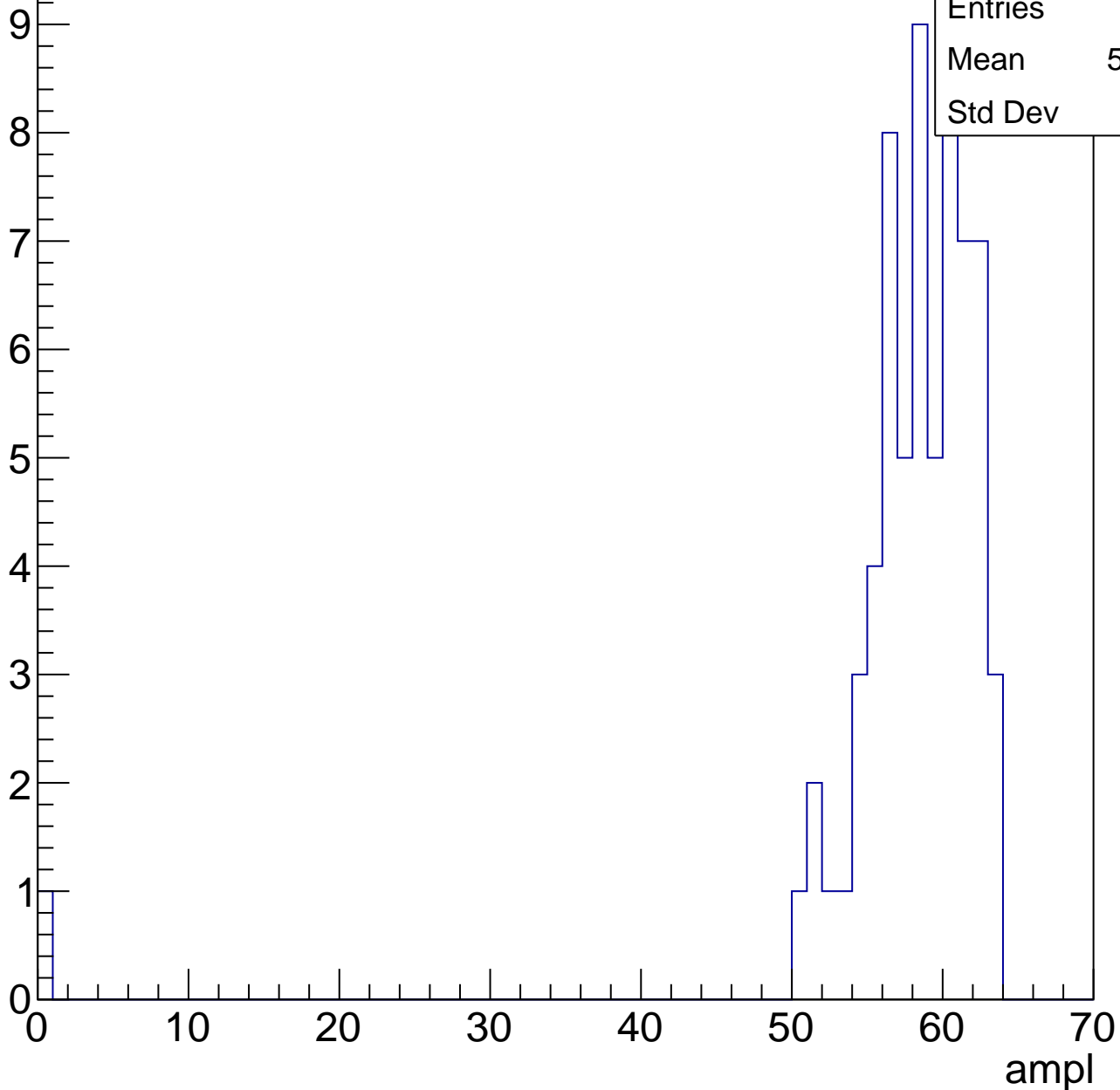


Entries	55
Mean	52.38
Std Dev	3.661

B1L101S, U14-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

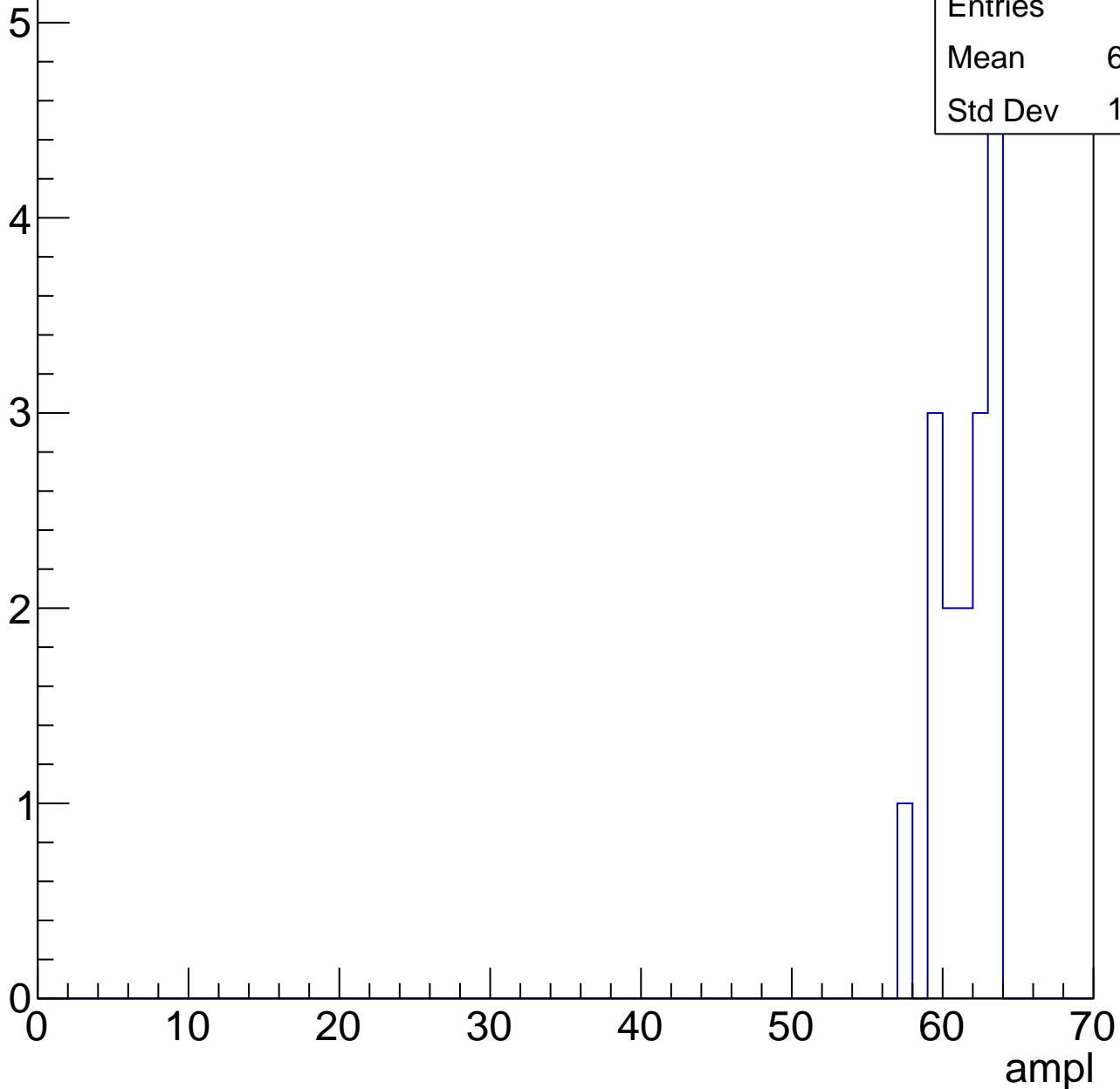


B1L101S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

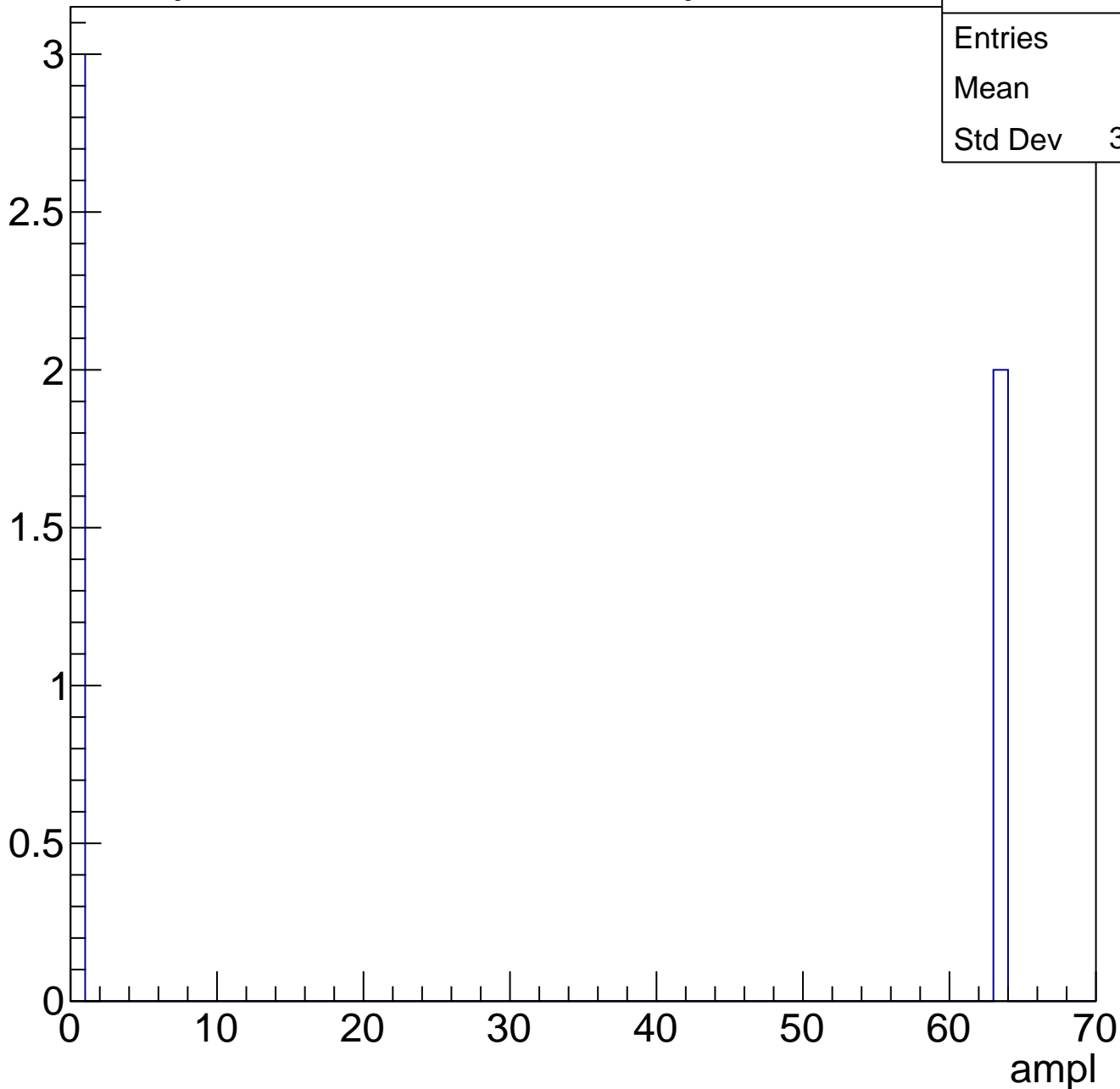
Entries	16
Mean	61.06
Std Dev	1.819



B1L101S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch65, adc0

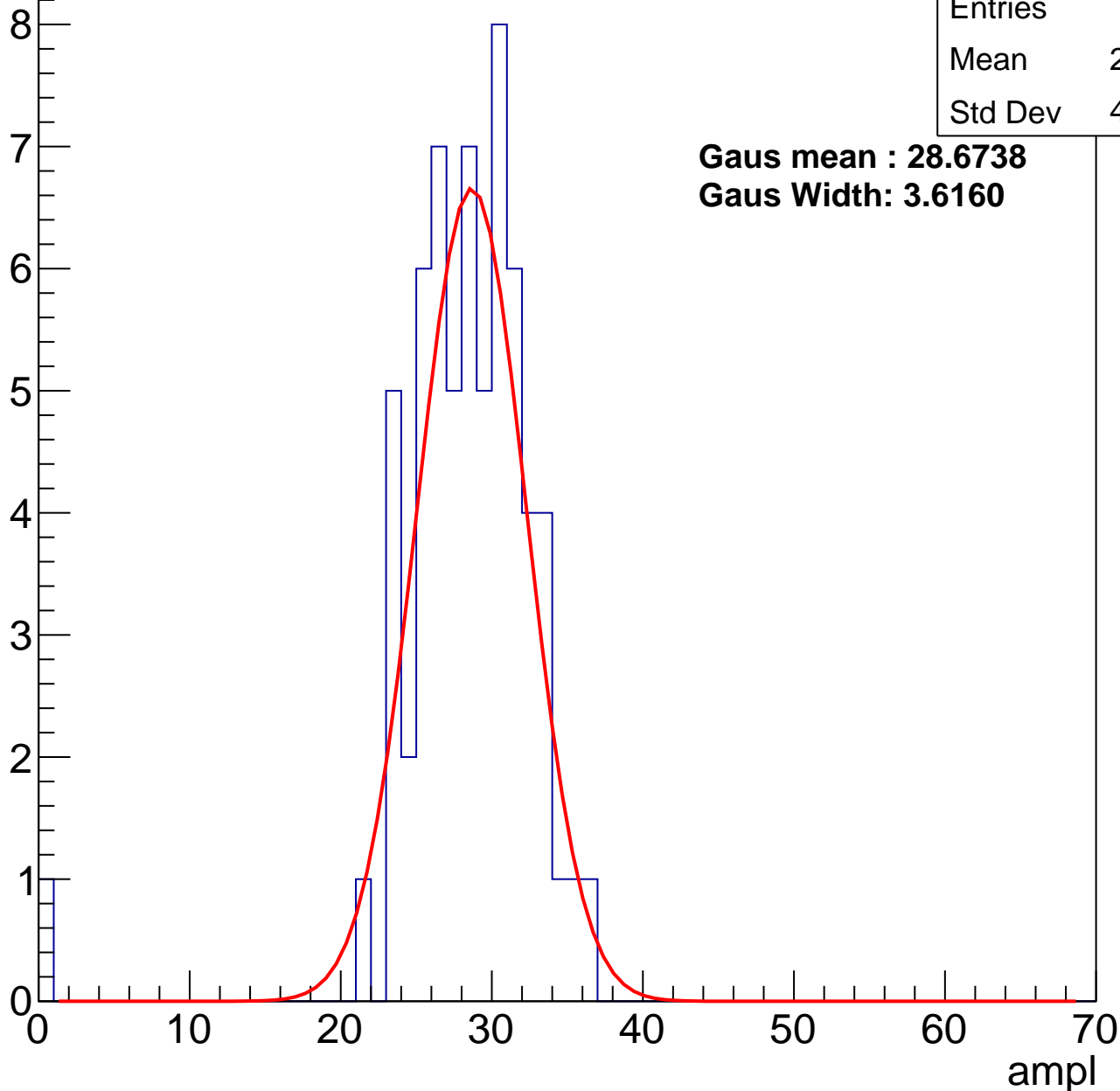
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.86
Std Dev	4.808

Gaus mean : 28.6738

Gaus Width: 3.6160



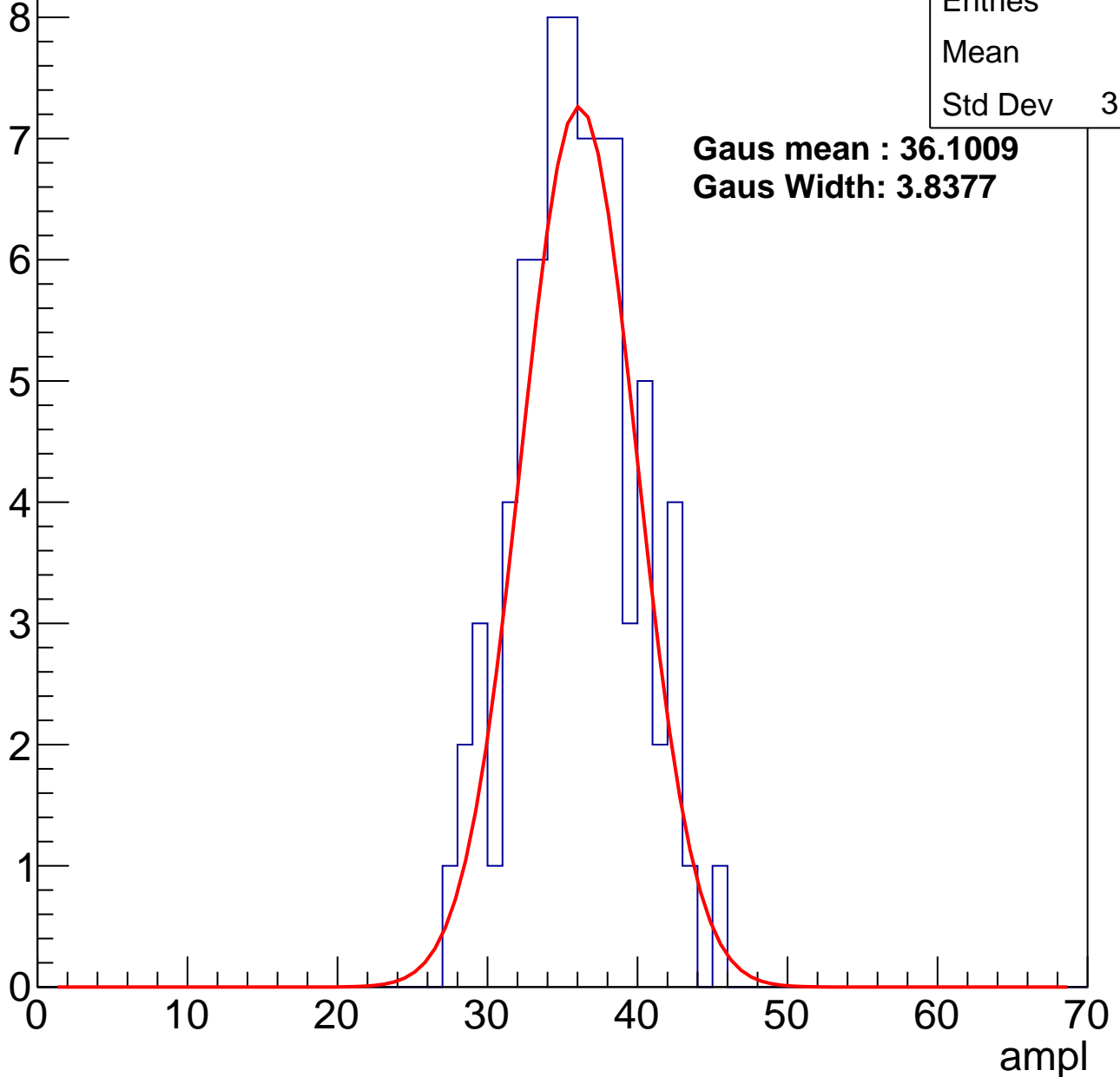
B1L101S, U14-ch65, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.5
Std Dev	3.854

Gaus mean : 36.1009
Gaus Width: 3.8377



B1L101S, U14-ch65, adc2

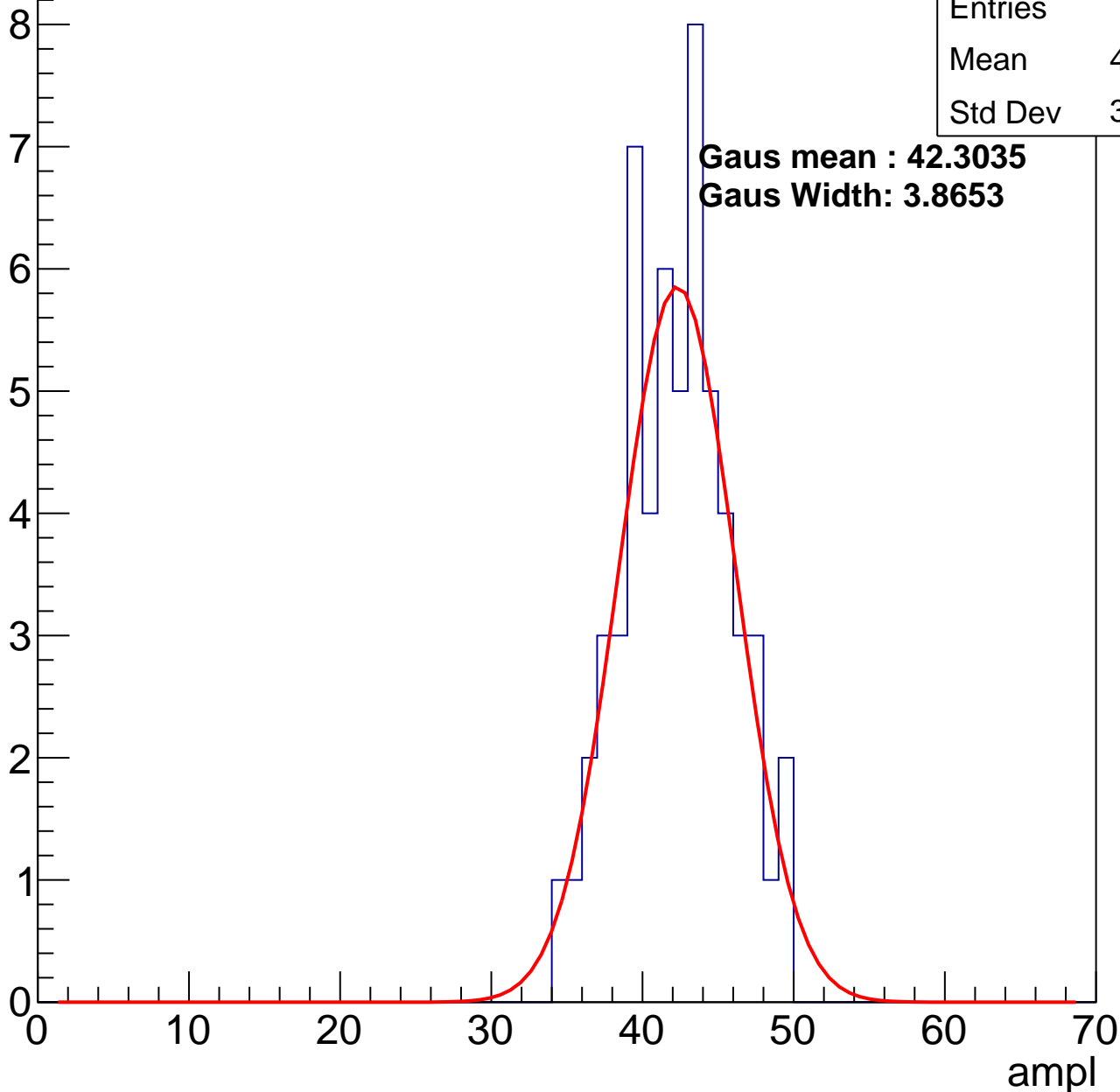
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.79
Std Dev	3.503

Gaus mean : 42.3035

Gaus Width: 3.8653

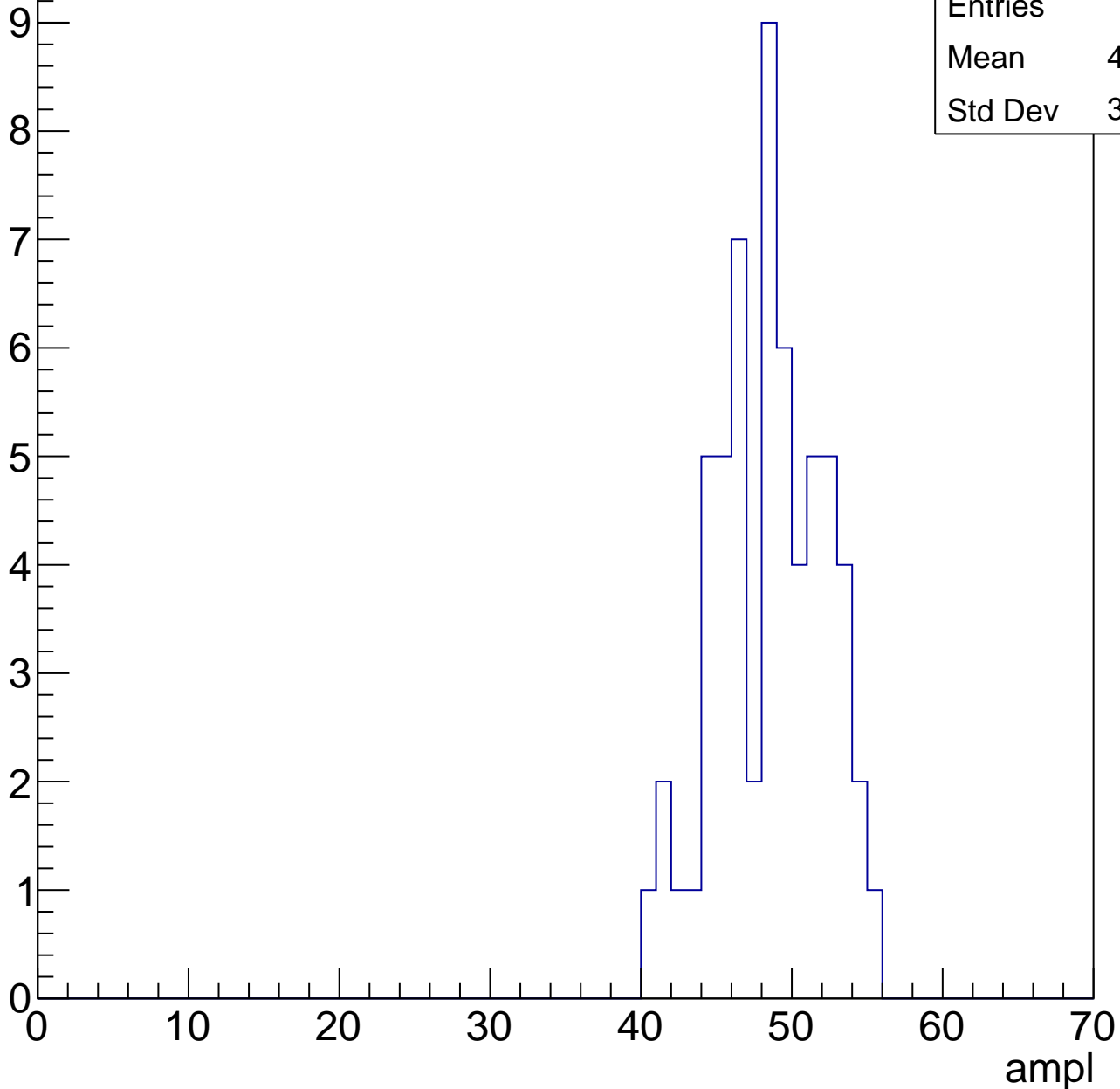


B1L101S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.07
Std Dev	3.525

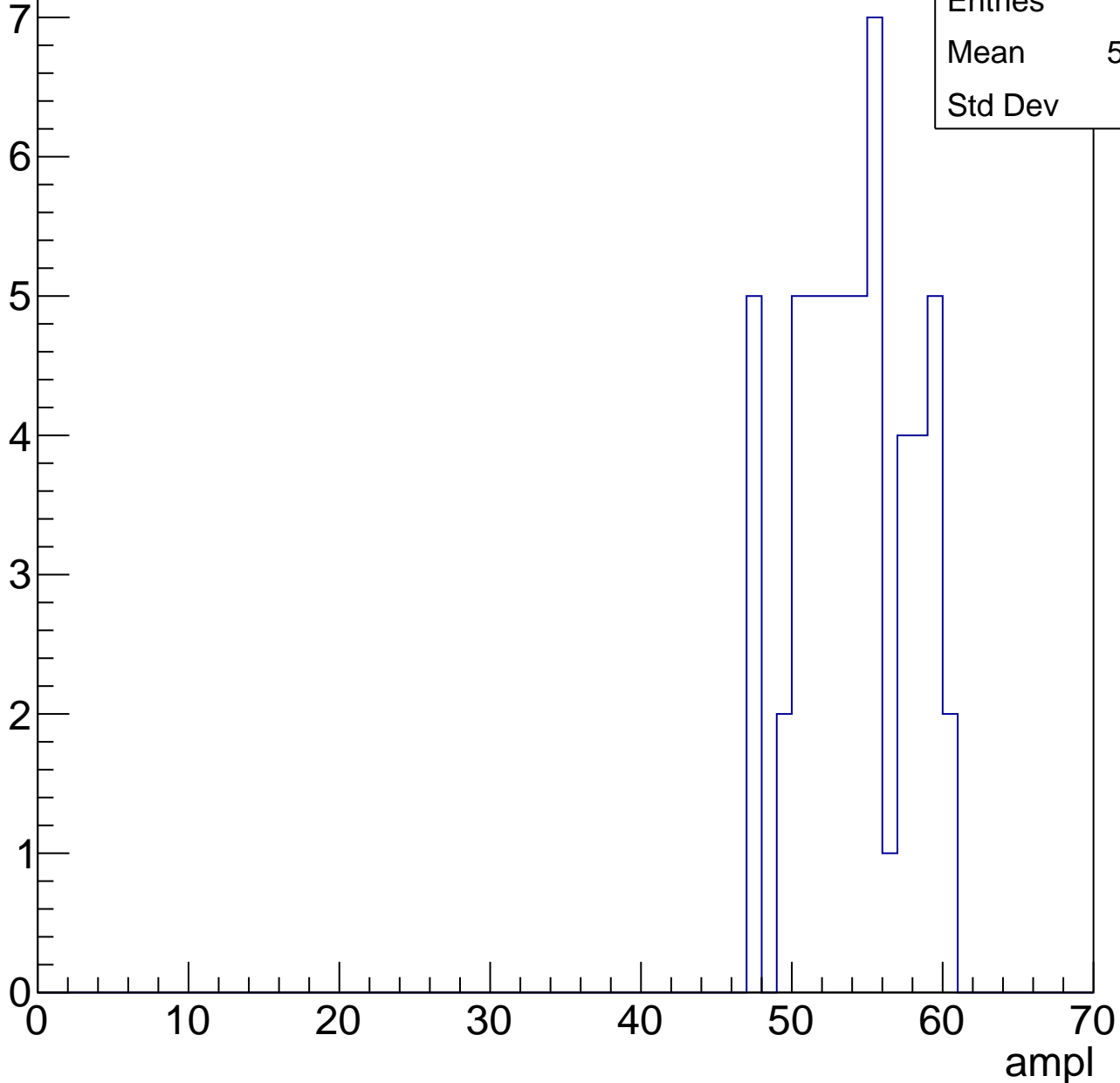


B1L101S, U14-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	53.62
Std Dev	3.68



B1L101S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

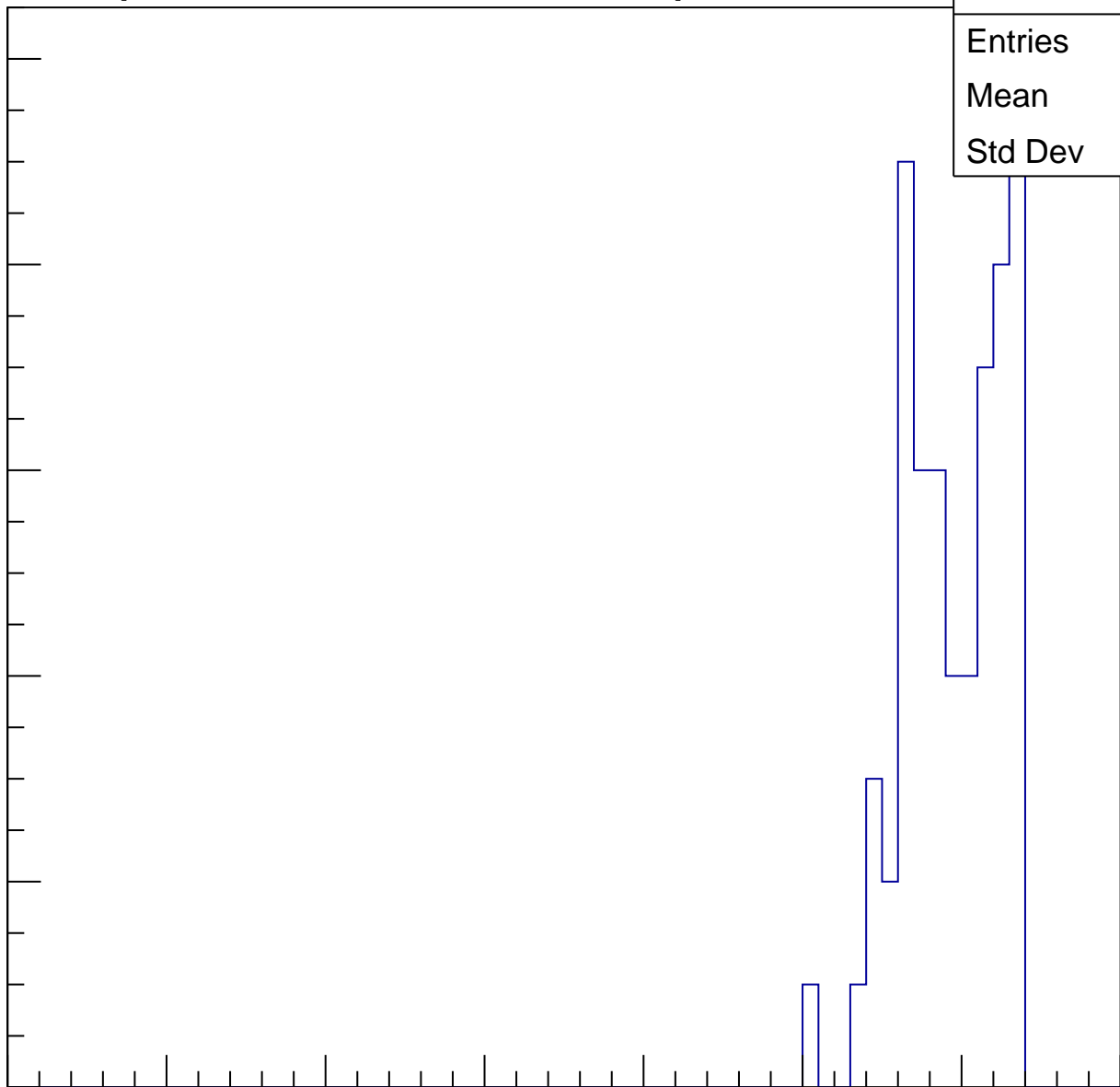
50

60

70

ampl

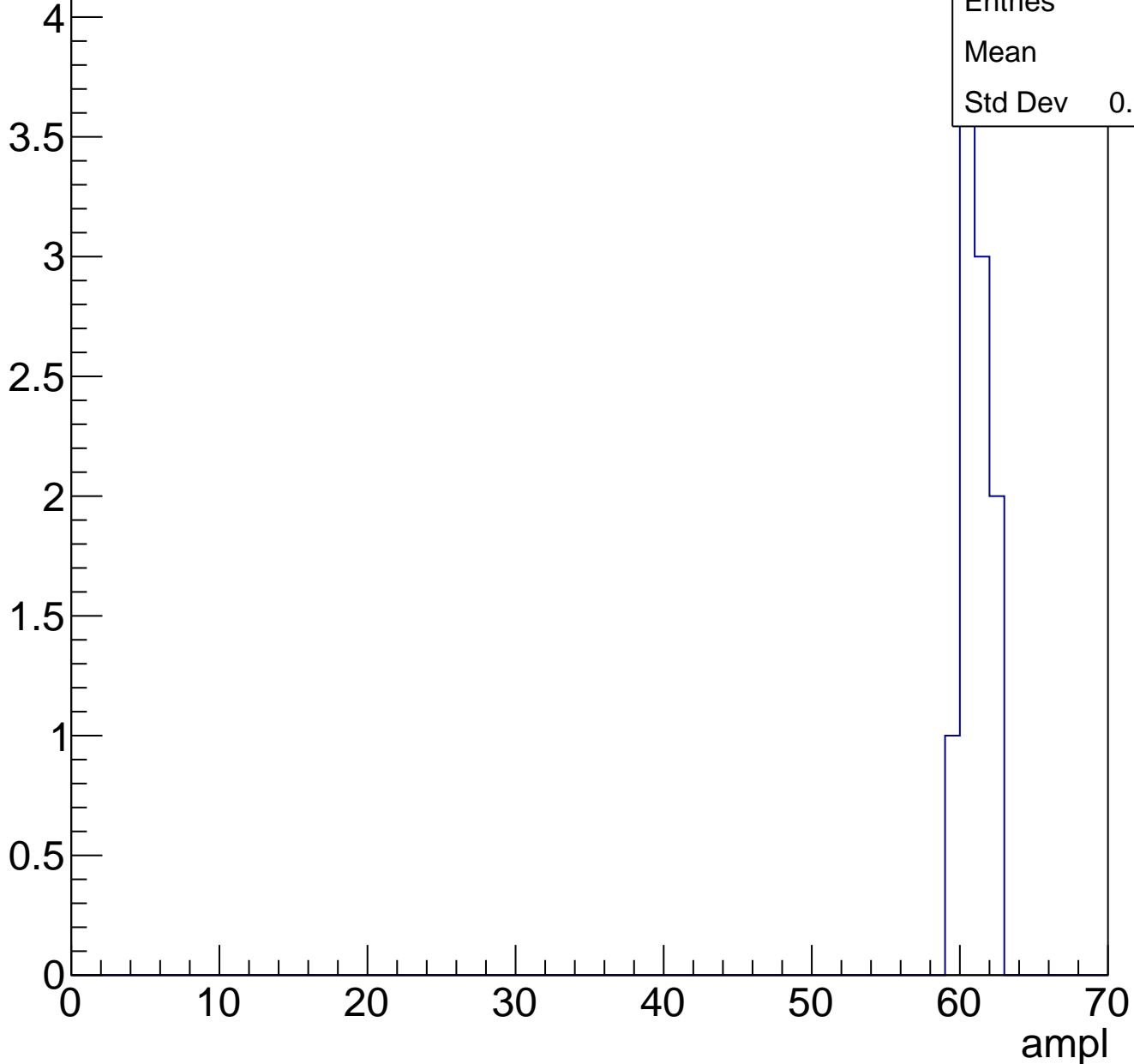
Entries	61
Mean	58.98
Std Dev	3.134



B1L101S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	60.6
Std Dev	0.9165

B1L101S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch66, adc0

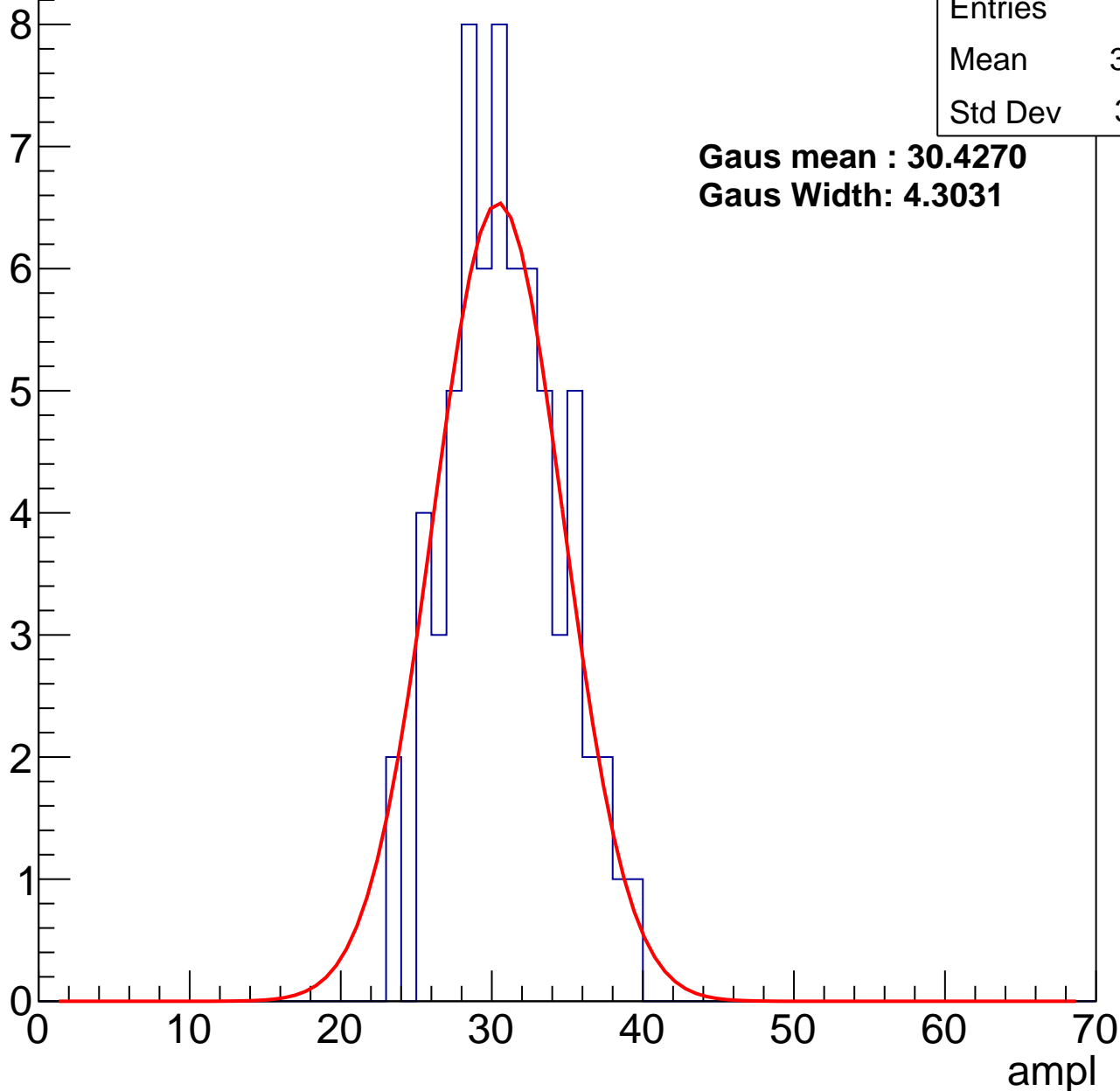
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.45
Std Dev	3.621

Gaus mean : 30.4270

Gaus Width: 4.3031



B1L101S, U14-ch66, adc1

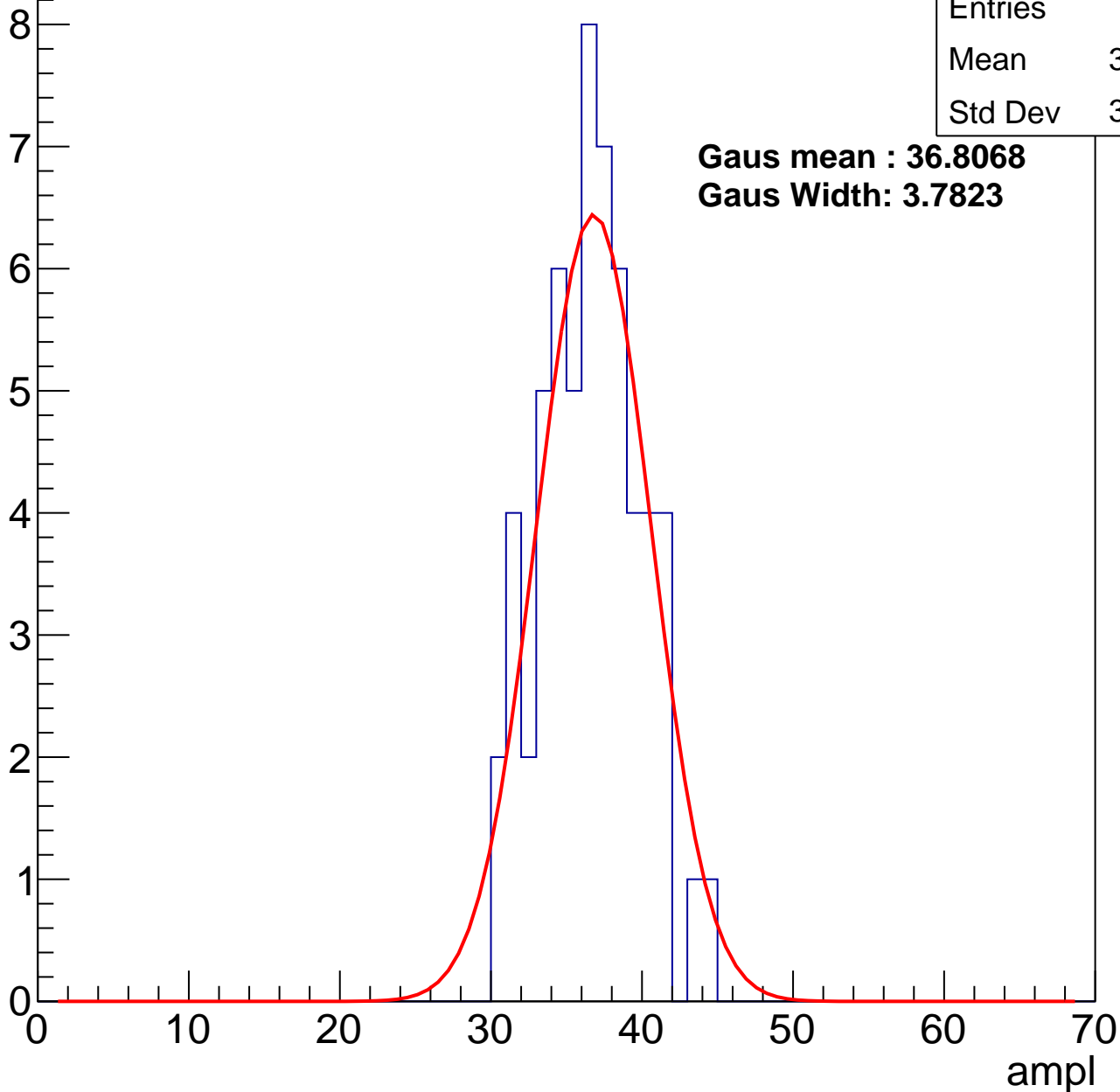
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.17
Std Dev	3.242

Gaus mean : 36.8068

Gaus Width: 3.7823



B1L101S, U14-ch66, adc2

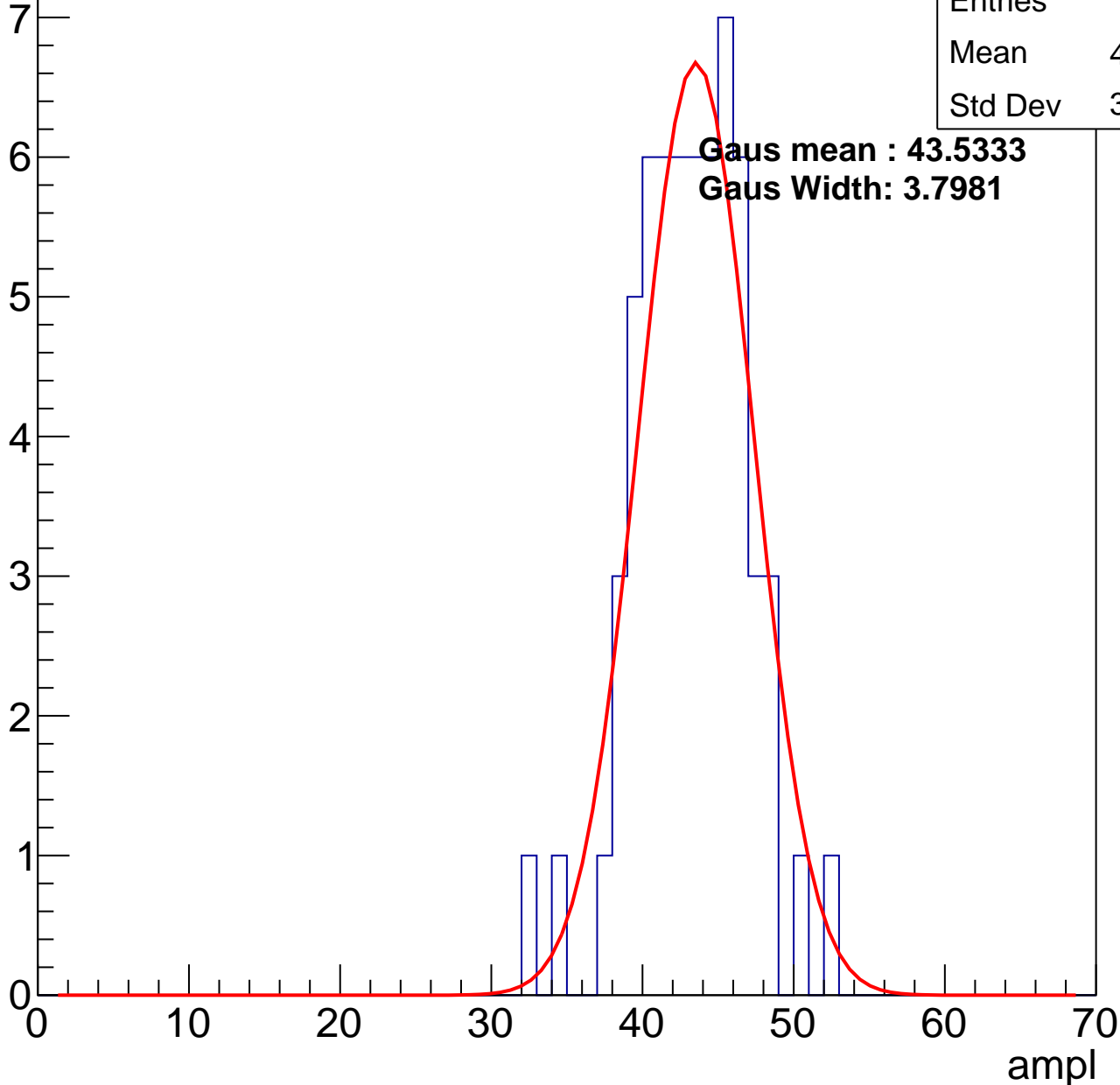
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.74
Std Dev	3.623

Gaus mean : 43.5333

Gaus Width: 3.7981

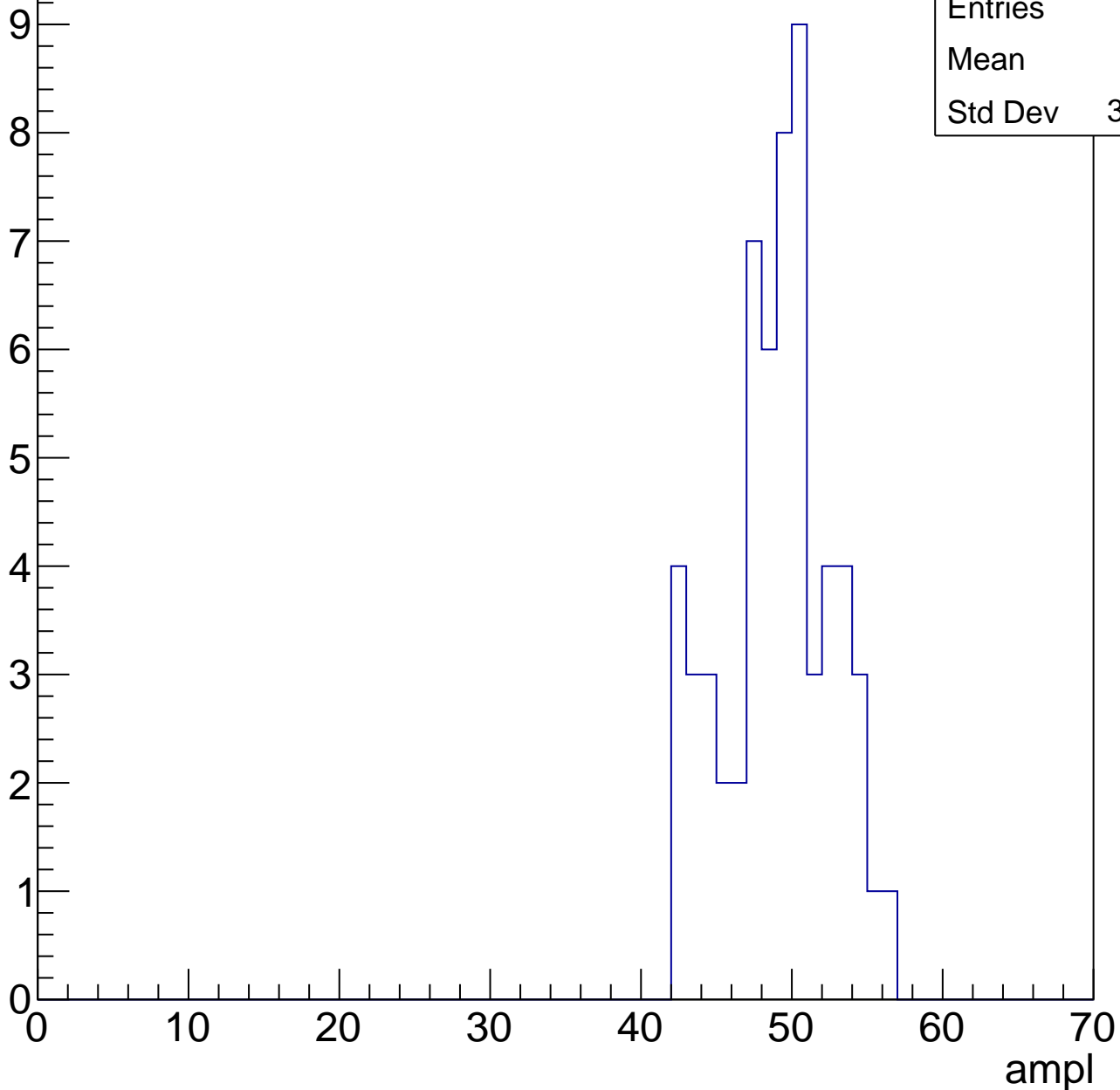


B1L101S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.6
Std Dev	3.499

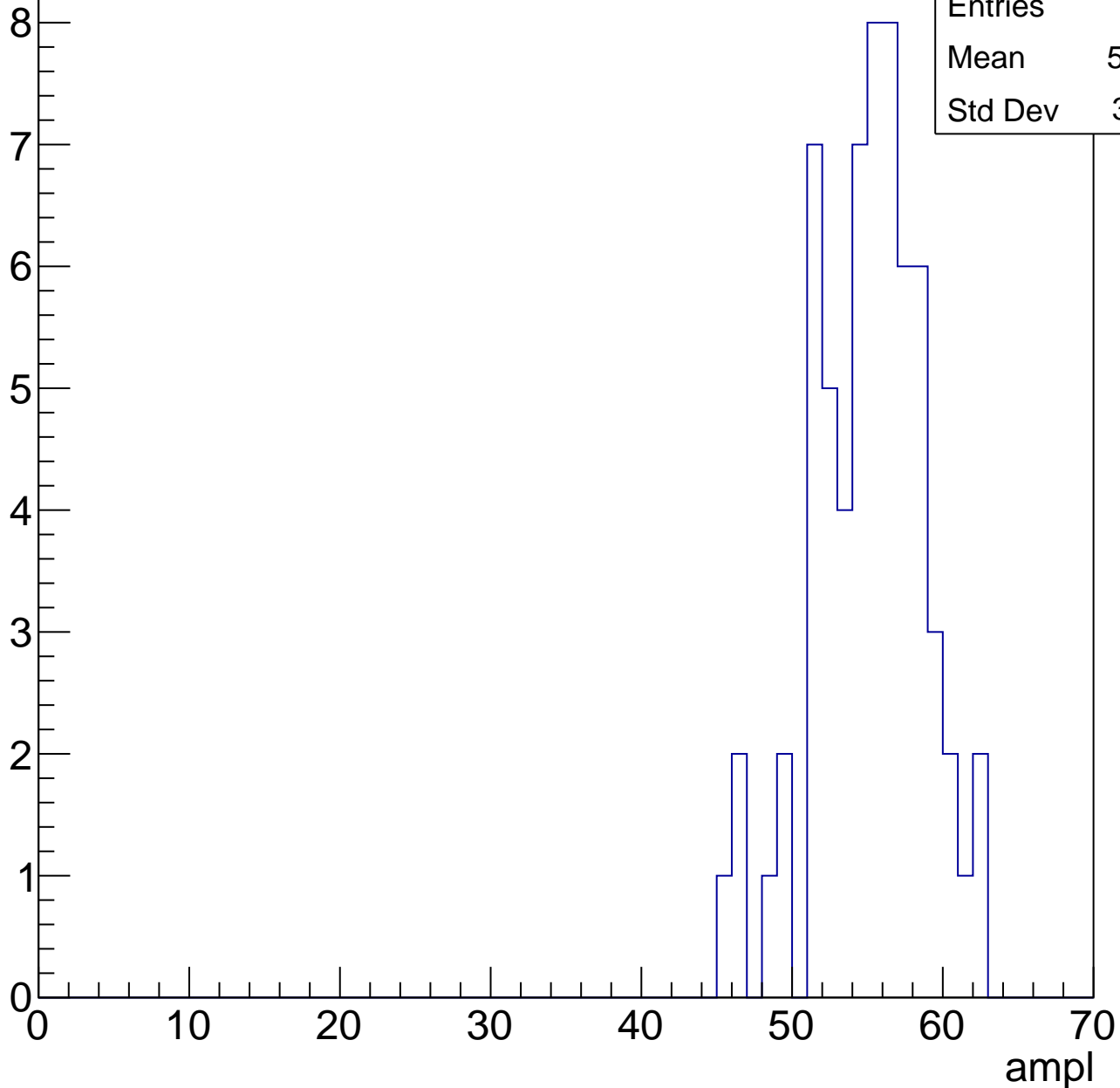


B1L101S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

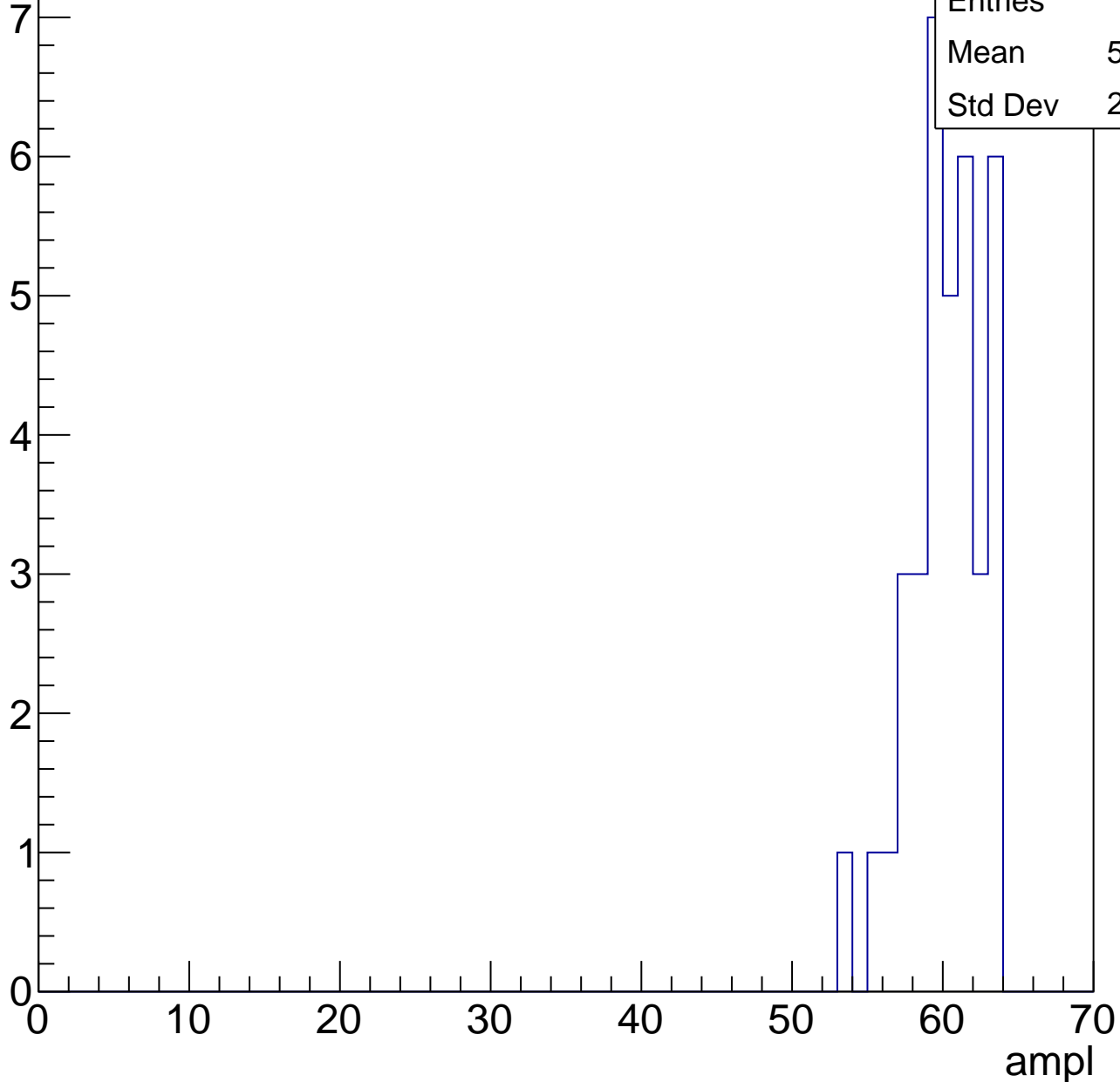
Entries	65
Mean	54.62
Std Dev	3.661



B1L101S, U14-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

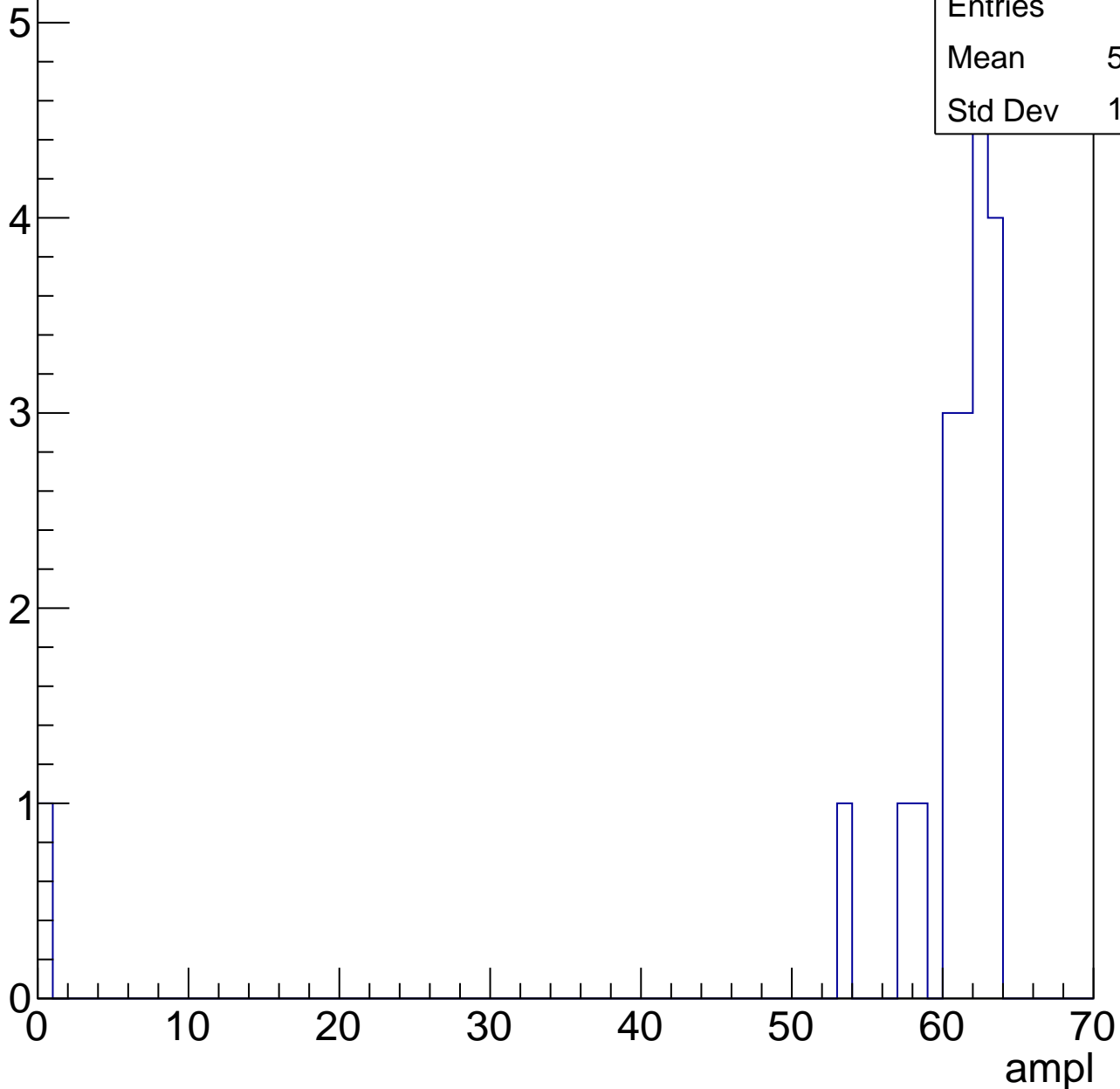


B1L101S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	57.53
Std Dev	13.77



B1L101S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U14-ch67, adc0

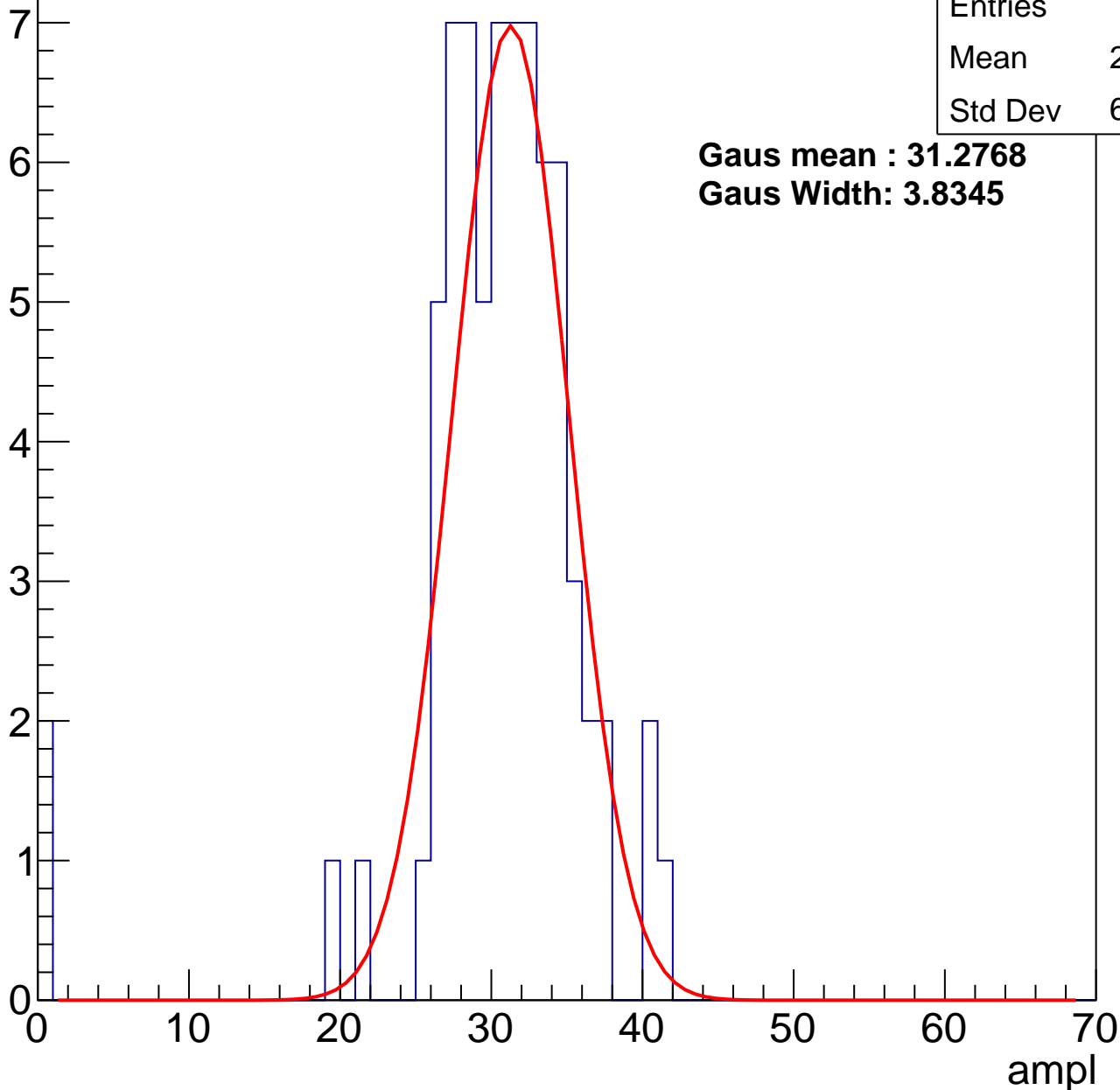
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.86
Std Dev	6.404

Gaus mean : 31.2768

Gaus Width: 3.8345



B1L101S, U14-ch67, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	38.12
Std Dev	4.013

Gaus mean : 38.4265

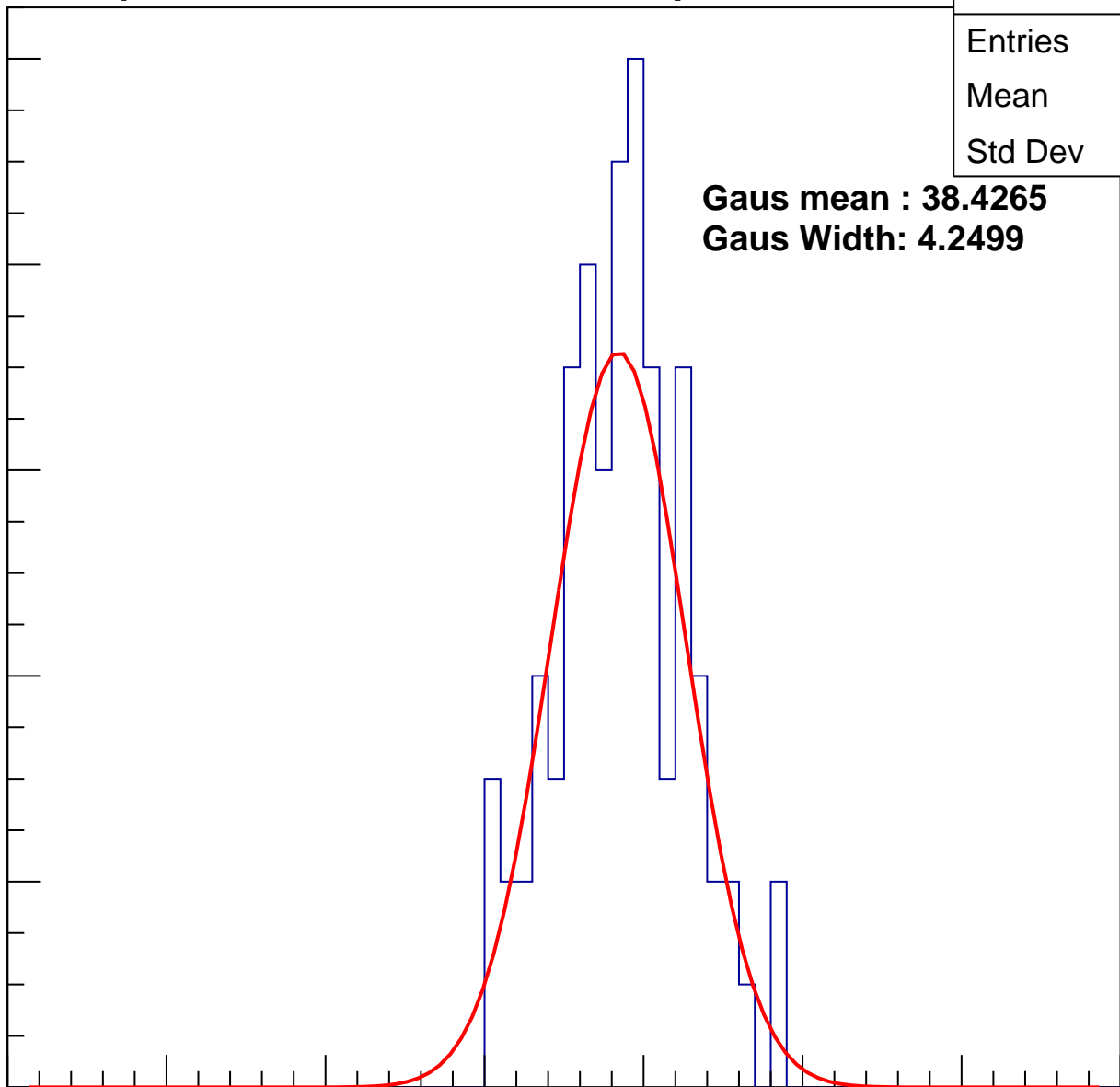
Gaus Width: 4.2499

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch67, adc2

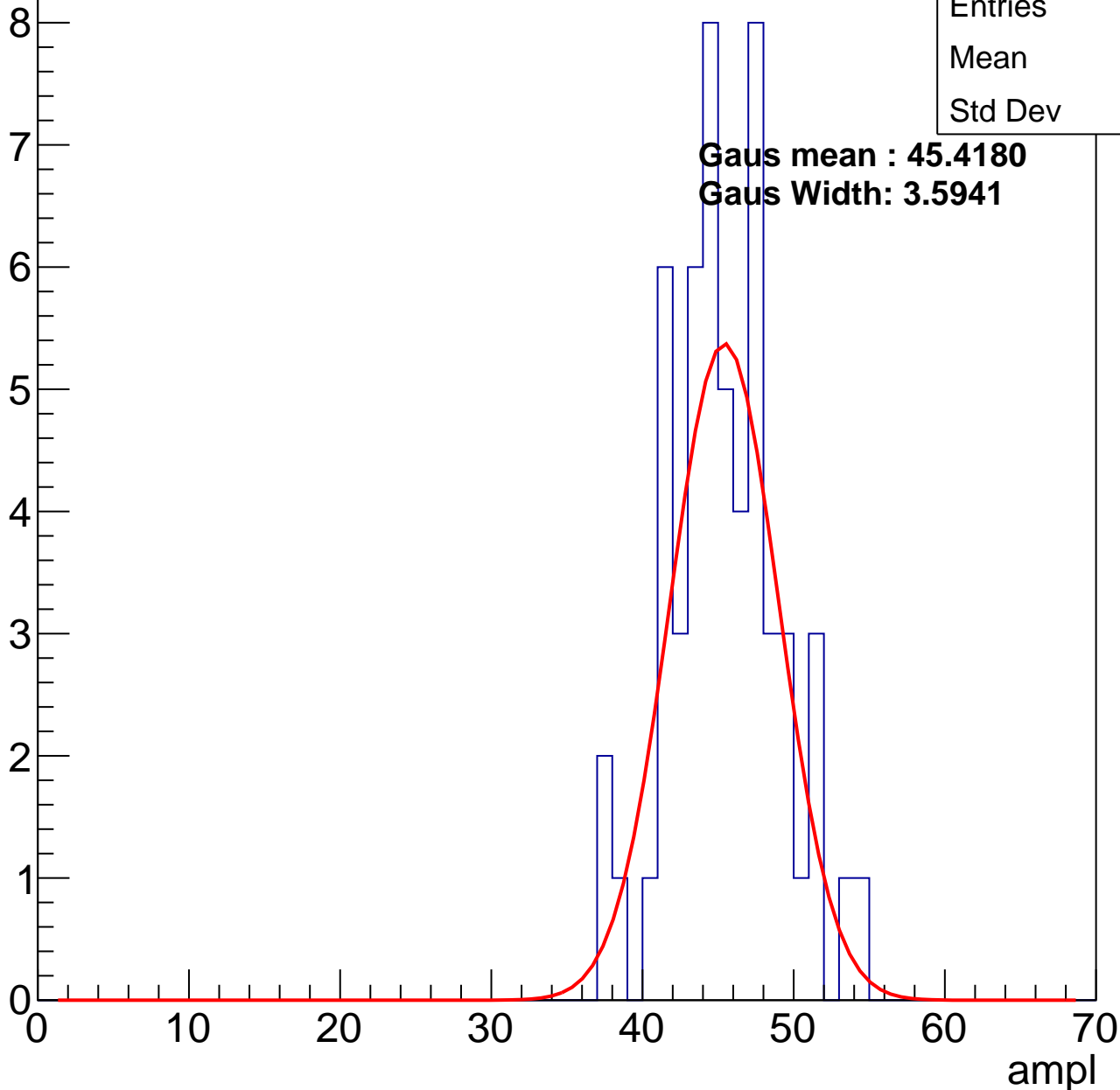
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	45
Std Dev	3.65

Gaus mean : 45.4180

Gaus Width: 3.5941

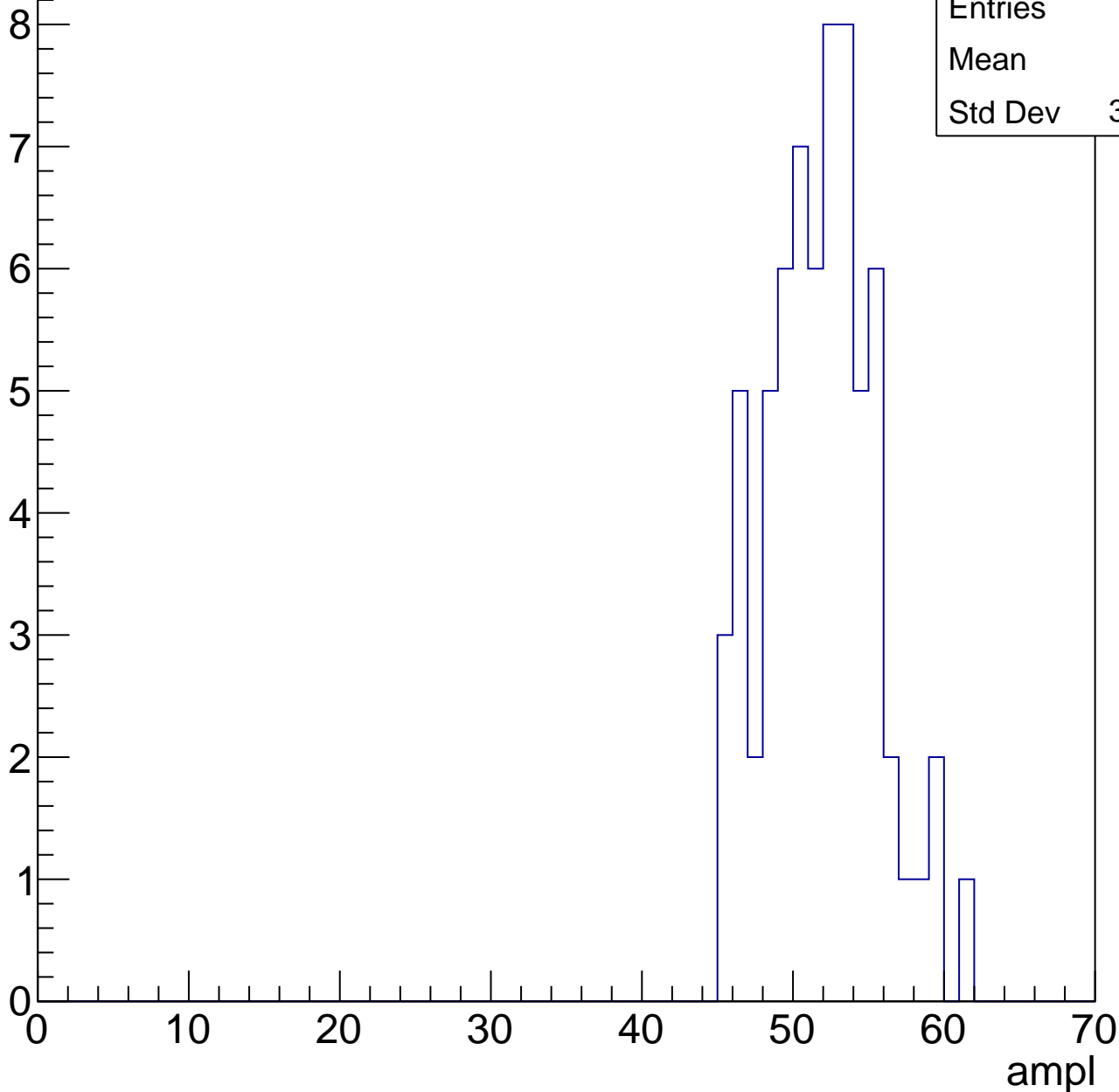


B1L101S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

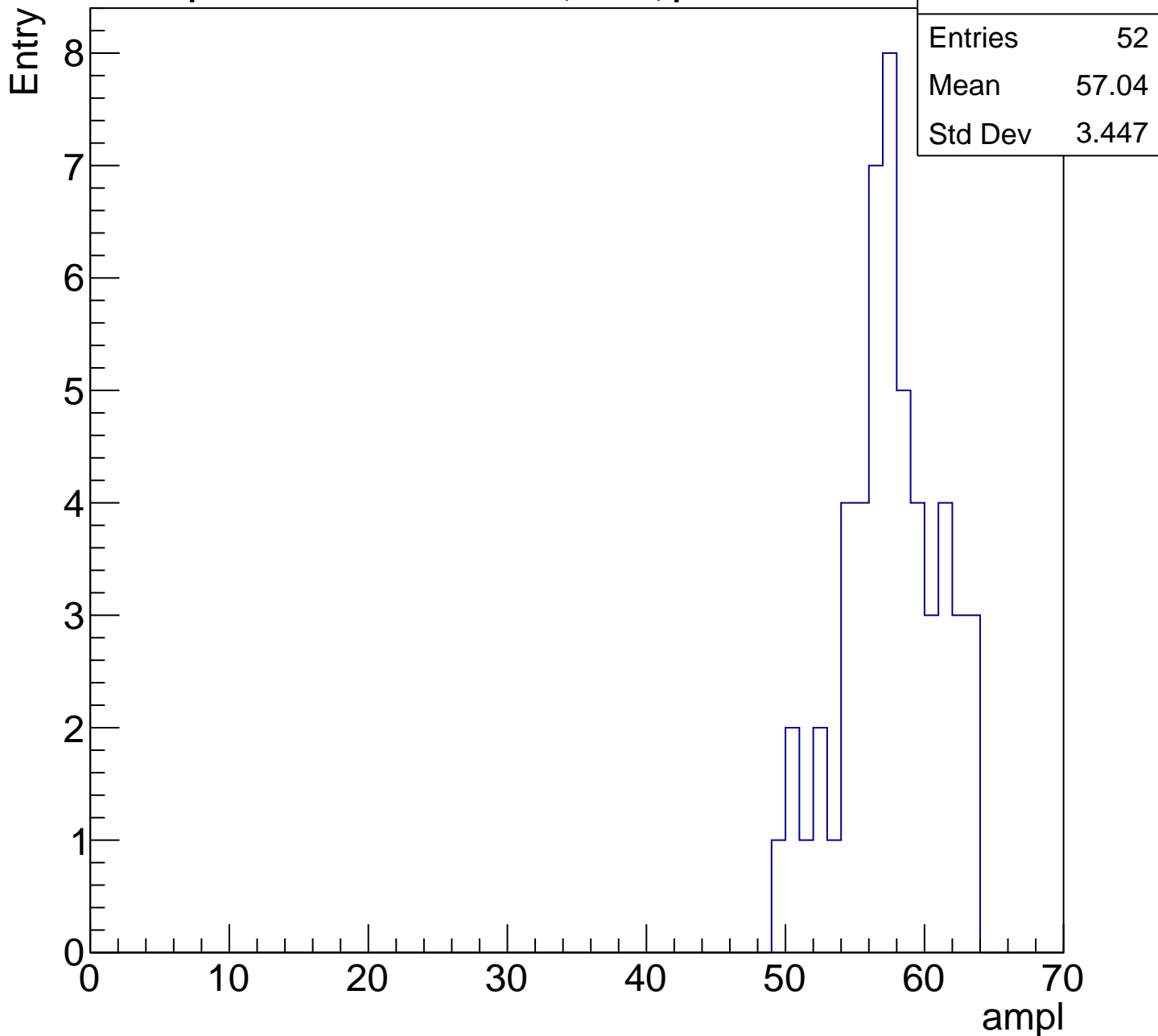
Entry

Entries	68
Mean	51.4
Std Dev	3.586



B1L101S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 36

Mean 58.89

Std Dev 10.13

ampl

0

10

20

30

40

50

60

70

B1L101S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch68, adc0

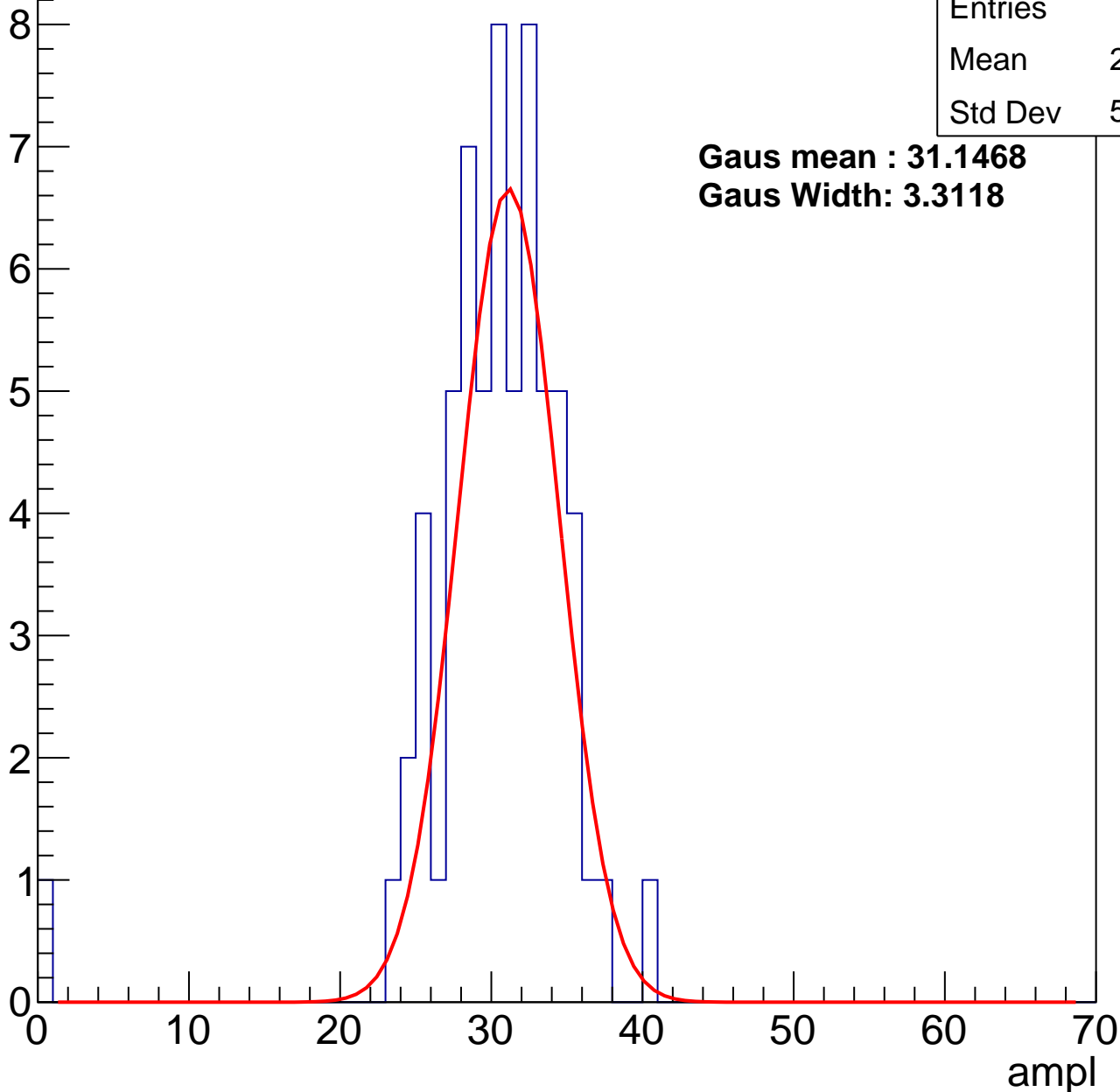
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.88
Std Dev	5.094

Gaus mean : 31.1468

Gaus Width: 3.3118



B1L101S, U14-ch68, adc1

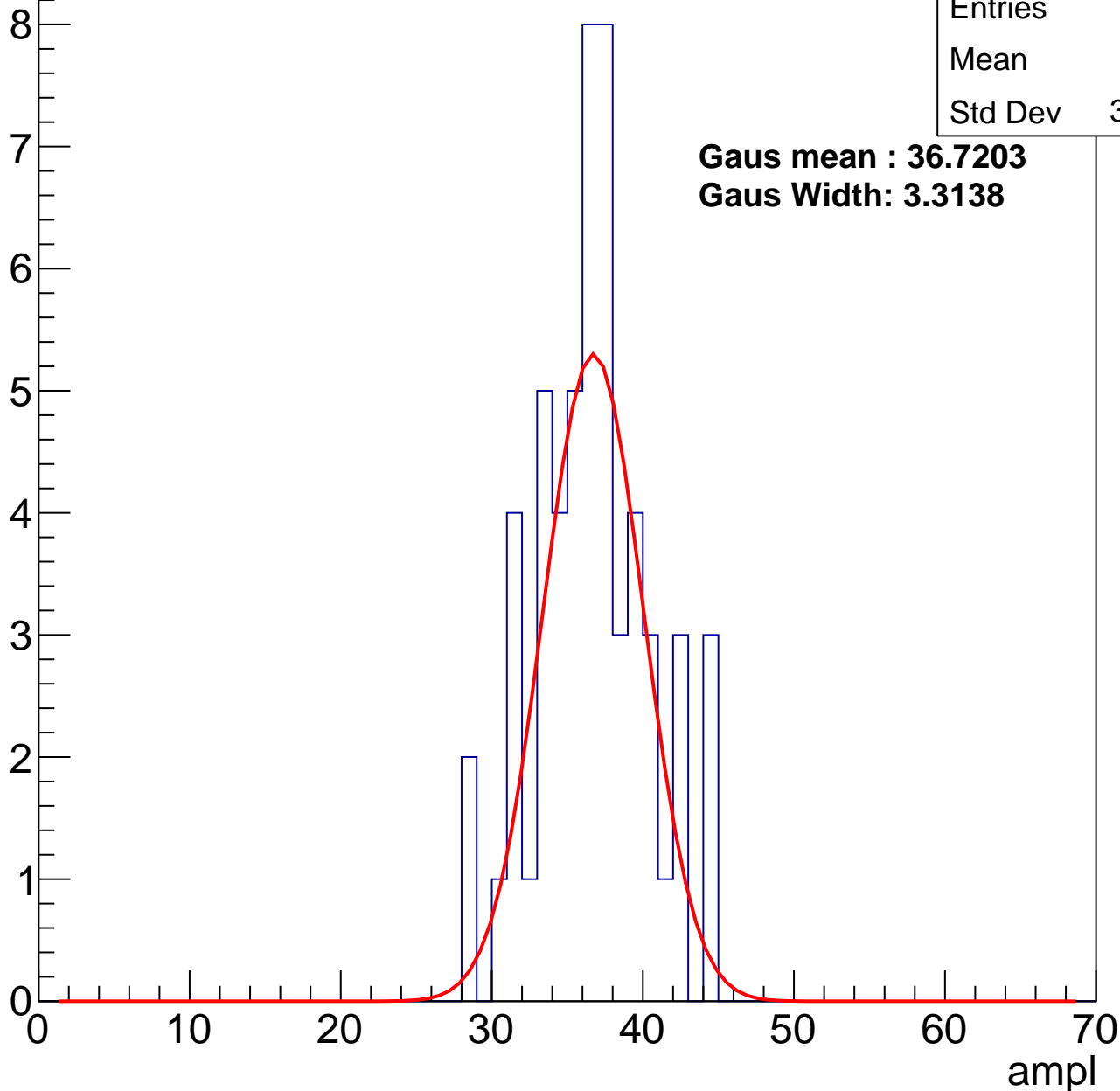
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.2
Std Dev	3.748

Gaus mean : 36.7203

Gaus Width: 3.3138



B1L101S, U14-ch68, adc2

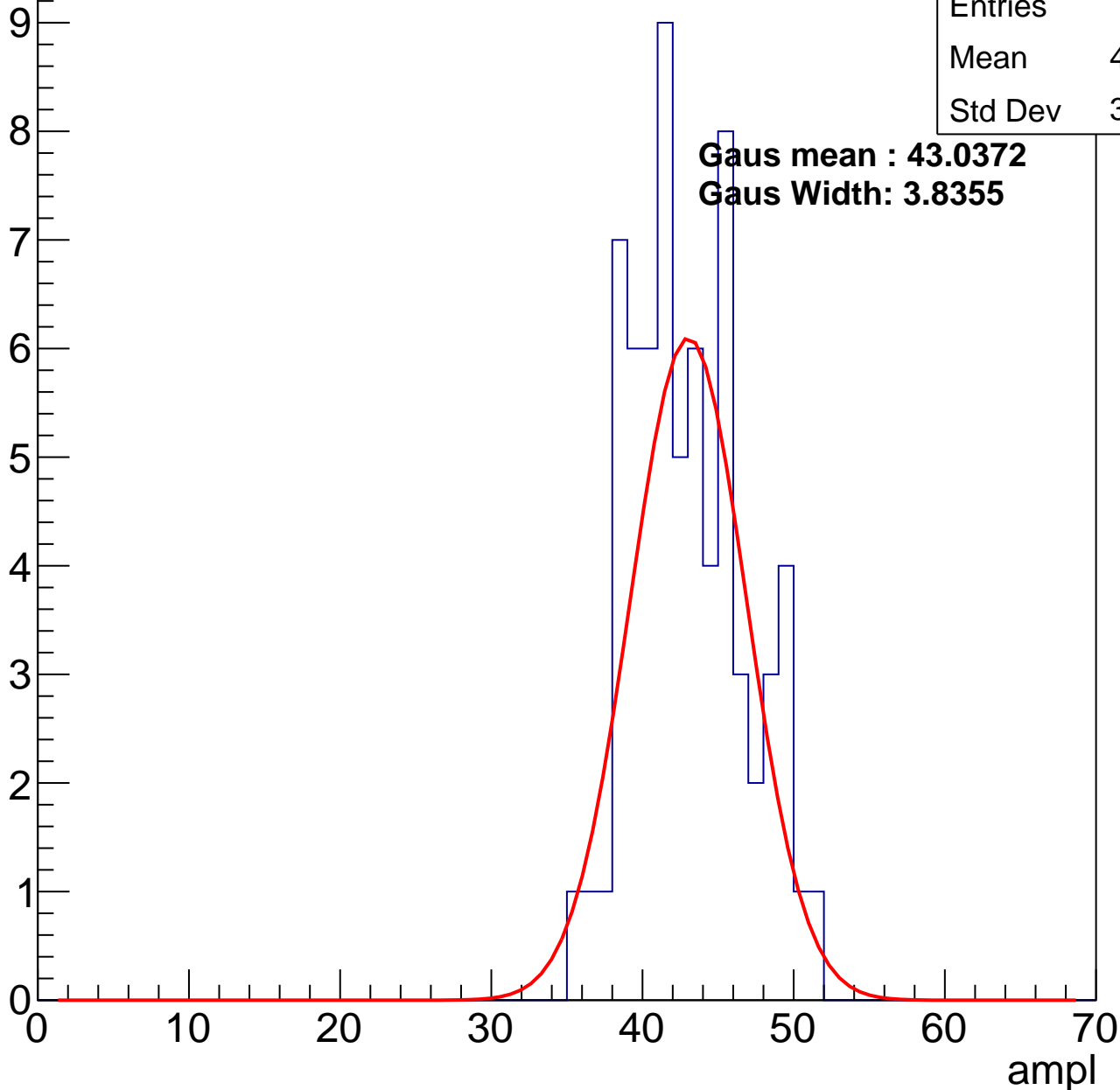
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.56
Std Dev	3.696

Gaus mean : 43.0372

Gaus Width: 3.8355

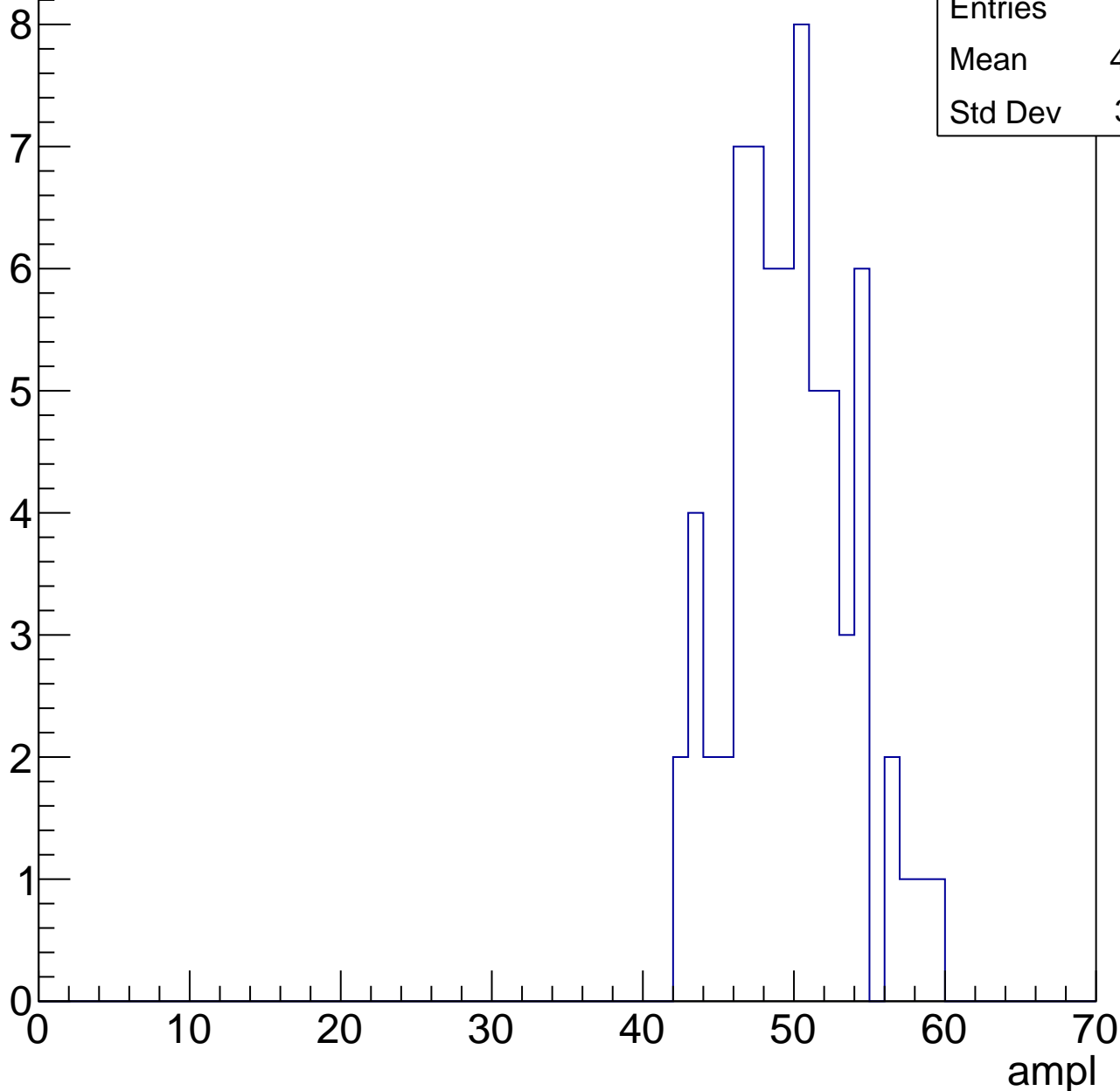


B1L101S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.28
Std Dev	3.891

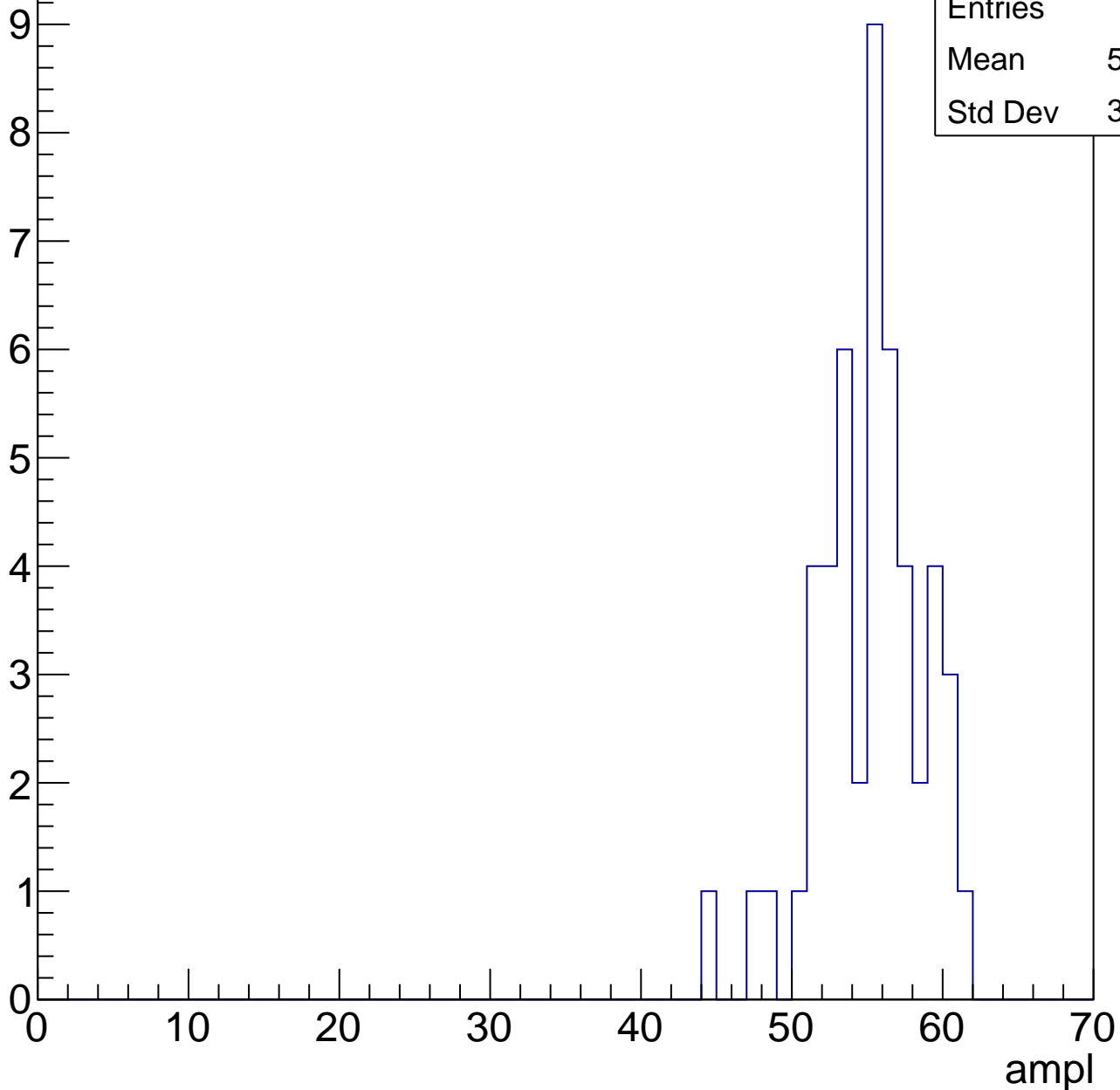


B1L101S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

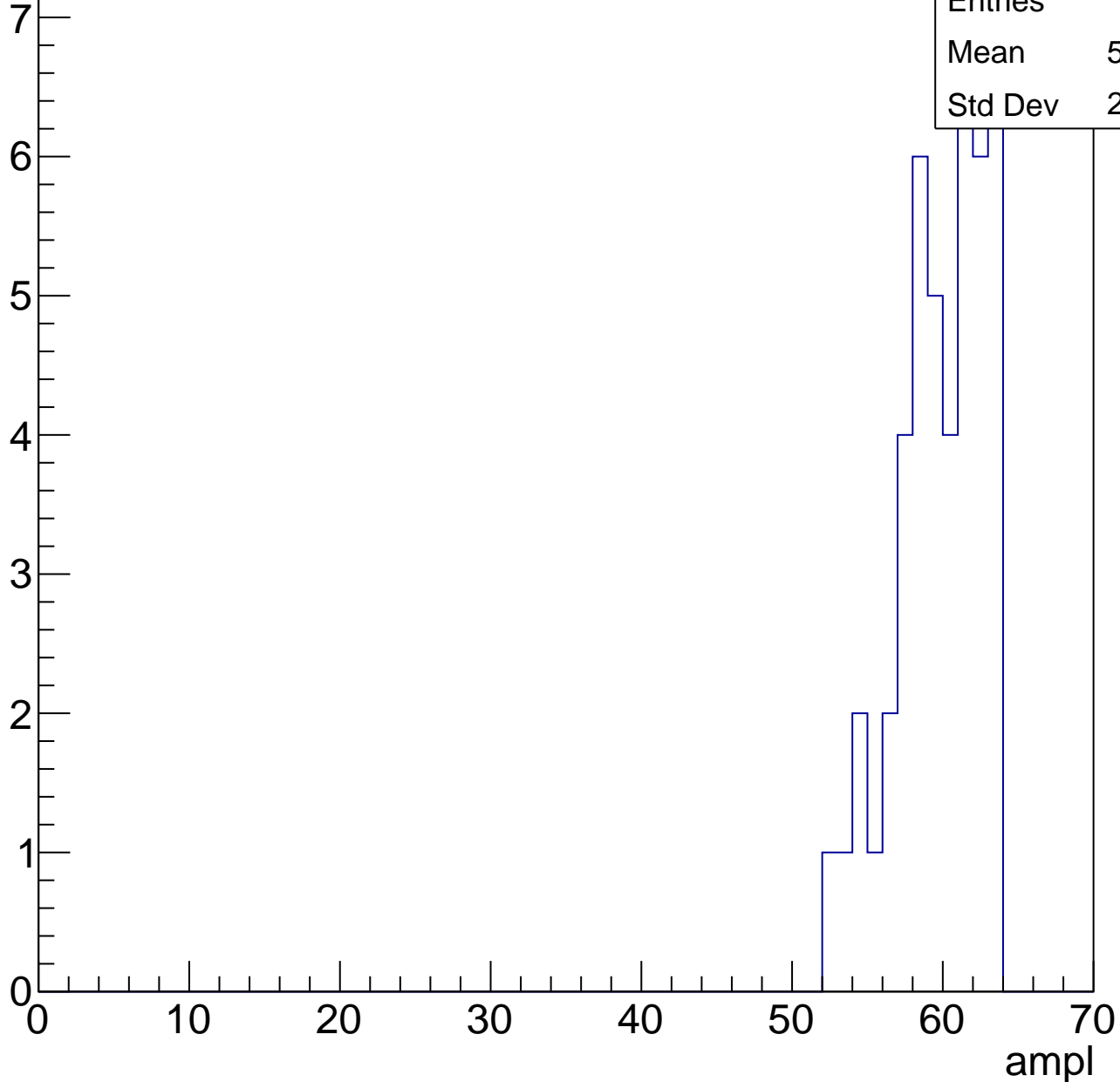
Entries	49
Mean	54.67
Std Dev	3.484



B1L101S, U14-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

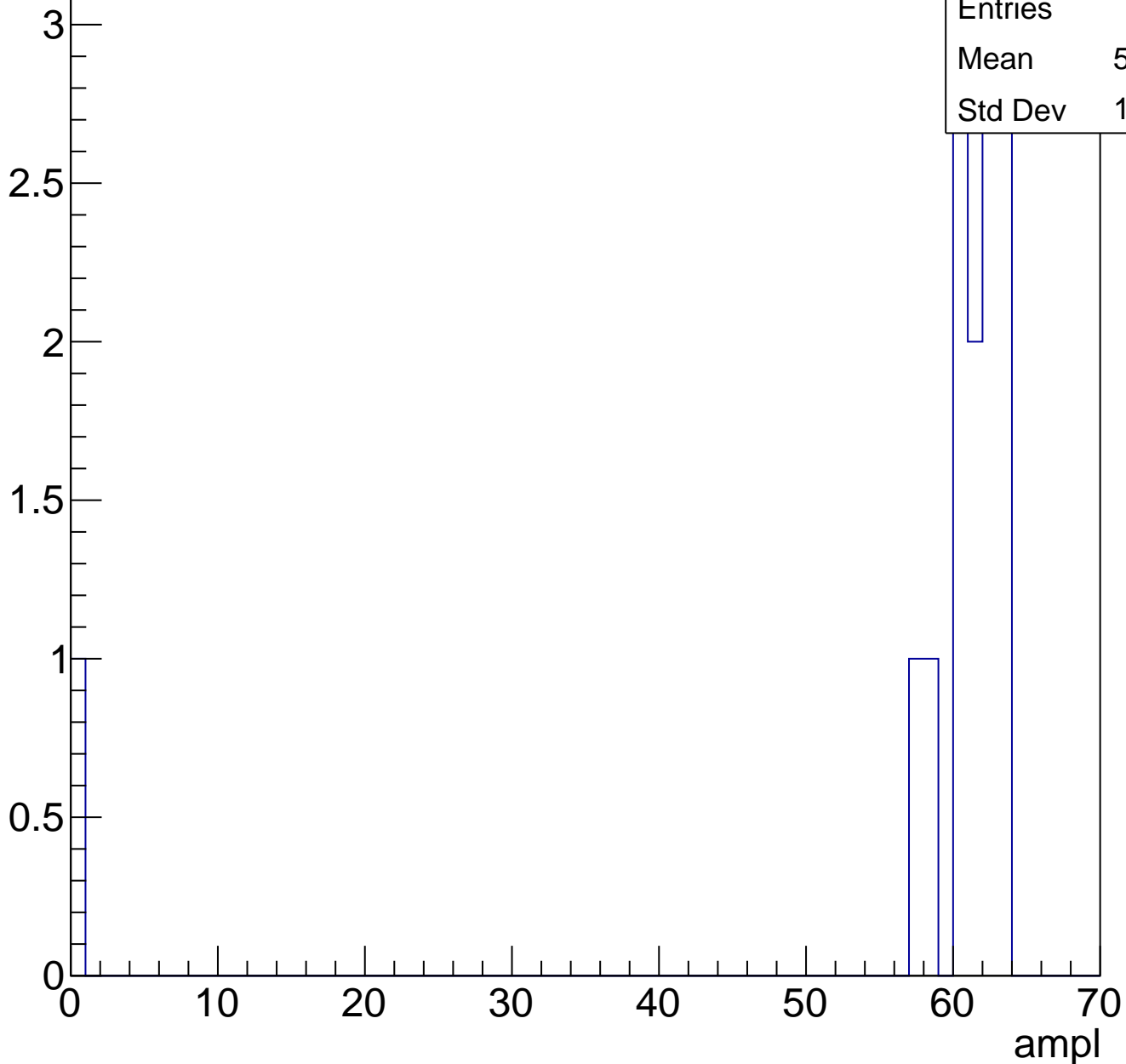
Entry



B1L101S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L101S, U14-ch69, adc0

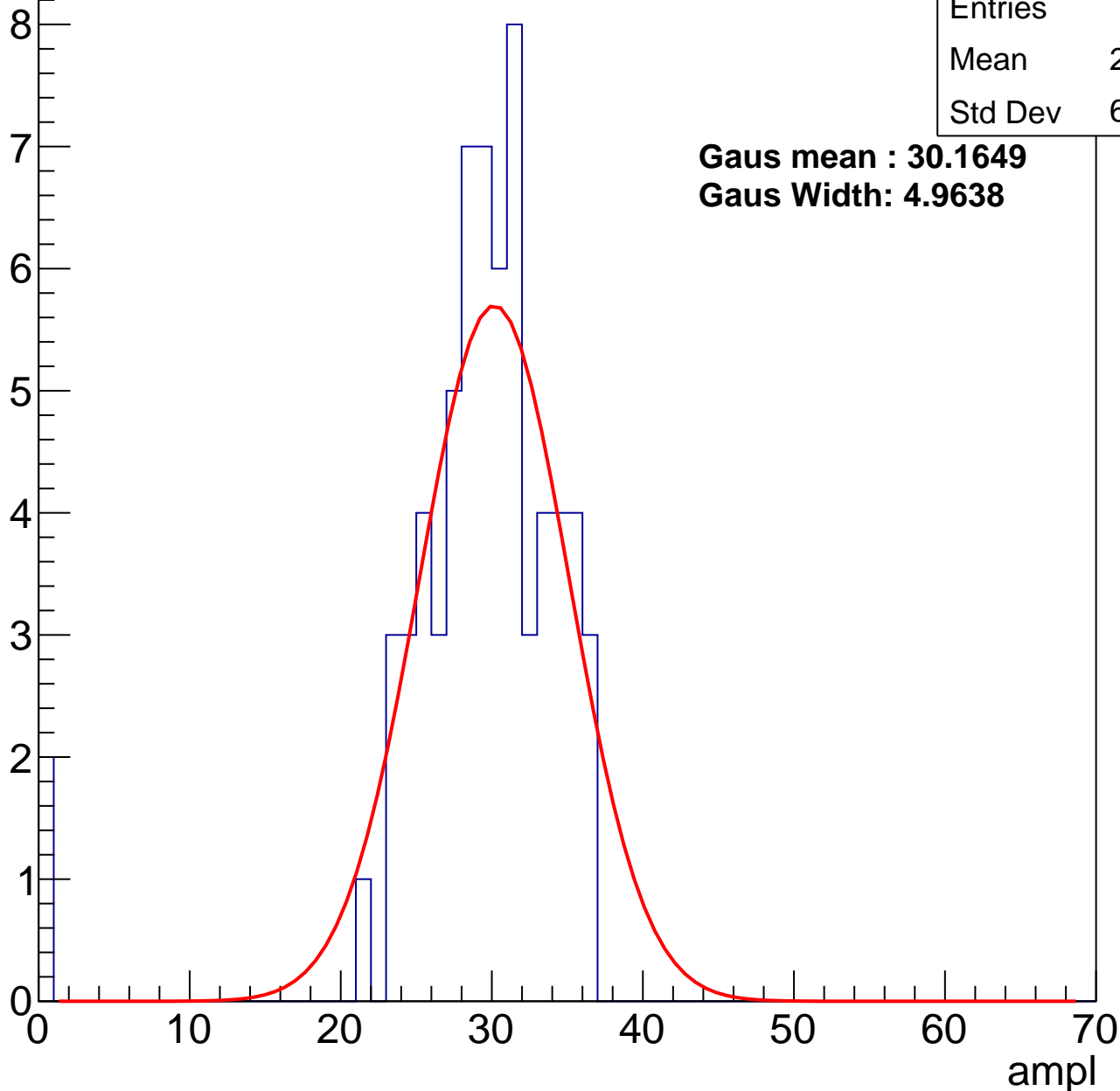
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.57
Std Dev	6.175

Gaus mean : 30.1649

Gaus Width: 4.9638



B1L101S, U14-ch69, adc1

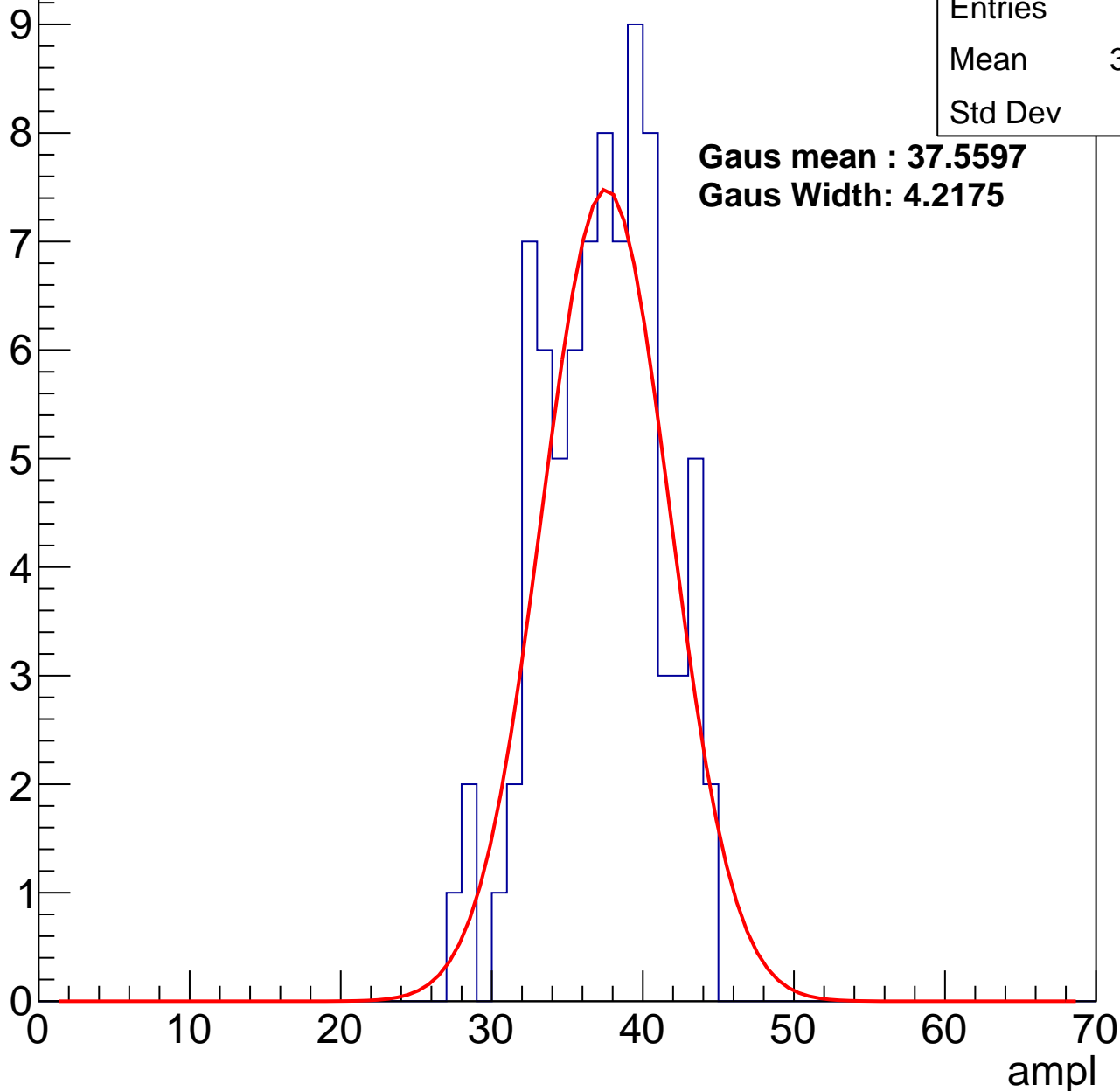
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.76
Std Dev	3.89

Gaus mean : 37.5597

Gaus Width: 4.2175



B1L101S, U14-ch69, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.91
Std Dev	3.45

Gaus mean : 44.7238

Gaus Width: 2.9629

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

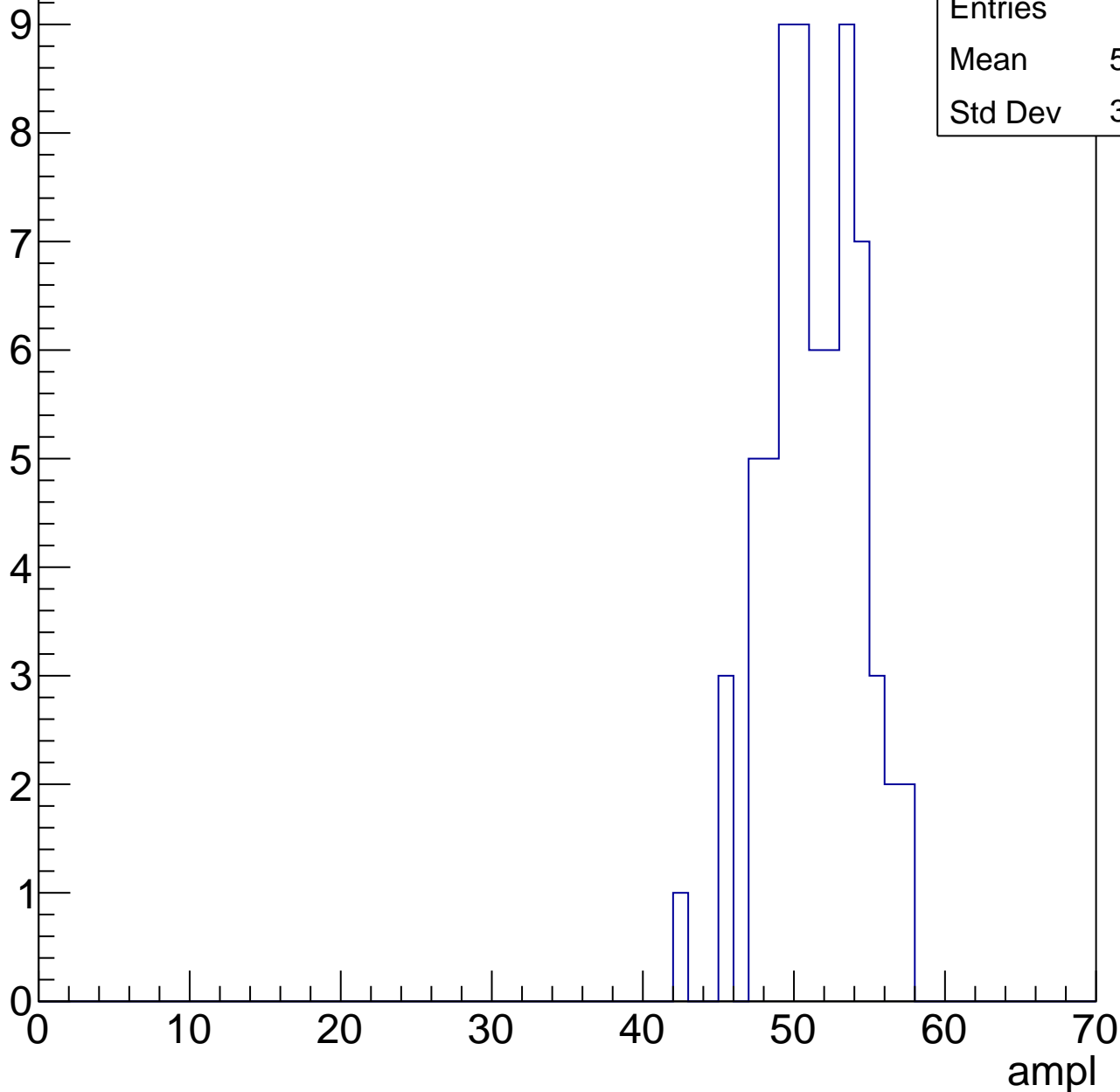
8

10

B1L101S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

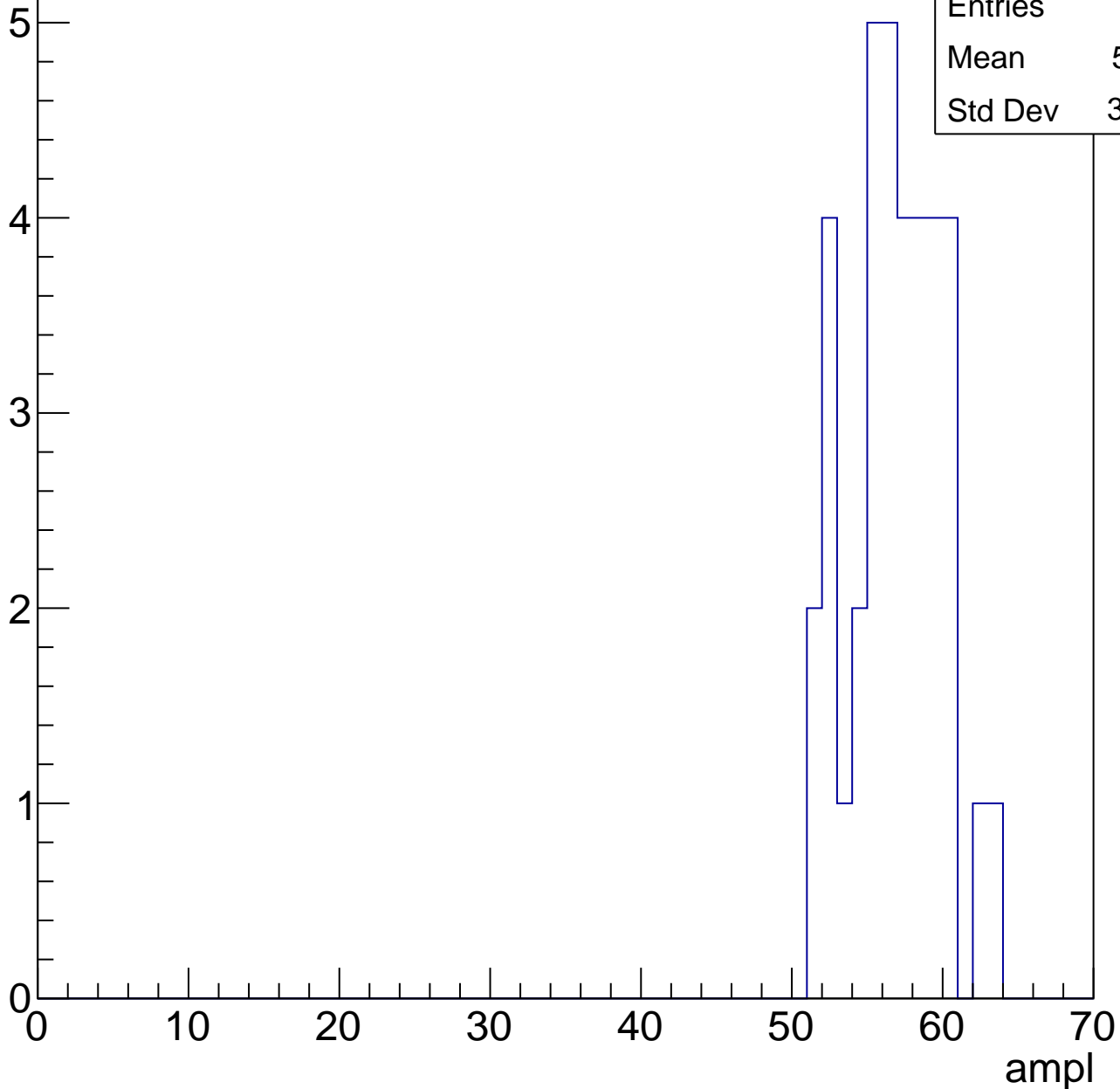


B1L101S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	56.41
Std Dev	3.009

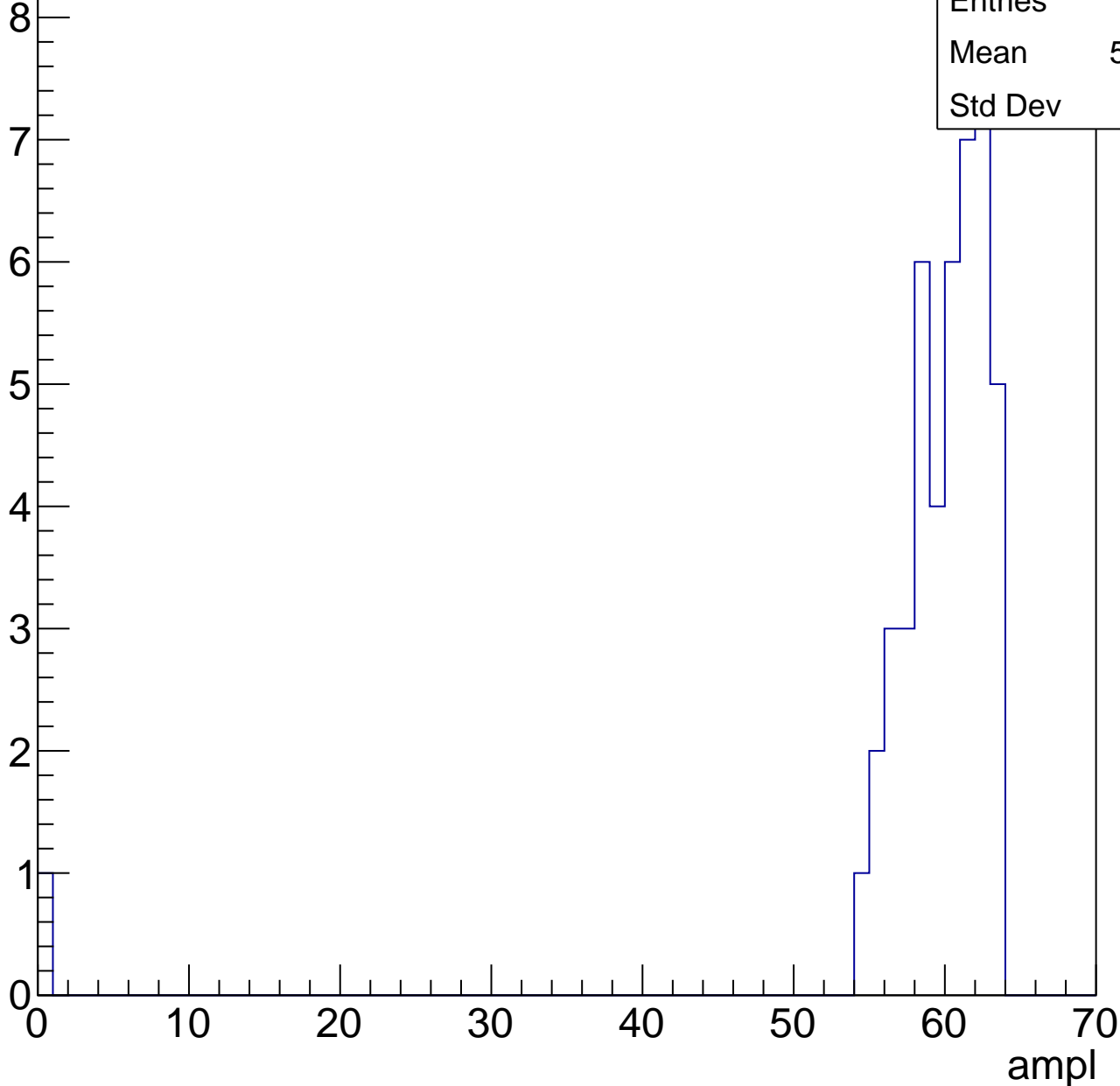


B1L101S, U14-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

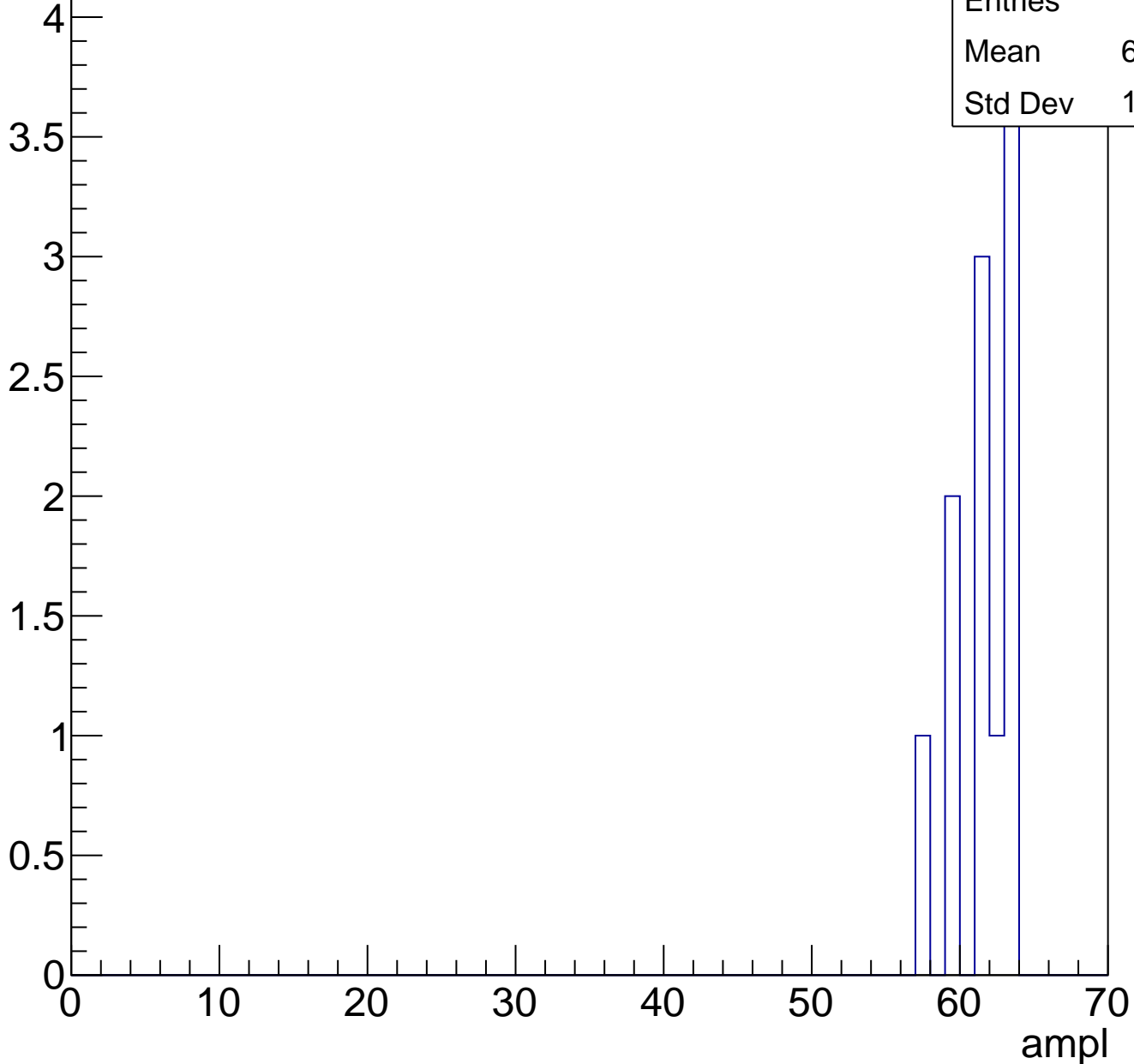
Entries	46
Mean	58.37
Std Dev	9.03



B1L101S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch70, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	27.95
Std Dev	6.558

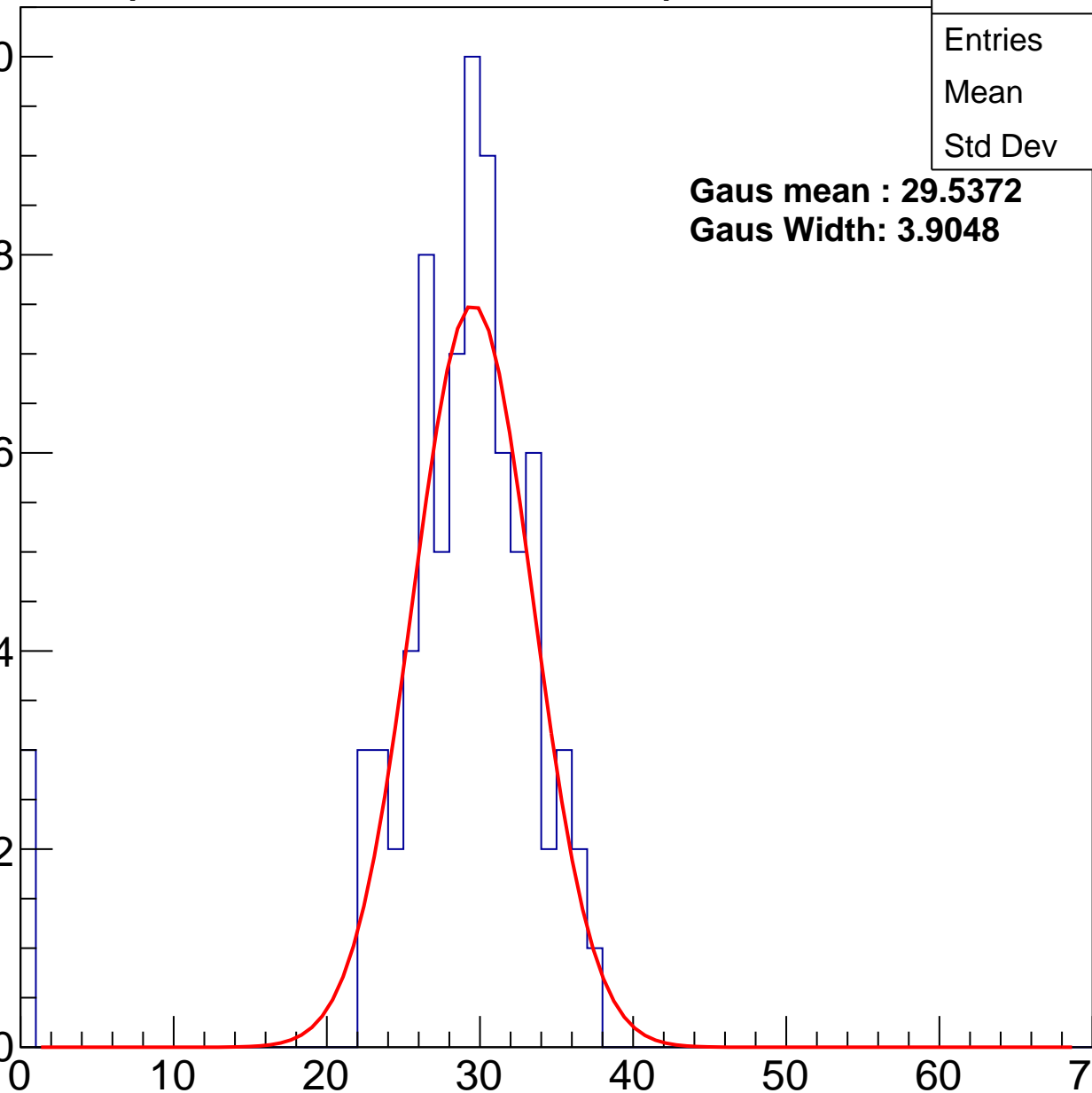
Gaus mean : 29.5372

Gaus Width: 3.9048

Entry

10
8
6
4
2
0

ampl



B1L101S, U14-ch70, adc1

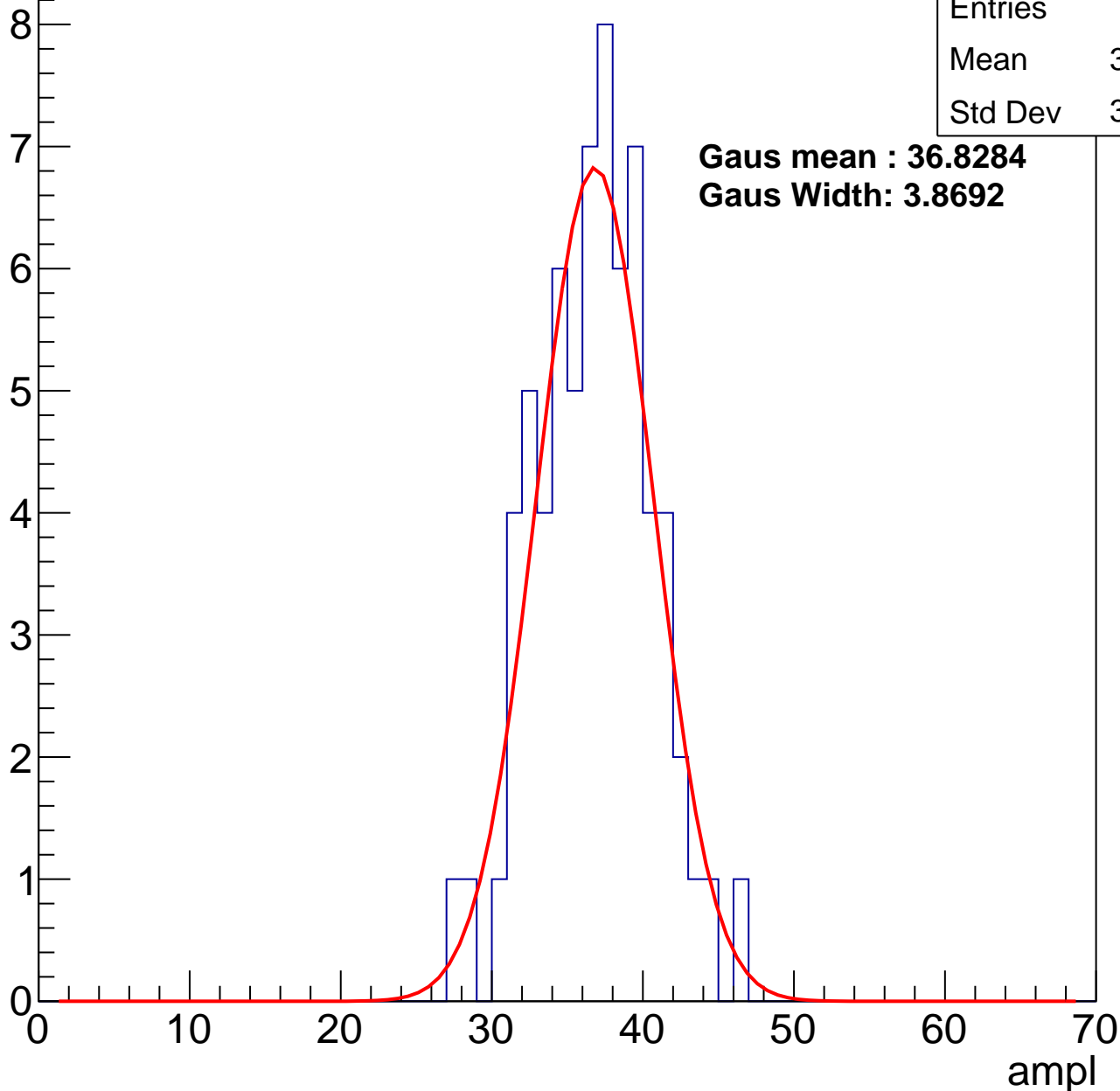
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.32
Std Dev	3.763

Gaus mean : 36.8284

Gaus Width: 3.8692



B1L101S, U14-ch70, adc2

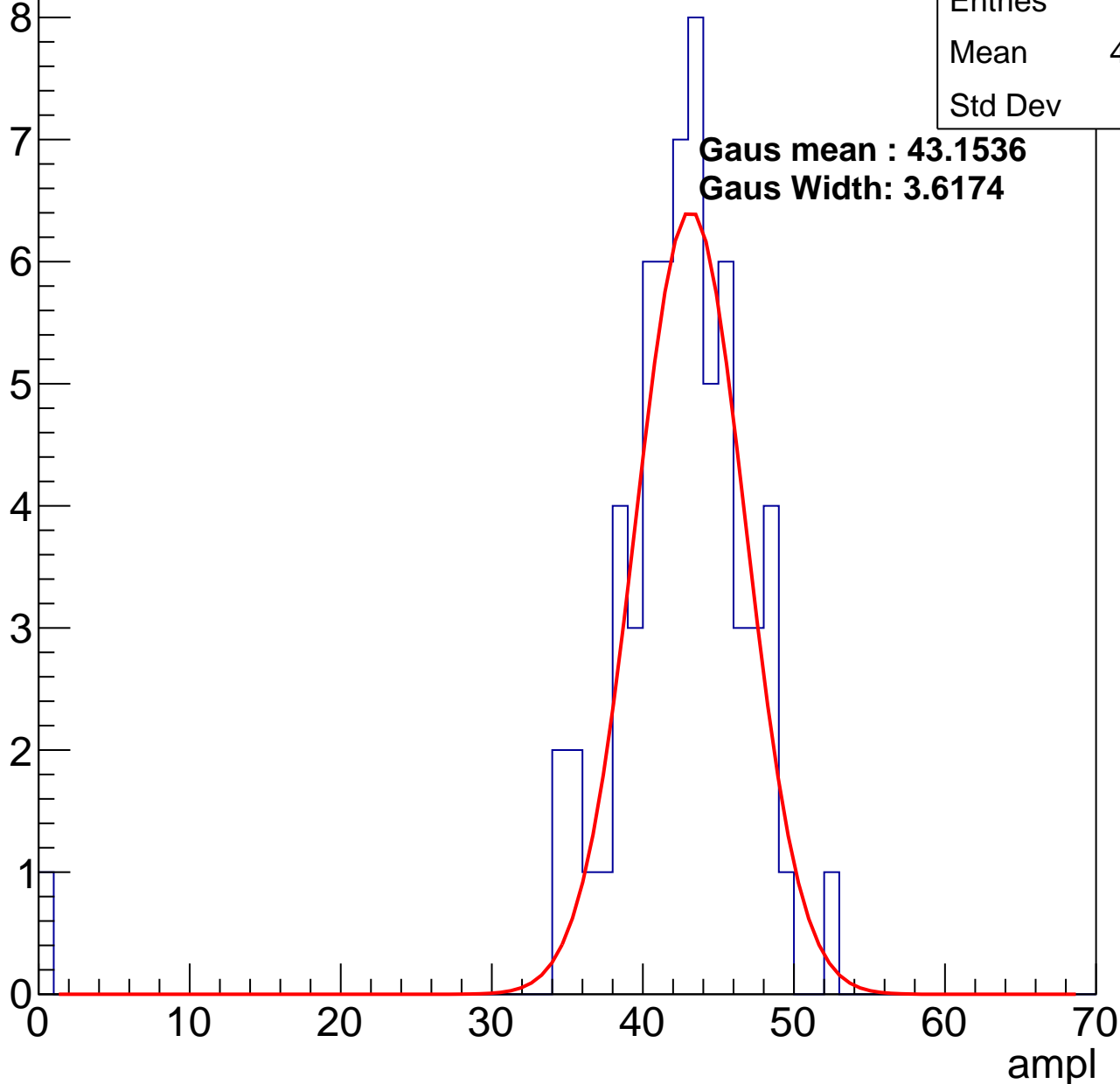
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	41.66
Std Dev	6.45

Gaus mean : 43.1536

Gaus Width: 3.6174

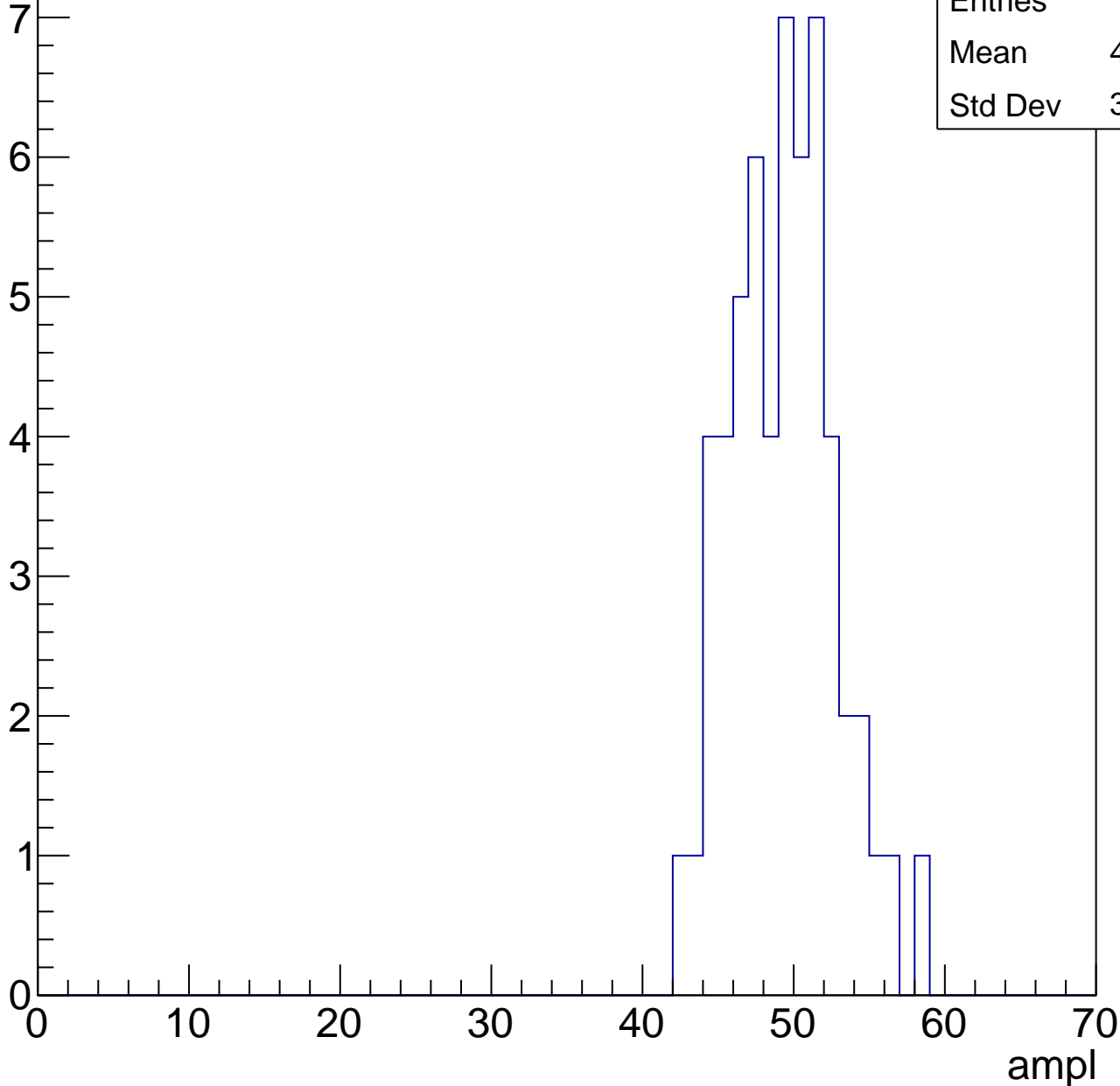


B1L101S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	48.86
Std Dev	3.378



B1L101S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	54.3
Std Dev	3.621

Entry

10

8

6

4

2

0

0

10

20

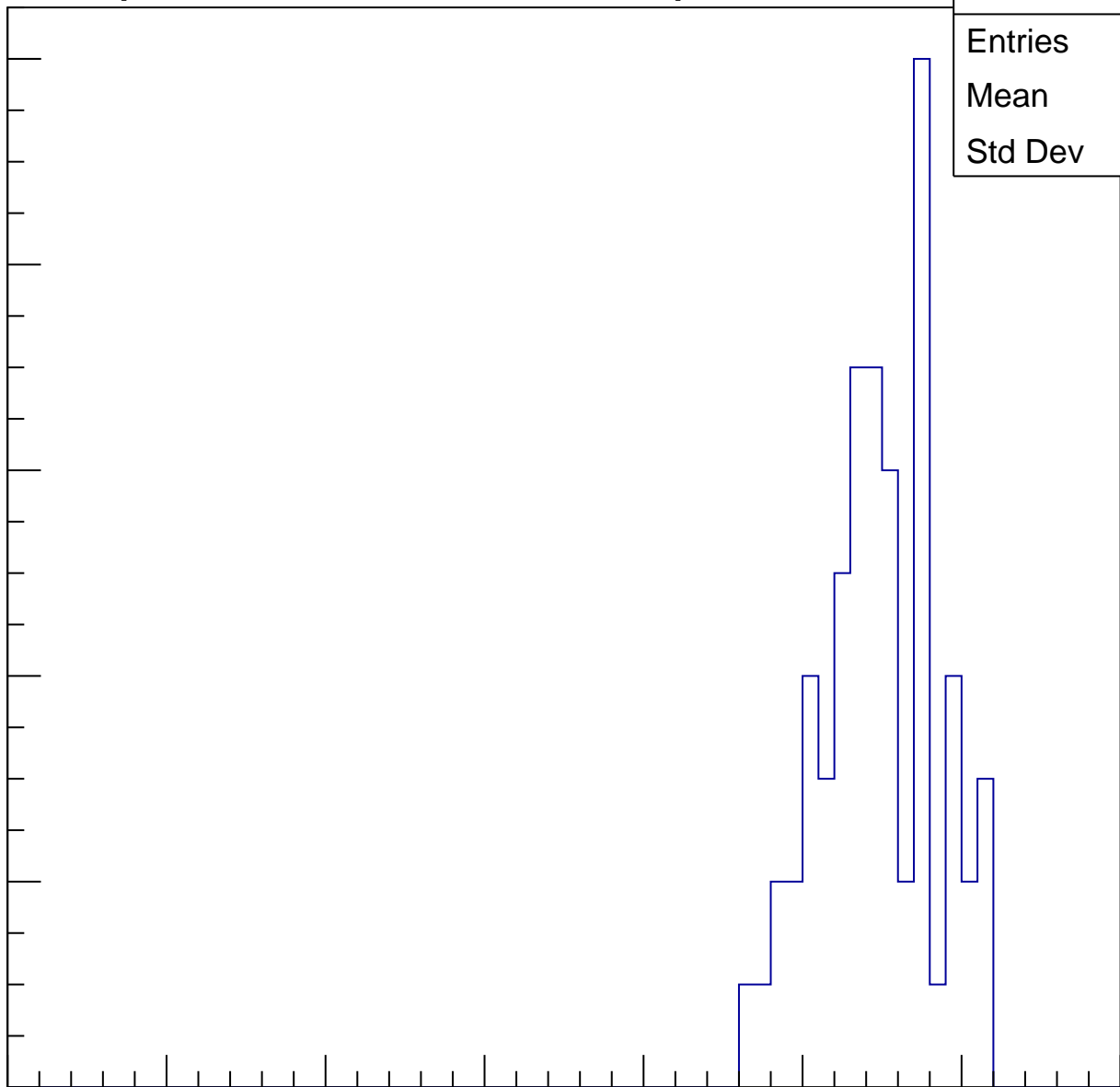
30

40

50

60

ampl

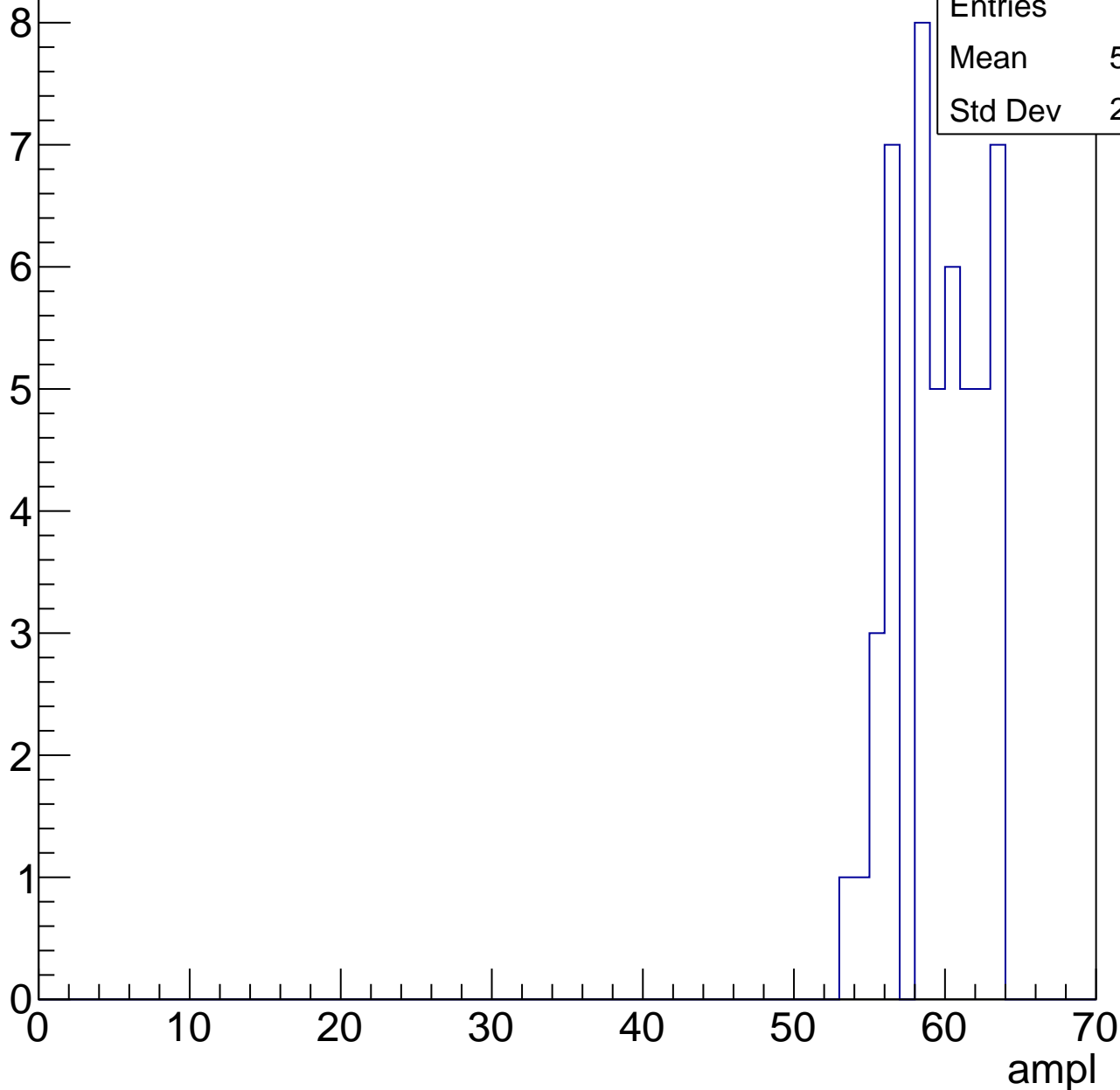


B1L101S, U14-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

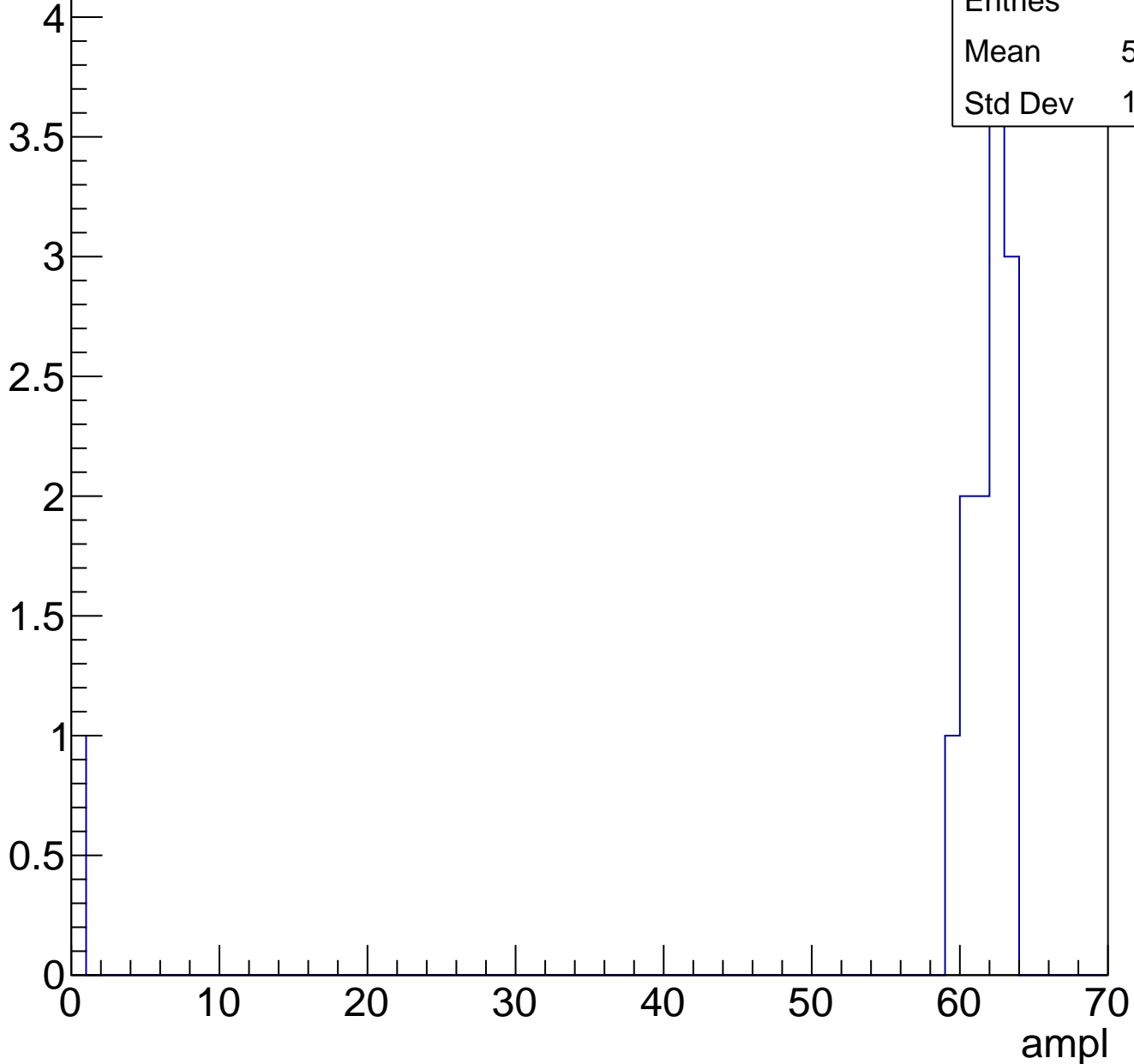
Entries	48
Mean	59.15
Std Dev	2.746



B1L101S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

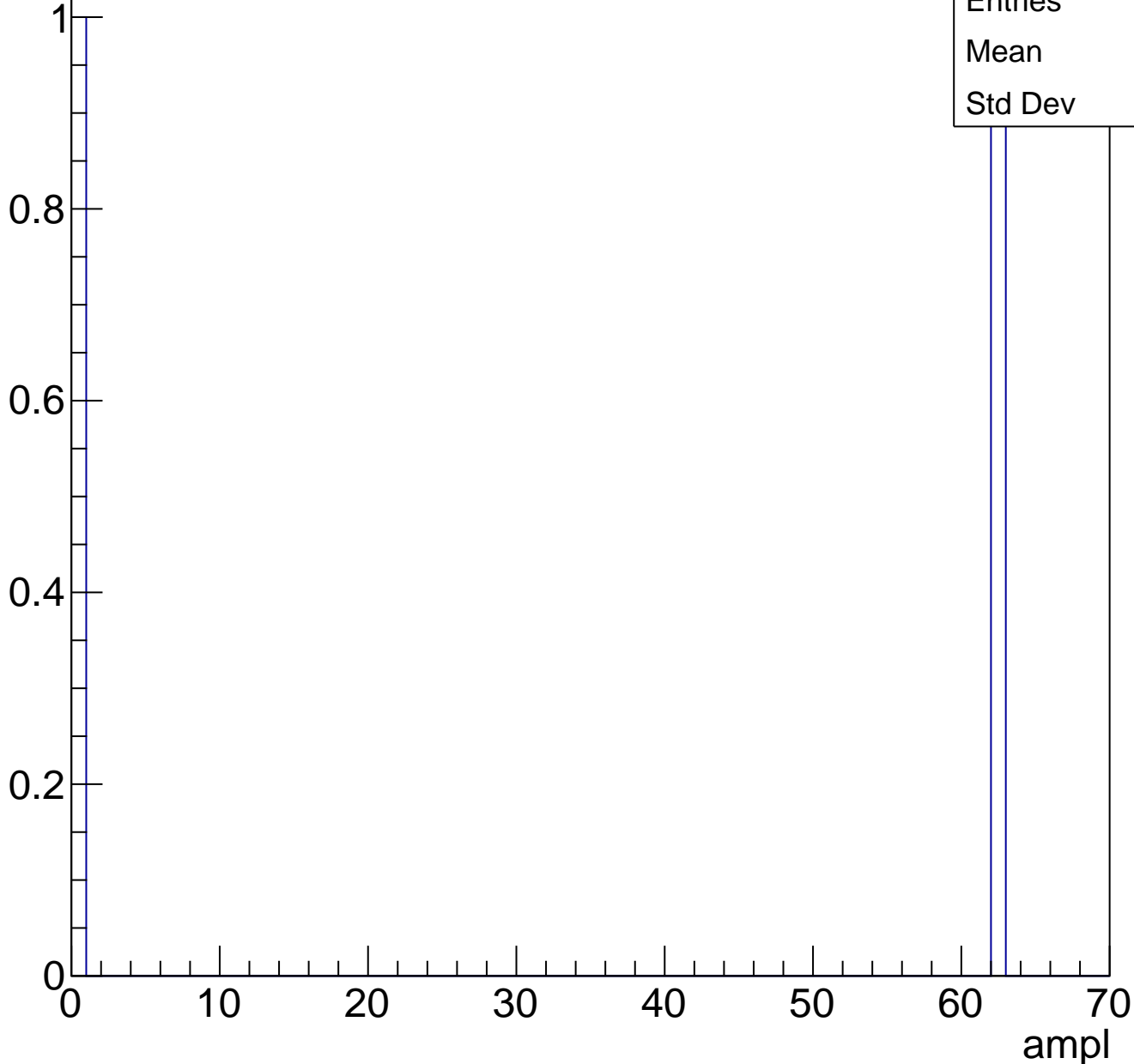
Entry



B1L101S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch71, adc0

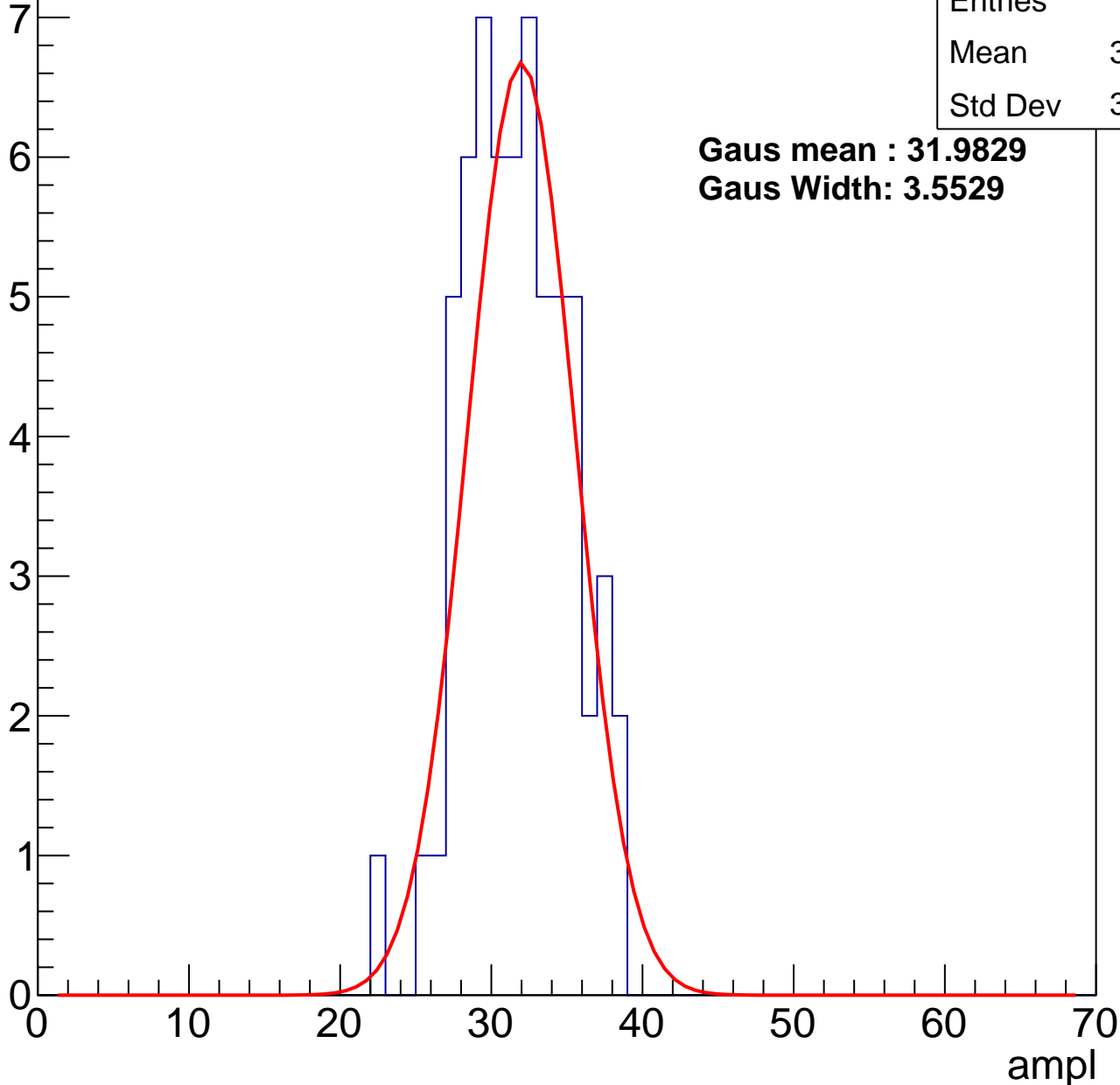
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.26
Std Dev	3.393

Gaus mean : 31.9829

Gaus Width: 3.5529



B1L101S, U14-ch71, adc1

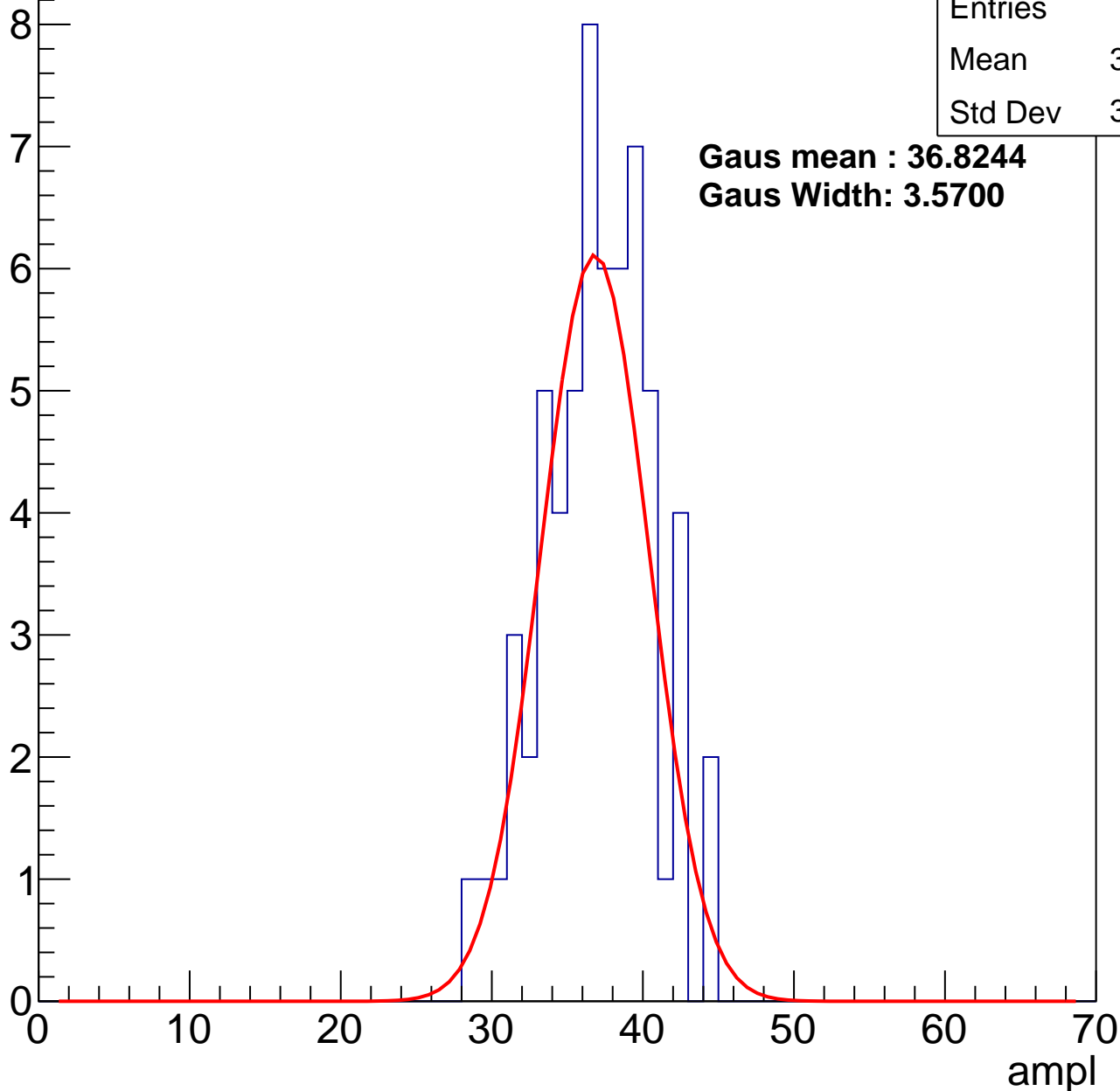
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.52
Std Dev	3.565

Gaus mean : 36.8244

Gaus Width: 3.5700



B1L101S, U14-ch71, adc2

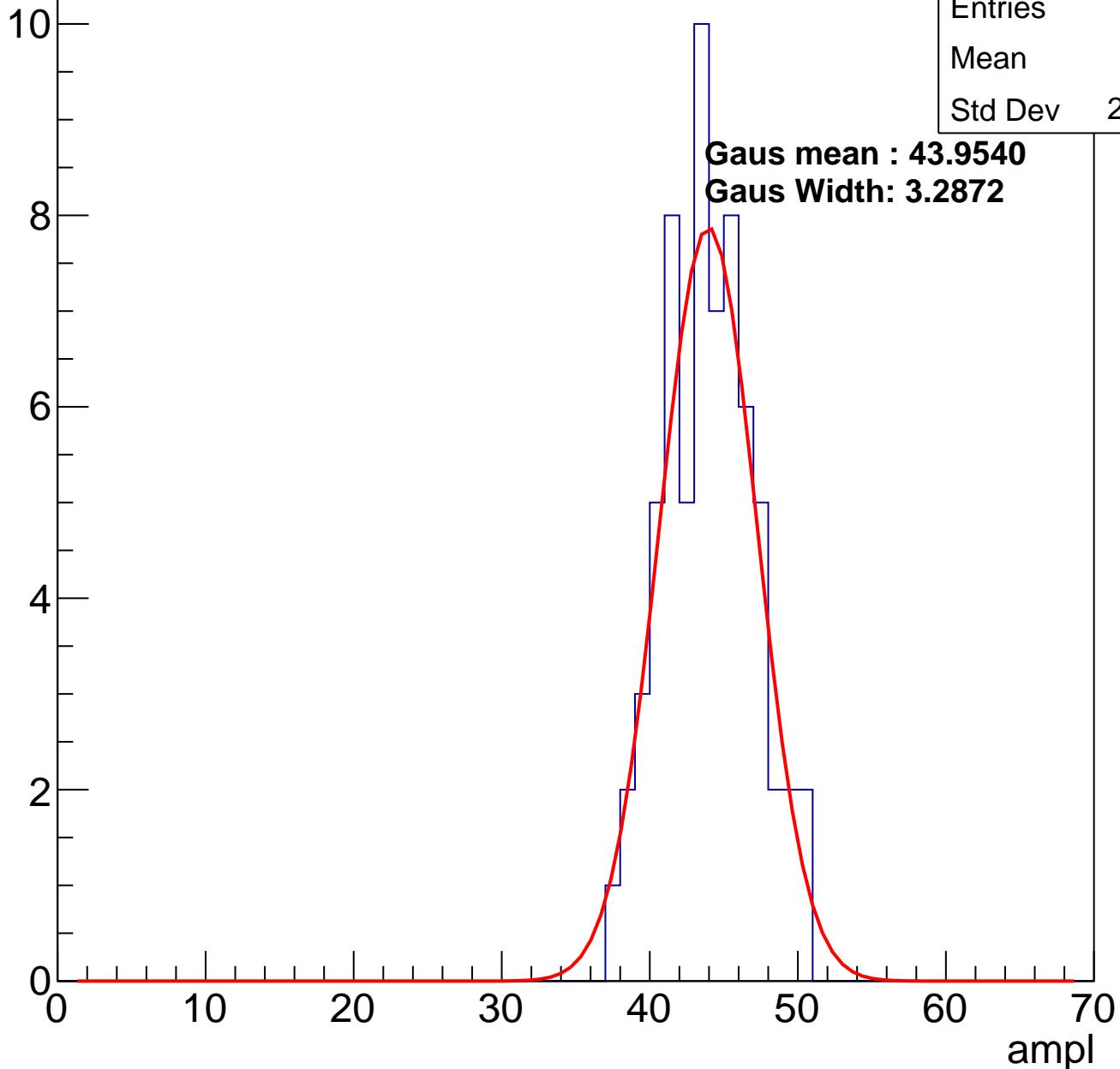
calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	43.5
Std Dev	2.996

Gaus mean : 43.9540

Gaus Width: 3.2872

Entry

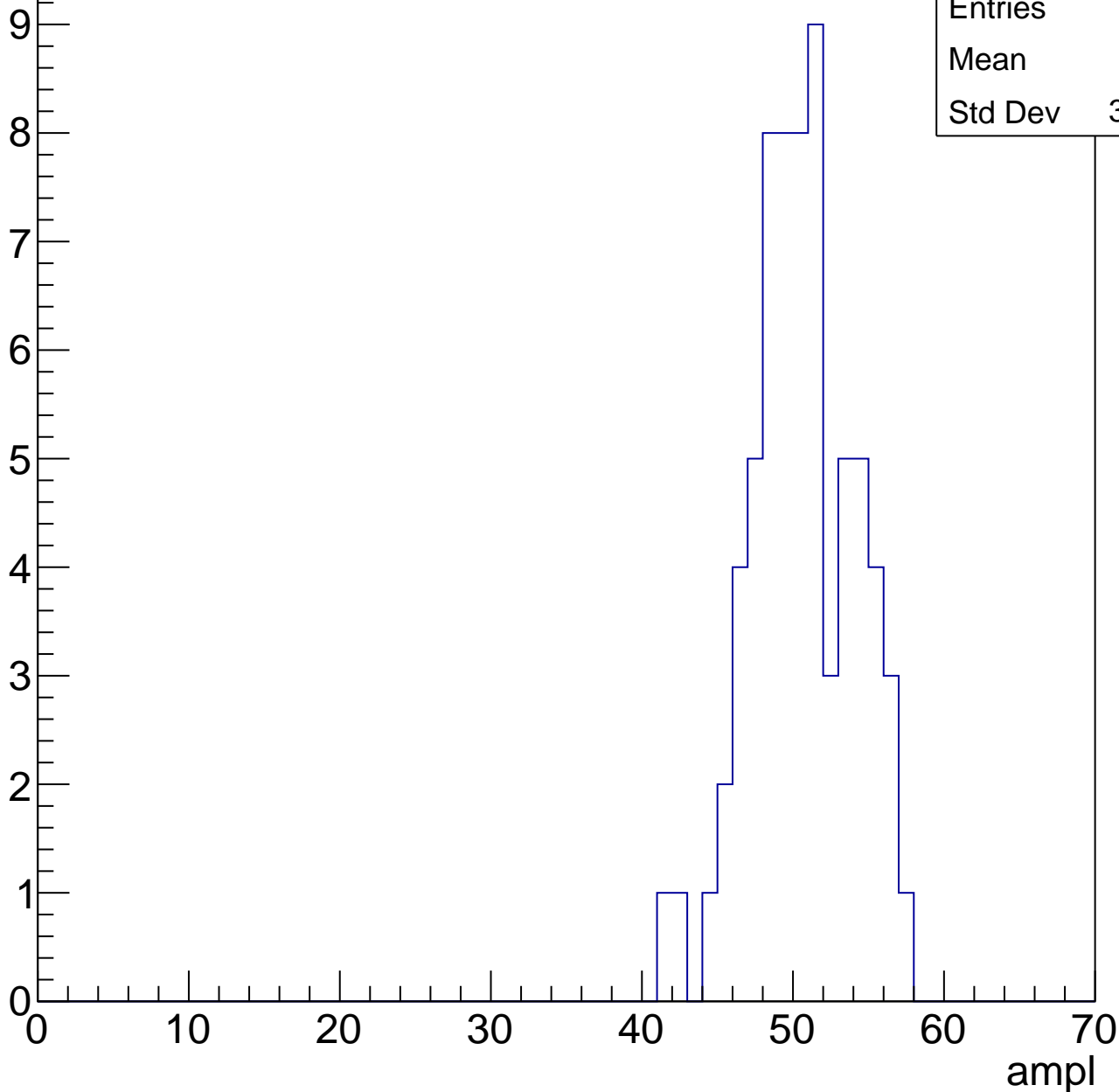


B1L101S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

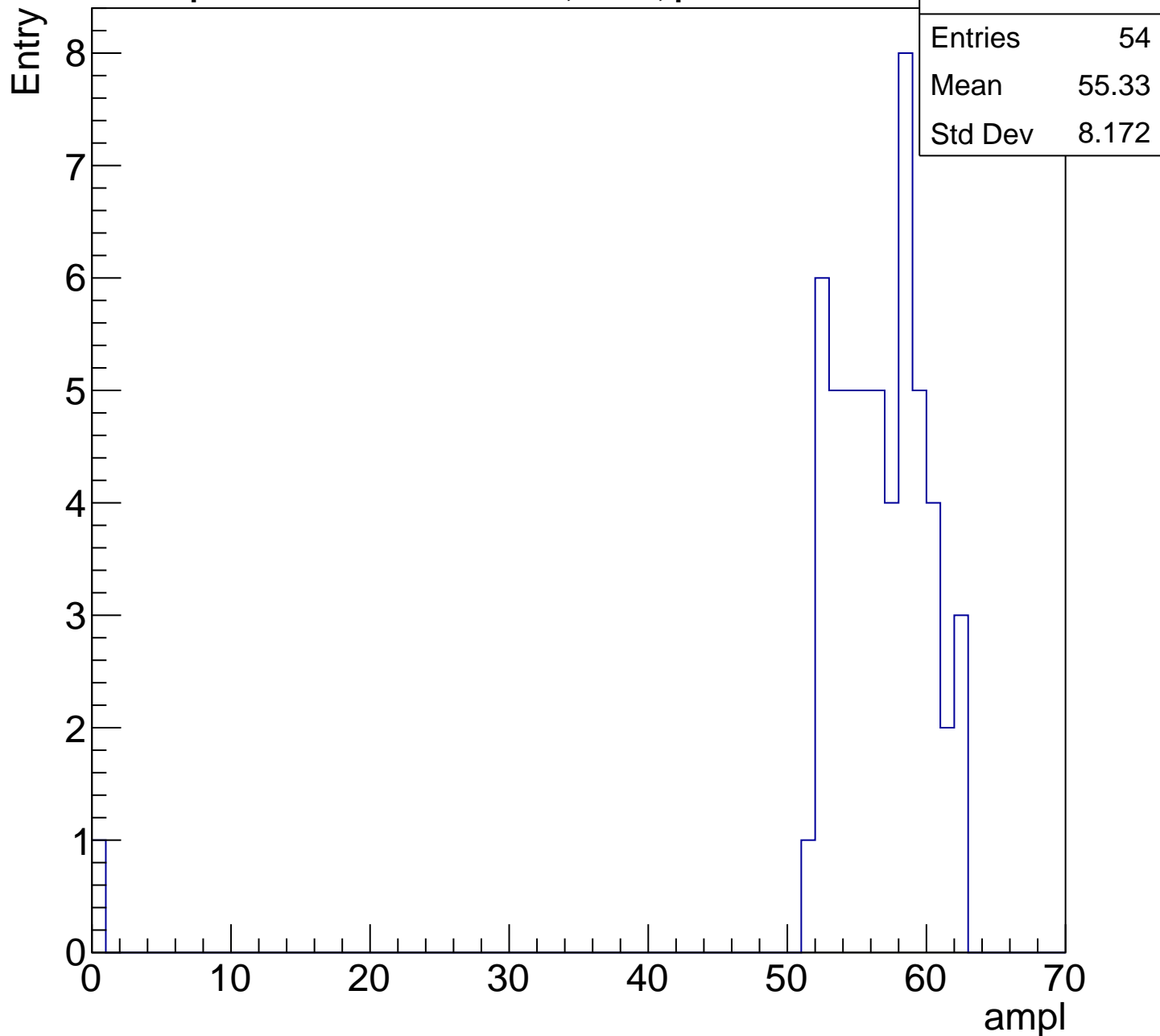
Entry

Entries	68
Mean	50.1
Std Dev	3.392



B1L101S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

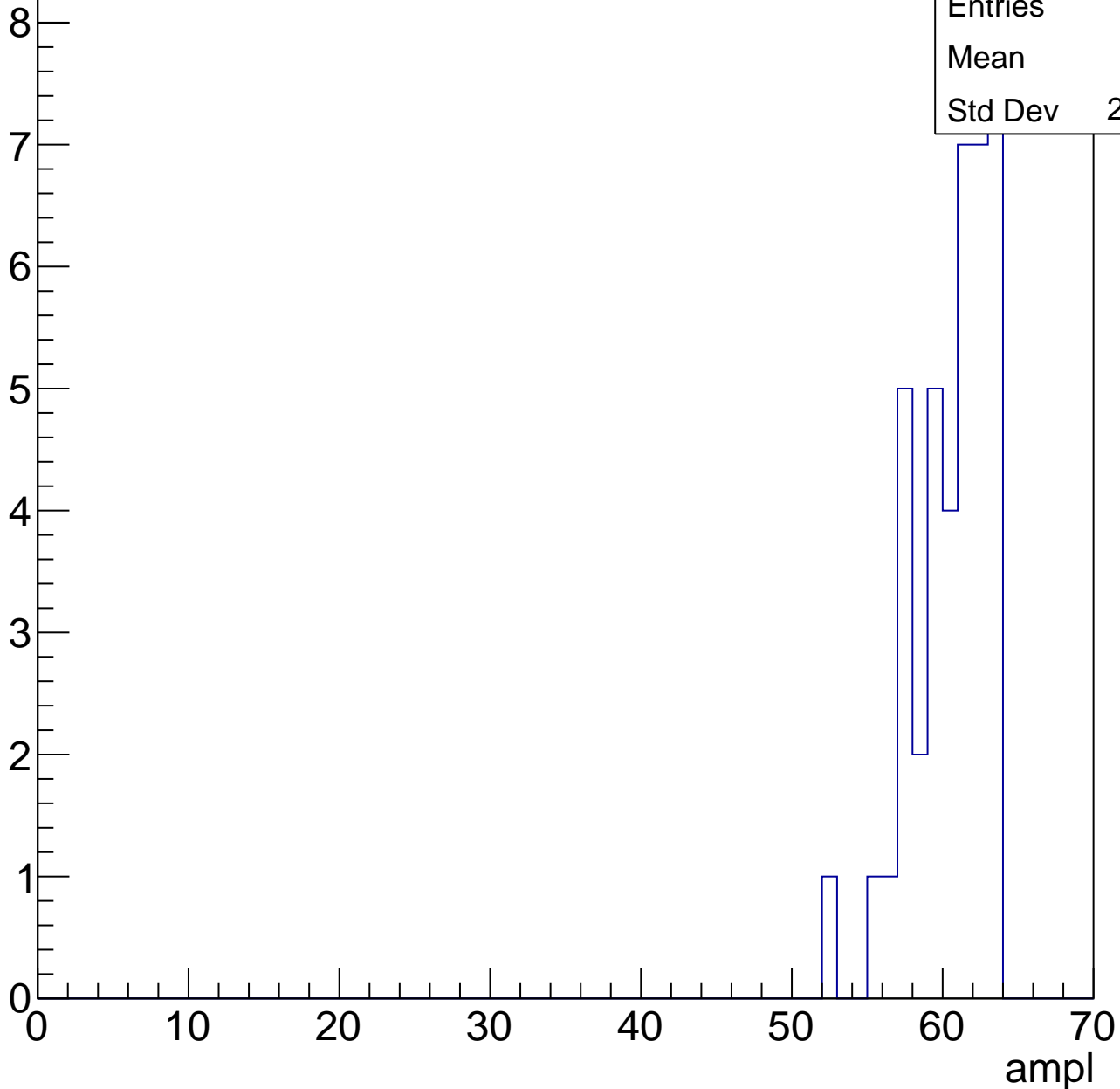


B1L101S, U14-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	60.1
Std Dev	2.564



B1L101S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

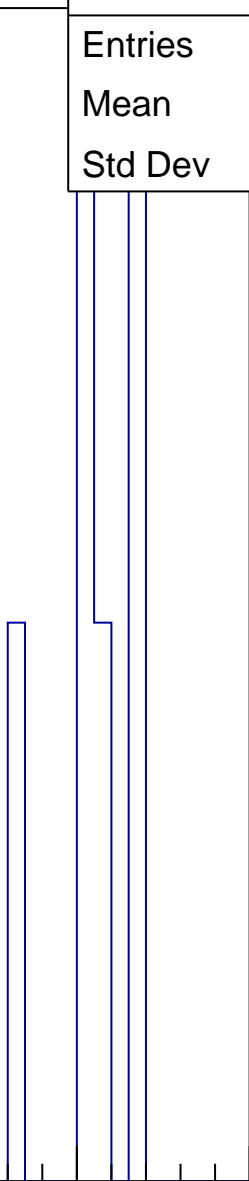
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.5
Std Dev	2.363

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch72, adc0

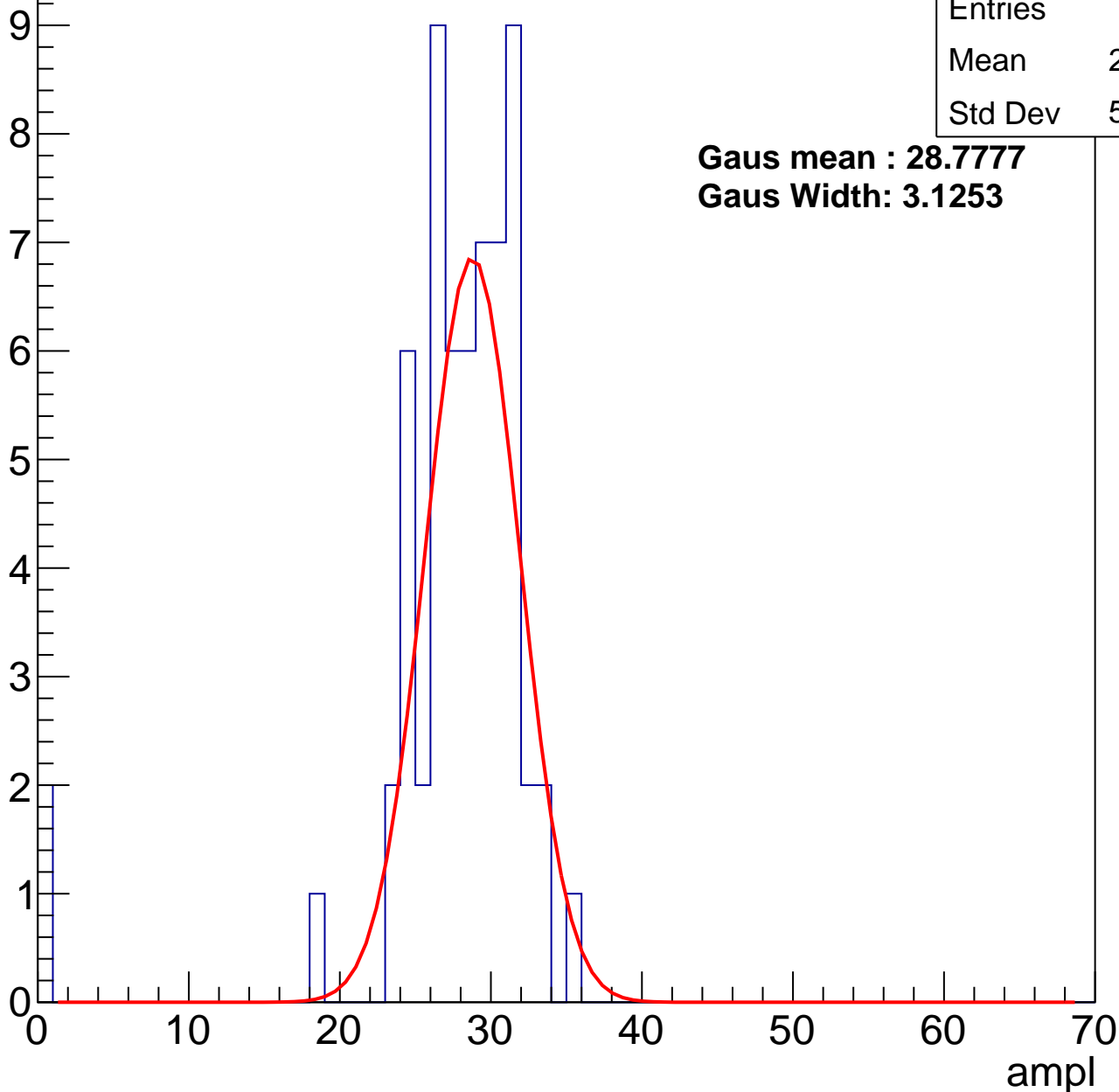
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	27.08
Std Dev	5.776

Gaus mean : 28.7777

Gaus Width: 3.1253



B1L101S, U14-ch72, adc1

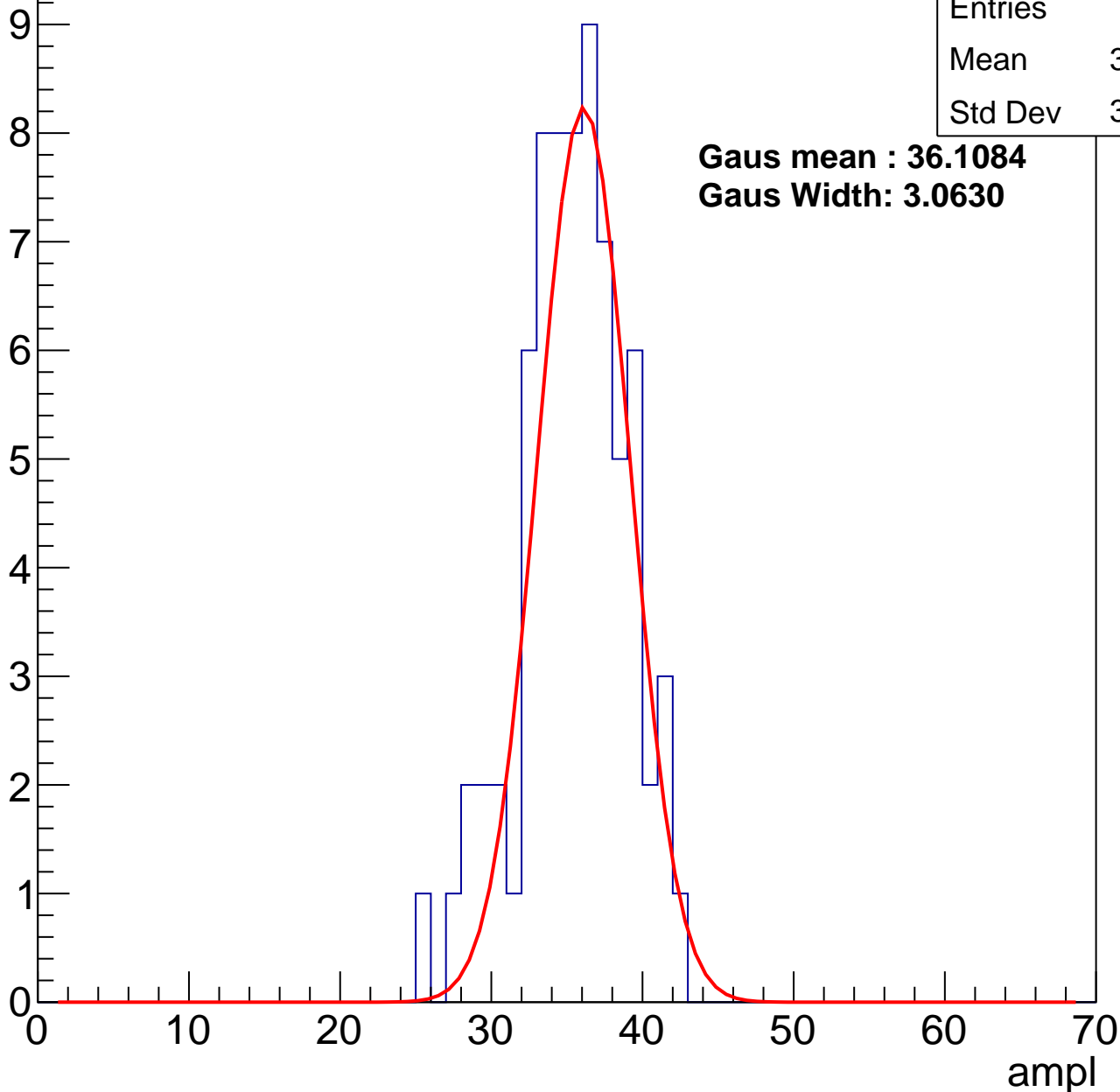
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	34.96
Std Dev	3.494

Gaus mean : 36.1084

Gaus Width: 3.0630



B1L101S, U14-ch72, adc2

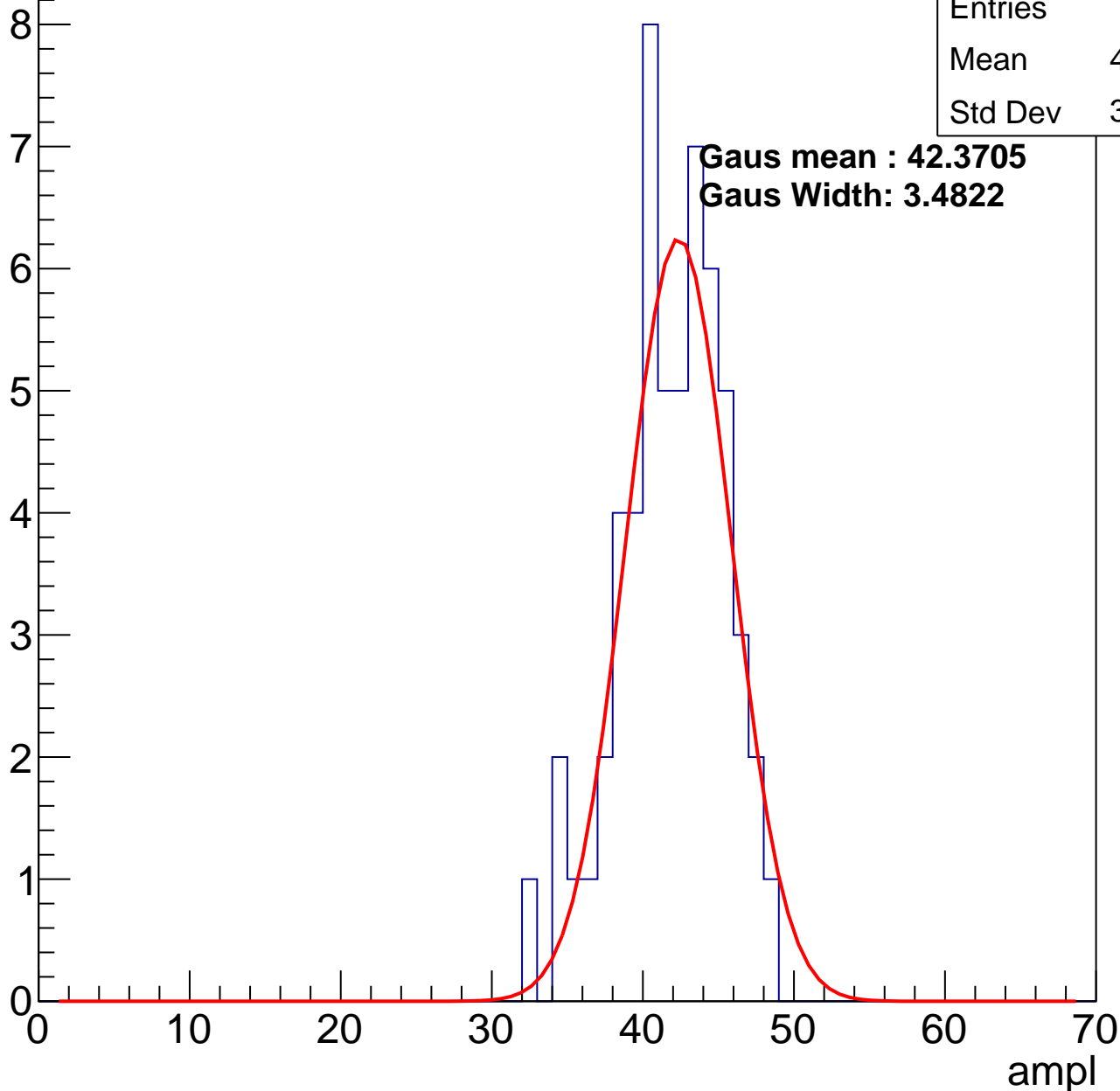
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.37
Std Dev	3.472

Gaus mean : 42.3705

Gaus Width: 3.4822

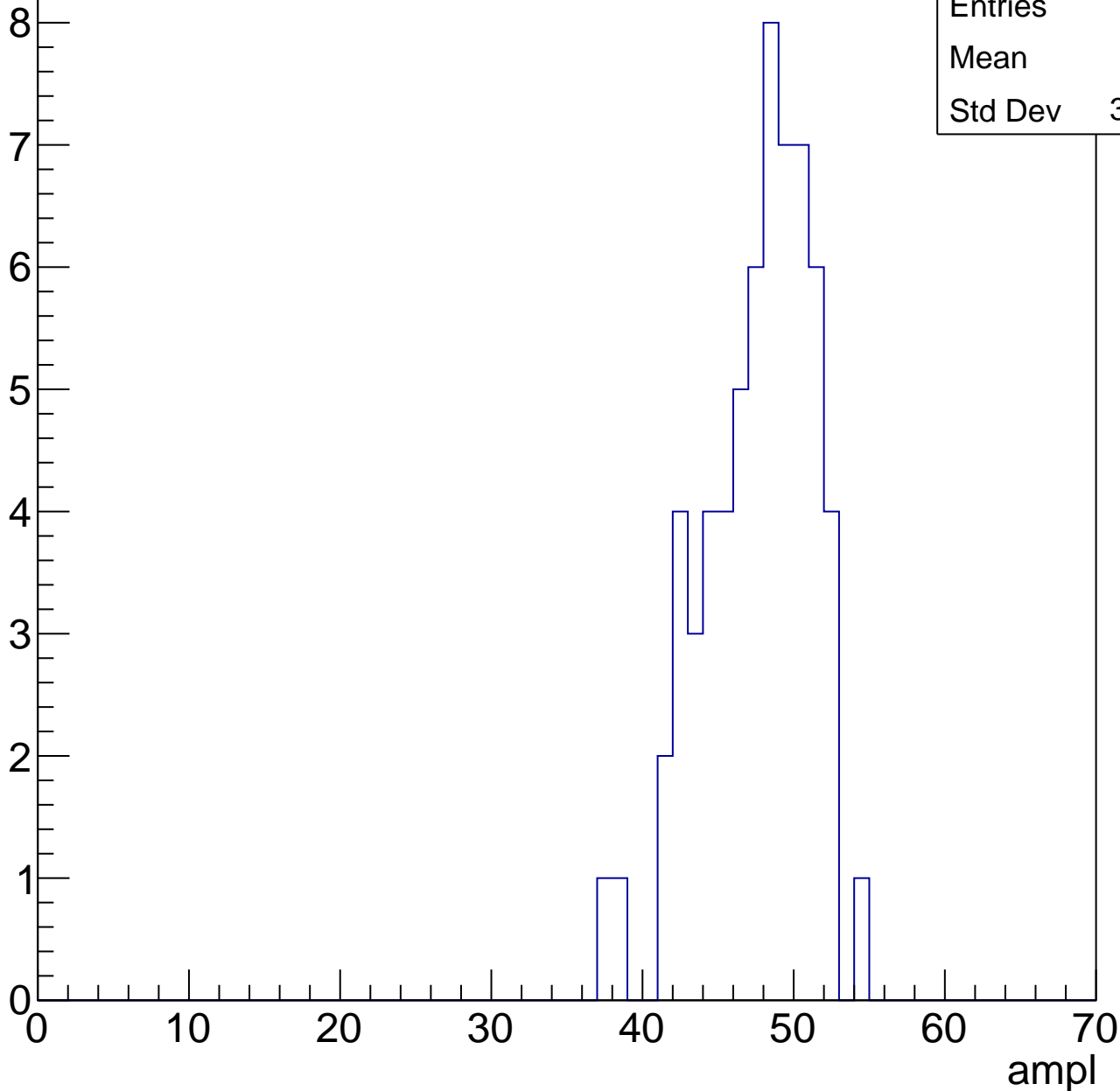


B1L101S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	47.1
Std Dev	3.567

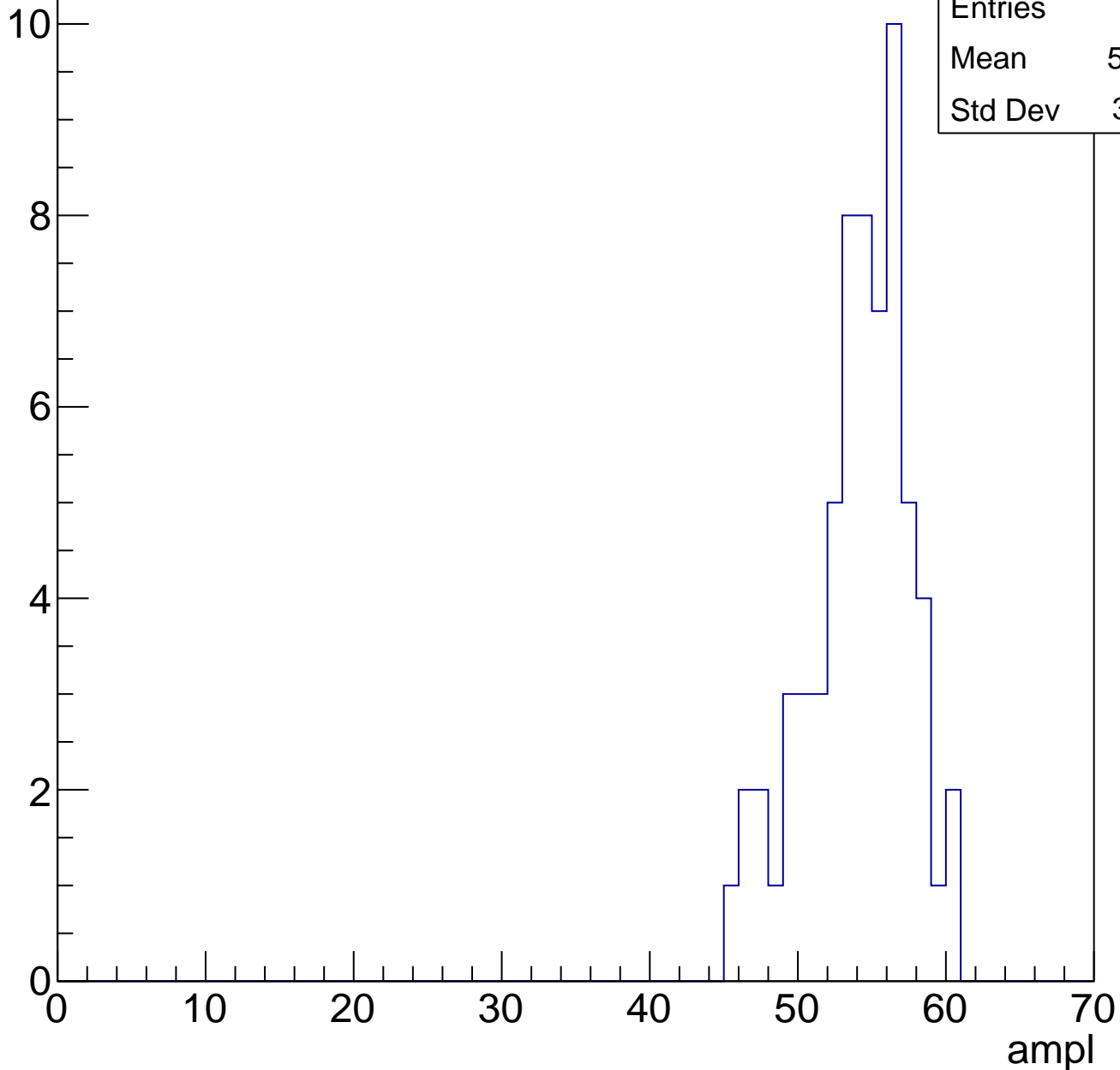


B1L101S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	53.63
Std Dev	3.431

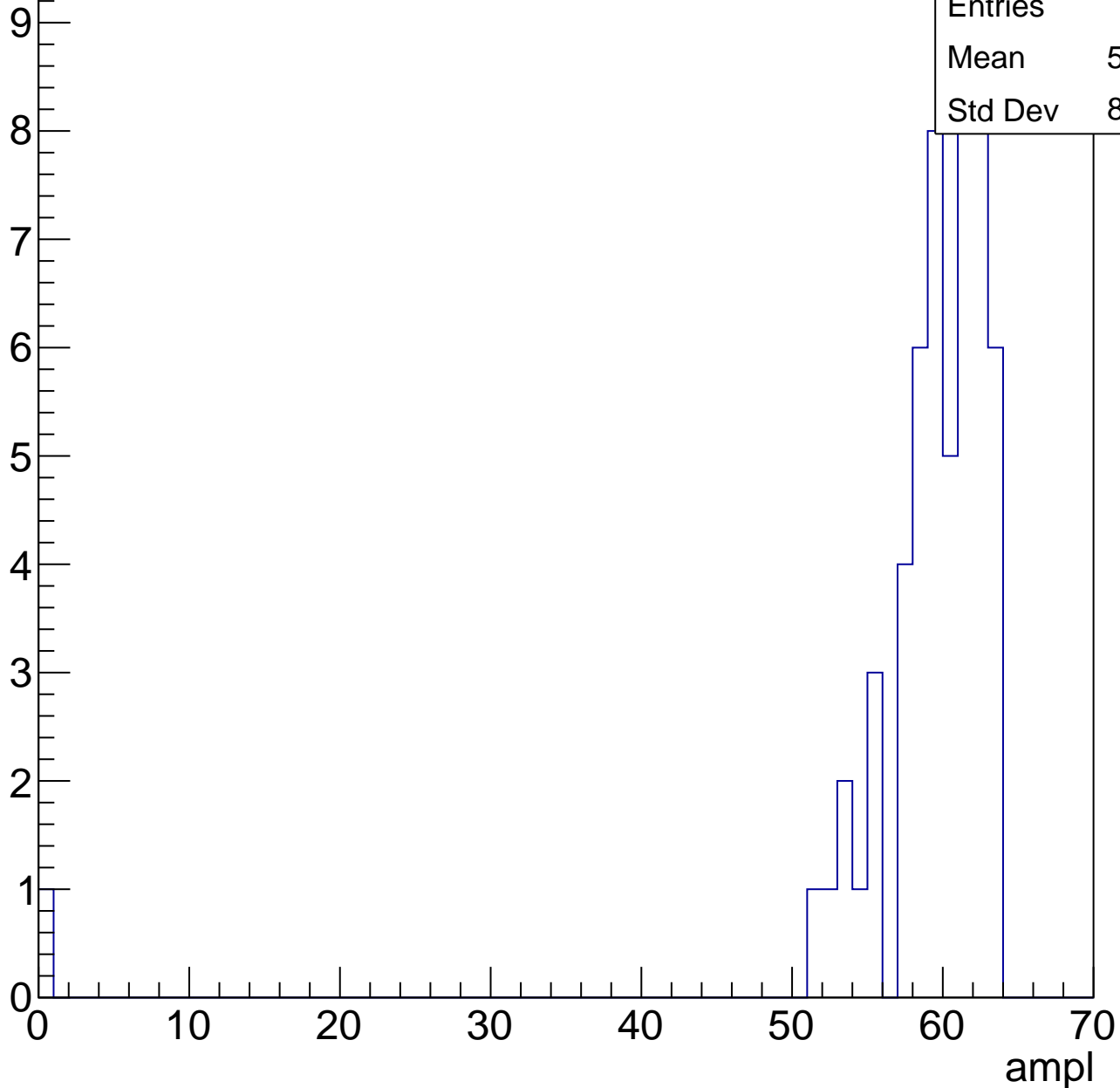
Entry



B1L101S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

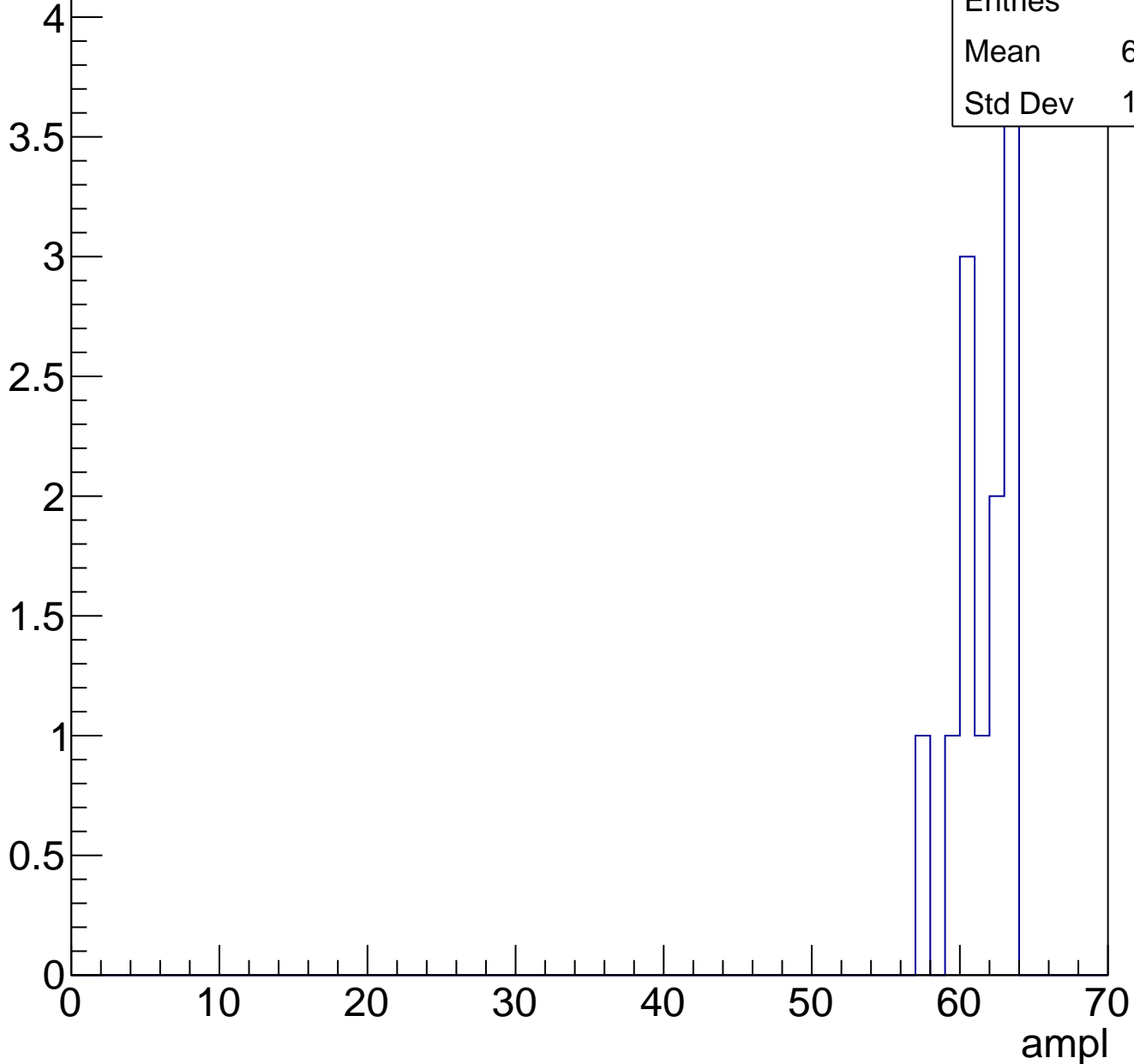
Entry



B1L101S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch73, adc0

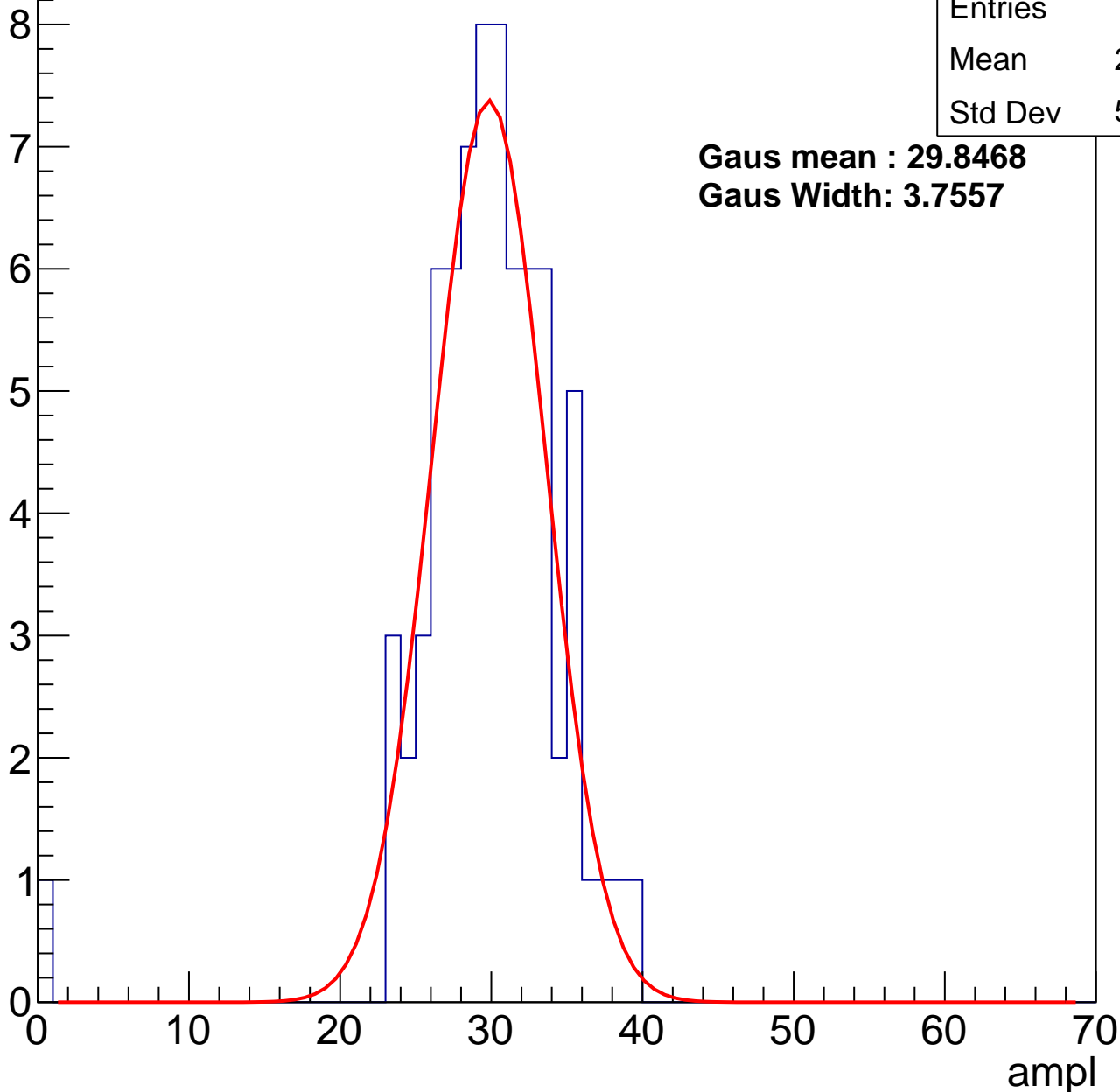
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.41
Std Dev	5.001

Gaus mean : 29.8468

Gaus Width: 3.7557



B1L101S, U14-ch73, adc1

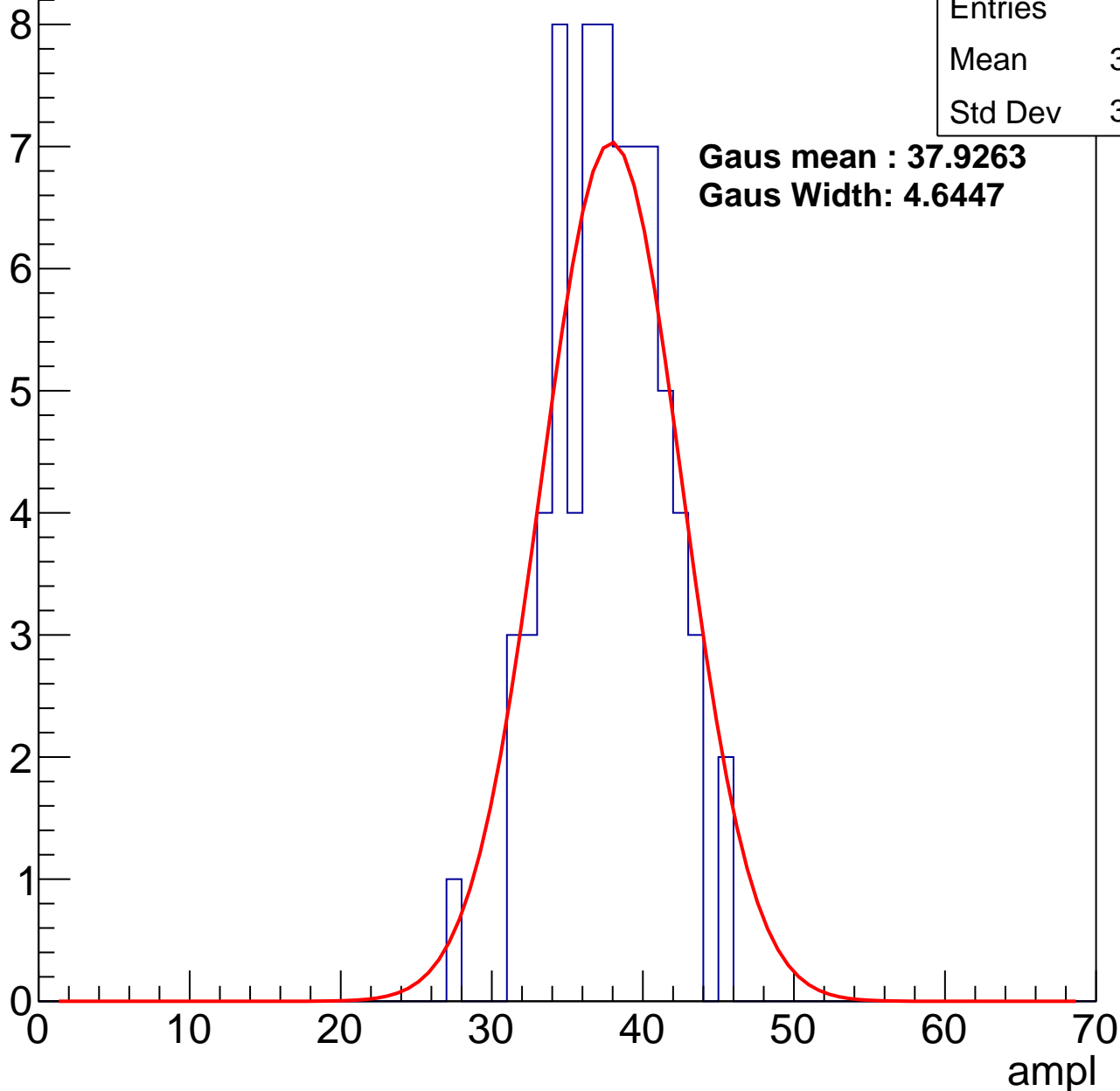
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.23
Std Dev	3.589

Gaus mean : 37.9263

Gaus Width: 4.6447



B1L101S, U14-ch73, adc2

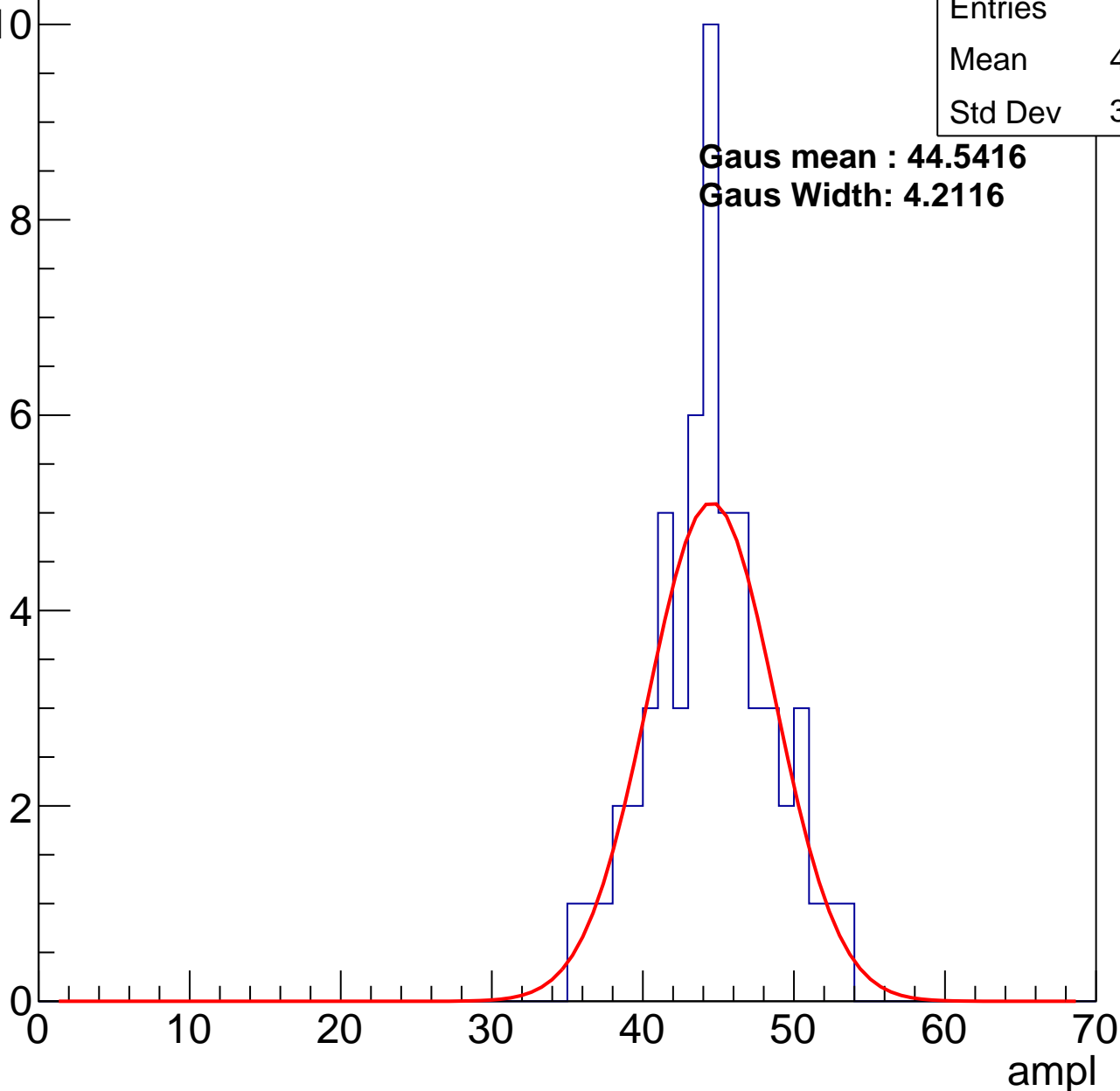
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.05
Std Dev	3.893

Gaus mean : 44.5416

Gaus Width: 4.2116

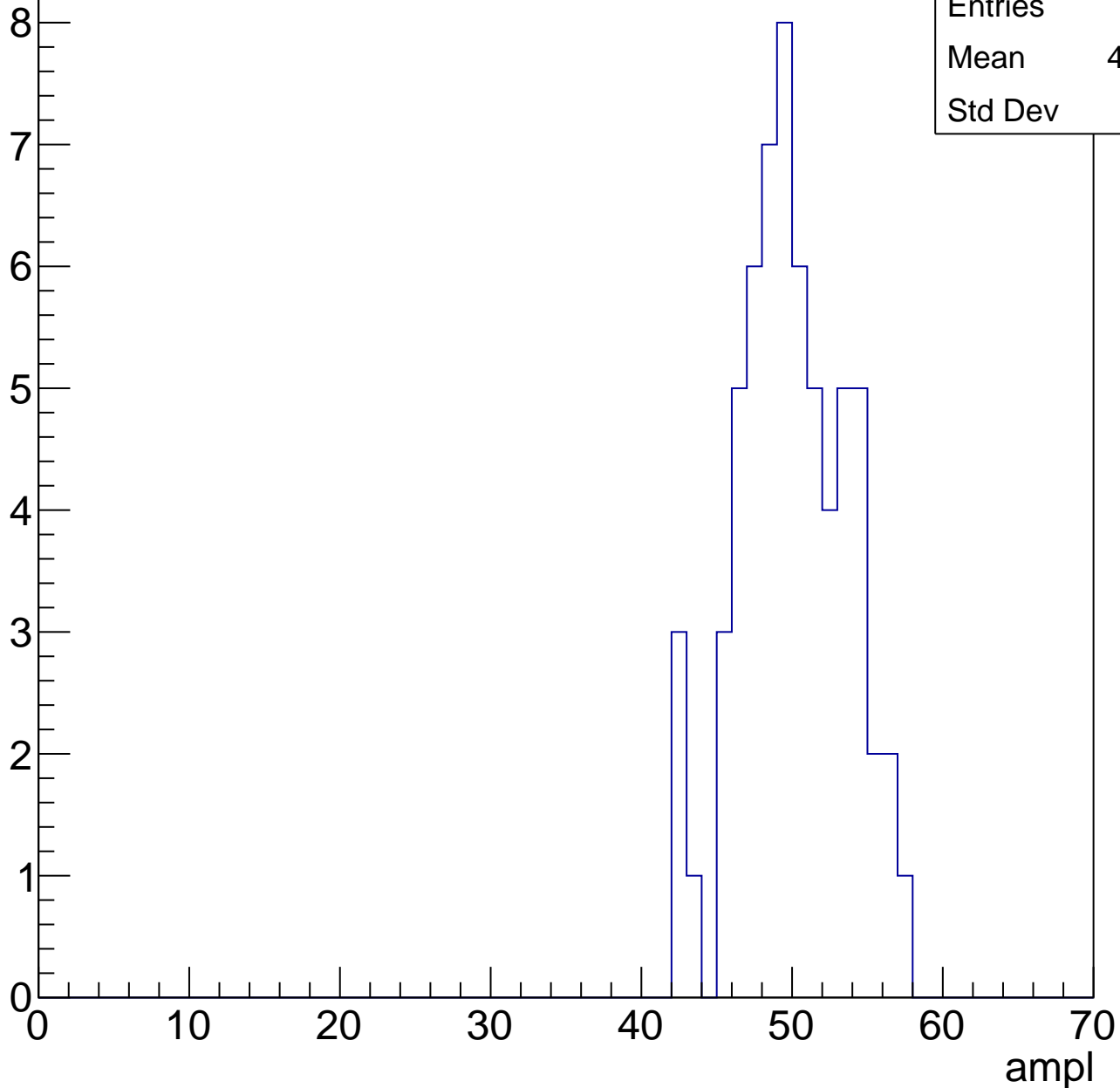


B1L101S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.54
Std Dev	3.54

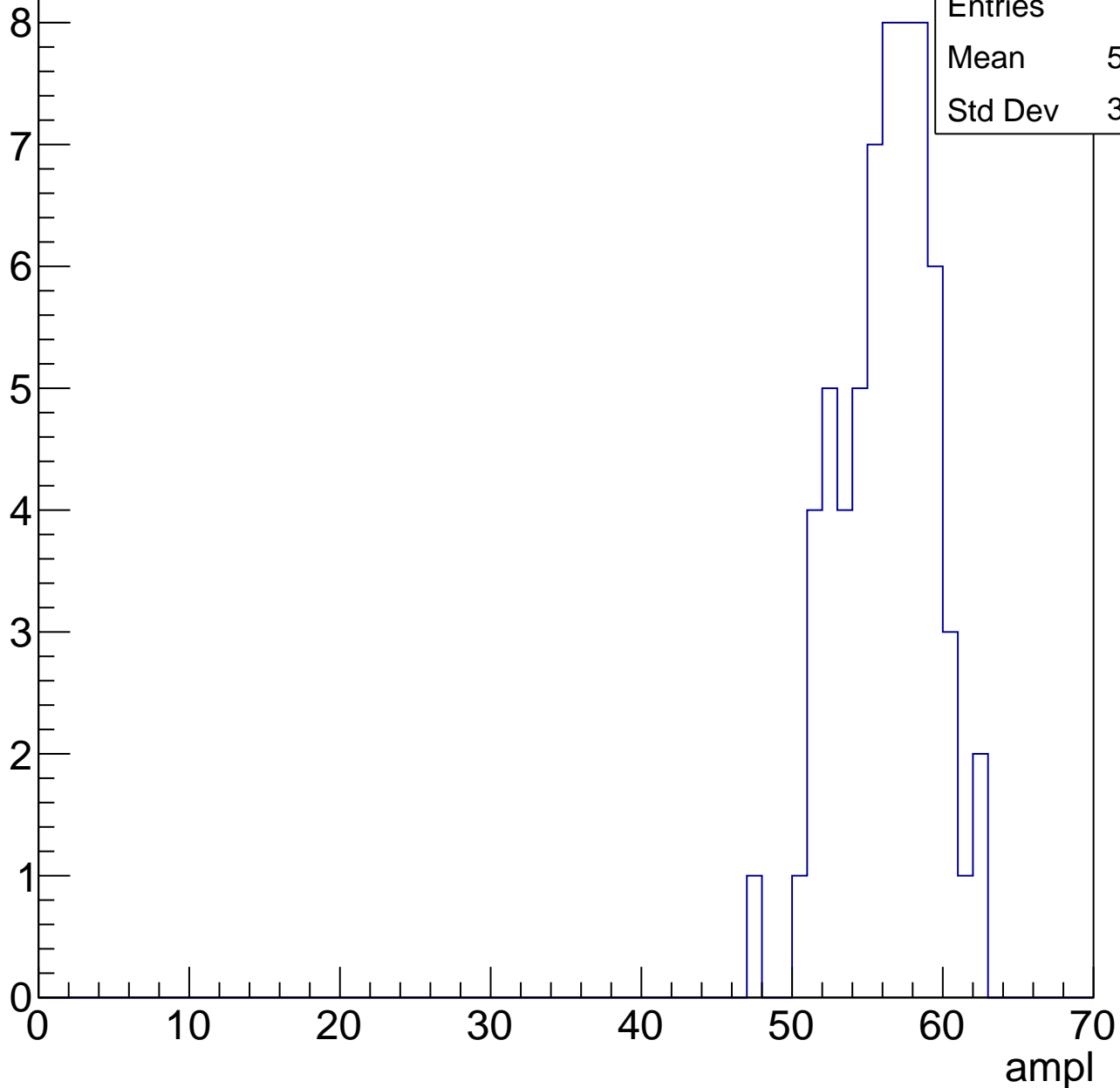


B1L101S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

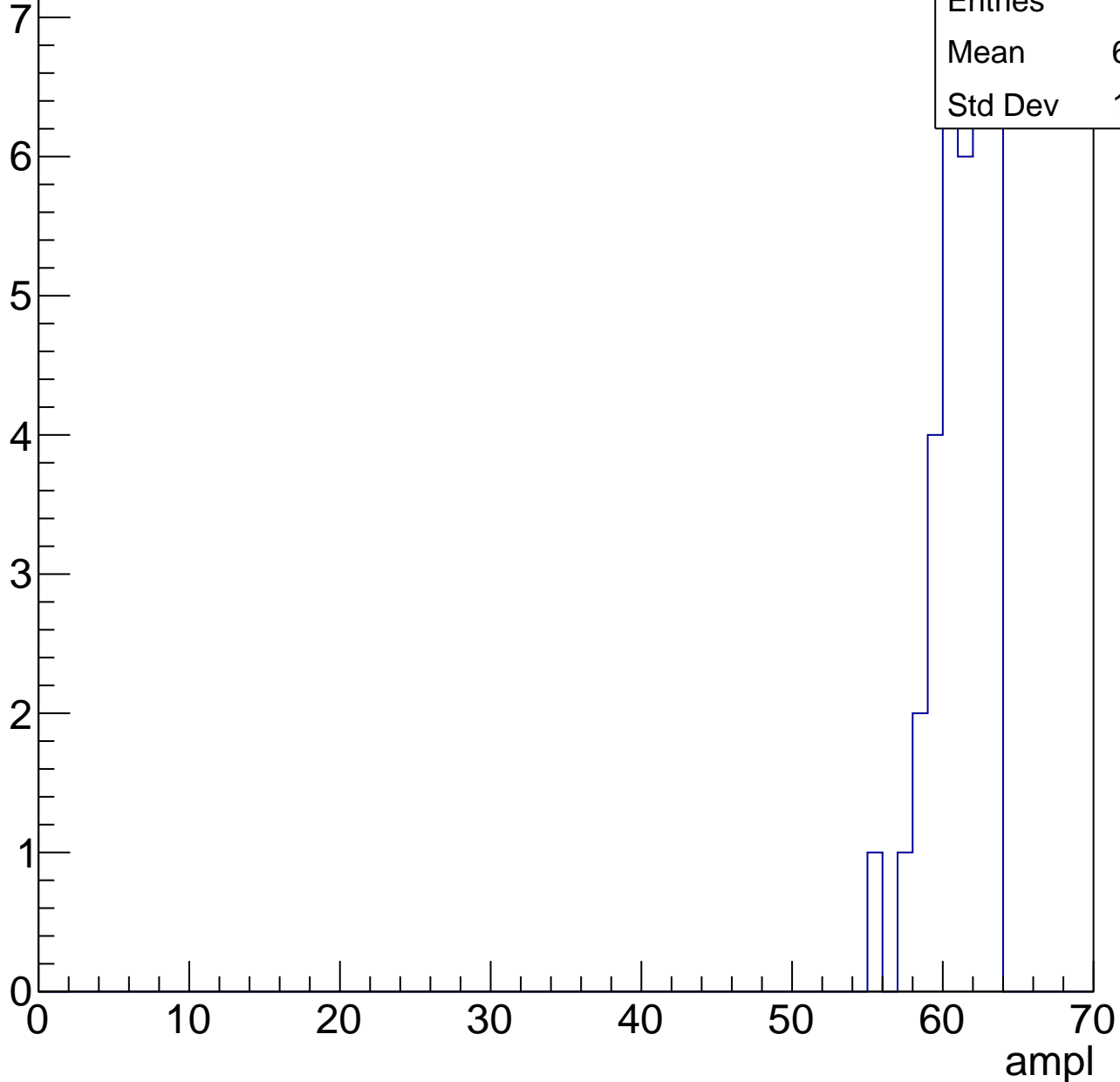
Entries	63
Mean	55.79
Std Dev	3.077



B1L101S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

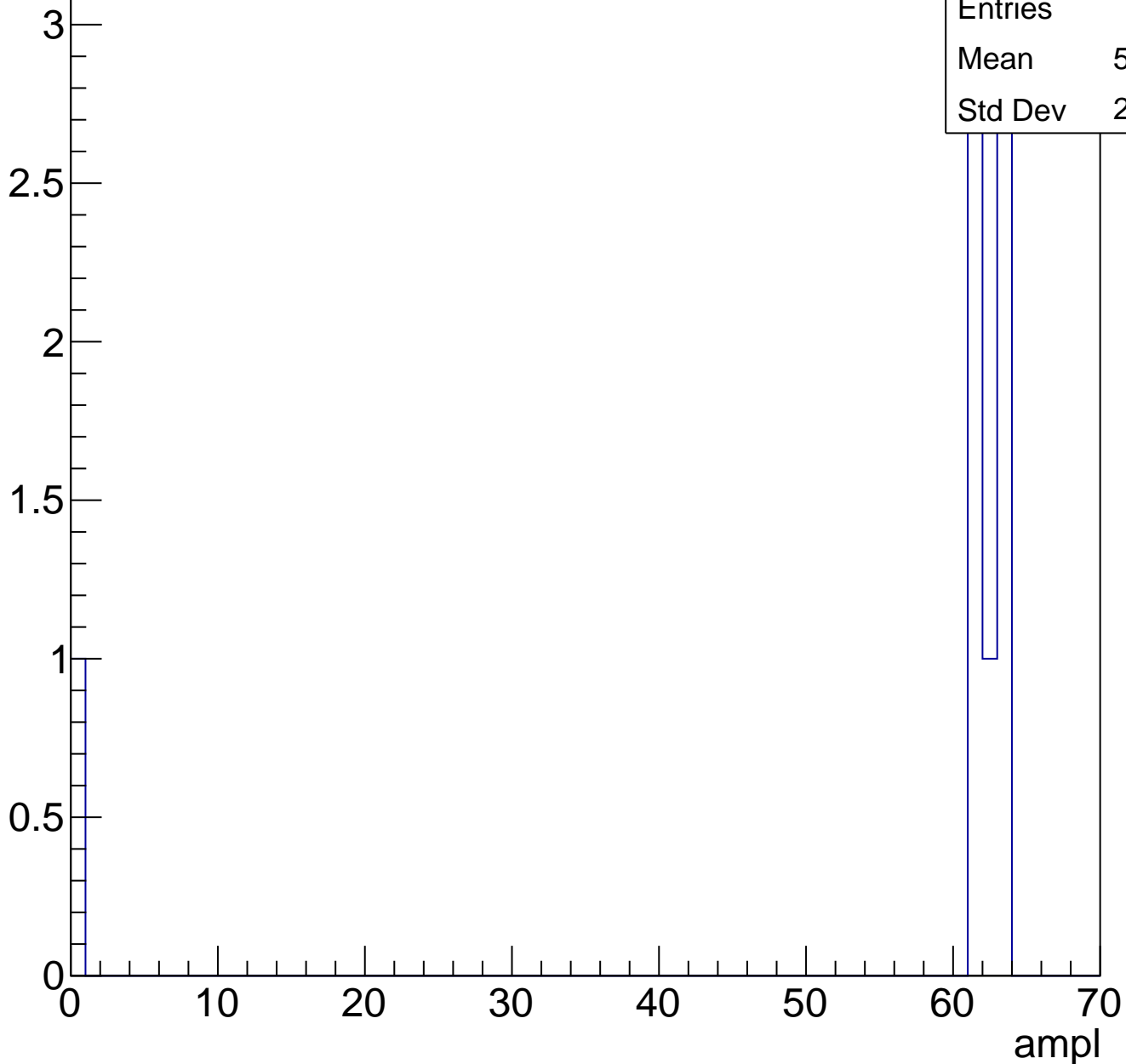


Entries	35
Mean	60.71
Std Dev	1.891

B1L101S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch74, adc0

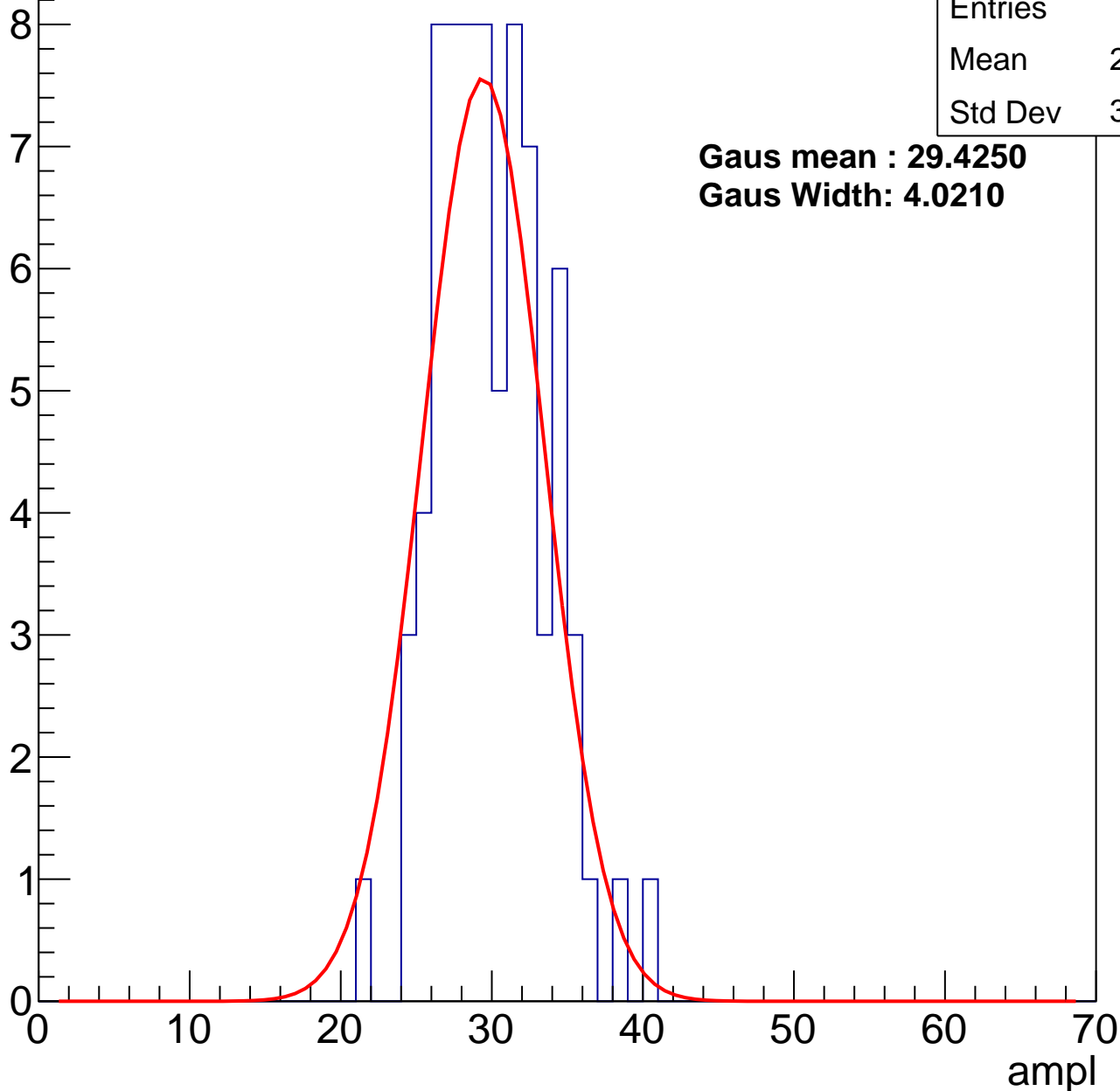
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.56
Std Dev	3.556

Gaus mean : 29.4250

Gaus Width: 4.0210



B1L101S, U14-ch74, adc1

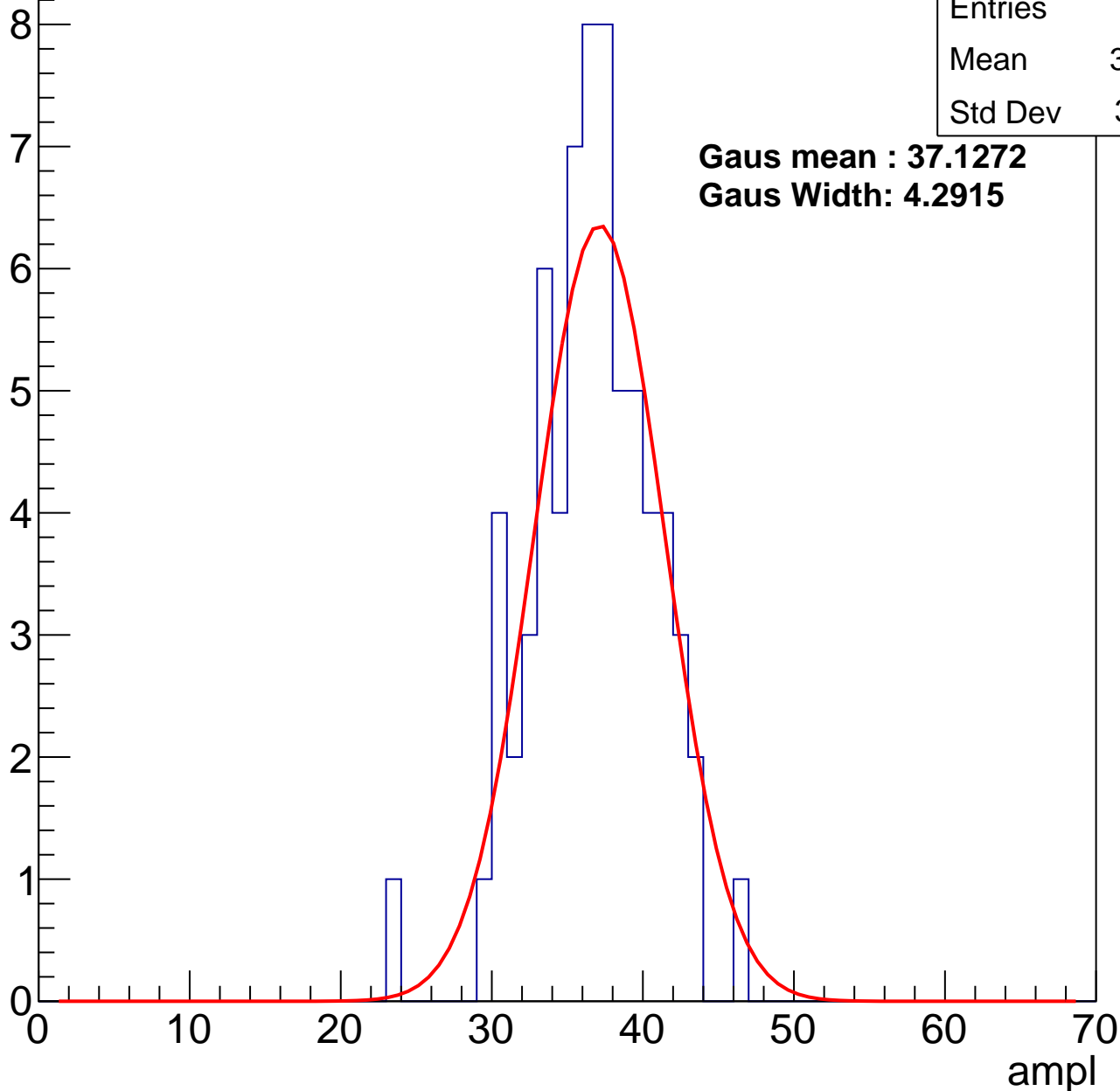
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.18
Std Dev	3.981

Gaus mean : 37.1272

Gaus Width: 4.2915



B1L101S, U14-ch74, adc2

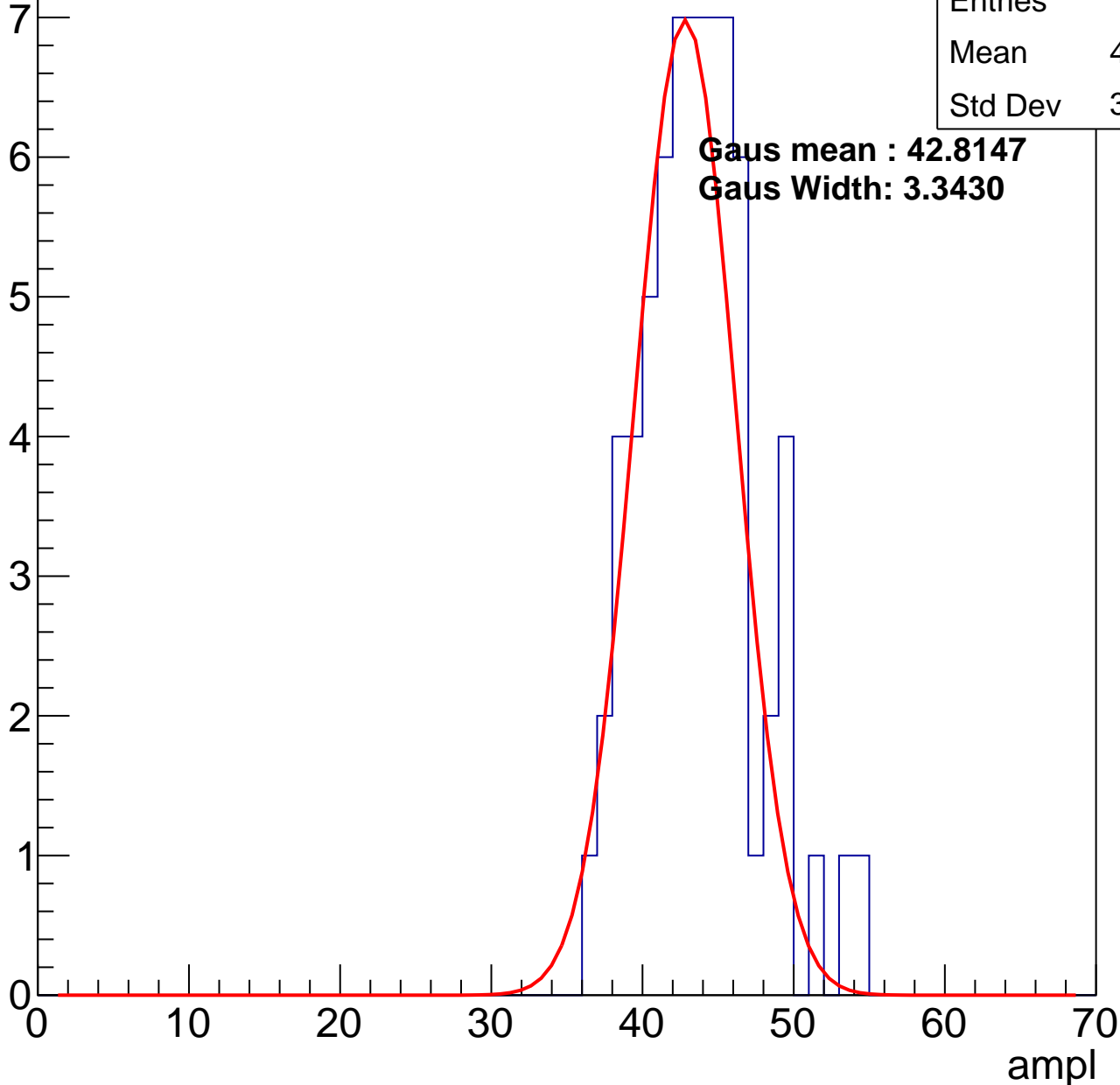
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.26
Std Dev	3.775

Gaus mean : 42.8147

Gaus Width: 3.3430

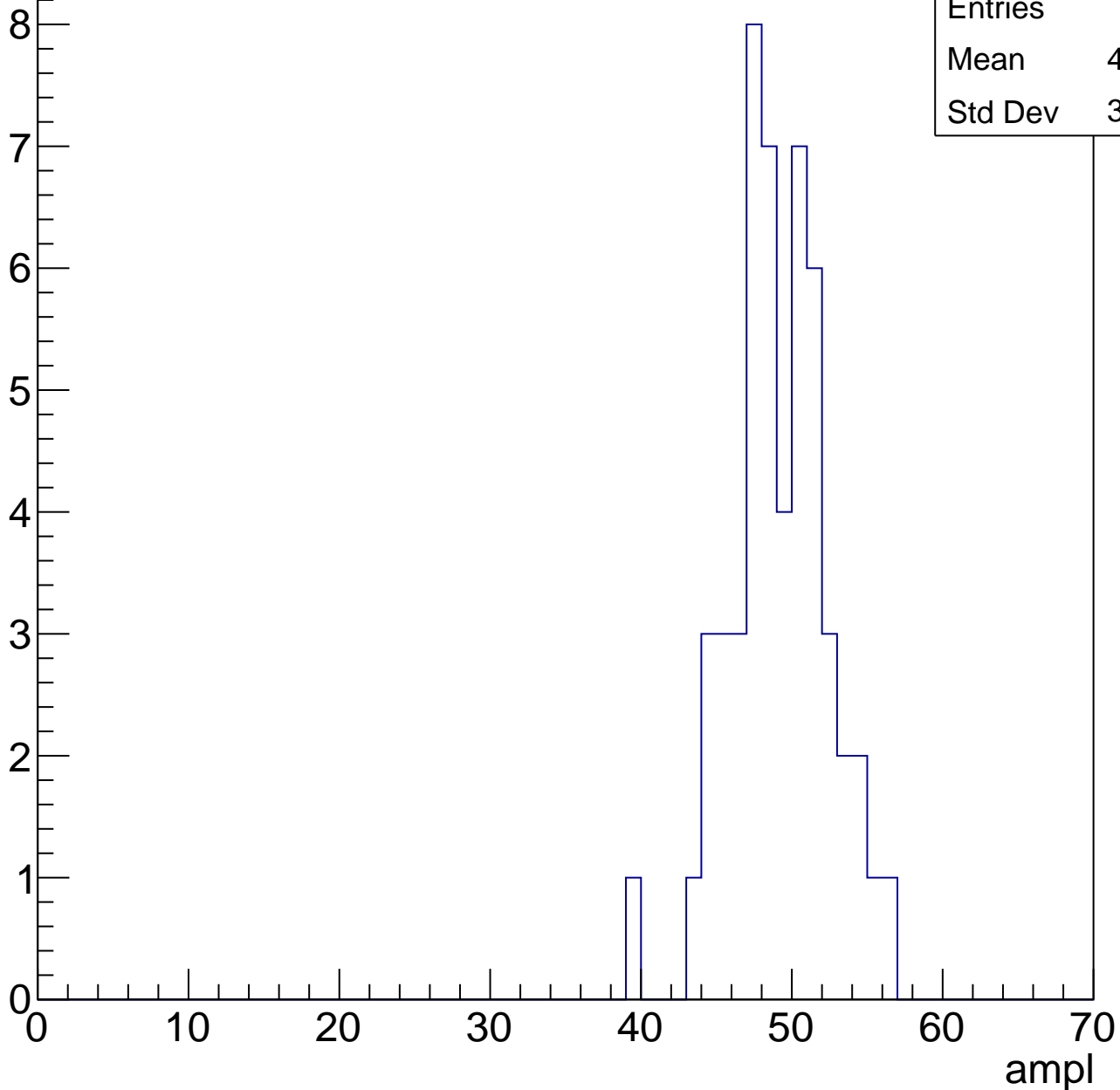


B1L101S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.69
Std Dev	3.238



B1L101S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 54.35

Std Dev 3.275

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

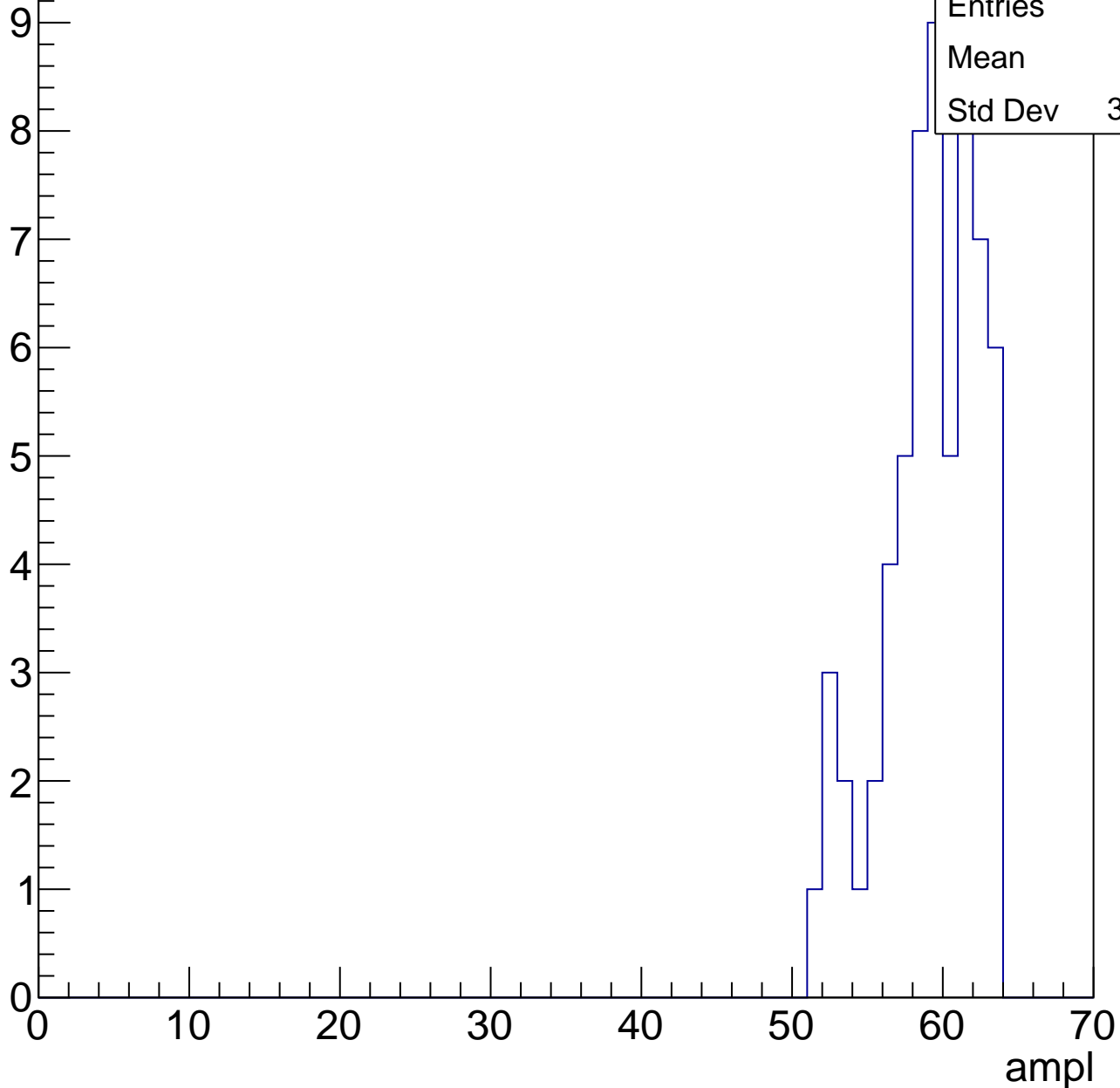
6

7

B1L101S, U14-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

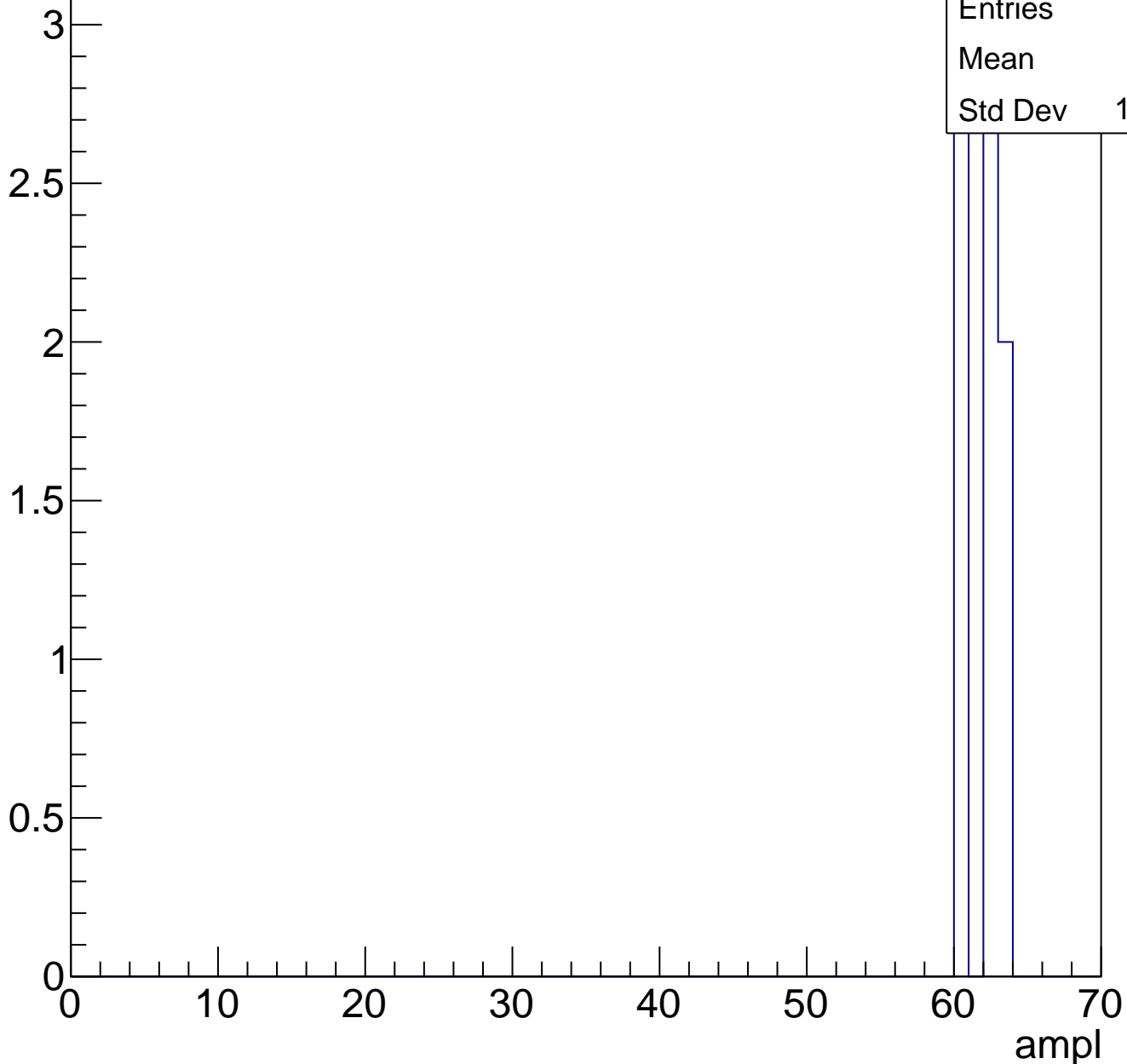
Entry



B1L101S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	61.5
Std Dev	1.225

B1L101S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

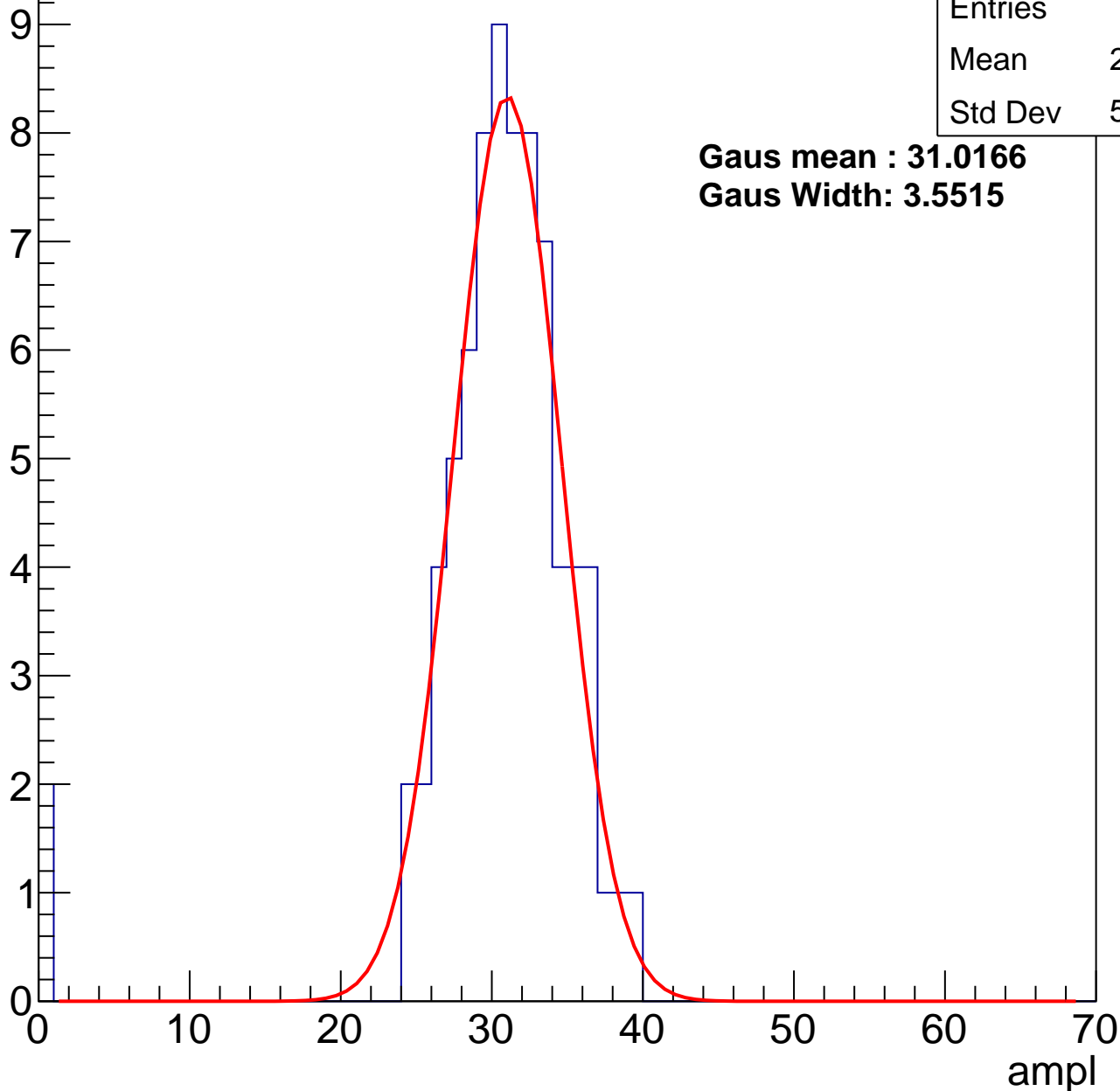
Entry



B1L101S, U14-ch75, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	76
Mean	29.95
Std Dev	5.925

Gaus mean : 31.0166
Gaus Width: 3.5515

B1L101S, U14-ch75, adc1

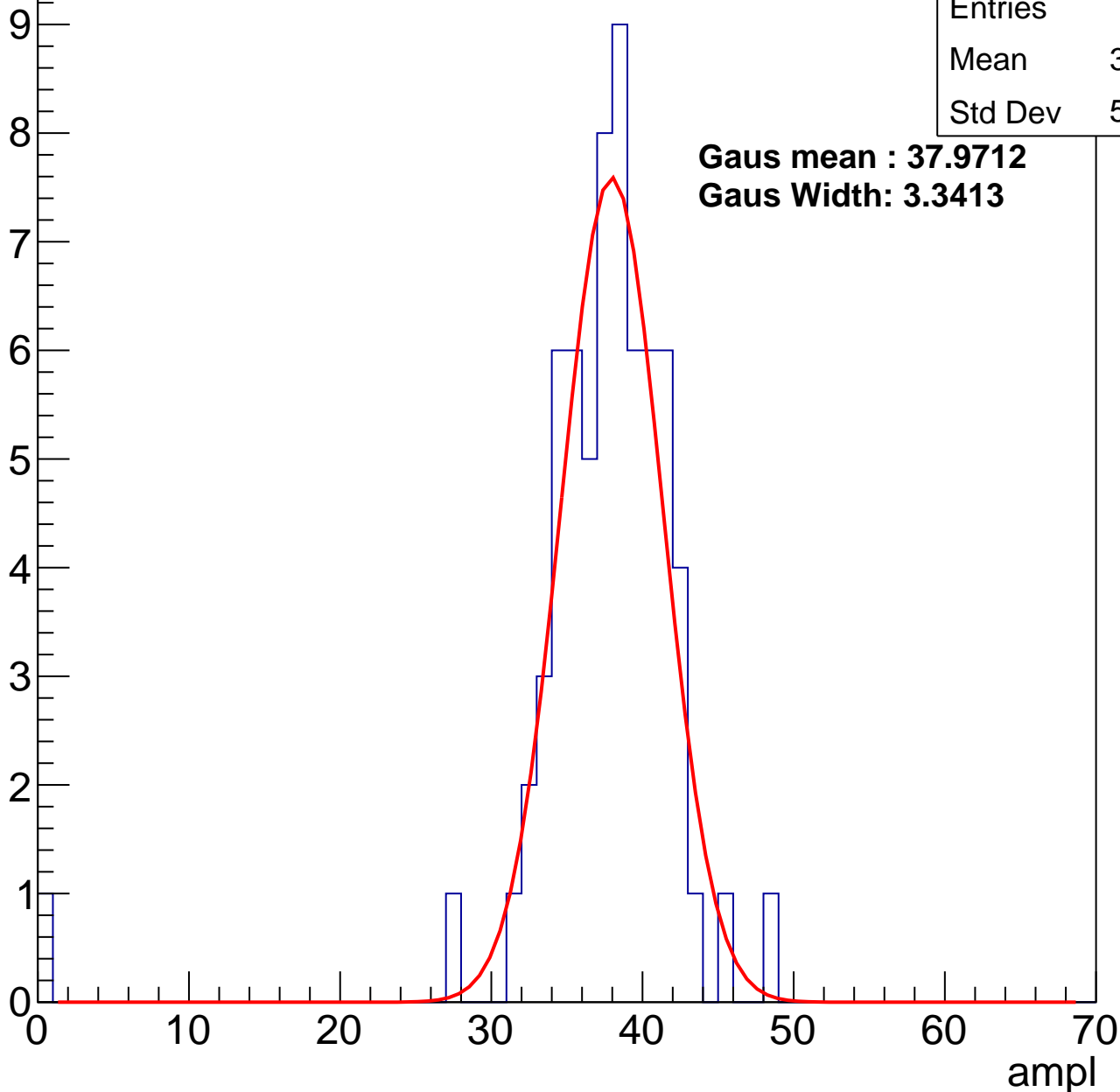
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.97
Std Dev	5.717

Gaus mean : 37.9712

Gaus Width: 3.3413



B1L101S, U14-ch75, adc2

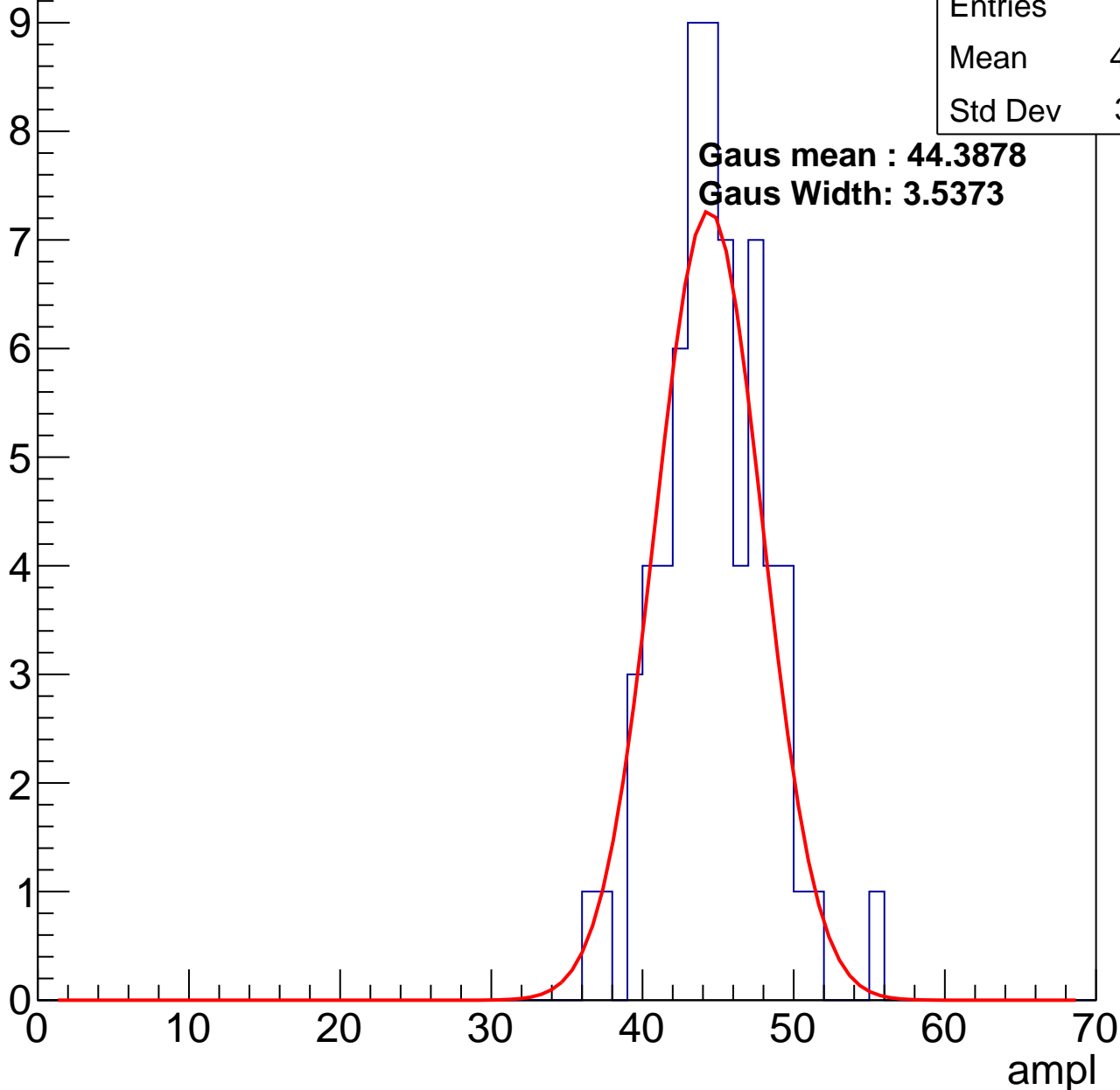
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.26
Std Dev	3.421

Gaus mean : 44.3878

Gaus Width: 3.5373

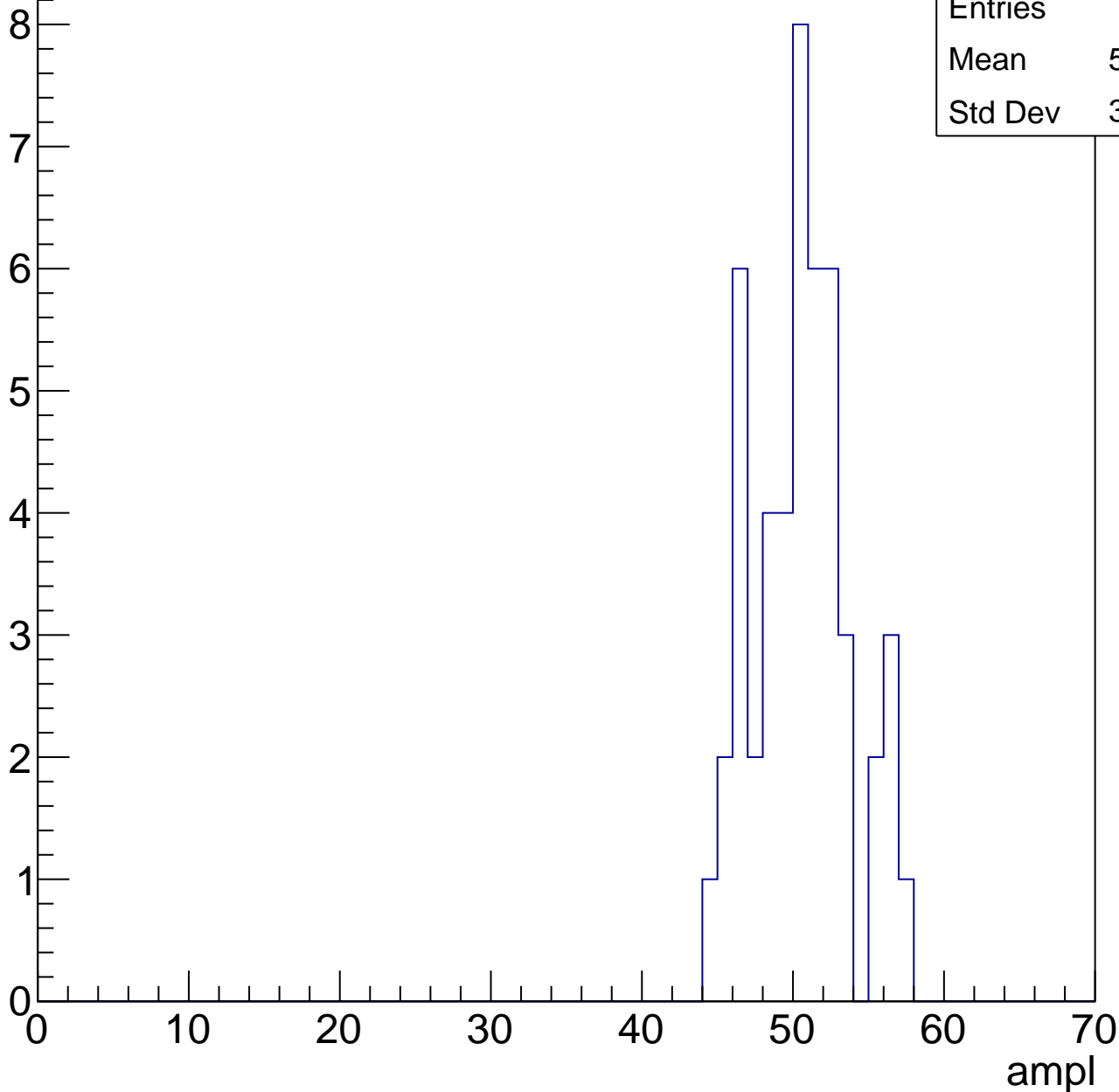


B1L101S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	50.08
Std Dev	3.174



B1L101S, U14-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

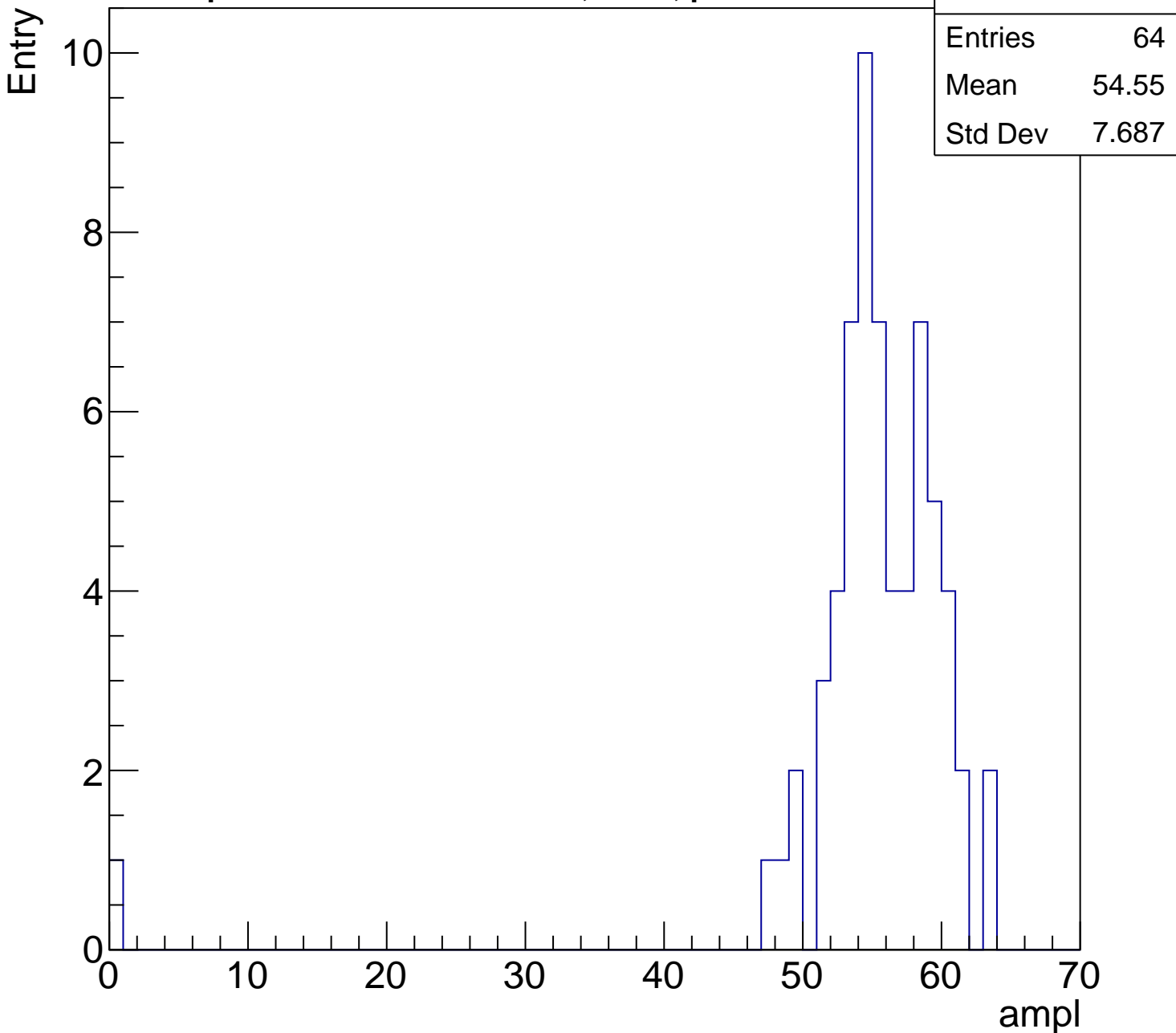
Entries	64
Mean	54.55
Std Dev	7.687

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

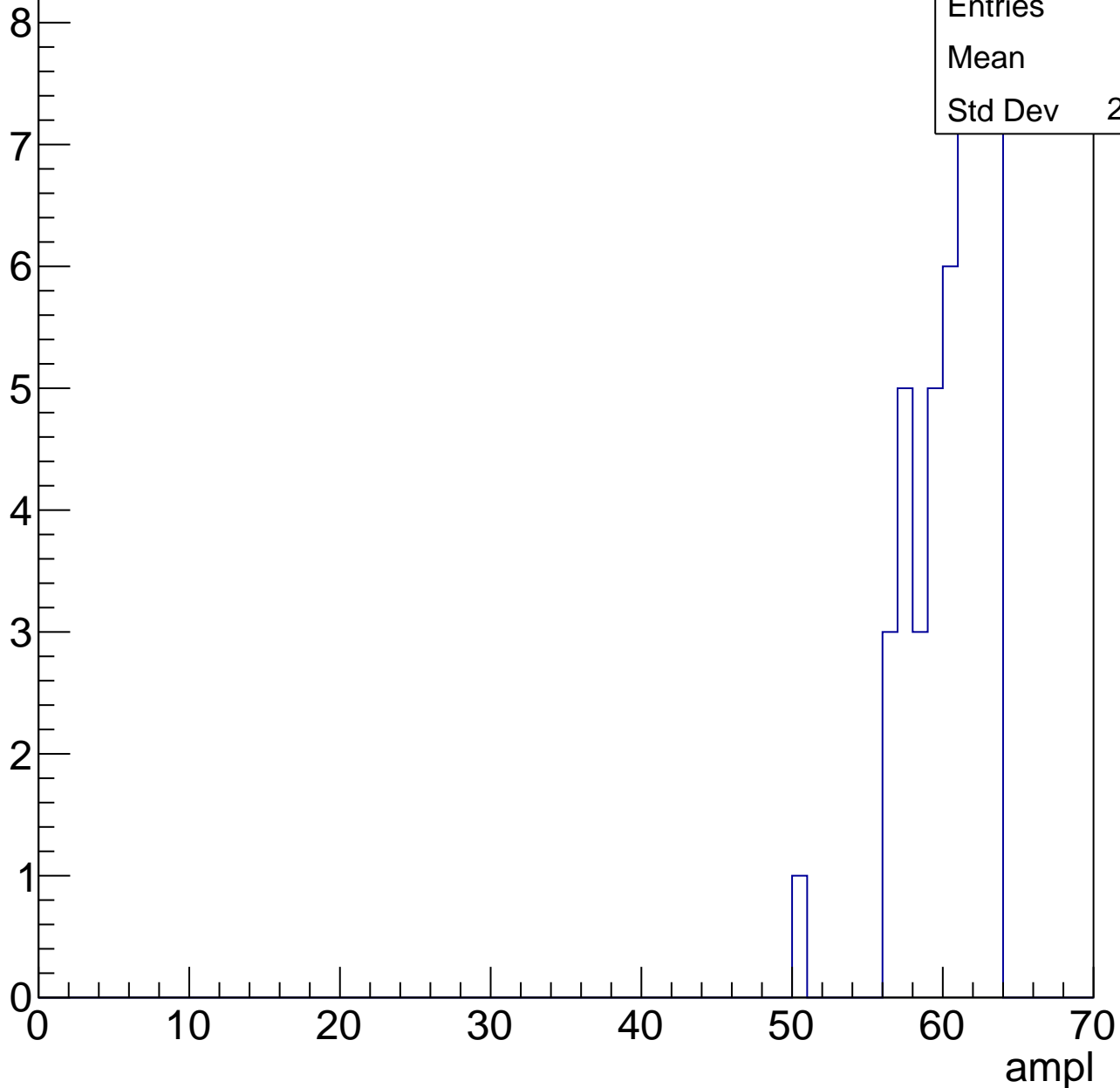
ampl



B1L101S, U14-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch76, adc0

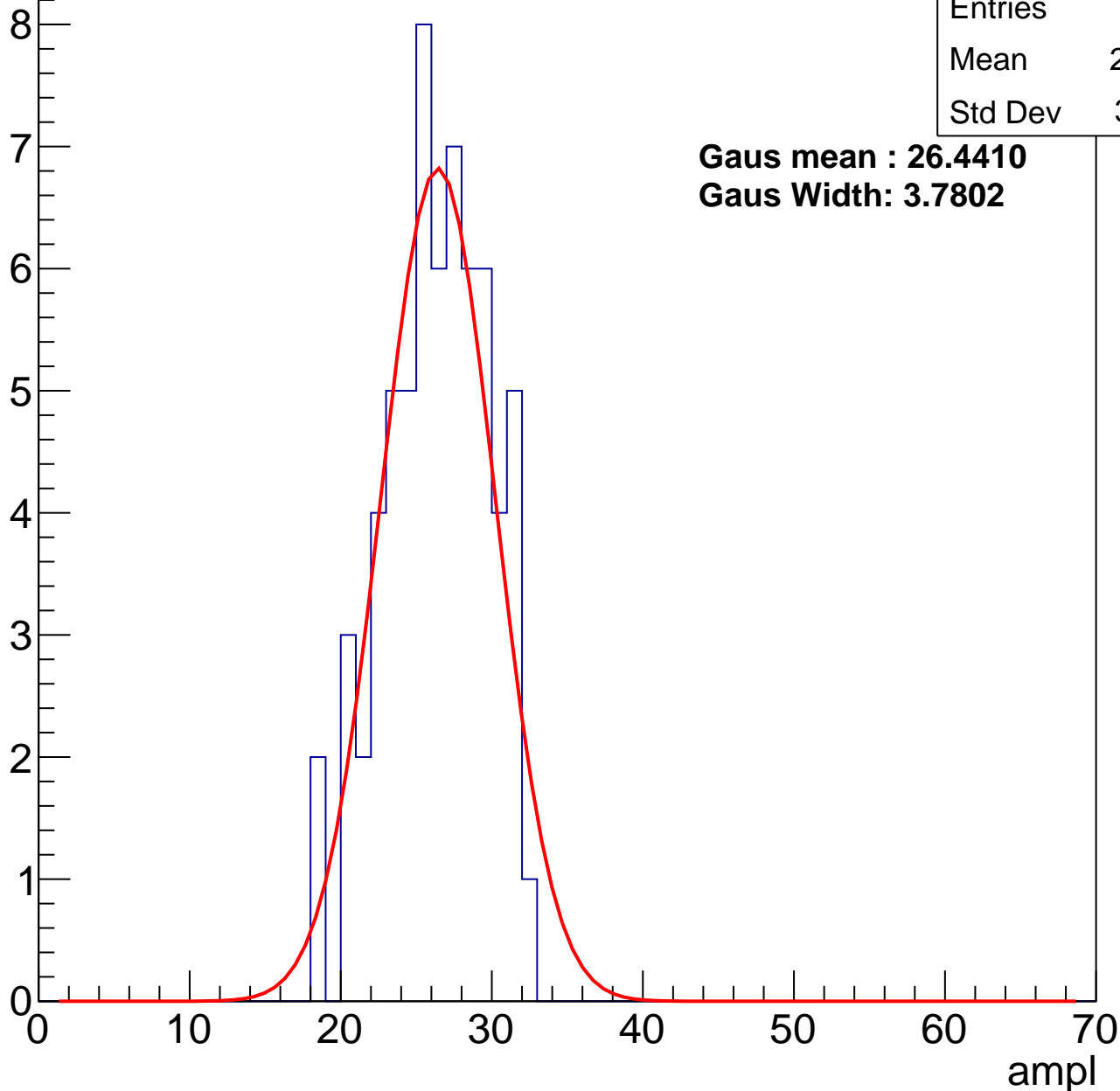
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	25.86
Std Dev	3.381

Gaus mean : 26.4410

Gaus Width: 3.7802



B1L101S, U14-ch76, adc1

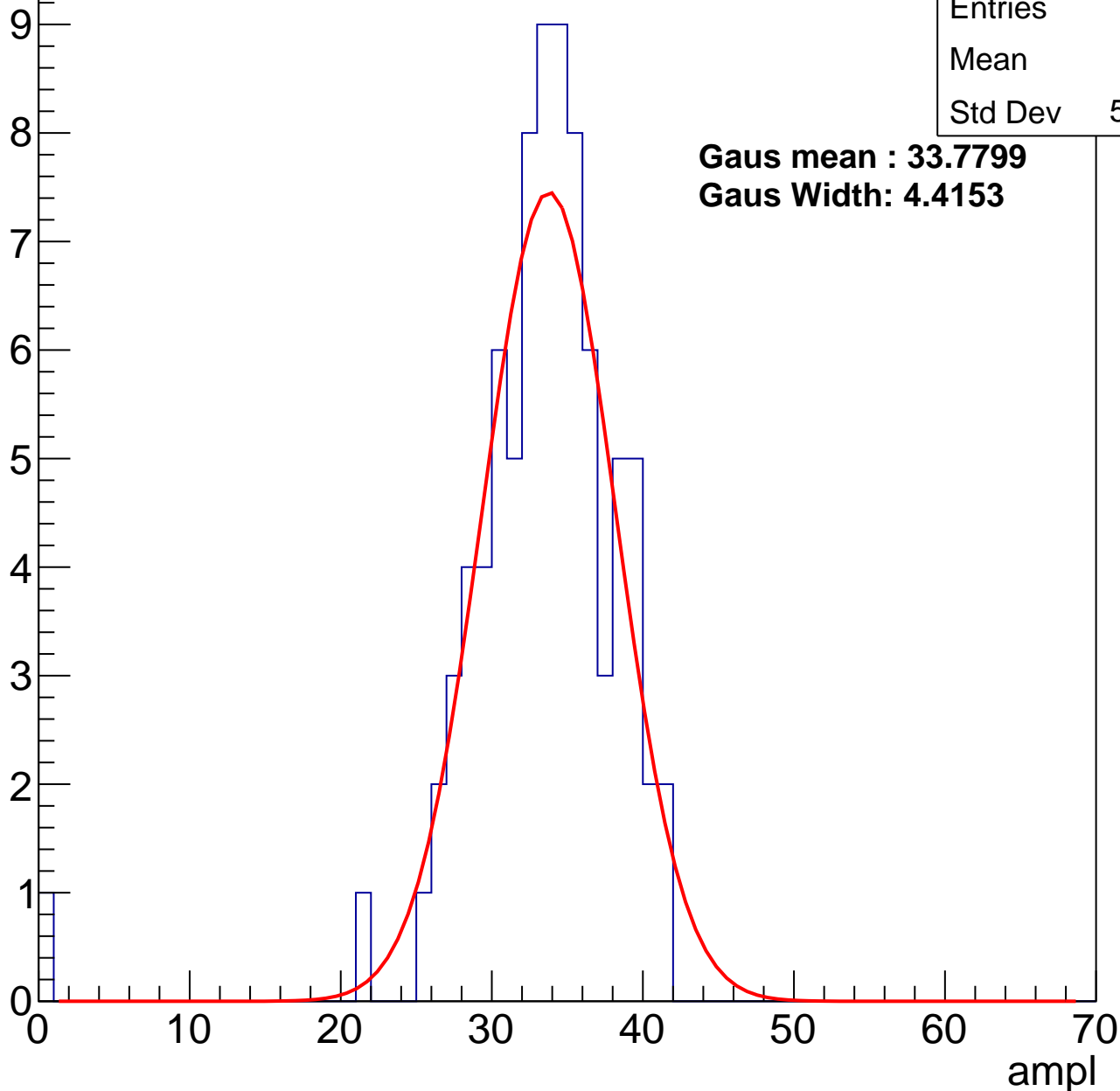
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	32.8
Std Dev	5.367

Gaus mean : 33.7799

Gaus Width: 4.4153



B1L101S, U14-ch76, adc2

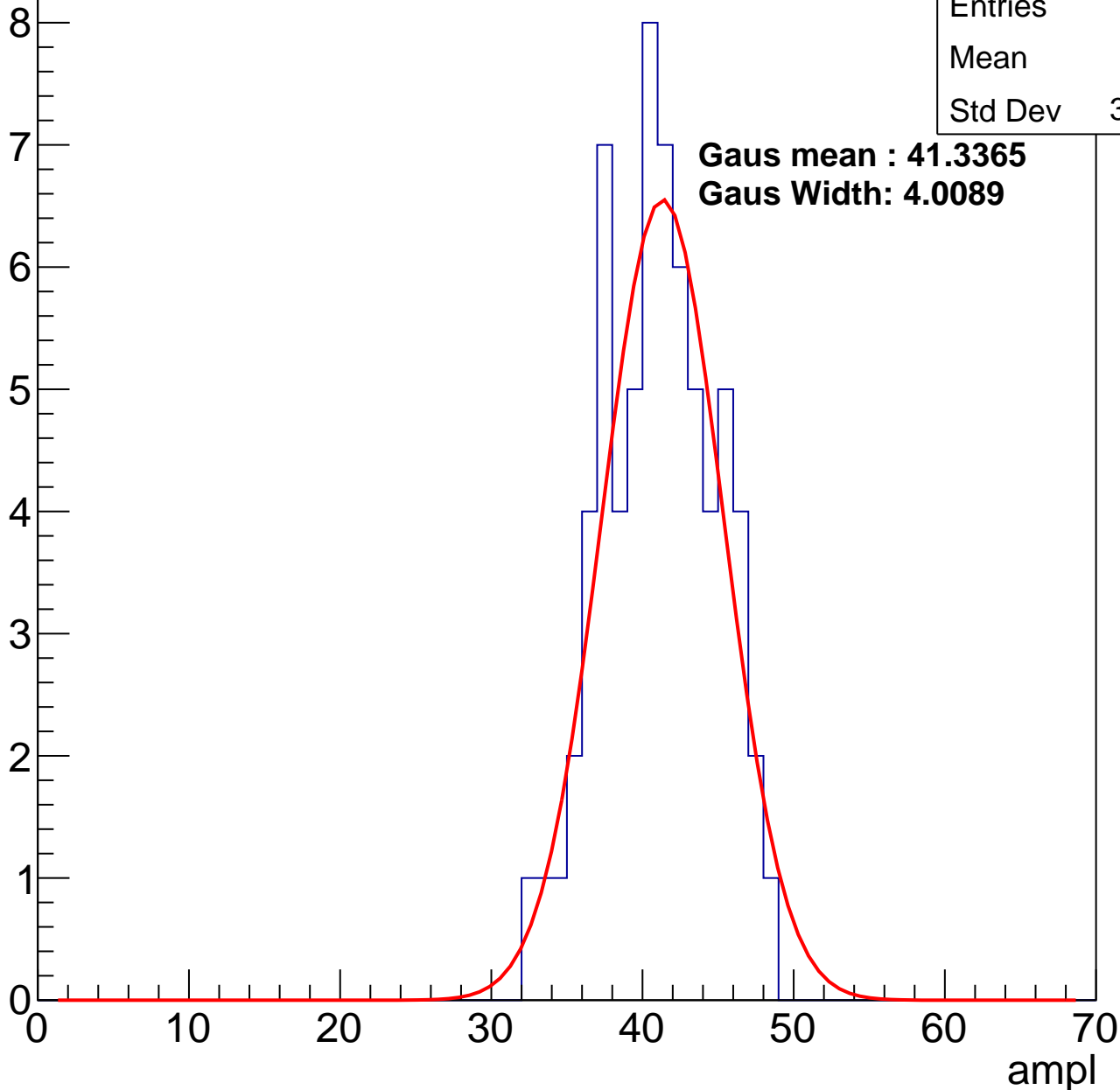
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	40.6
Std Dev	3.653

Gaus mean : 41.3365

Gaus Width: 4.0089

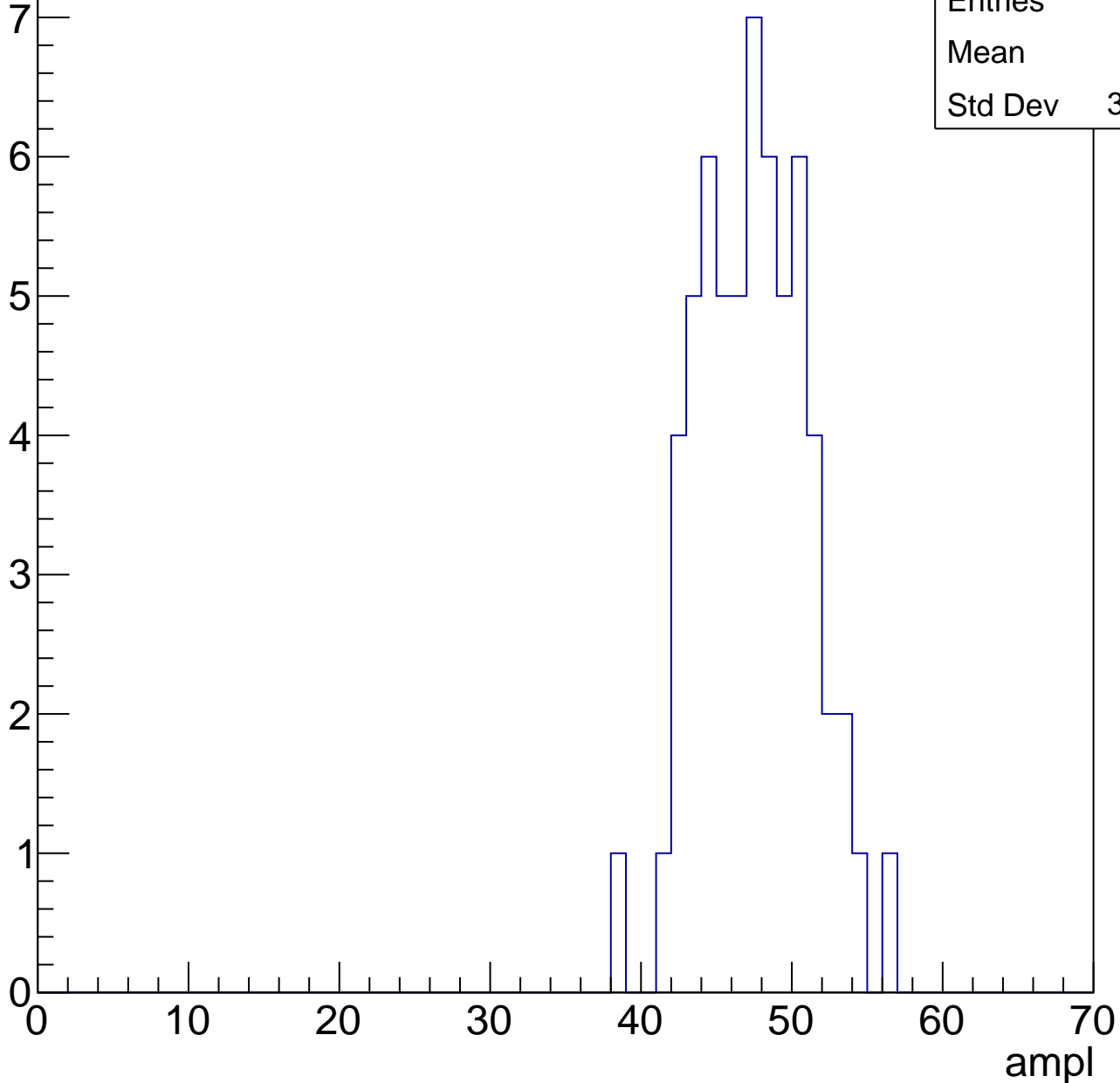


B1L101S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

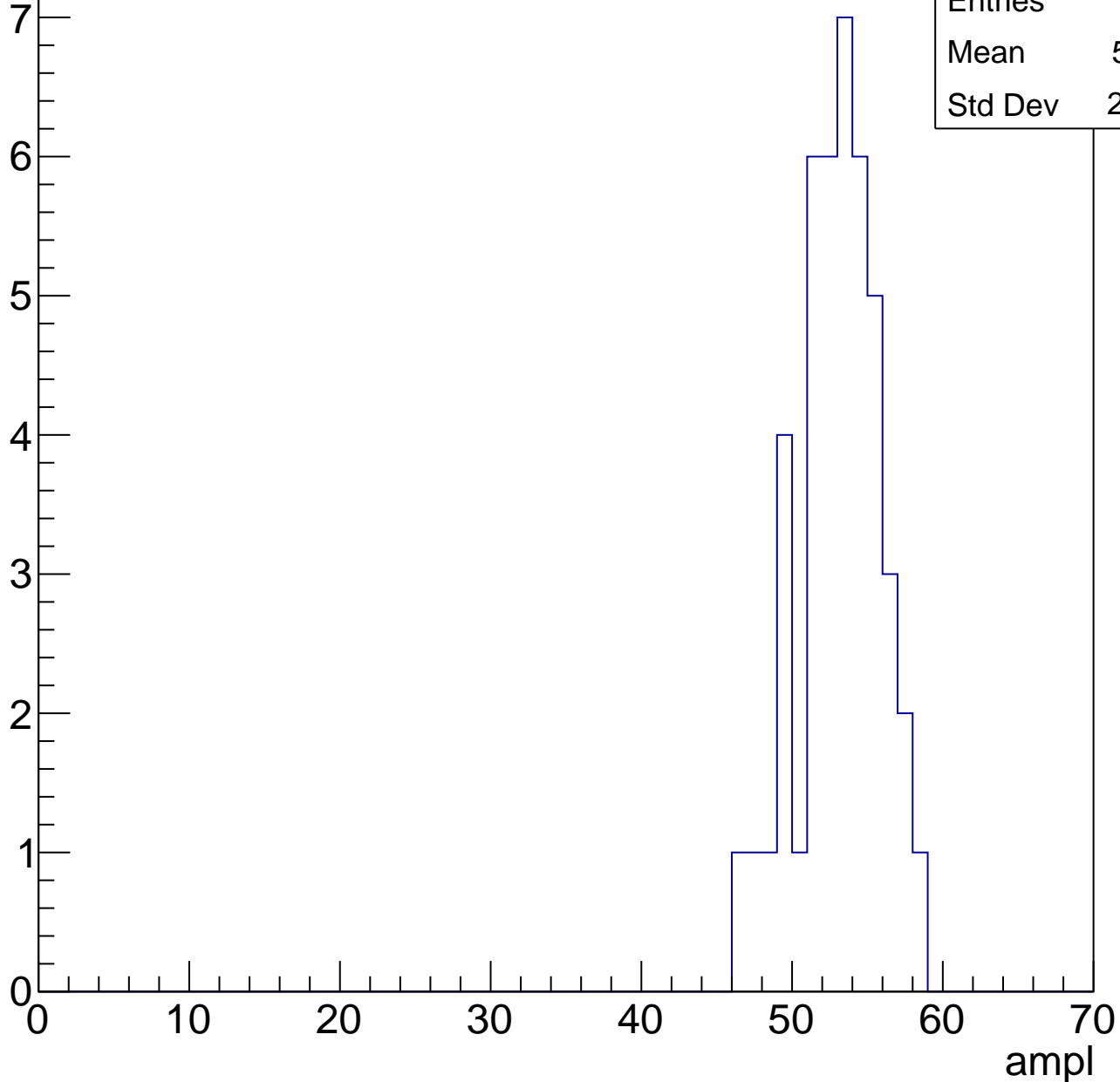
Entries	61
Mean	47
Std Dev	3.557



B1L101S, U14-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

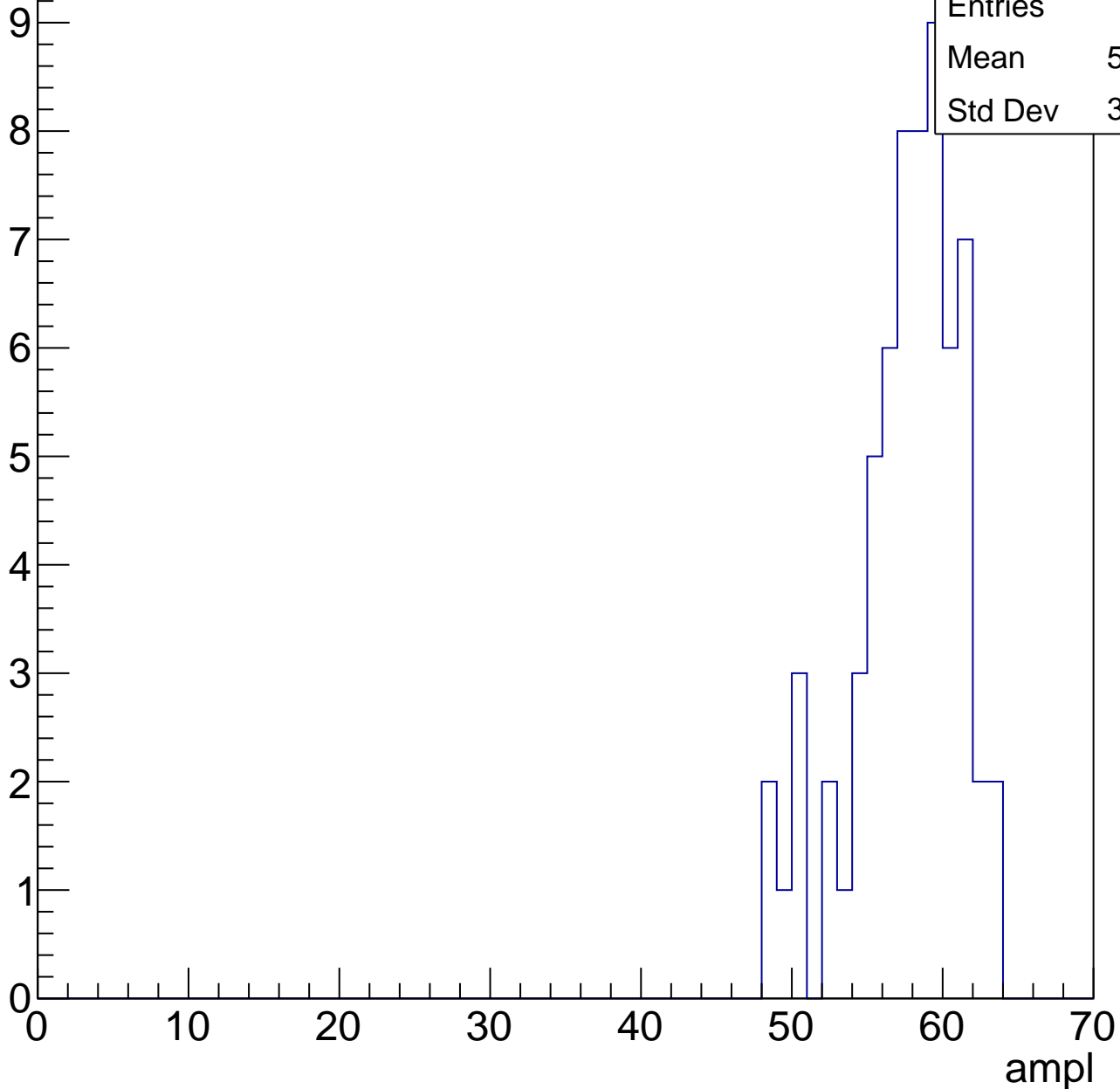


B1L101S, U14-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	57.12
Std Dev	3.558

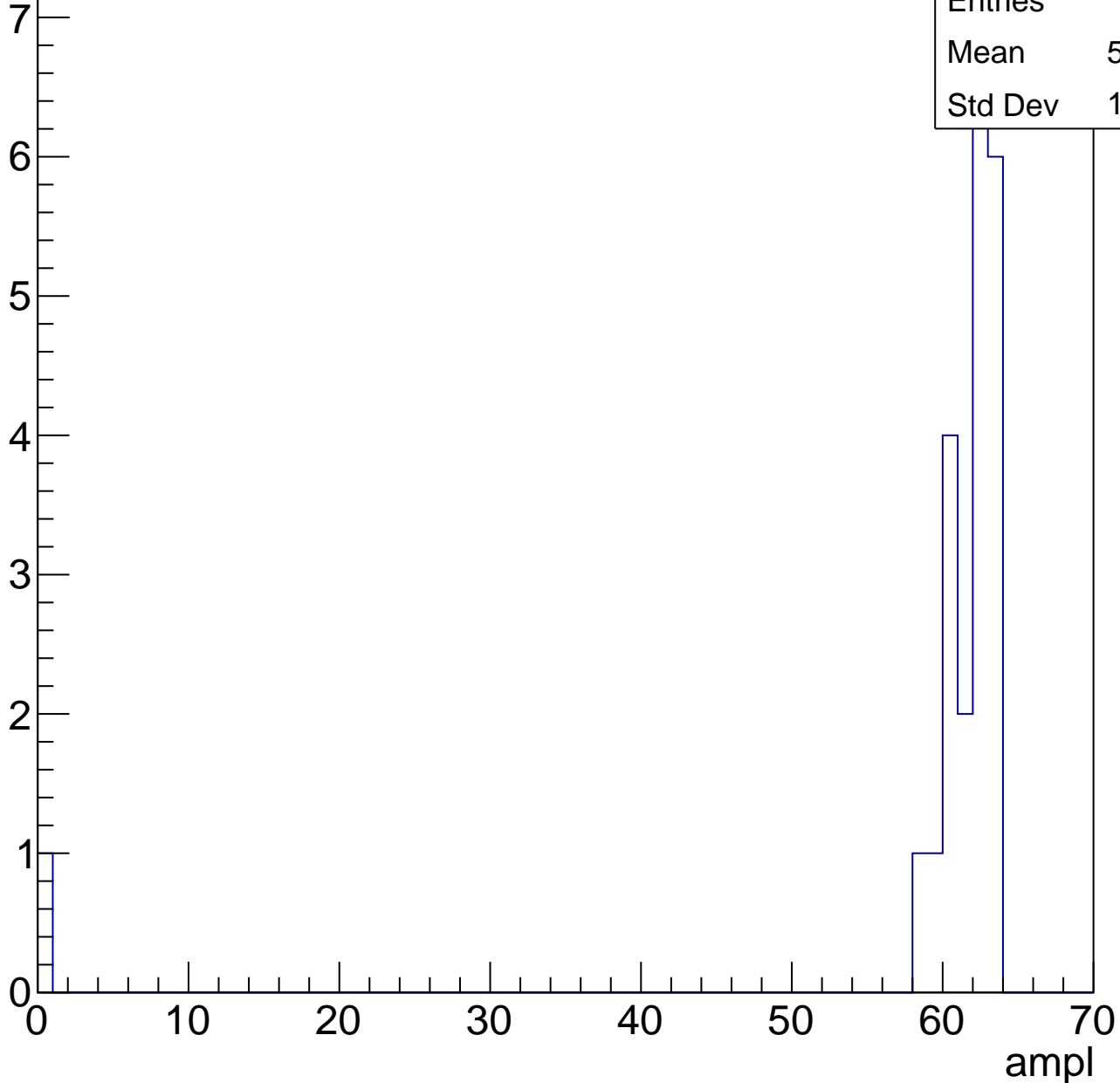


B1L101S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

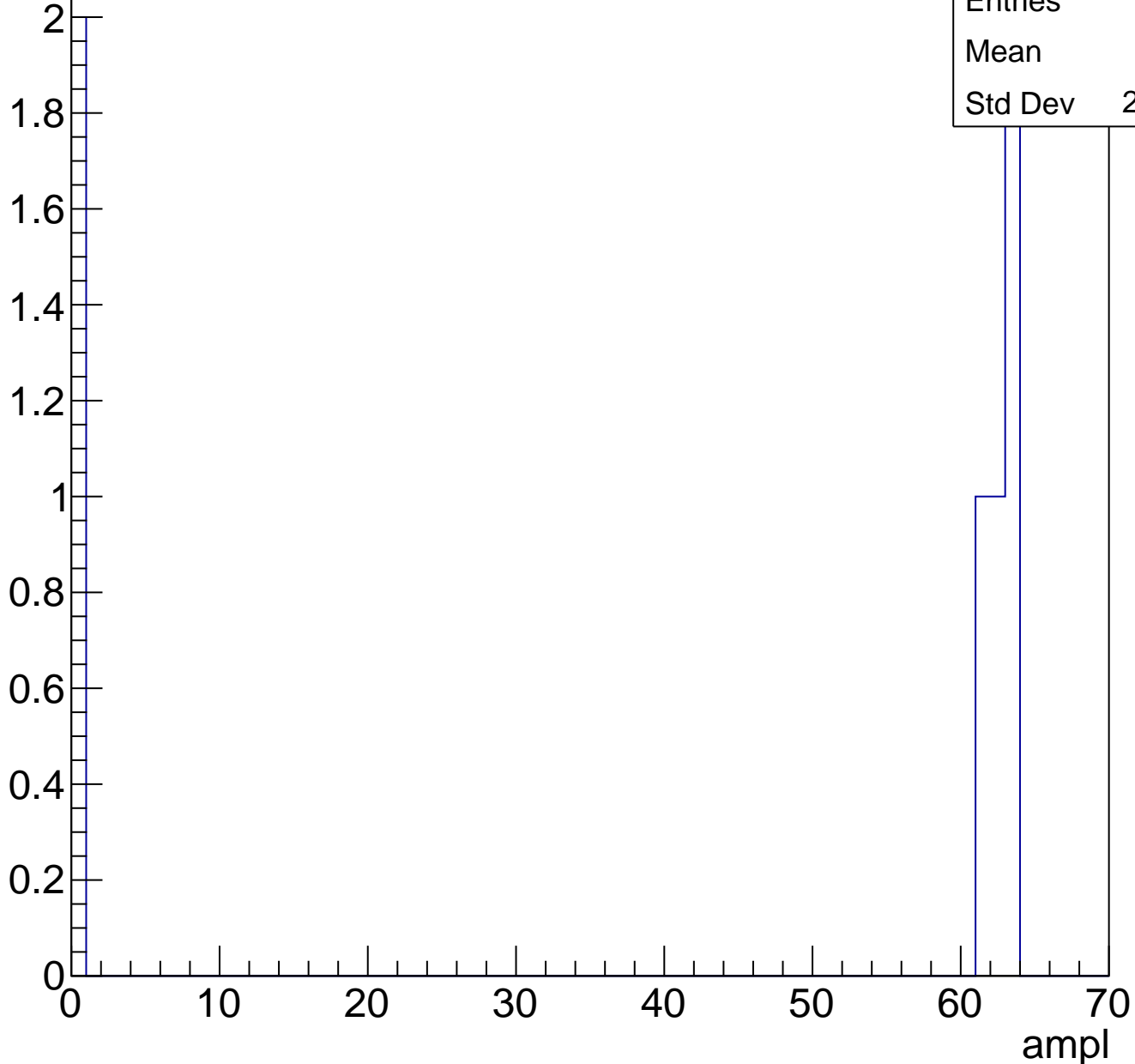
Entries	22
Mean	58.68
Std Dev	12.88



B1L101S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch77, adc0

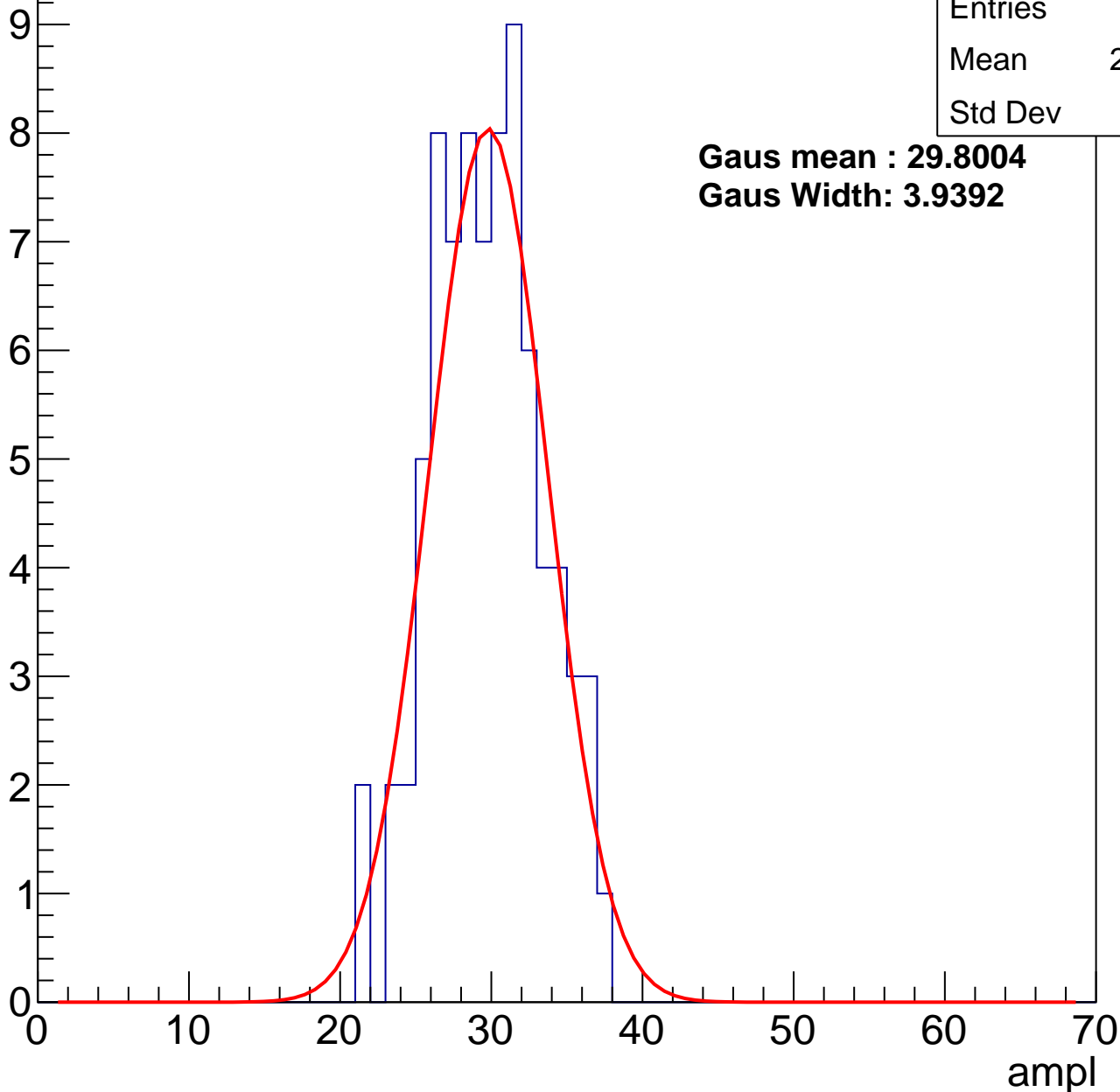
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.29
Std Dev	3.58

Gaus mean : 29.8004

Gaus Width: 3.9392



B1L101S, U14-ch77, adc1

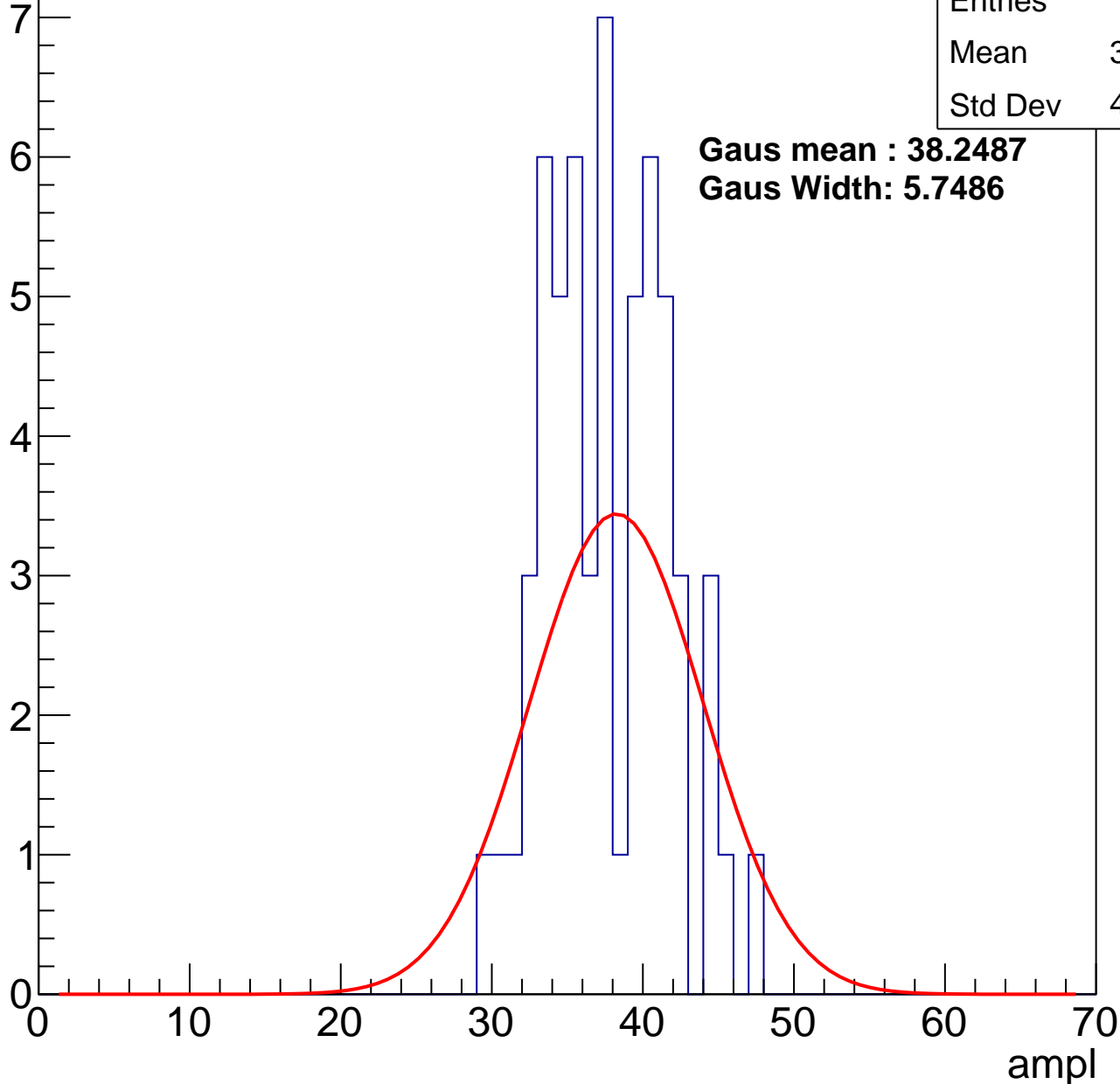
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.22
Std Dev	4.004

Gaus mean : 38.2487

Gaus Width: 5.7486



B1L101S, U14-ch77, adc2

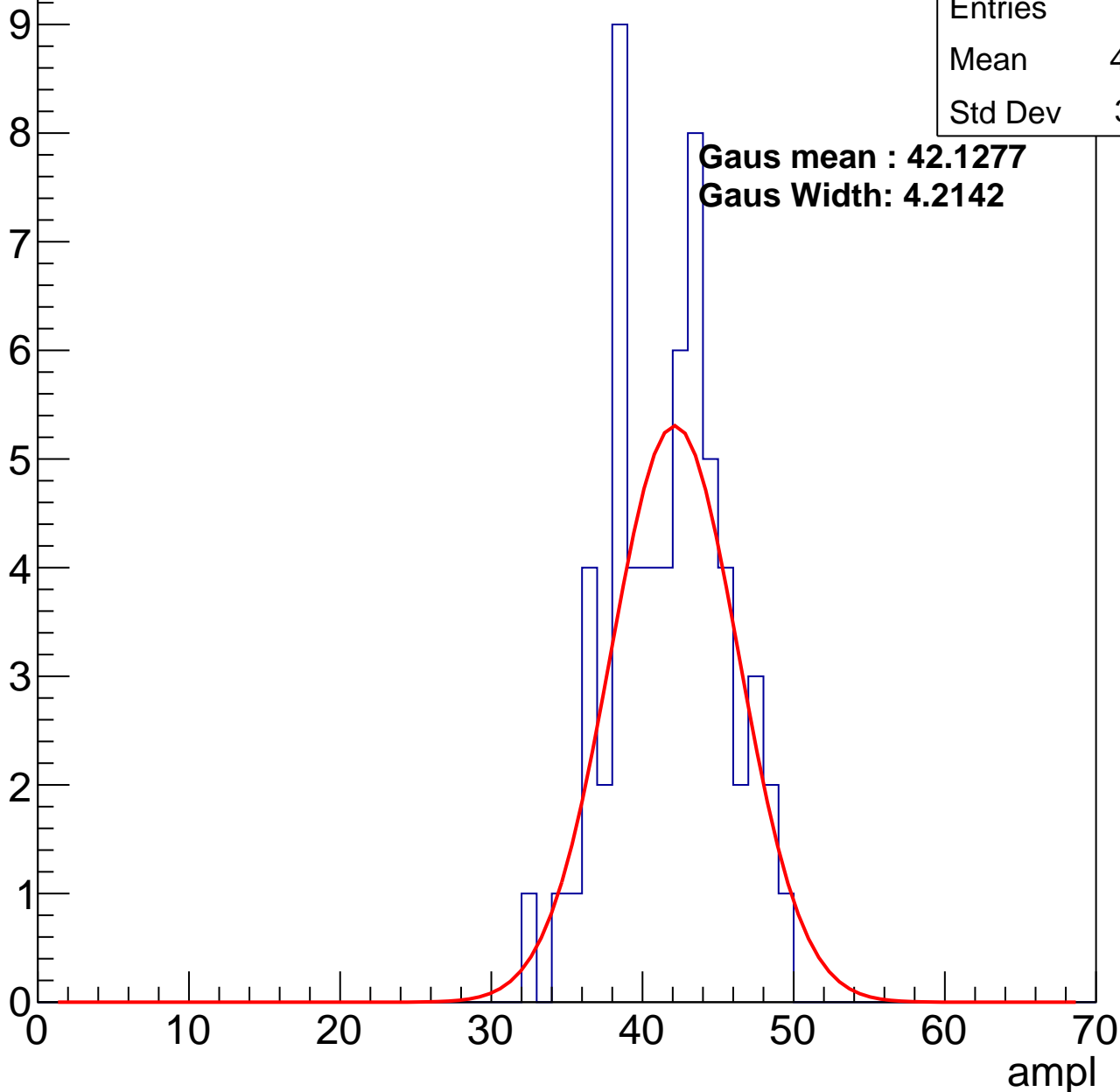
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.23
Std Dev	3.761

Gaus mean : 42.1277

Gaus Width: 4.2142

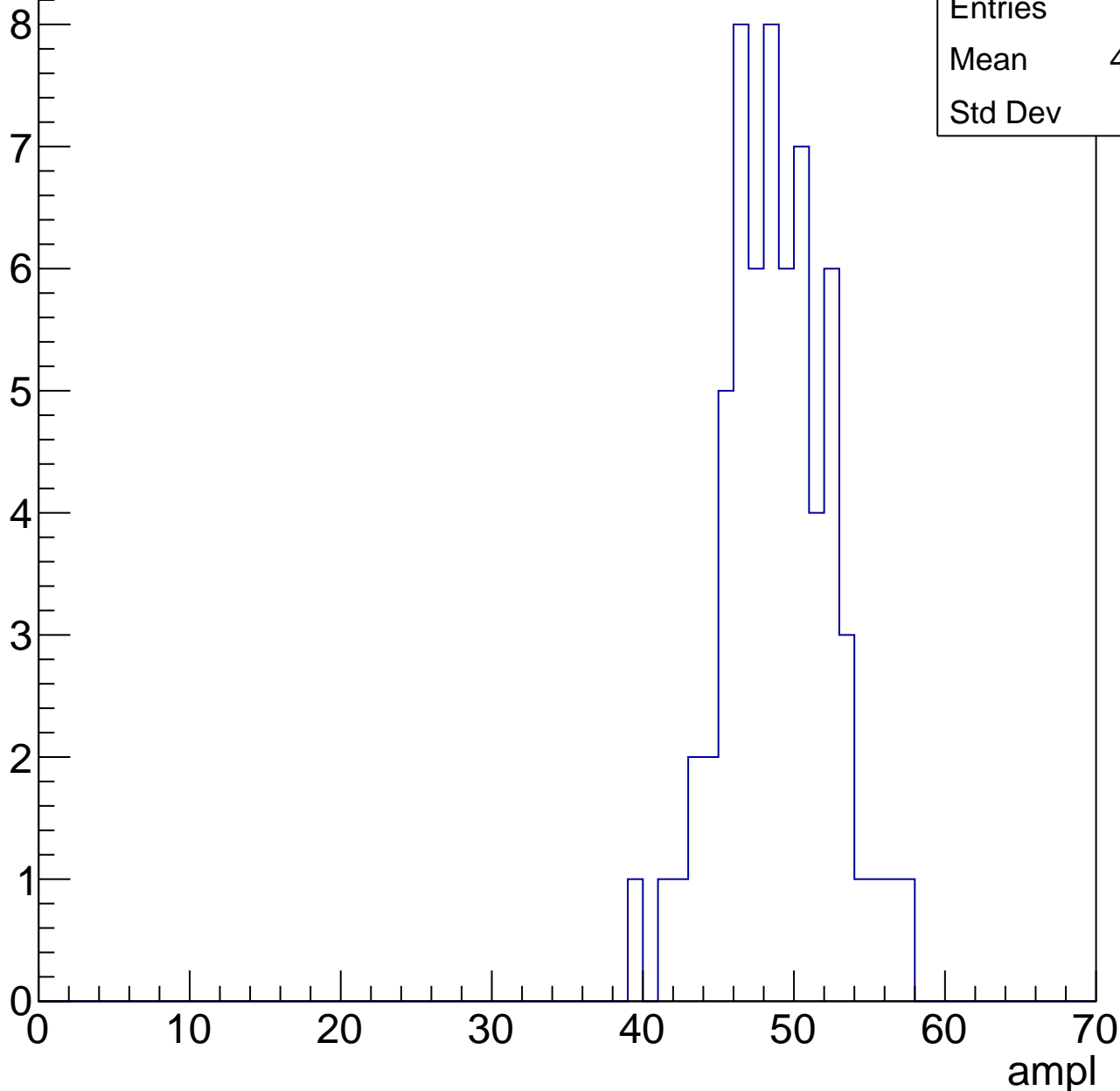


B1L101S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.38
Std Dev	3.52

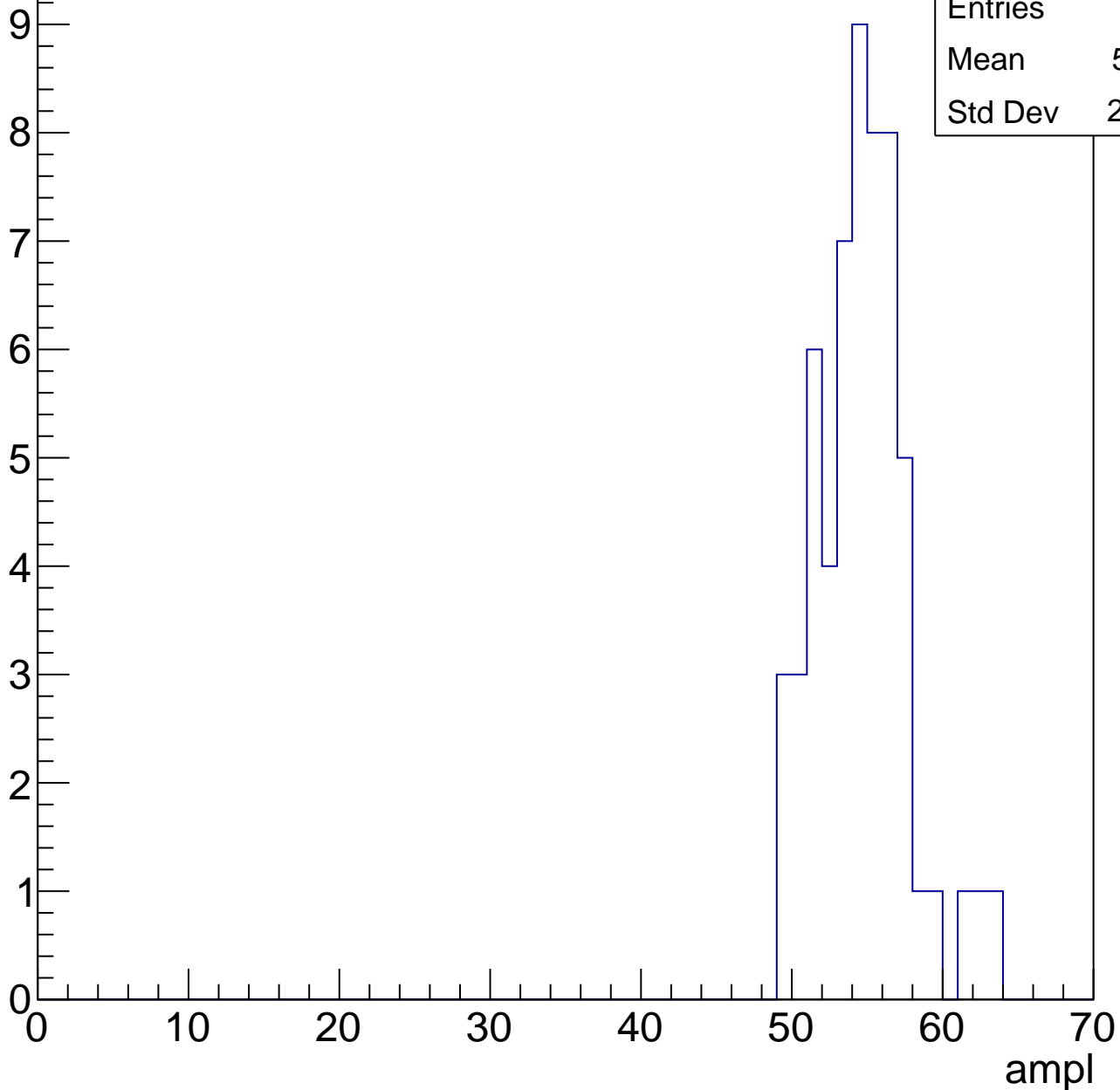


B1L101S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

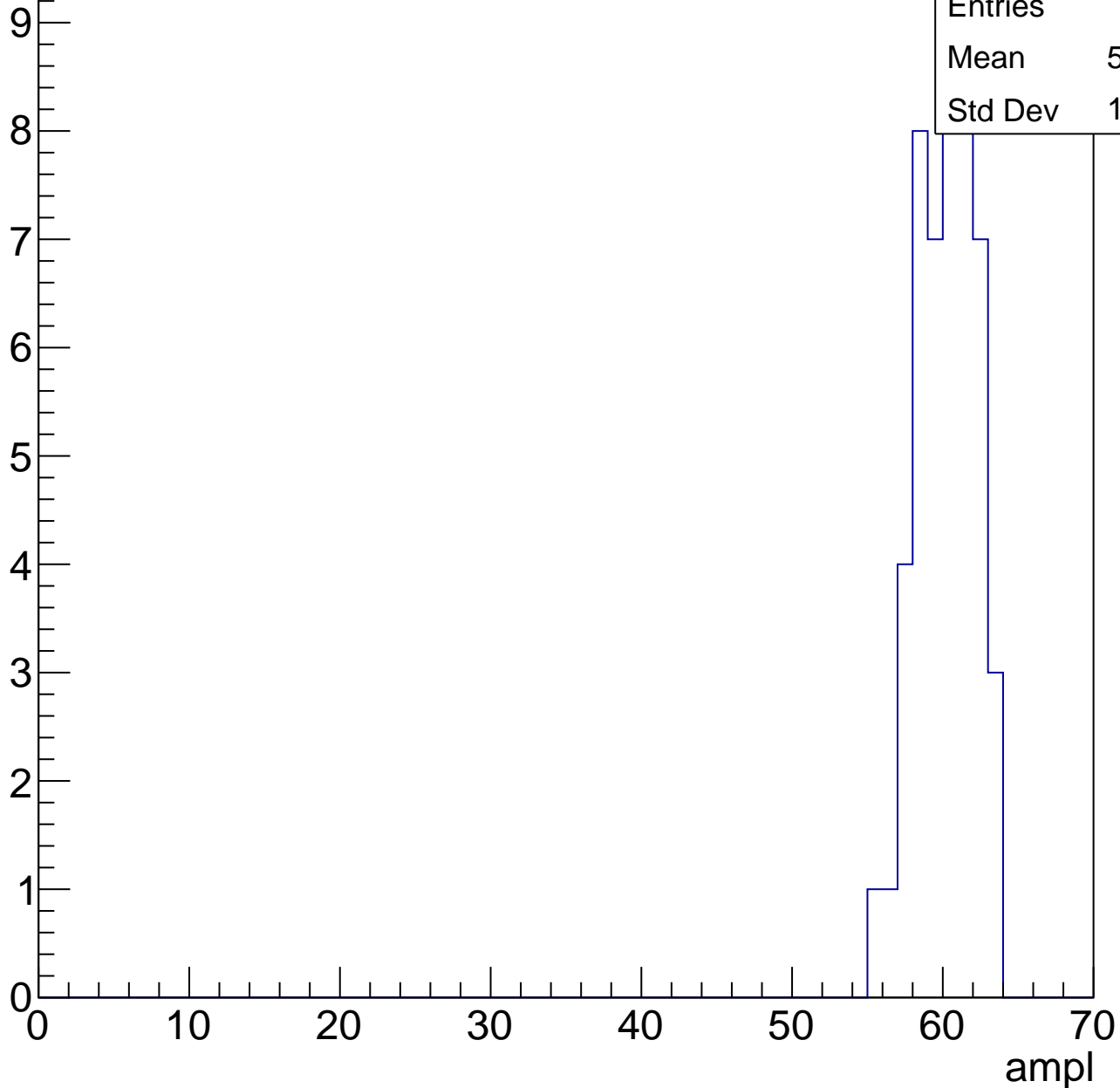
Entries	58
Mean	54.21
Std Dev	2.987



B1L101S, U14-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

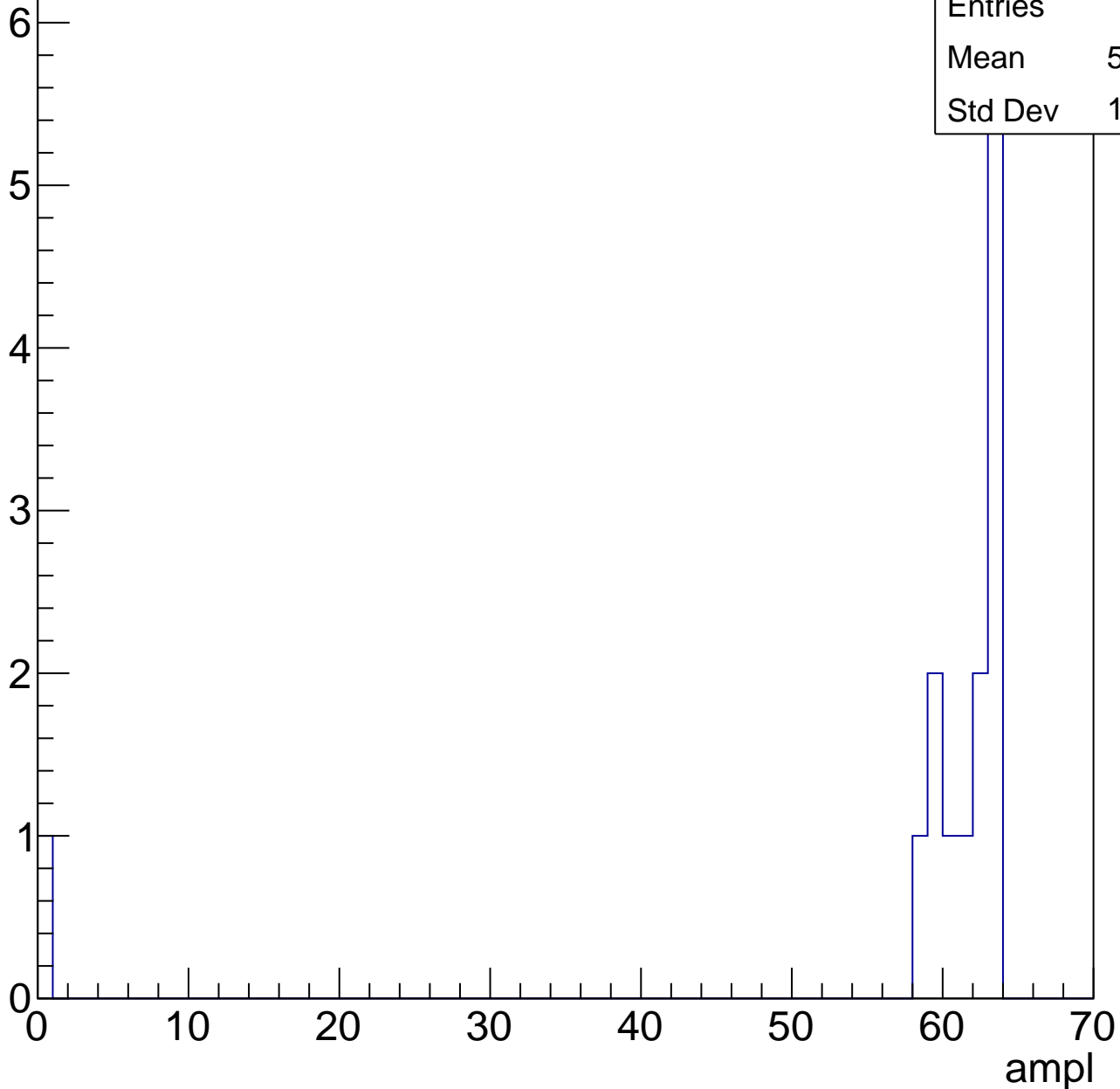


B1L101S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.07
Std Dev	15.92



B1L101S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch78, adc0

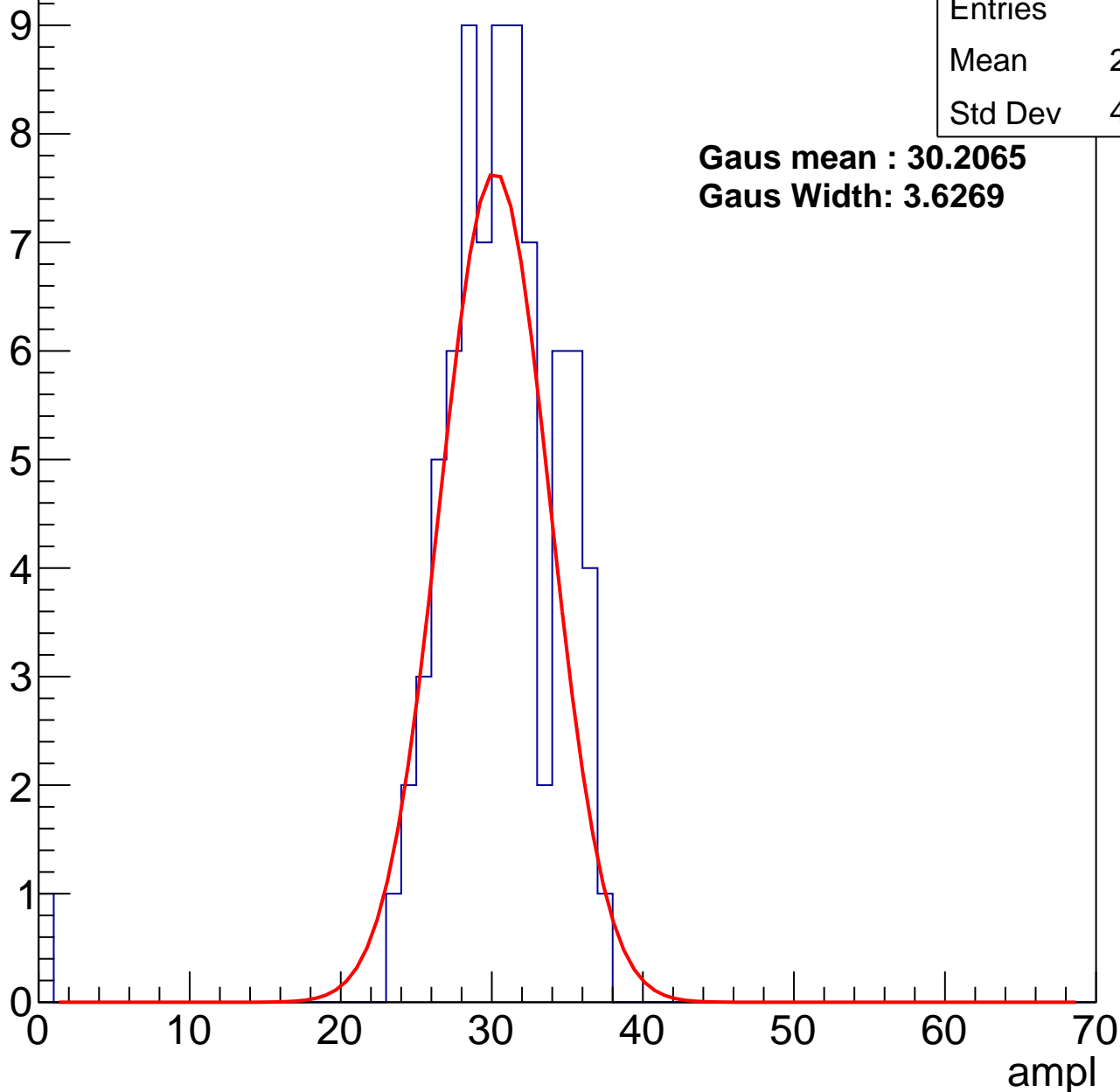
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.83
Std Dev	4.757

Gaus mean : 30.2065

Gaus Width: 3.6269



B1L101S, U14-ch78, adc1

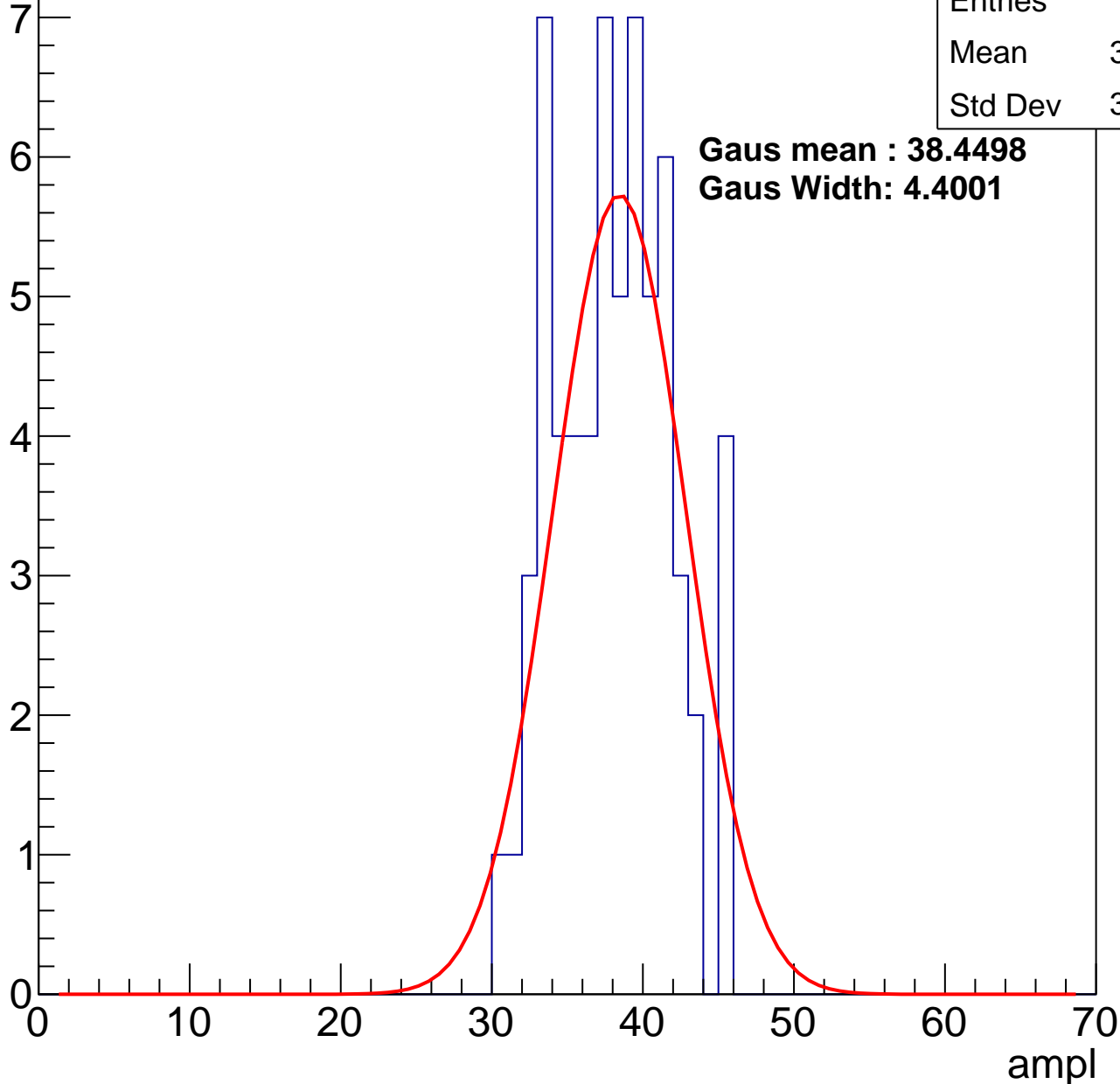
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.59
Std Dev	3.744

Gaus mean : 38.4498

Gaus Width: 4.4001



B1L101S, U14-ch78, adc2

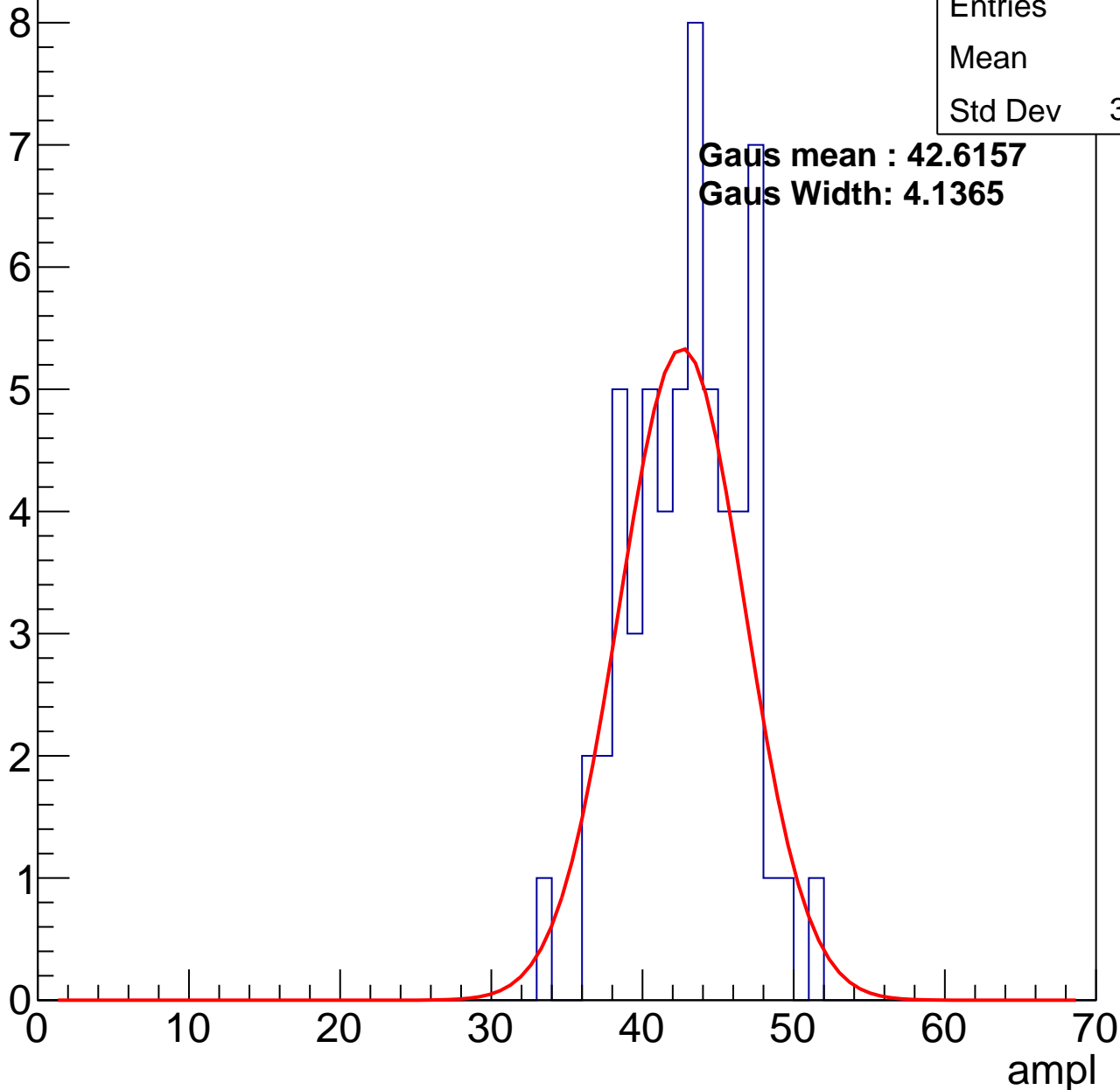
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.5
Std Dev	3.687

Gaus mean : 42.6157

Gaus Width: 4.1365

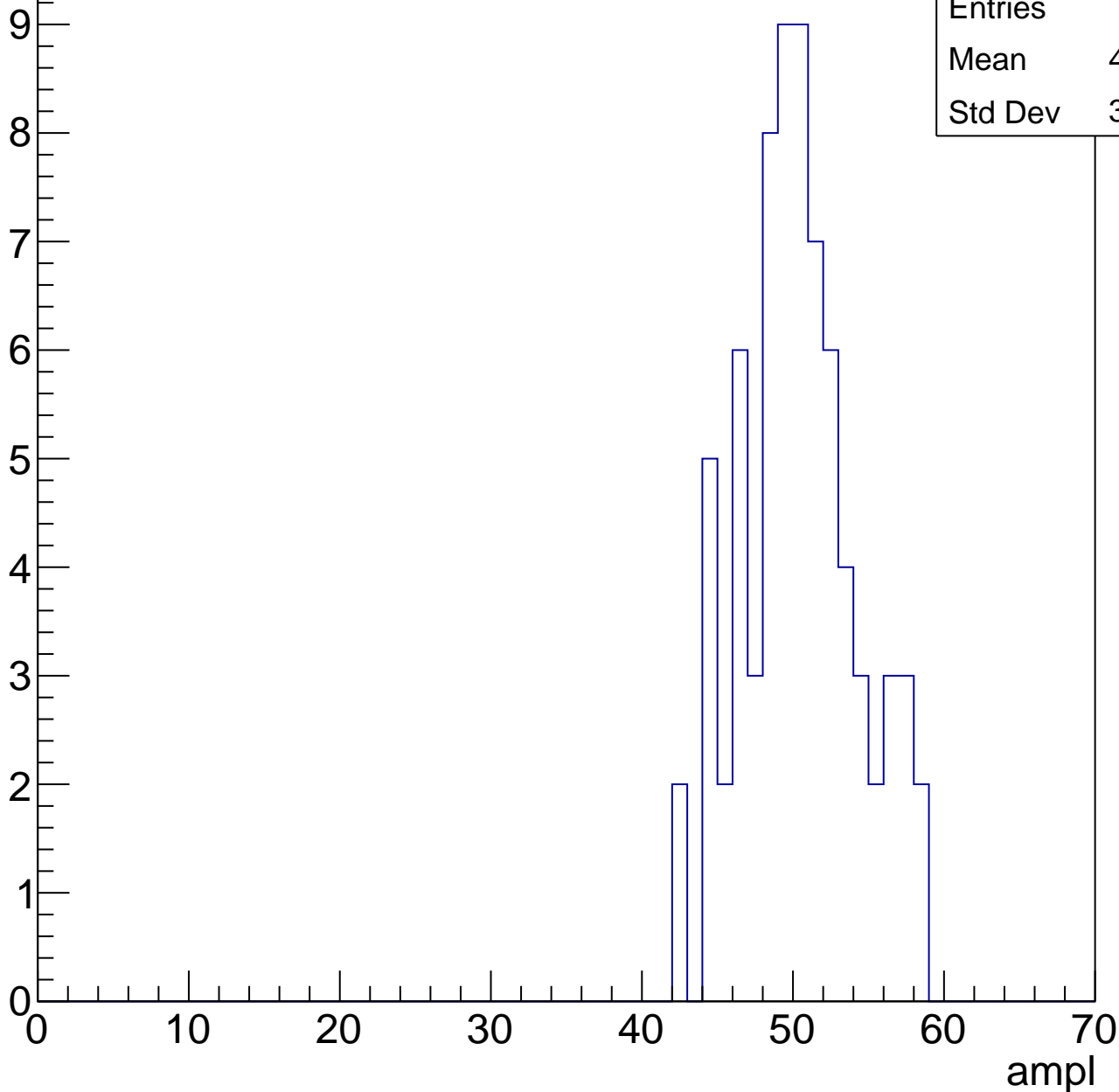


B1L101S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

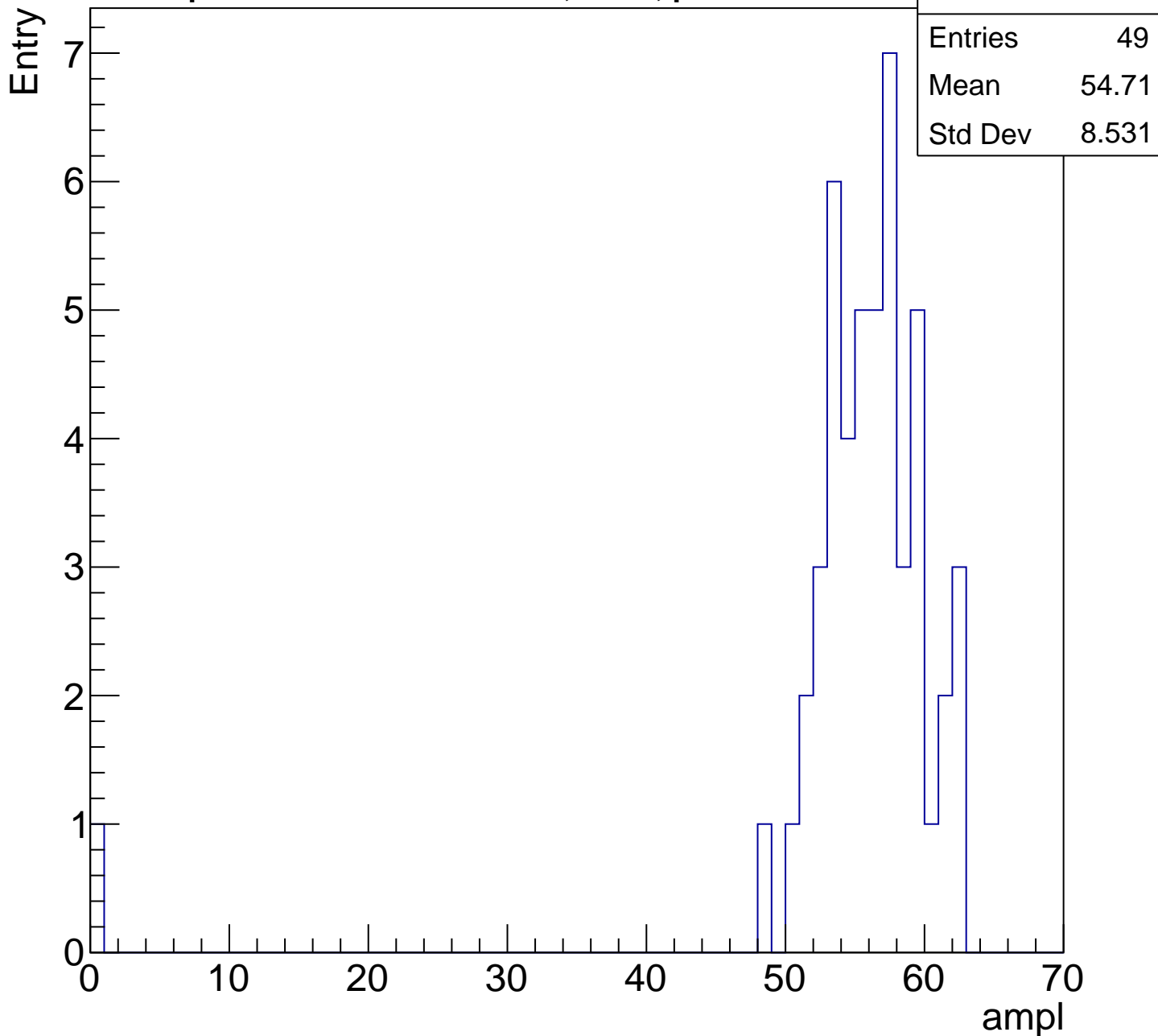
Entry

Entries	74
Mean	49.92
Std Dev	3.802



B1L101S, U14-ch78, adc4

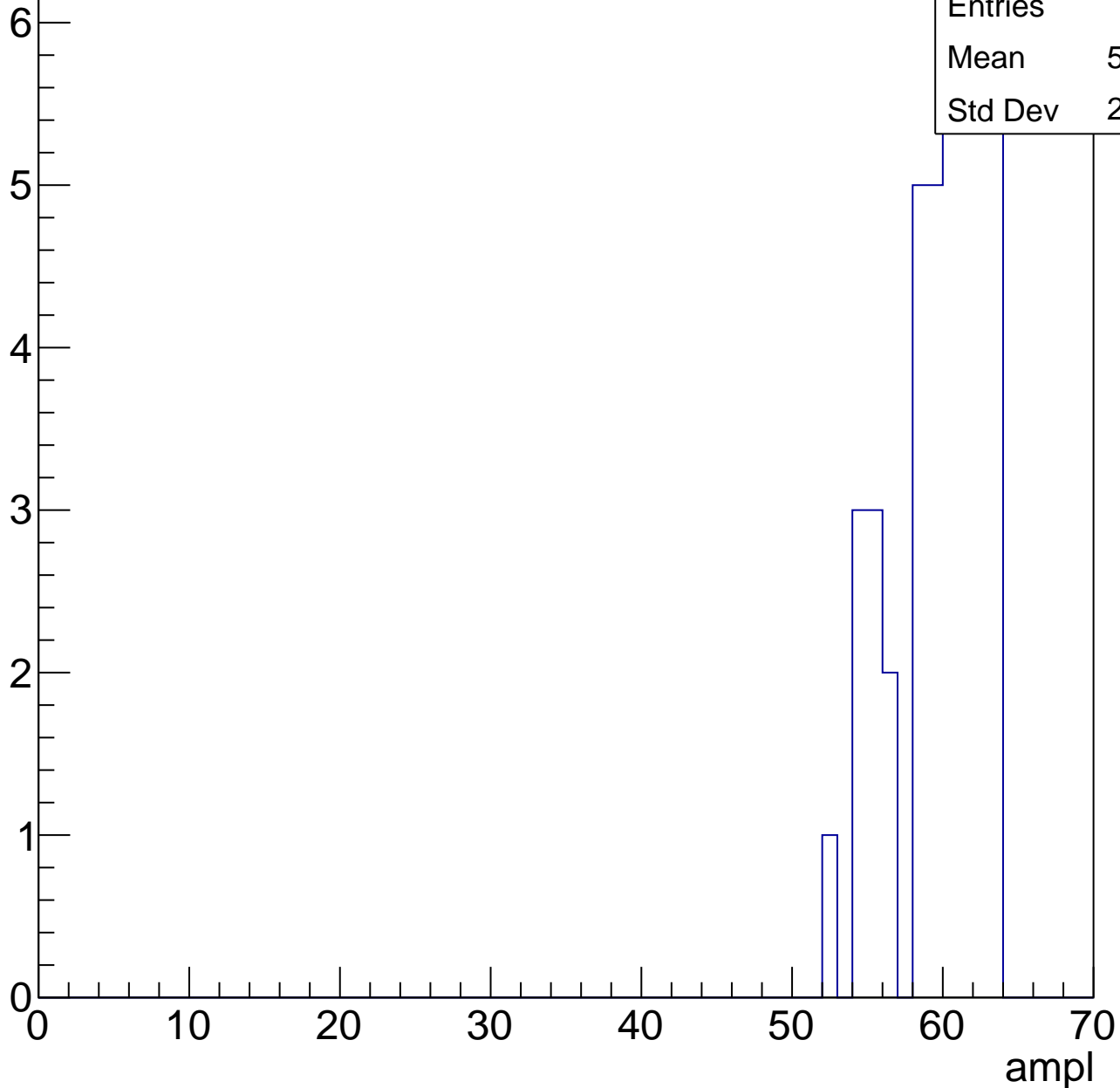
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

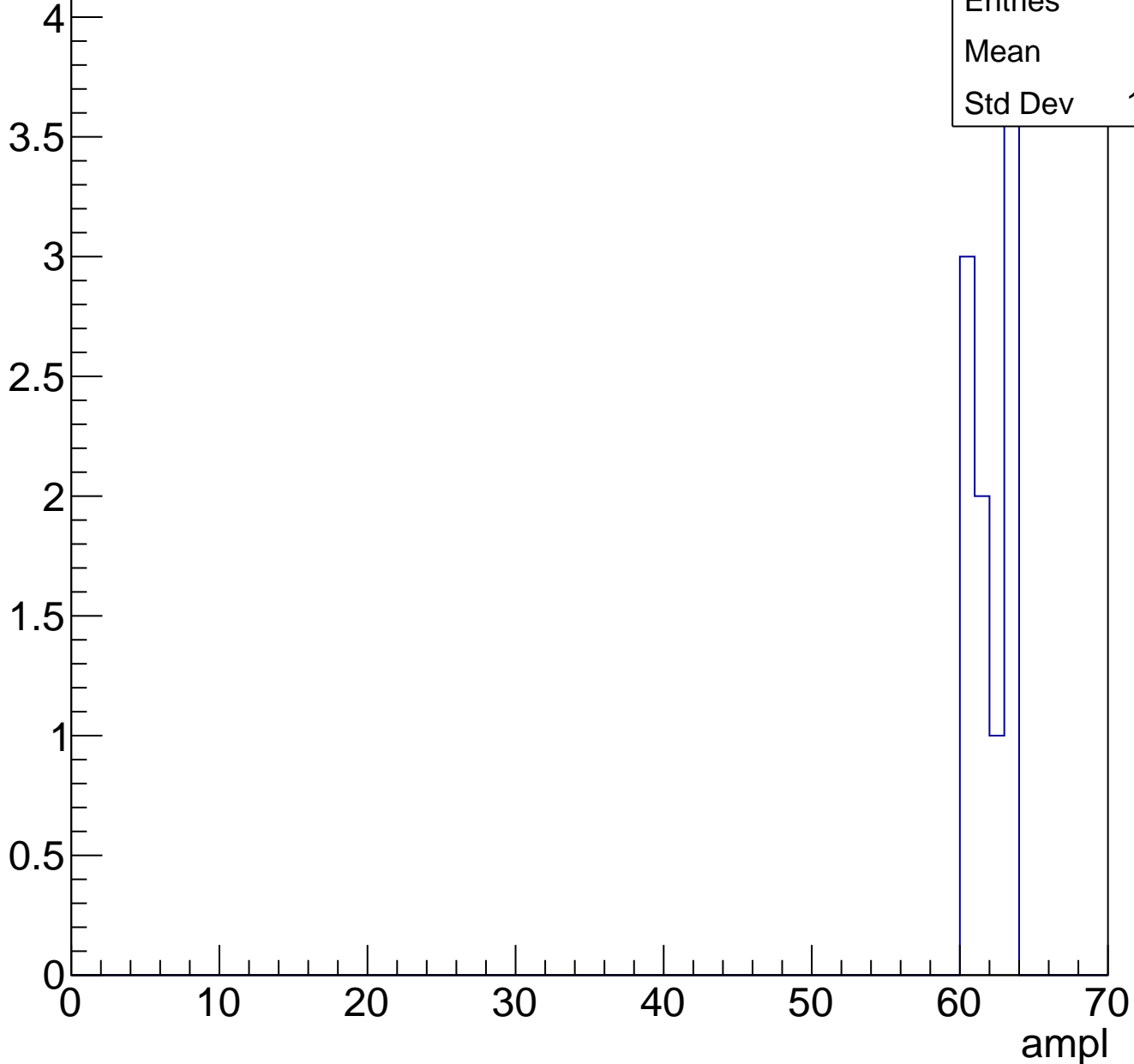
Entry



B1L101S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	61.6
Std Dev	1.281

B1L101S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L101S, U14-ch79, adc0

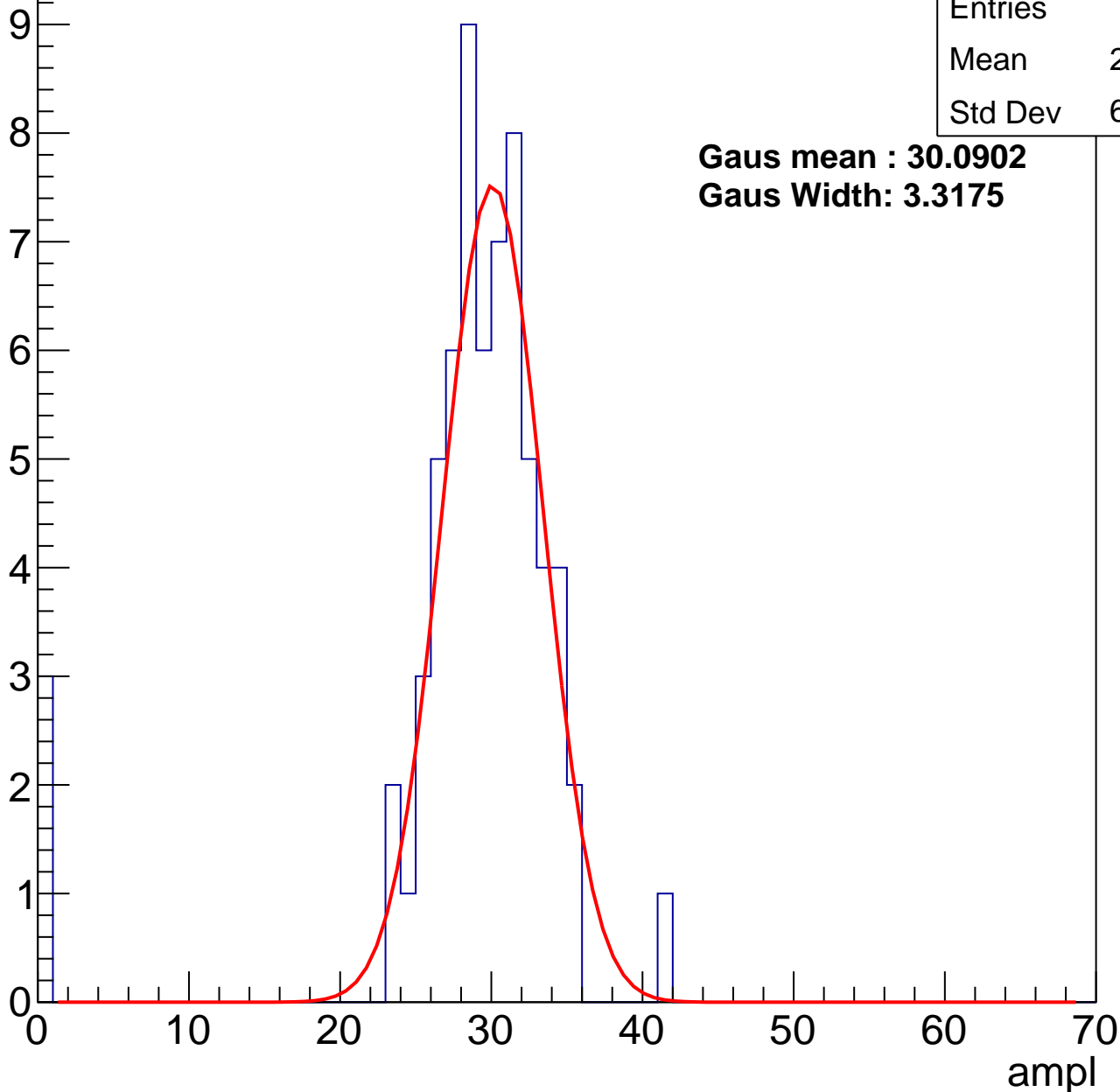
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.18
Std Dev	6.926

Gaus mean : 30.0902

Gaus Width: 3.3175



B1L101S, U14-ch79, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	36.71
Std Dev	2.955

Gaus mean : 36.9436

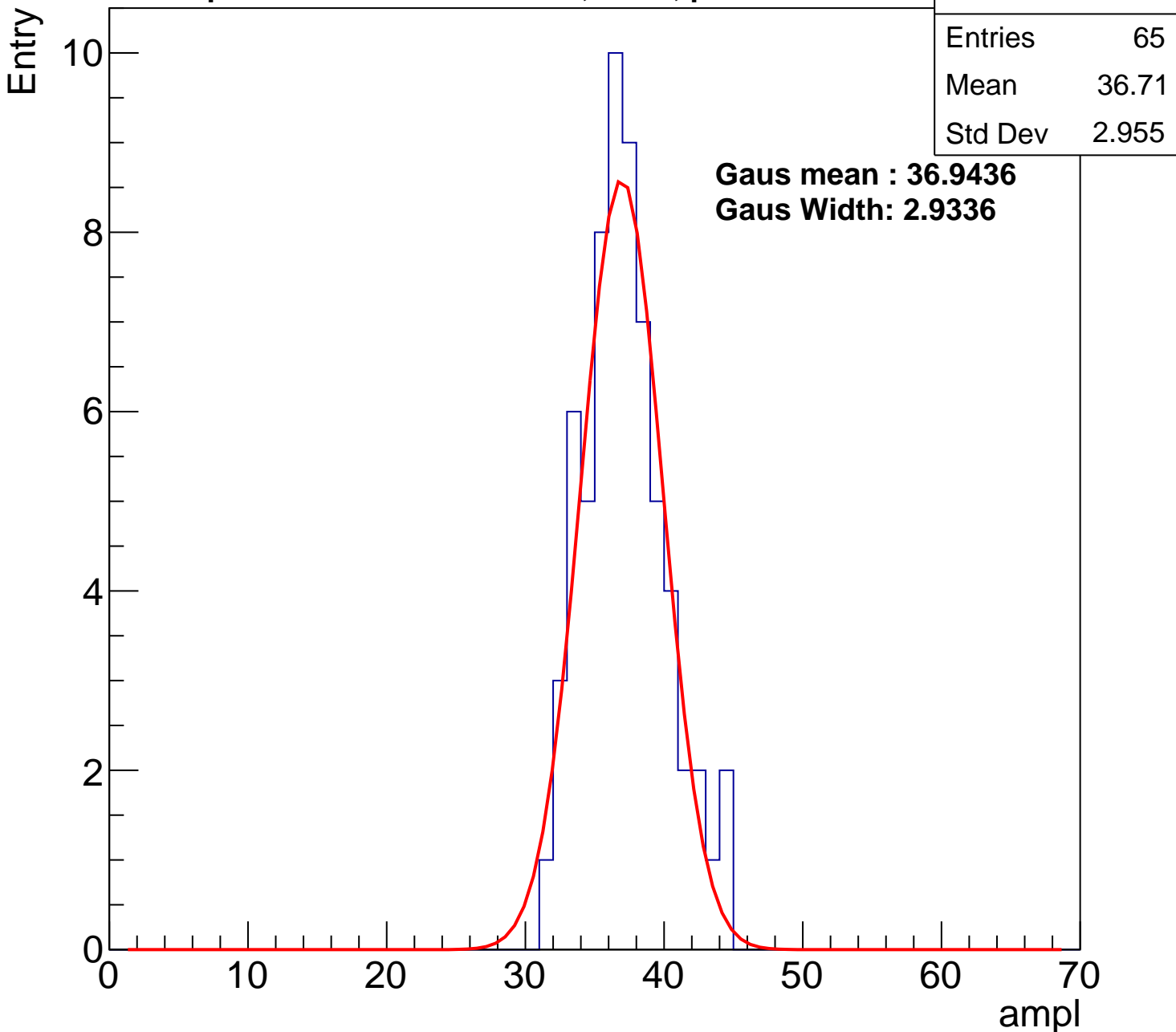
Gaus Width: 2.9336

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch79, adc2

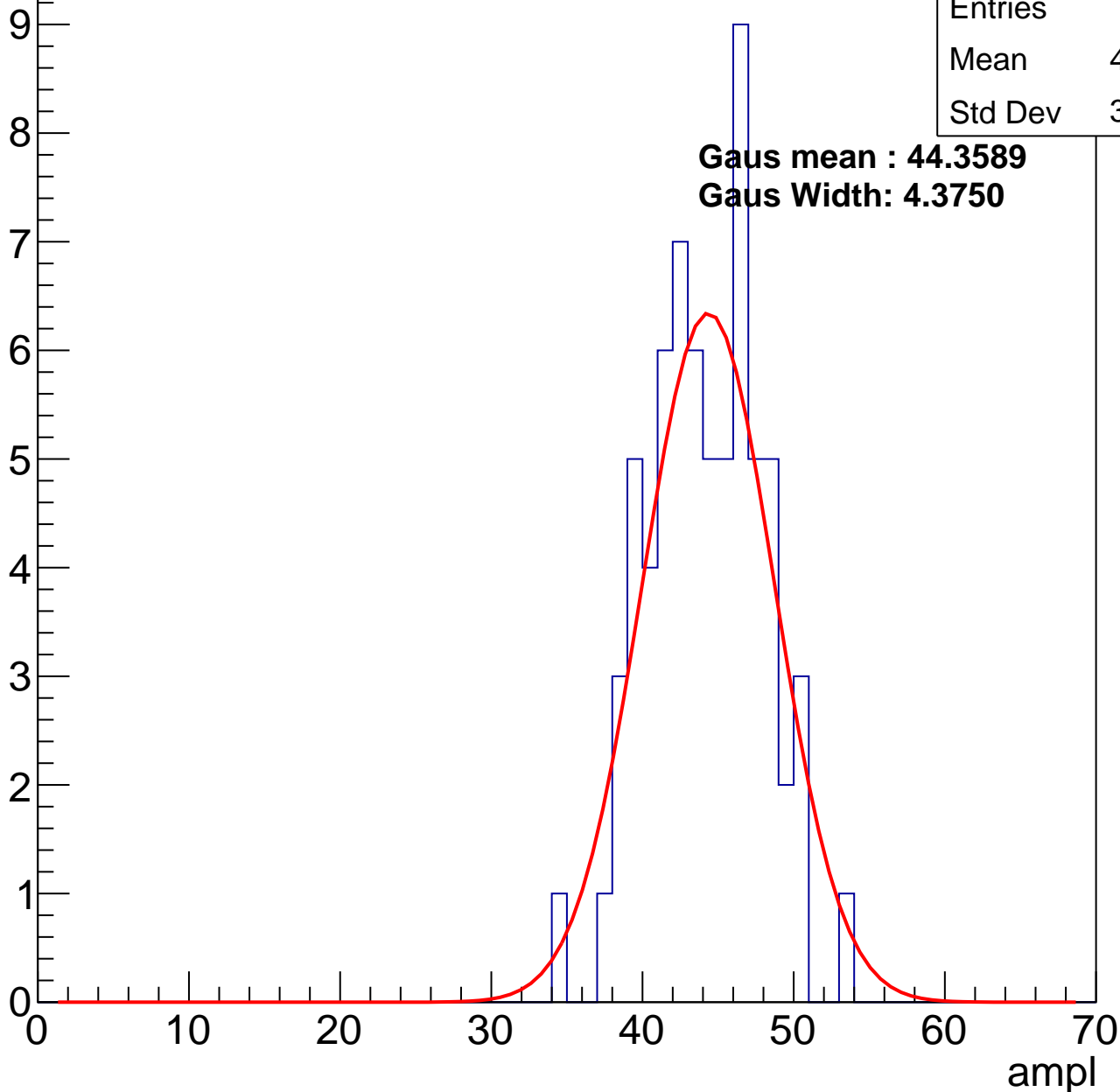
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.72
Std Dev	3.706

Gaus mean : 44.3589

Gaus Width: 4.3750

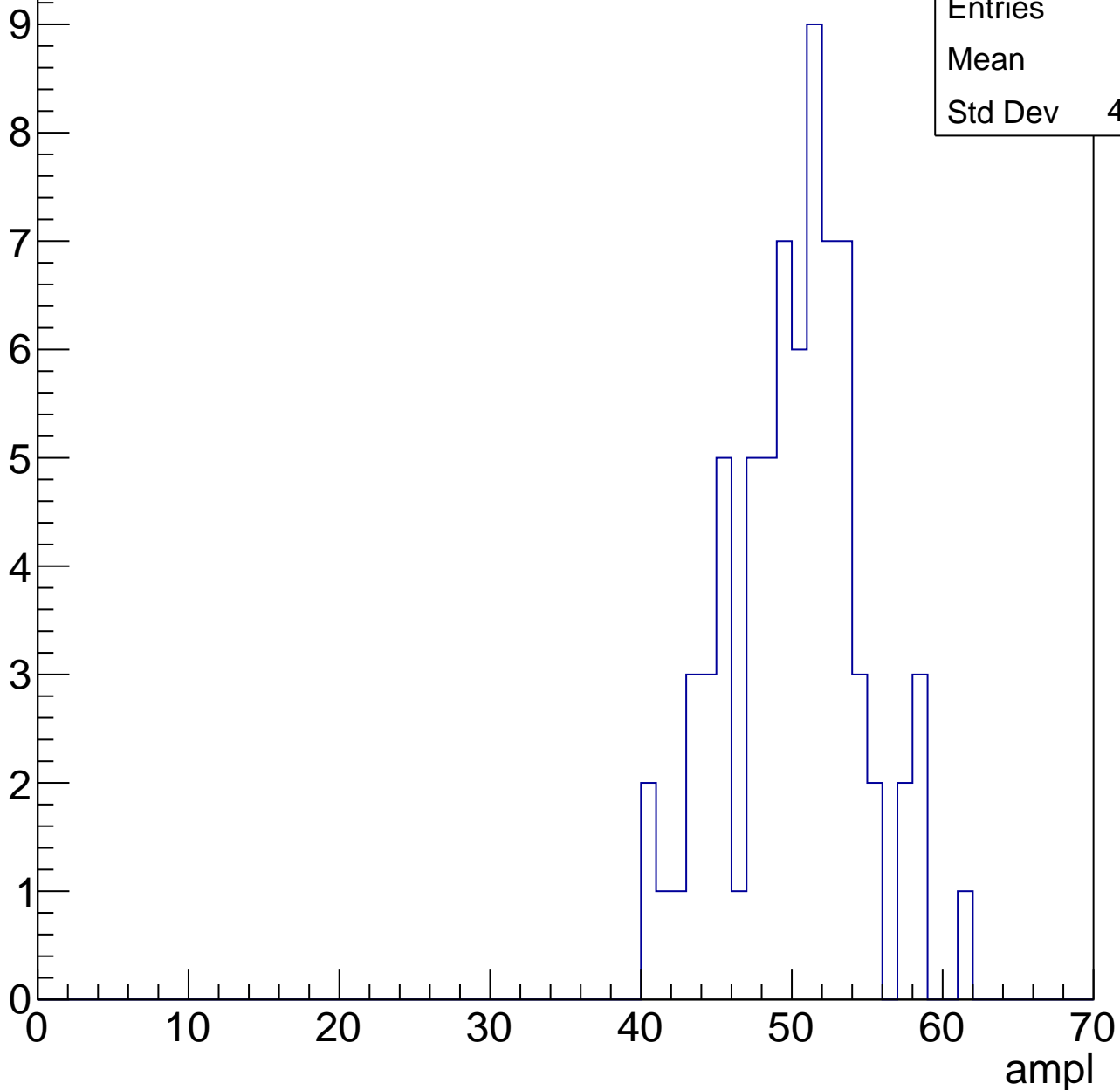


B1L101S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

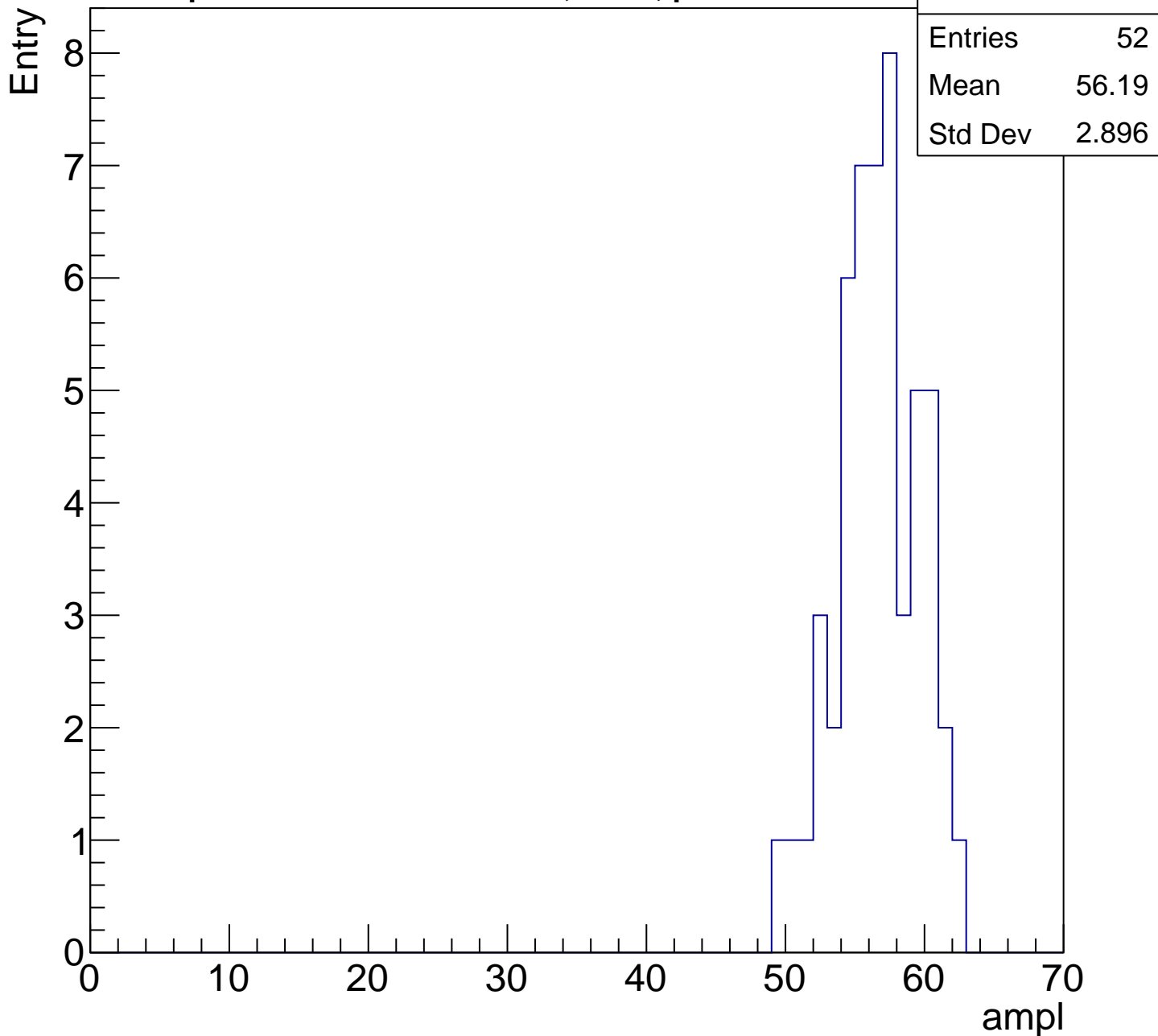
Entry

Entries	73
Mean	49.7
Std Dev	4.403



B1L101S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	58.51
Std Dev	9.785

ampl

0

10

20

30

40

50

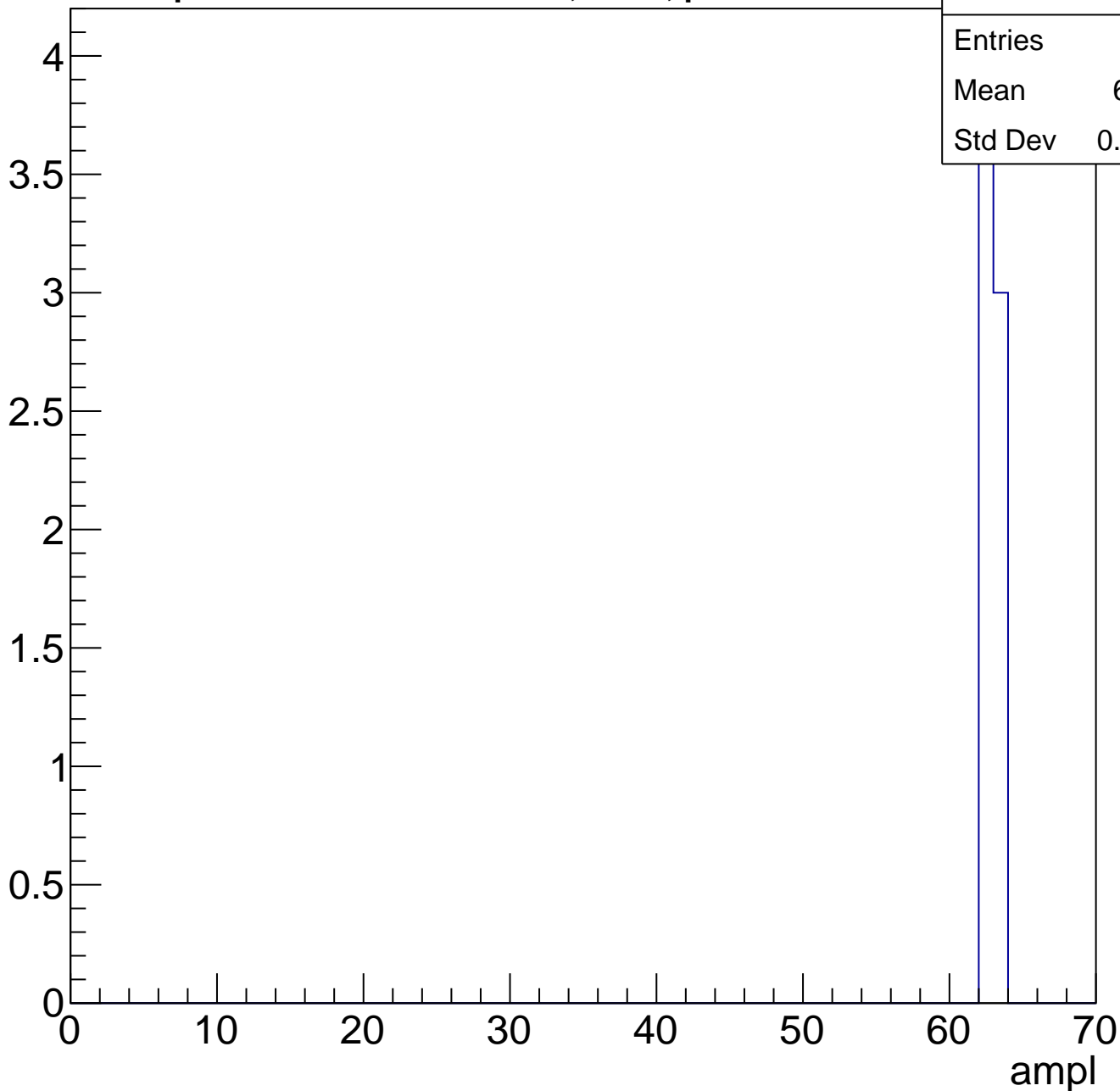
60

70

B1L101S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch80, adc0

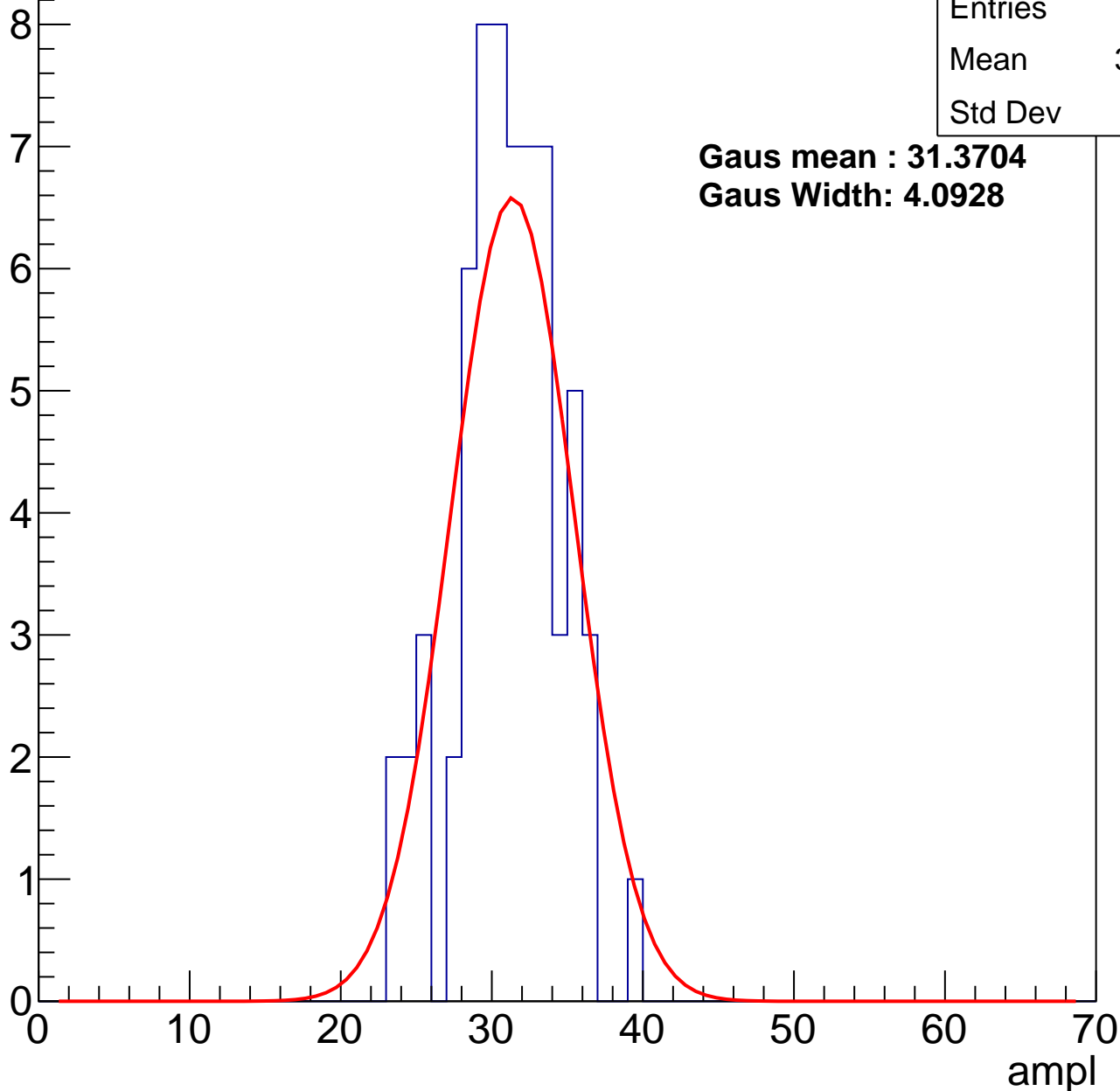
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.61
Std Dev	3.38

Gaus mean : 31.3704

Gaus Width: 4.0928



B1L101S, U14-ch80, adc1

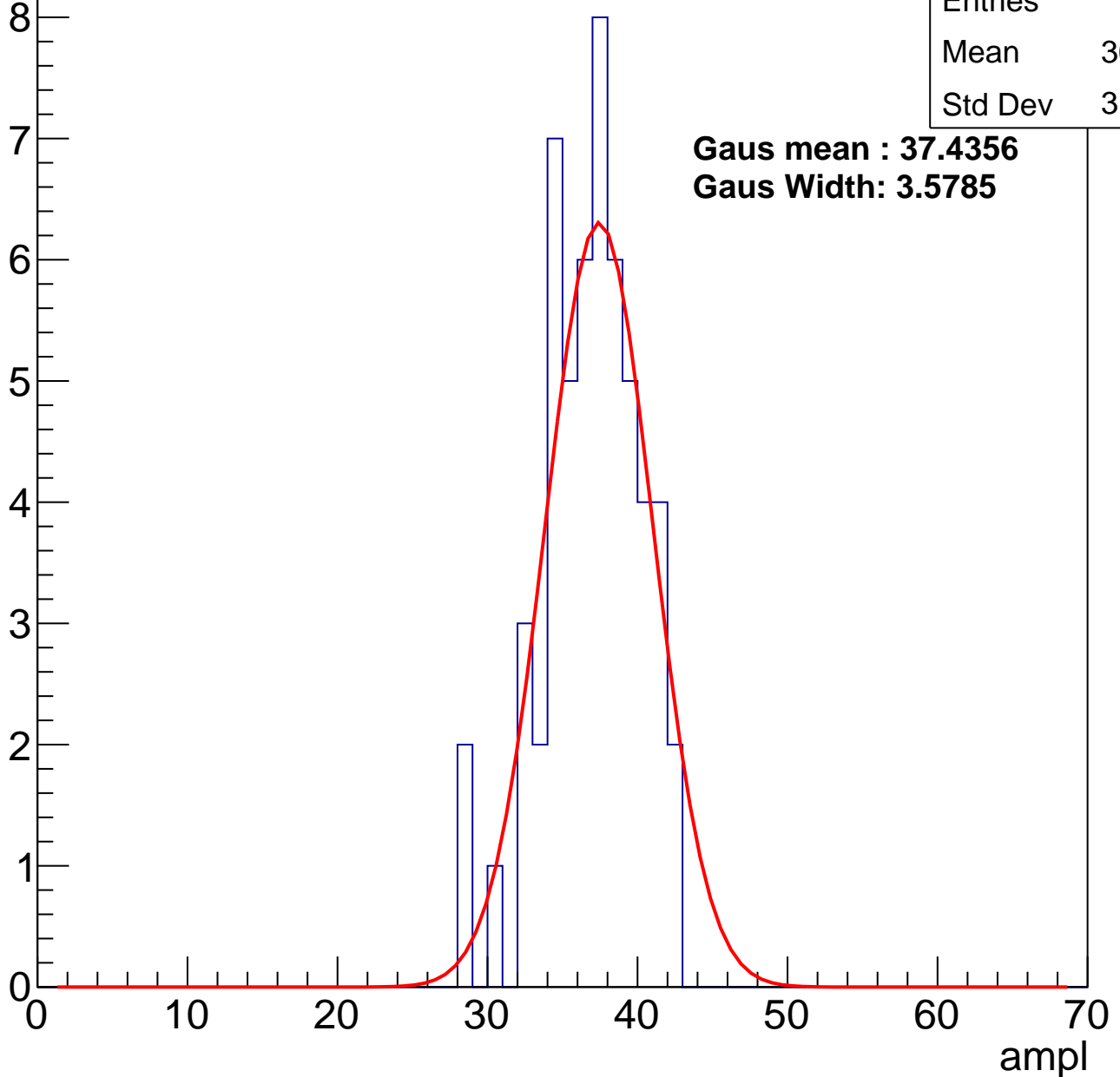
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.44
Std Dev	3.207

Gaus mean : 37.4356

Gaus Width: 3.5785



B1L101S, U14-ch80, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	43.34
Std Dev	3.489

Gaus mean : 43.6481

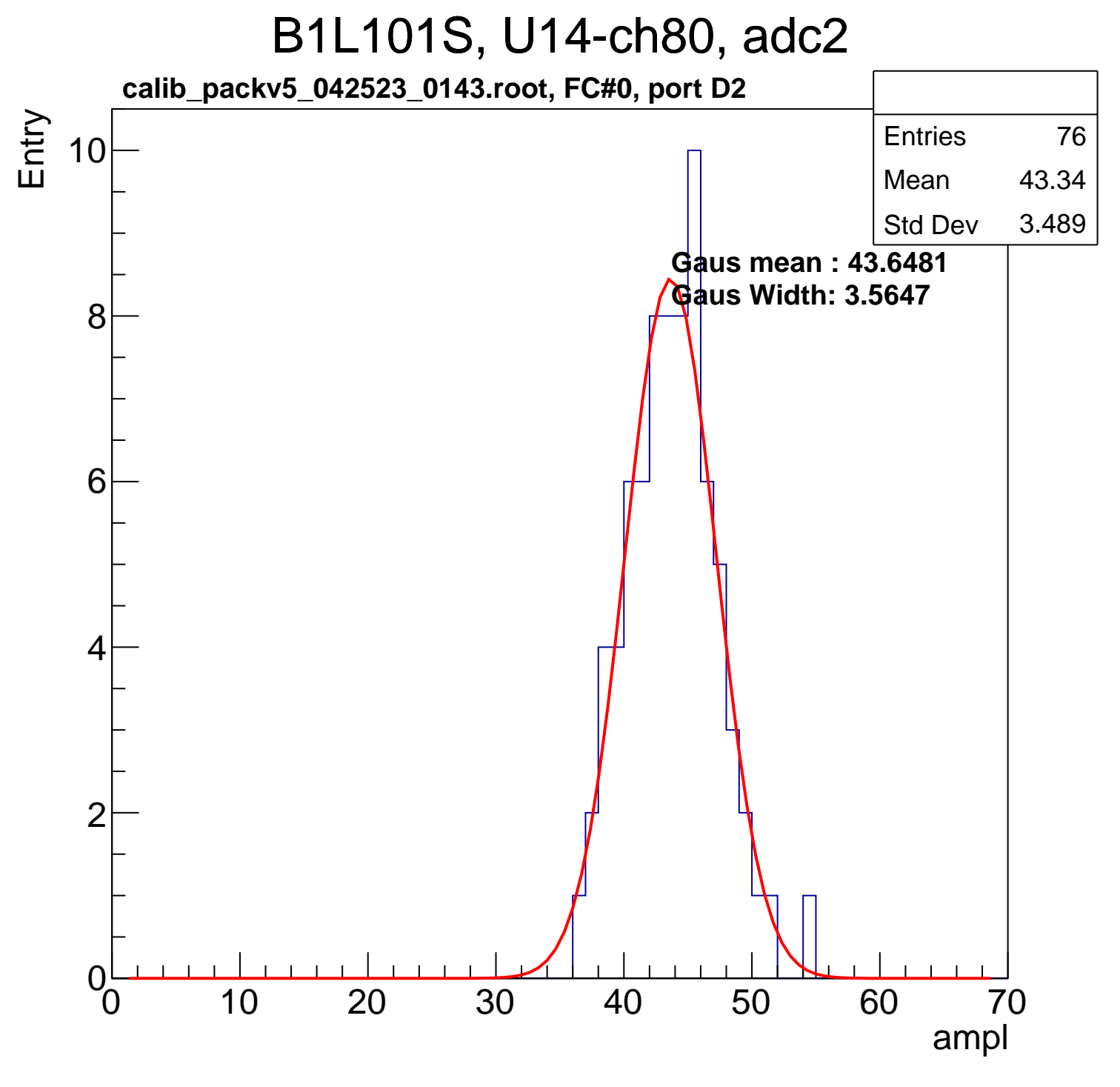
Gaus Width: 3.5647

Entry

10
8
6
4
2
0

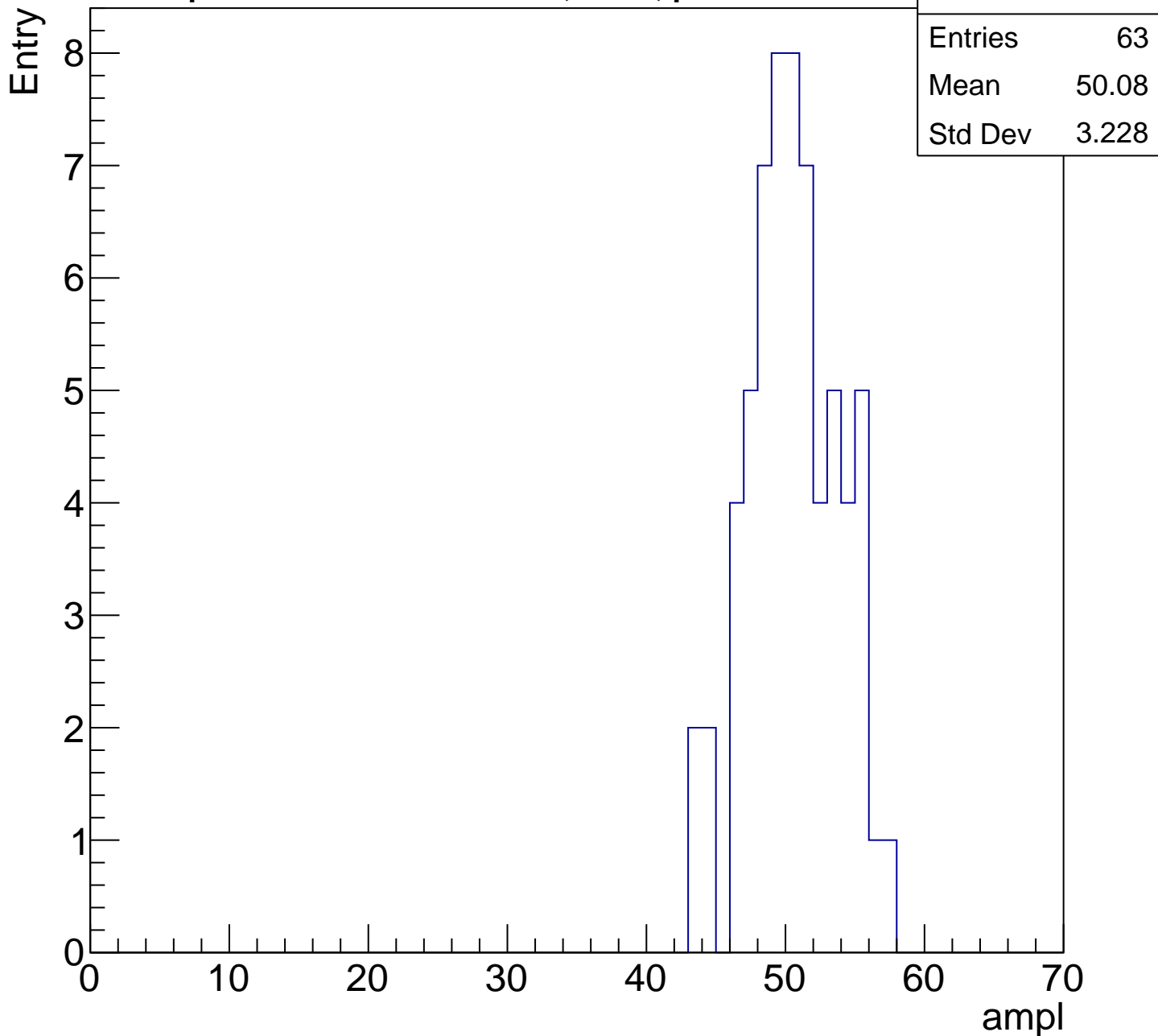
ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

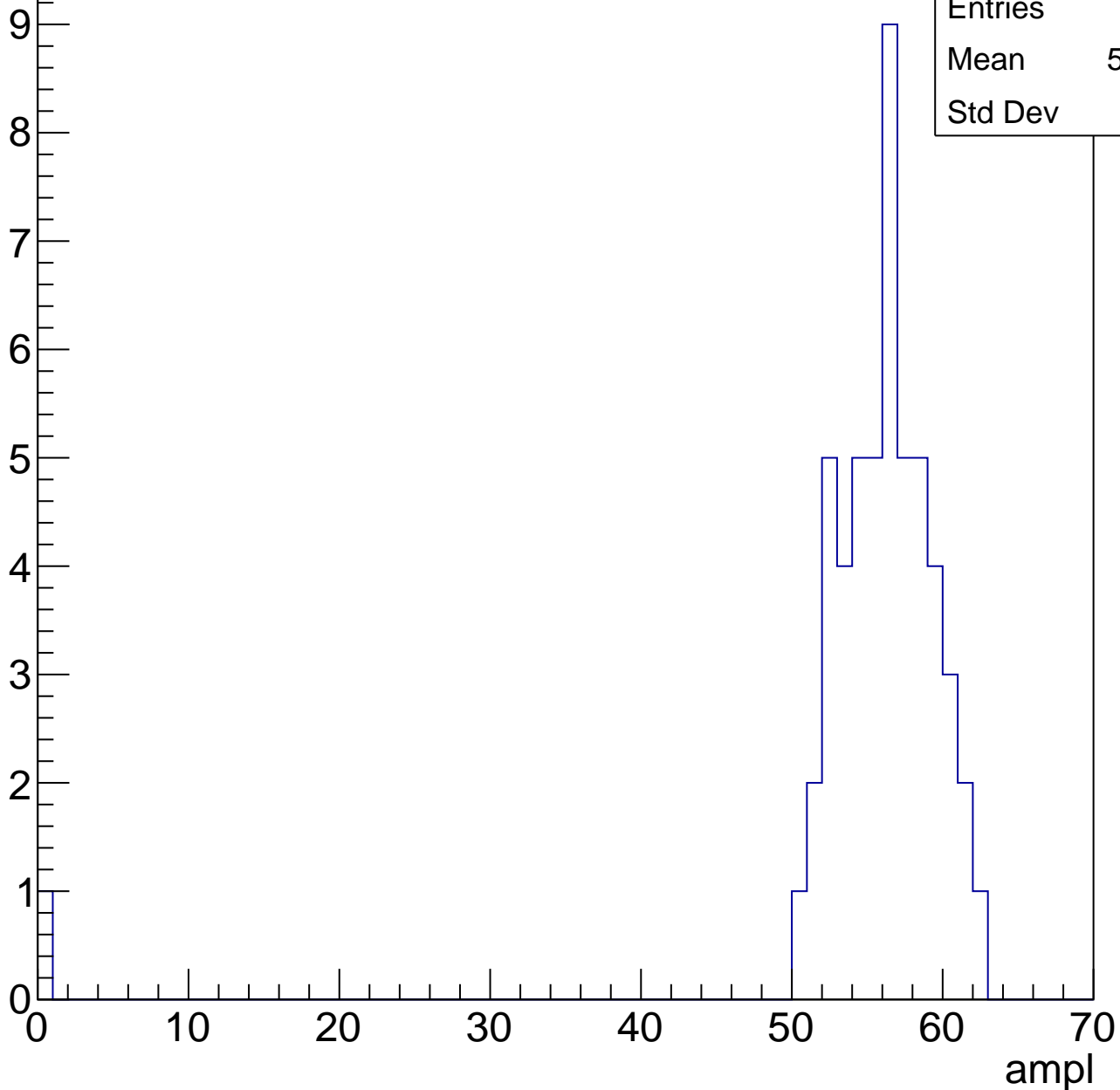


B1L101S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.77
Std Dev	8.18

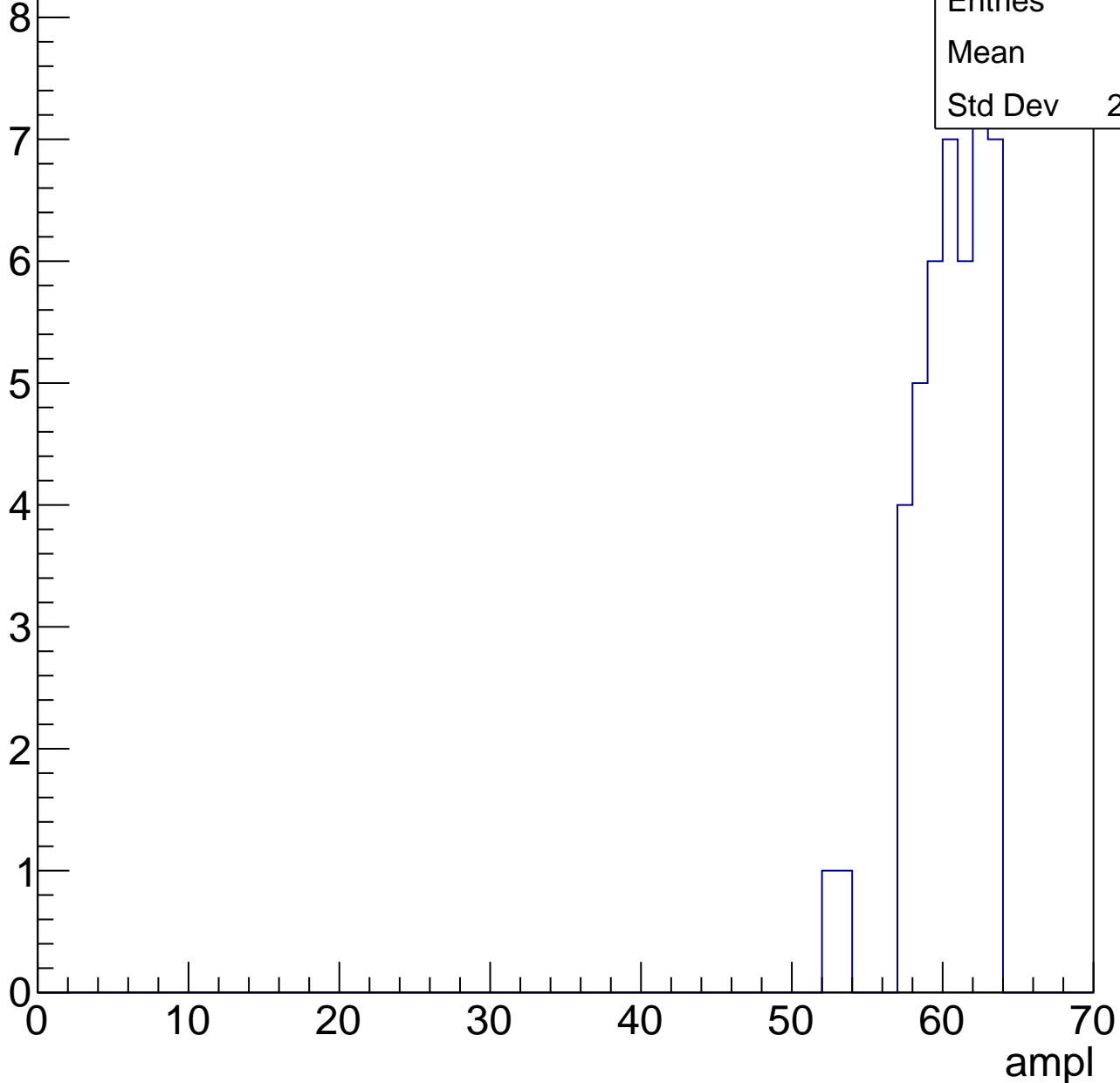


B1L101S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	60
Std Dev	2.477



B1L101S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch81, adc0

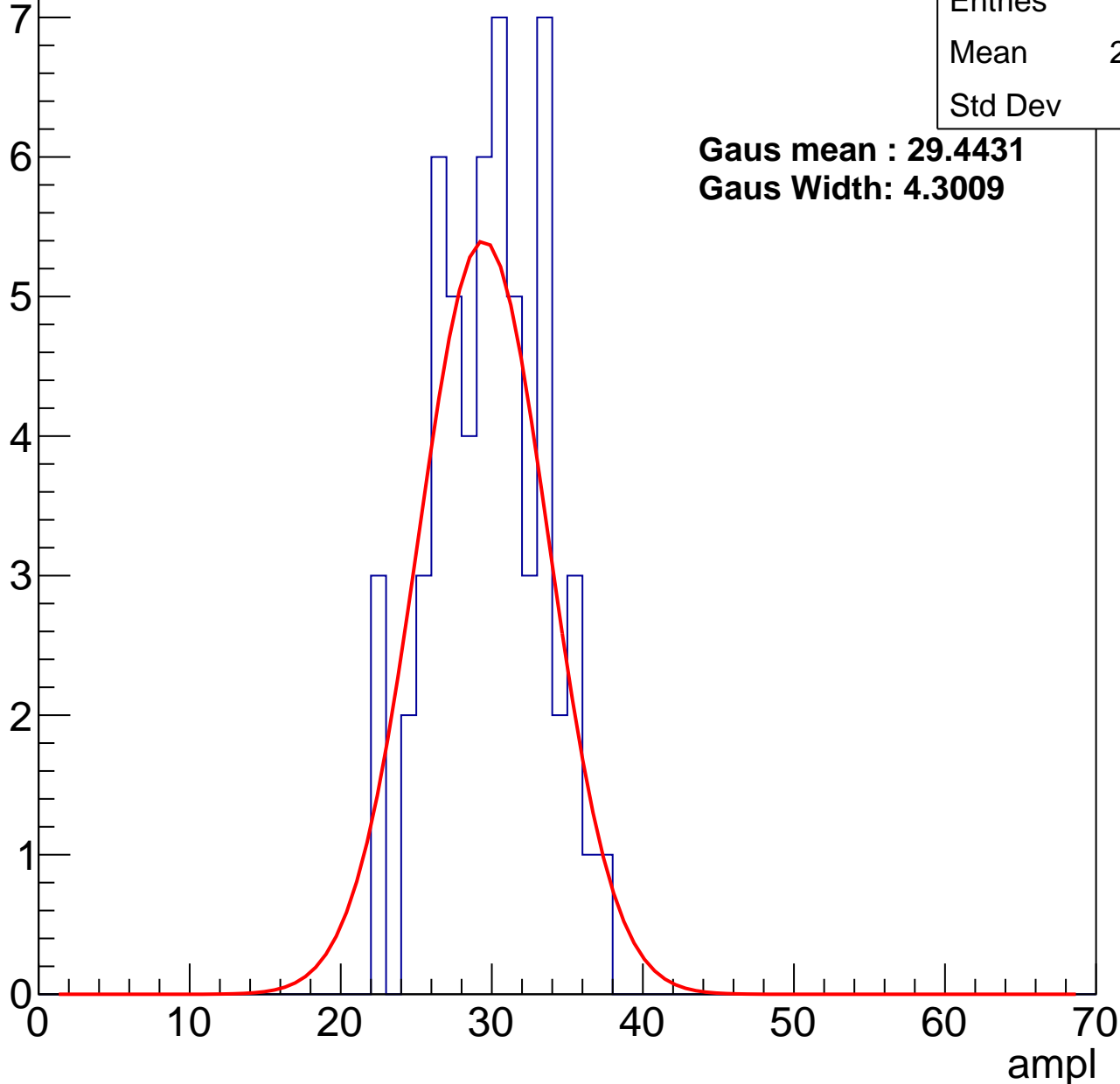
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.38
Std Dev	3.6

Gaus mean : 29.4431

Gaus Width: 4.3009



B1L101S, U14-ch81, adc1

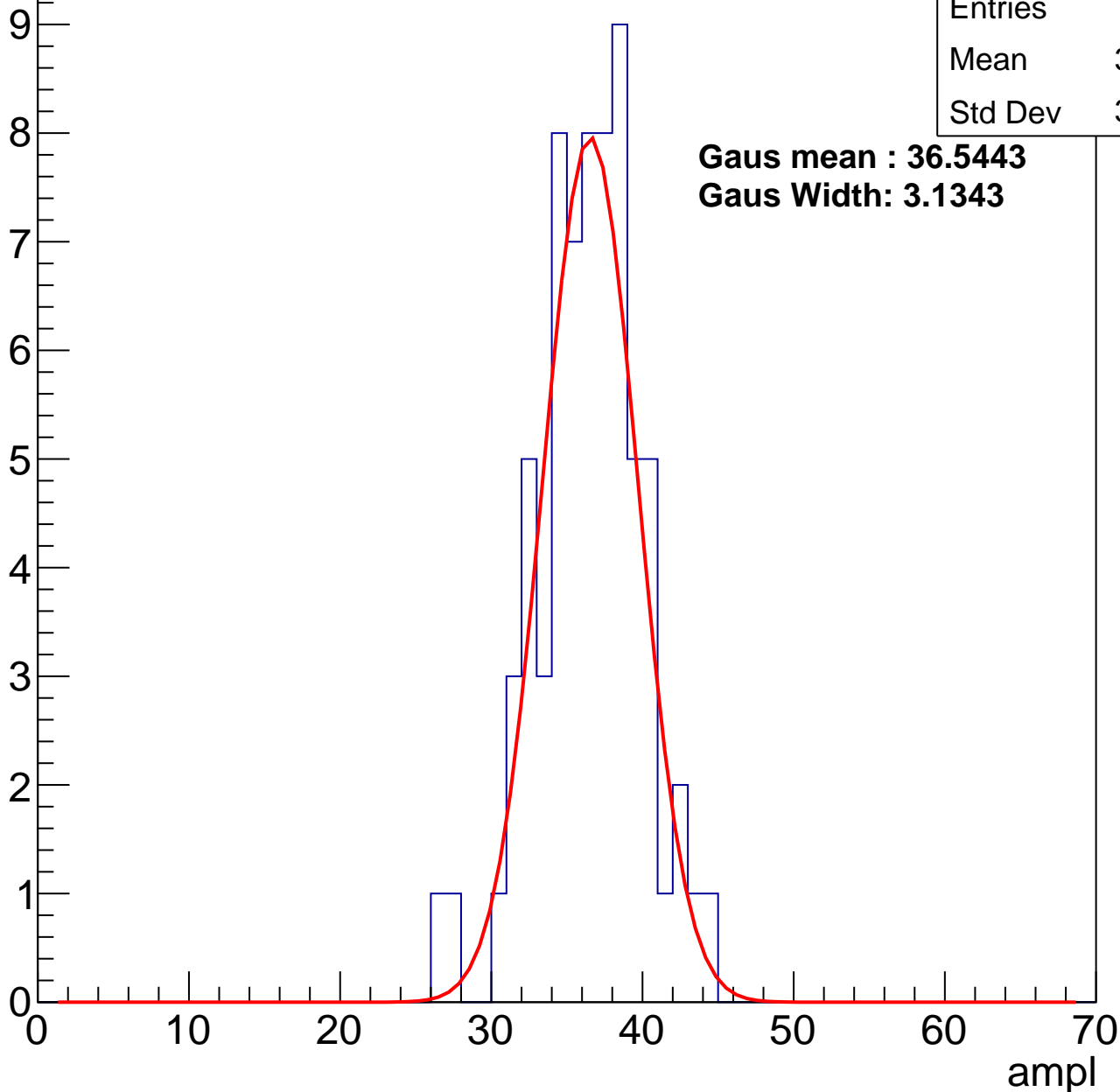
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.01
Std Dev	3.441

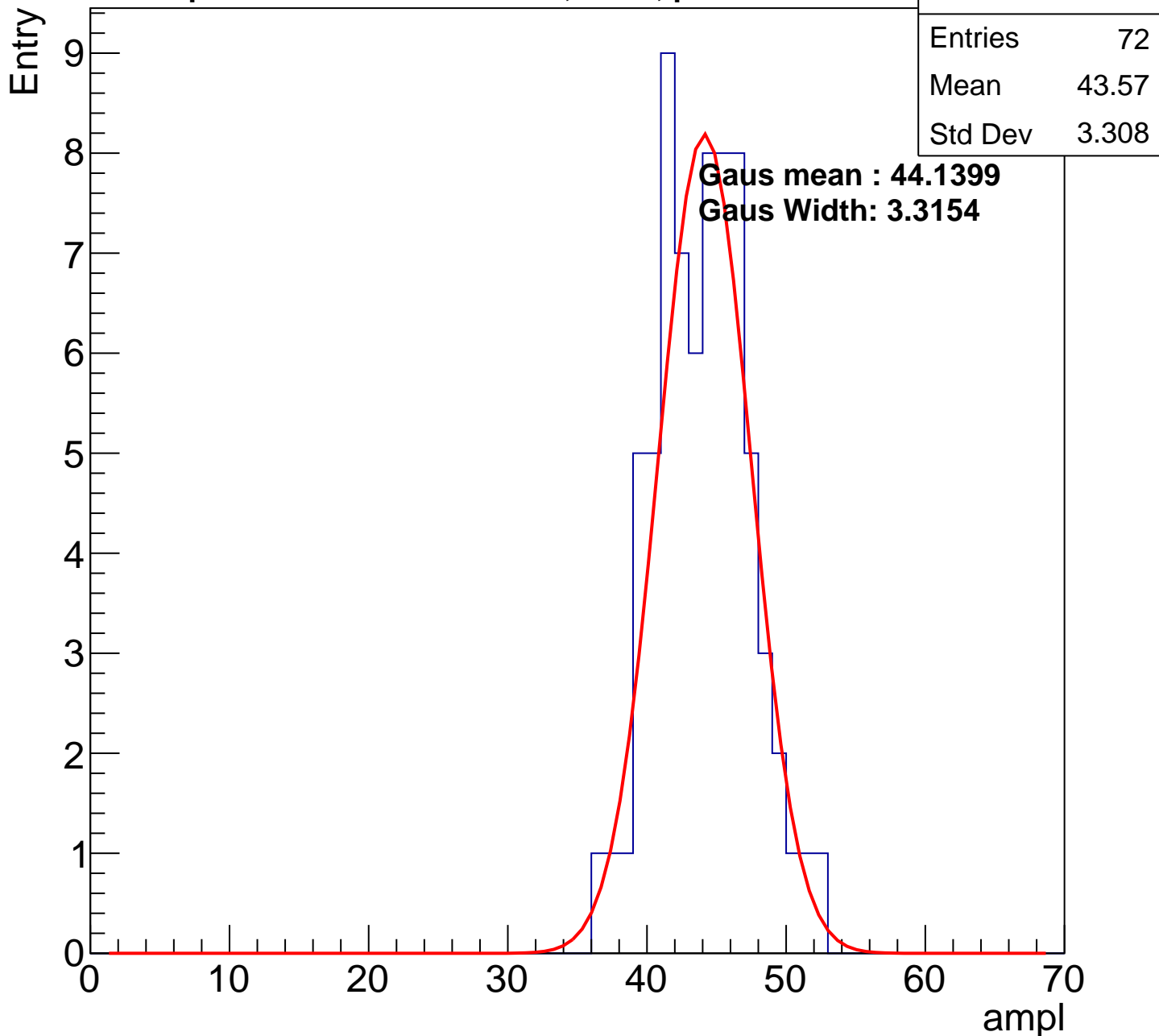
Gaus mean : 36.5443

Gaus Width: 3.1343



B1L101S, U14-ch81, adc2

calib_packv5_042523_0143.root, FC#0, port D2

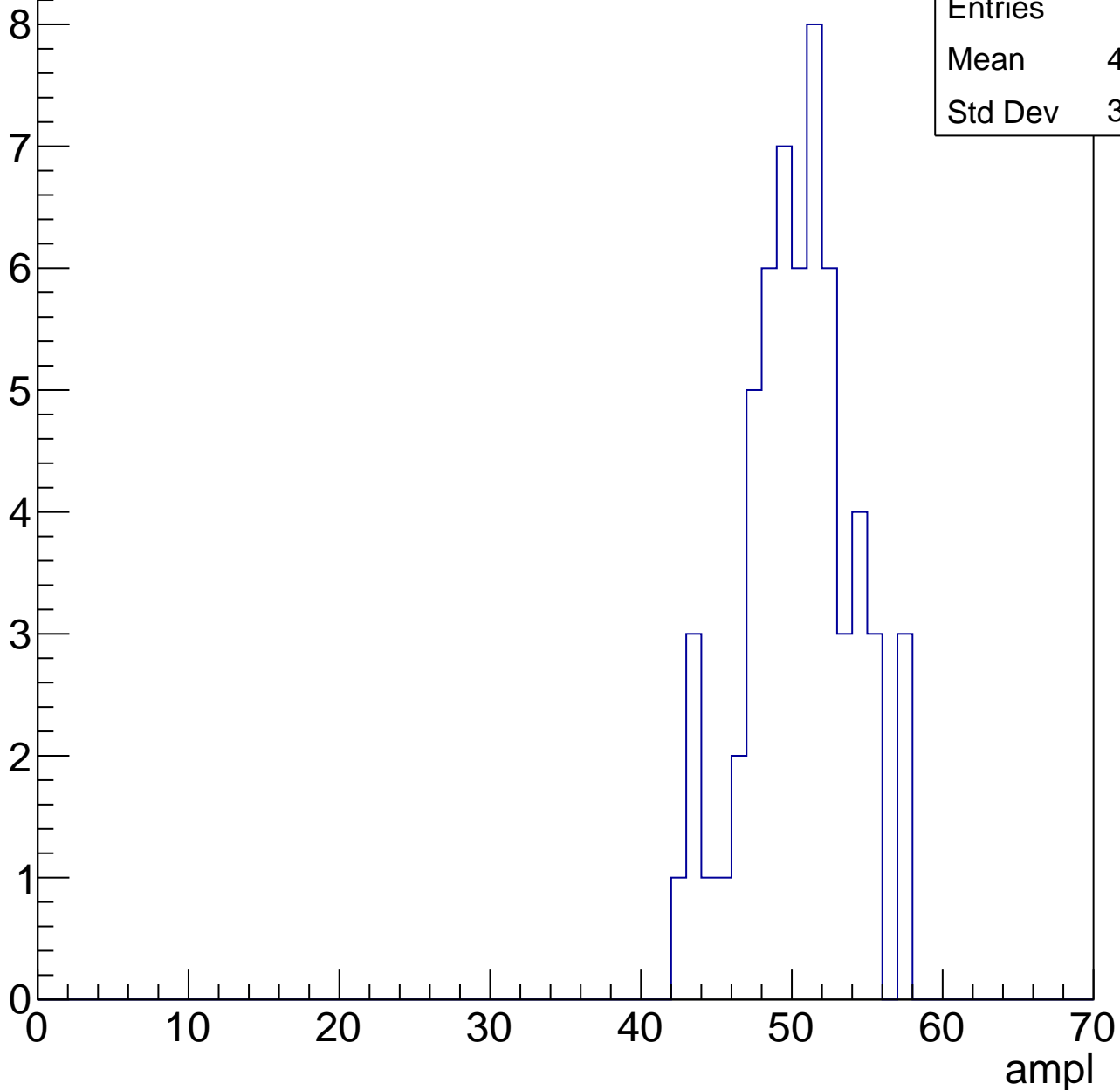


B1L101S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.98
Std Dev	3.505

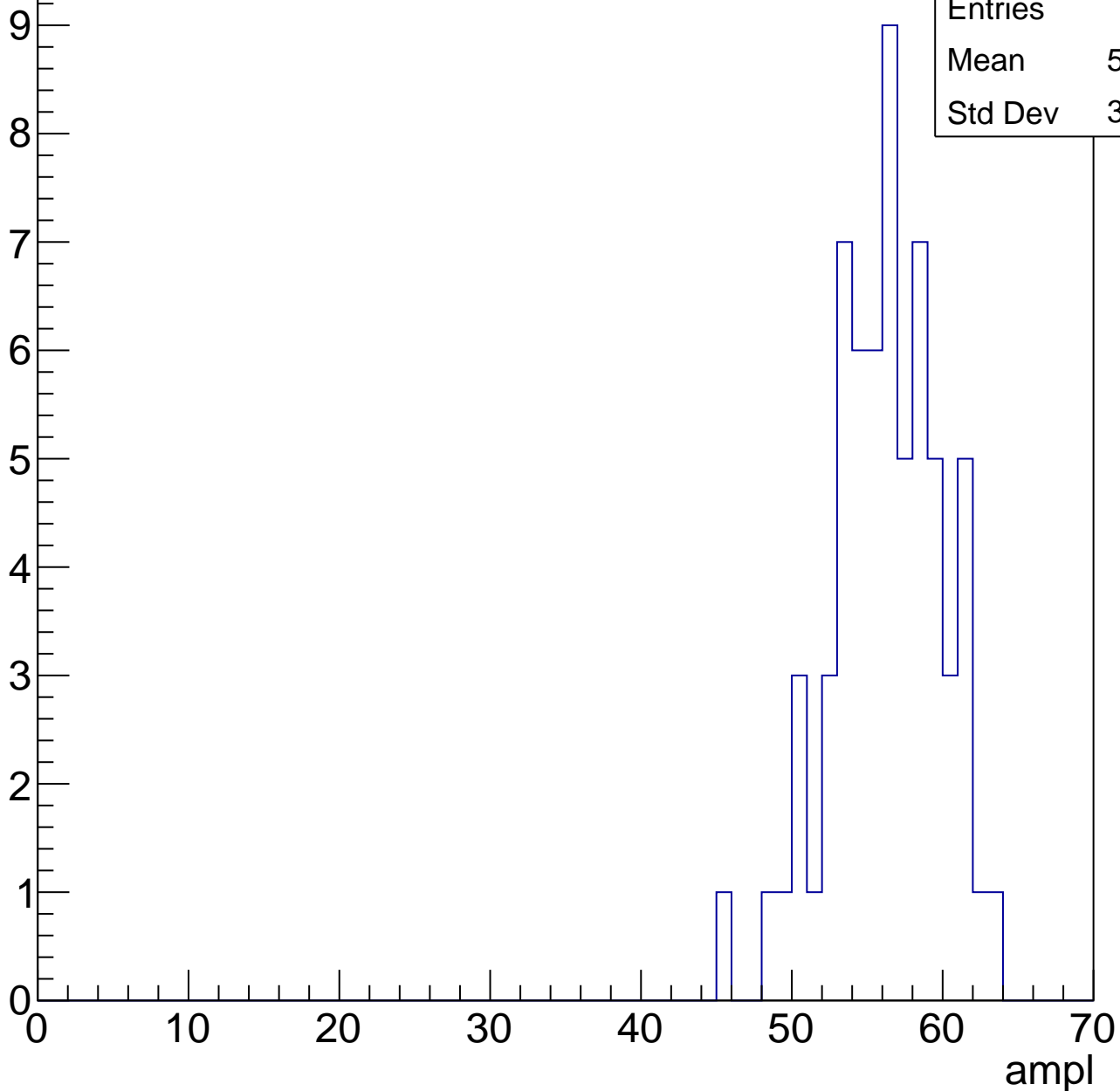


B1L101S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.75
Std Dev	3.595



B1L101S, U14-ch81, adc5

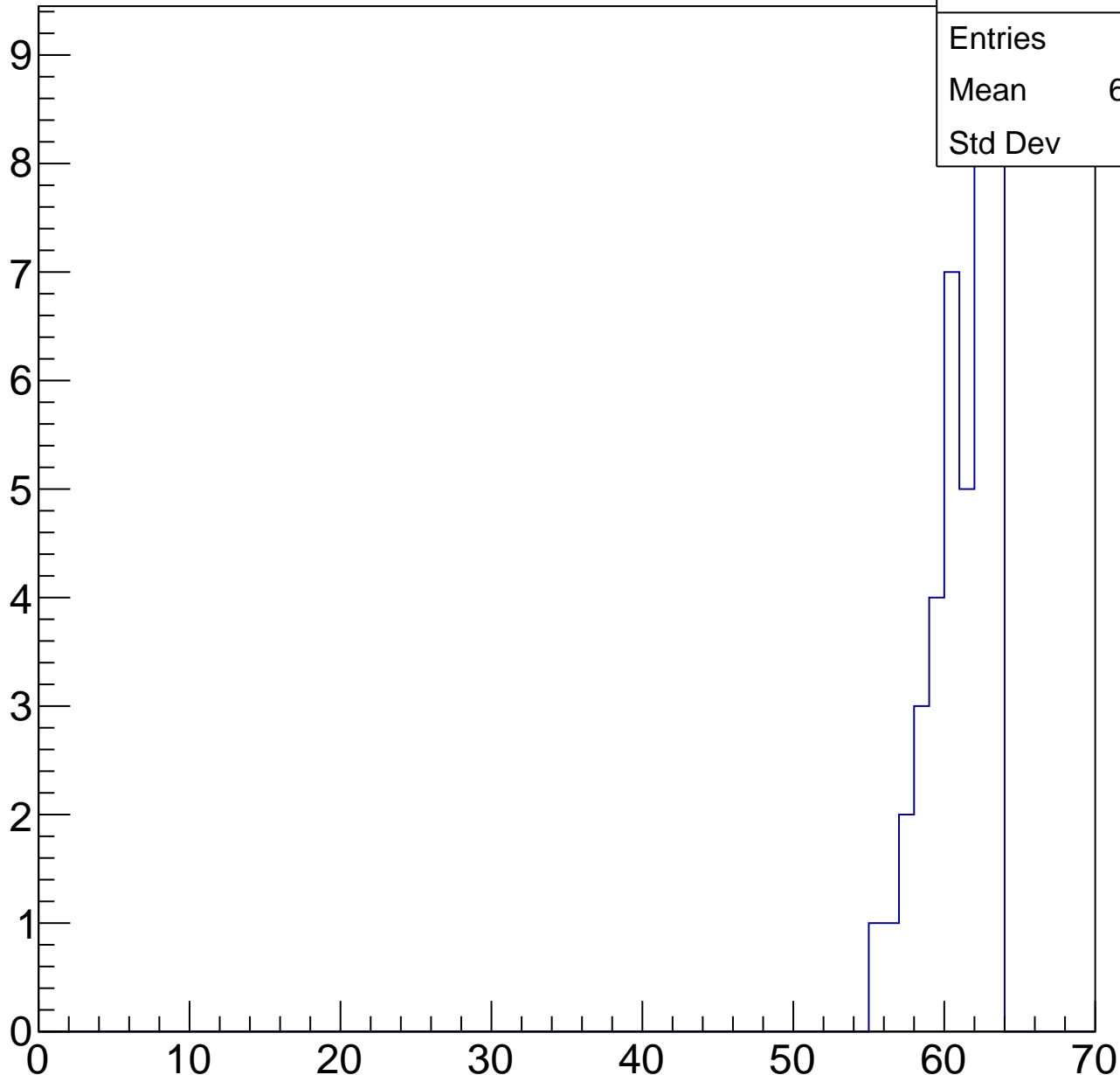
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.58
Std Dev	2.12

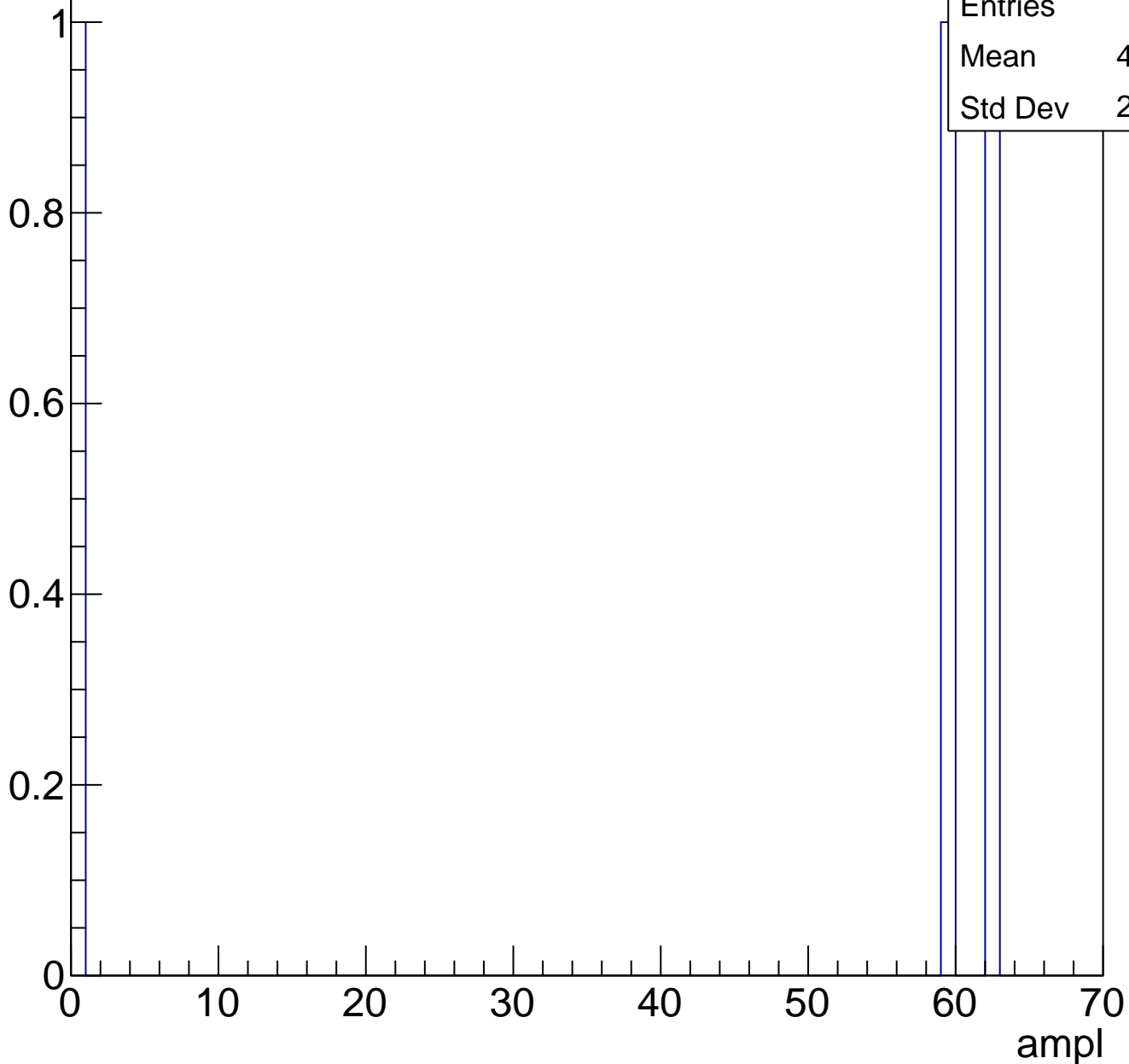
ampl



B1L101S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch82, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	28.18
Std Dev	6.61

Gaus mean : 30.0107

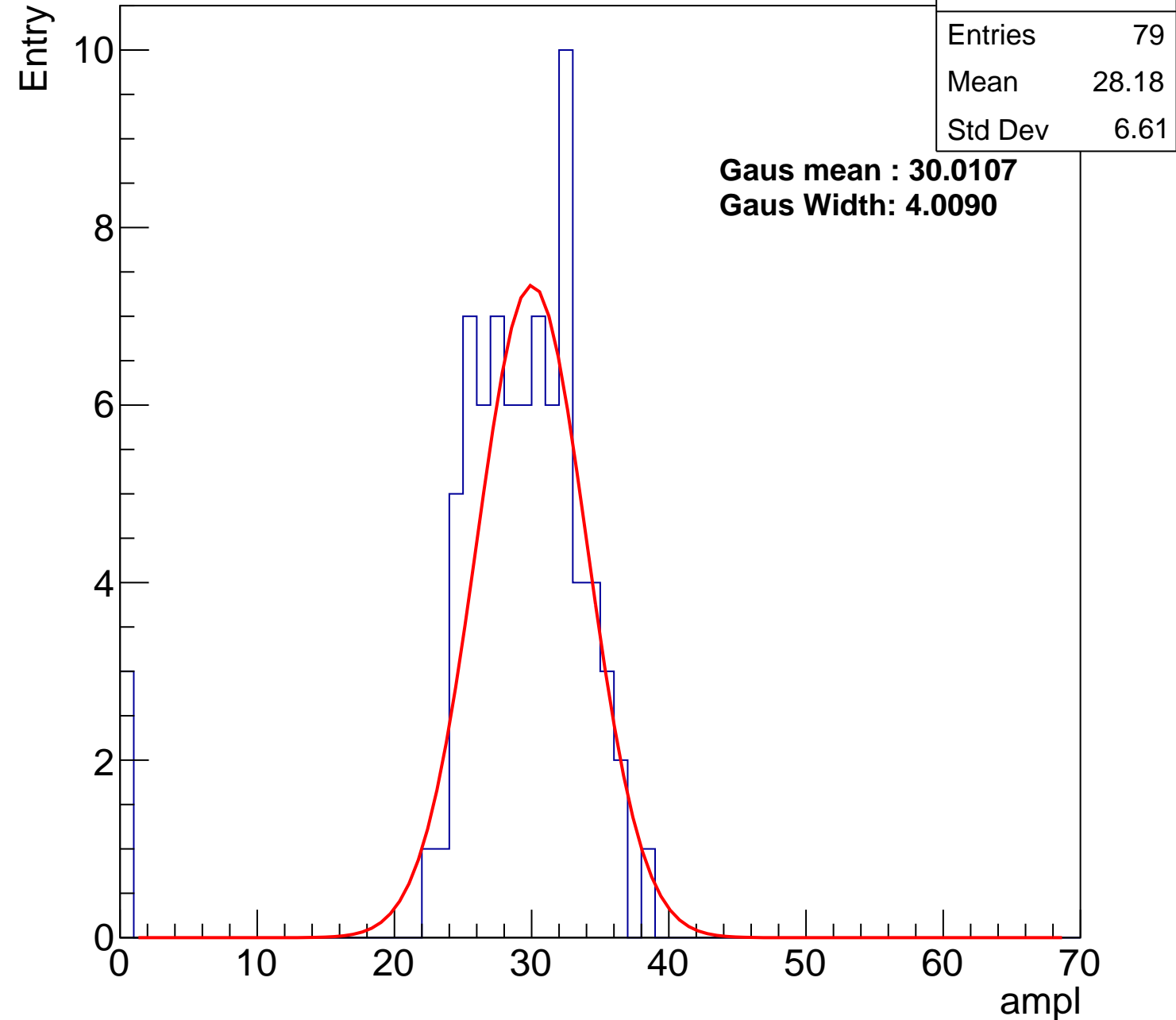
Gaus Width: 4.0090

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch82, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	36
Std Dev	3.491

Gaus mean : 36.0392

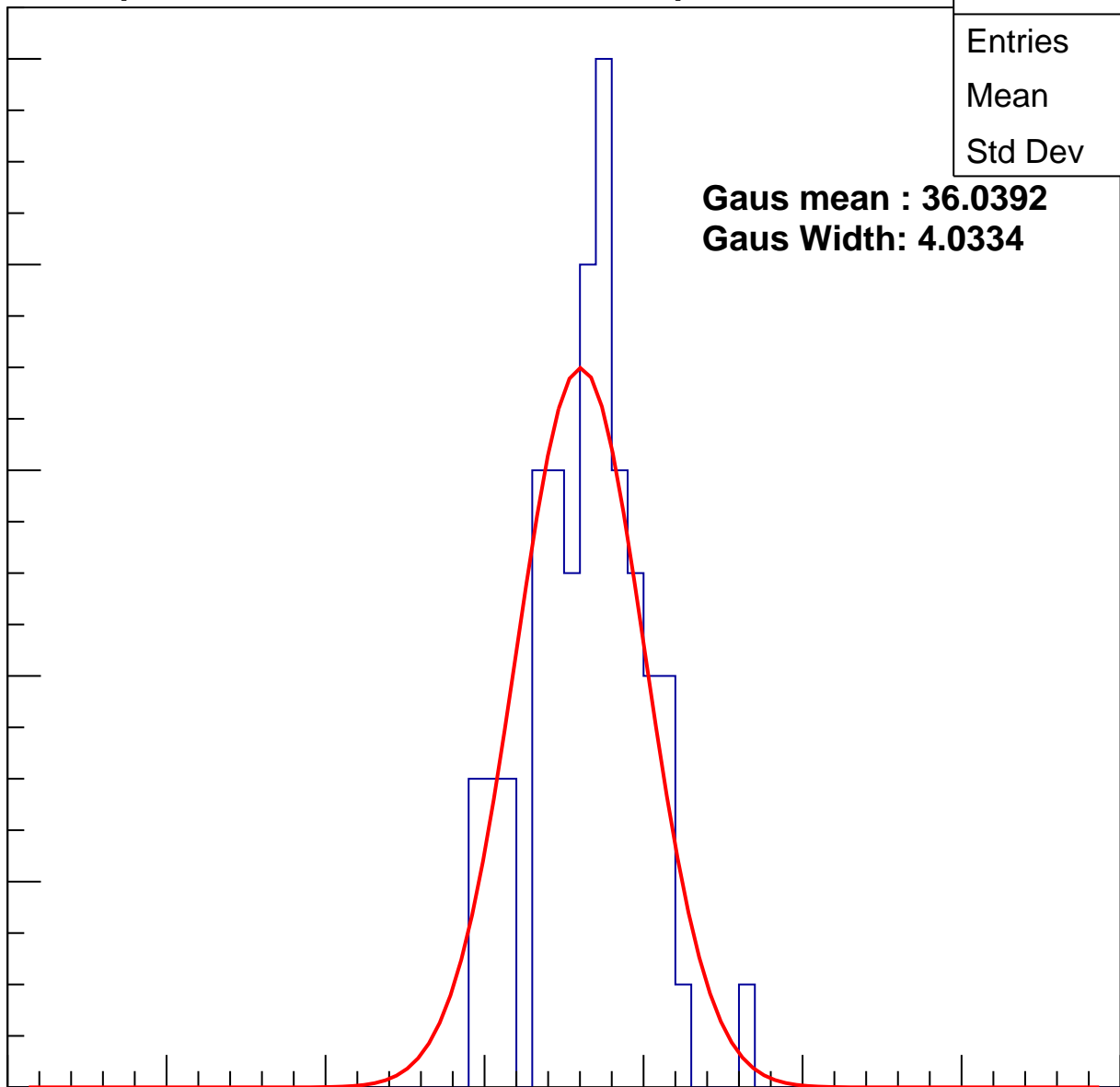
Gaus Width: 4.0334

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch82, adc2

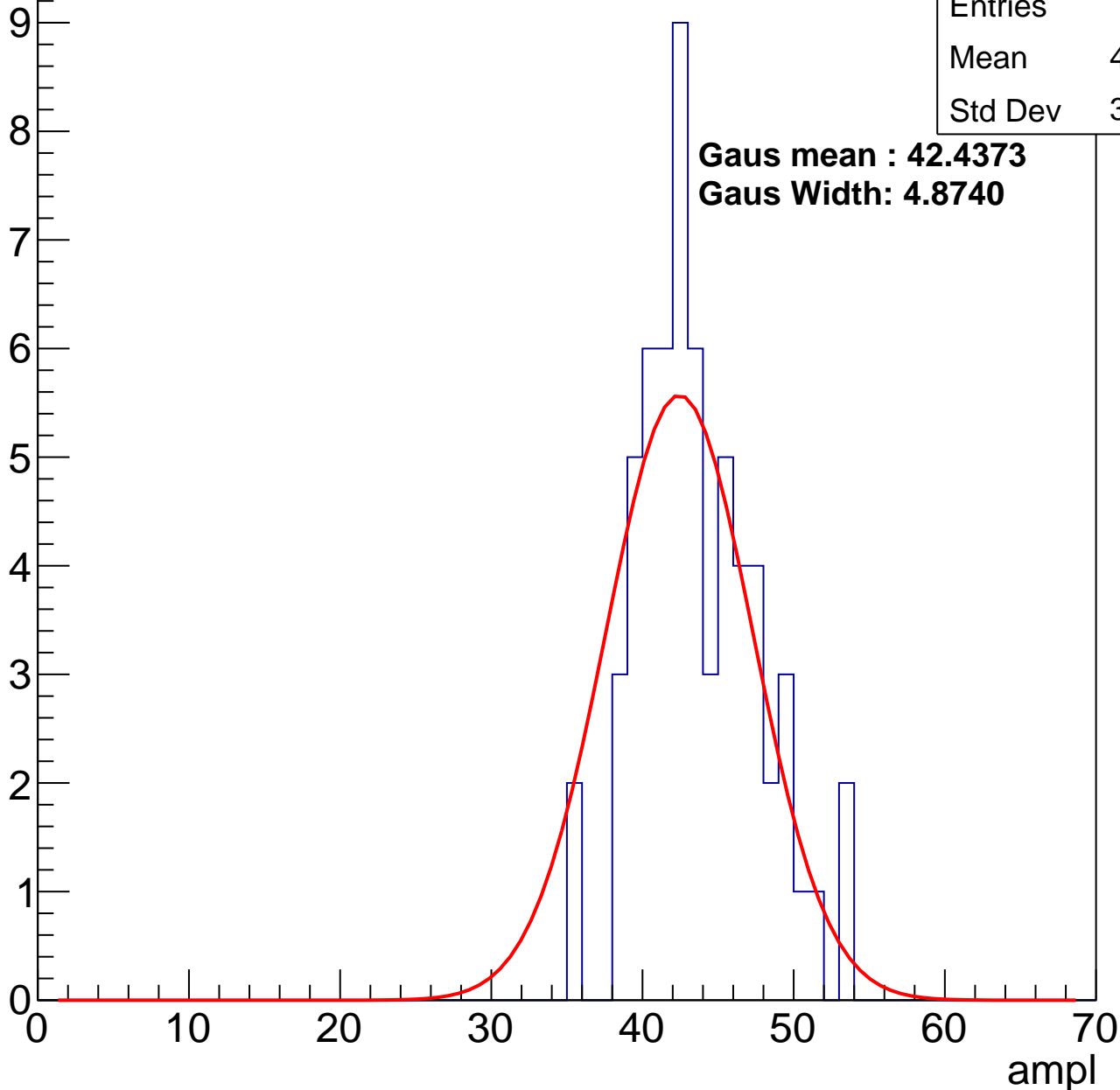
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.23
Std Dev	3.937

Gaus mean : 42.4373

Gaus Width: 4.8740

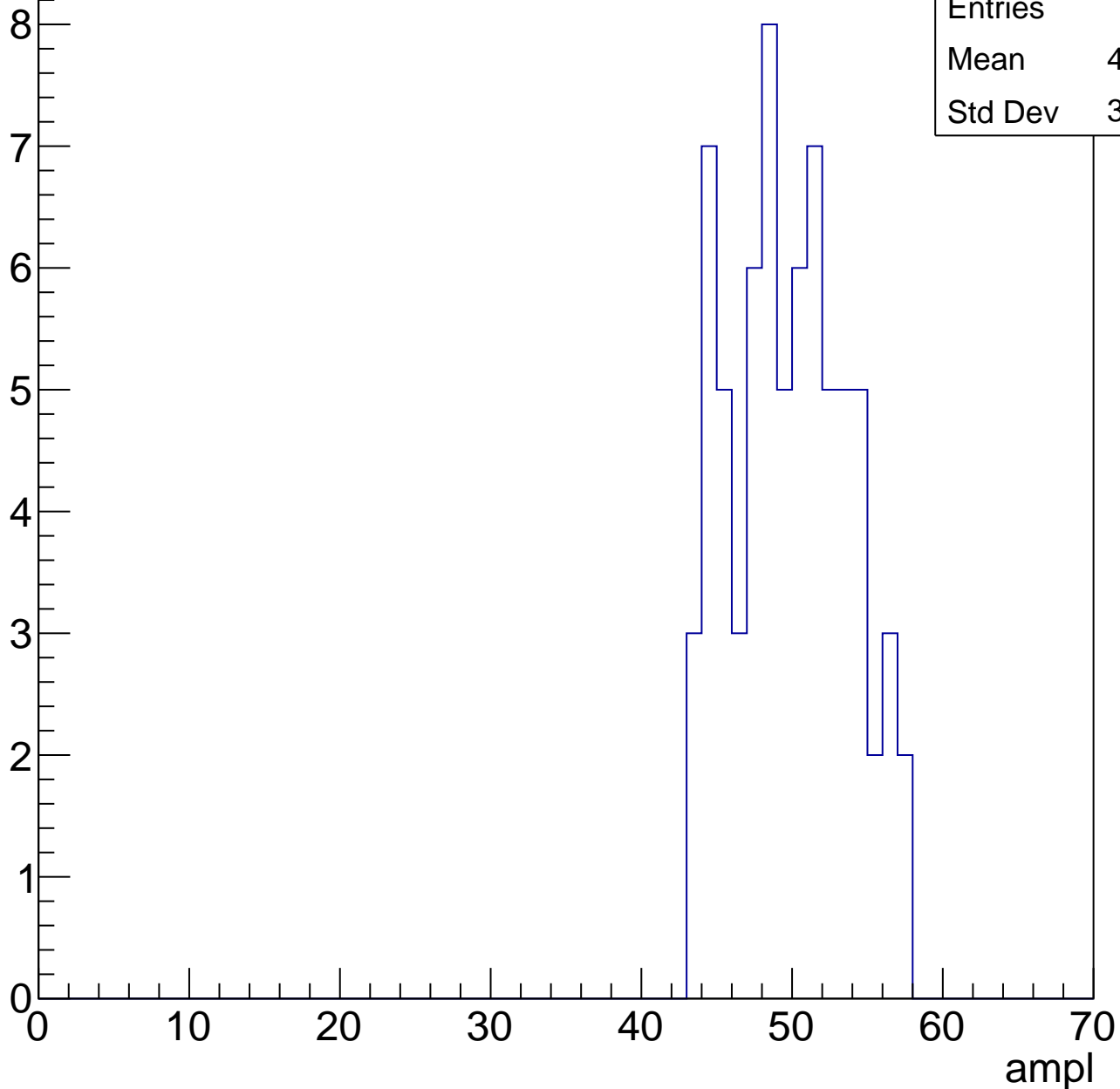


B1L101S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	49.38
Std Dev	3.806

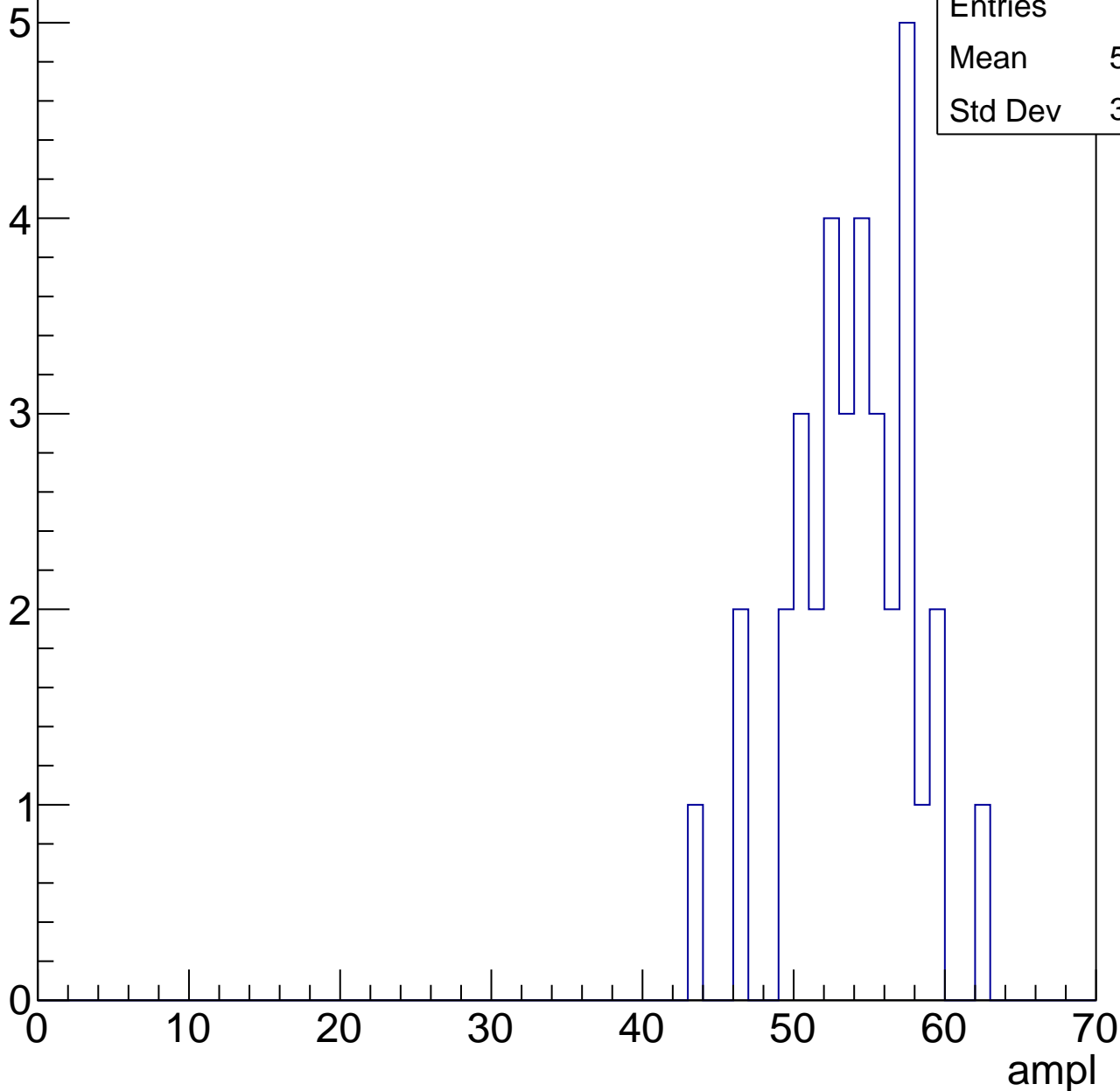


B1L101S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	53.37
Std Dev	3.986

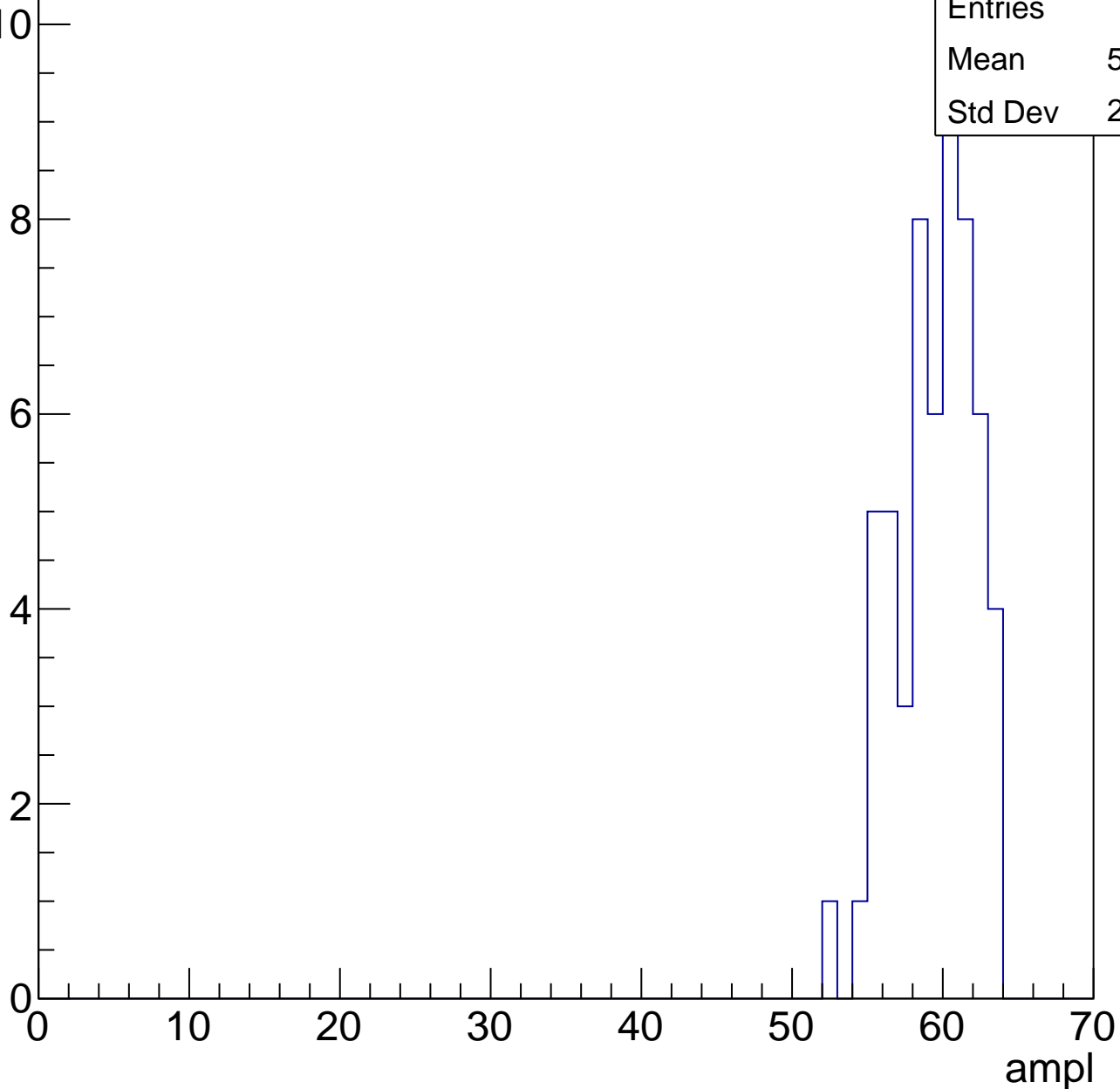


B1L101S, U14-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	58.98
Std Dev	2.579

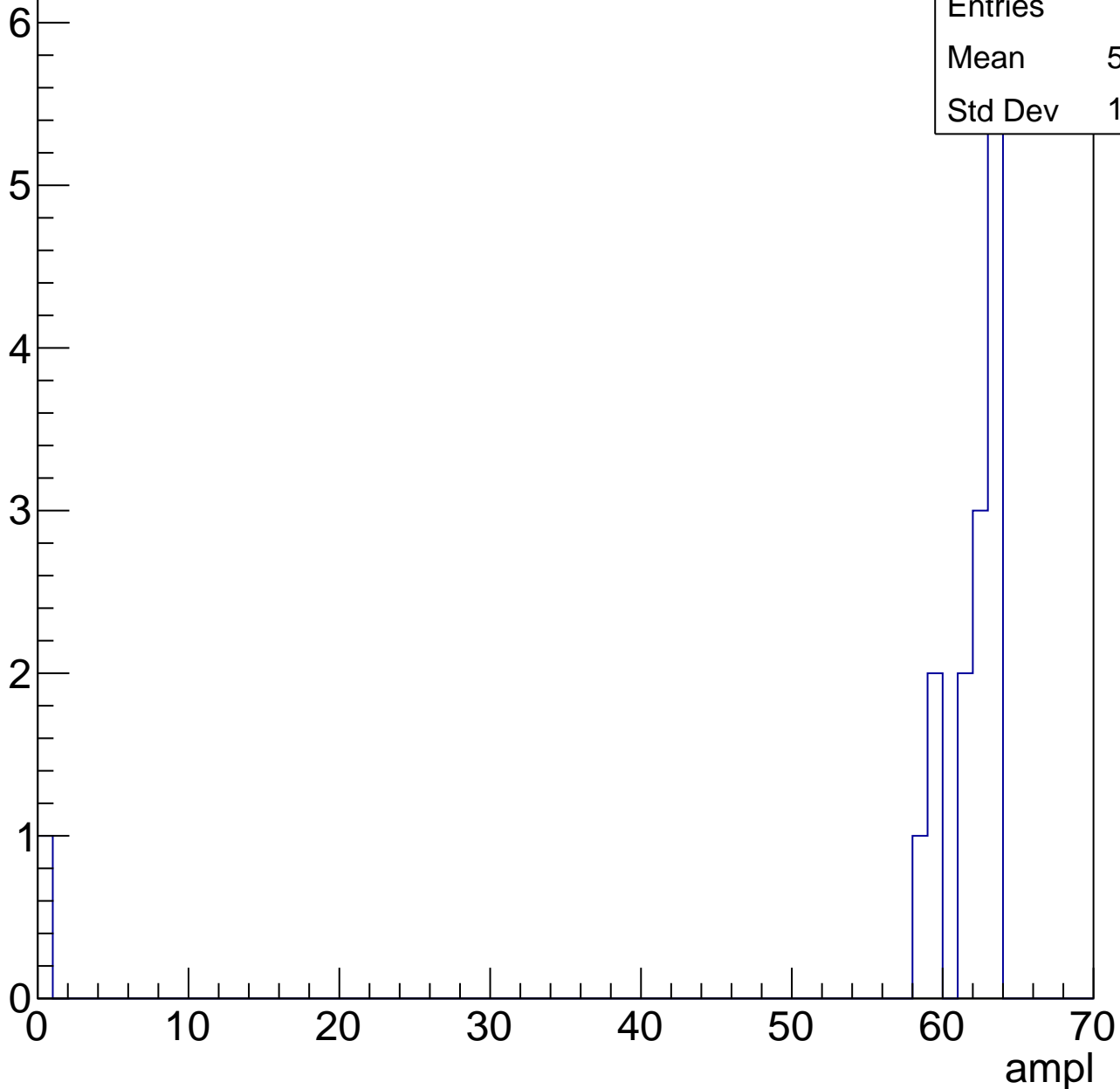


B1L101S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.47
Std Dev	15.44



B1L101S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch83, adc0

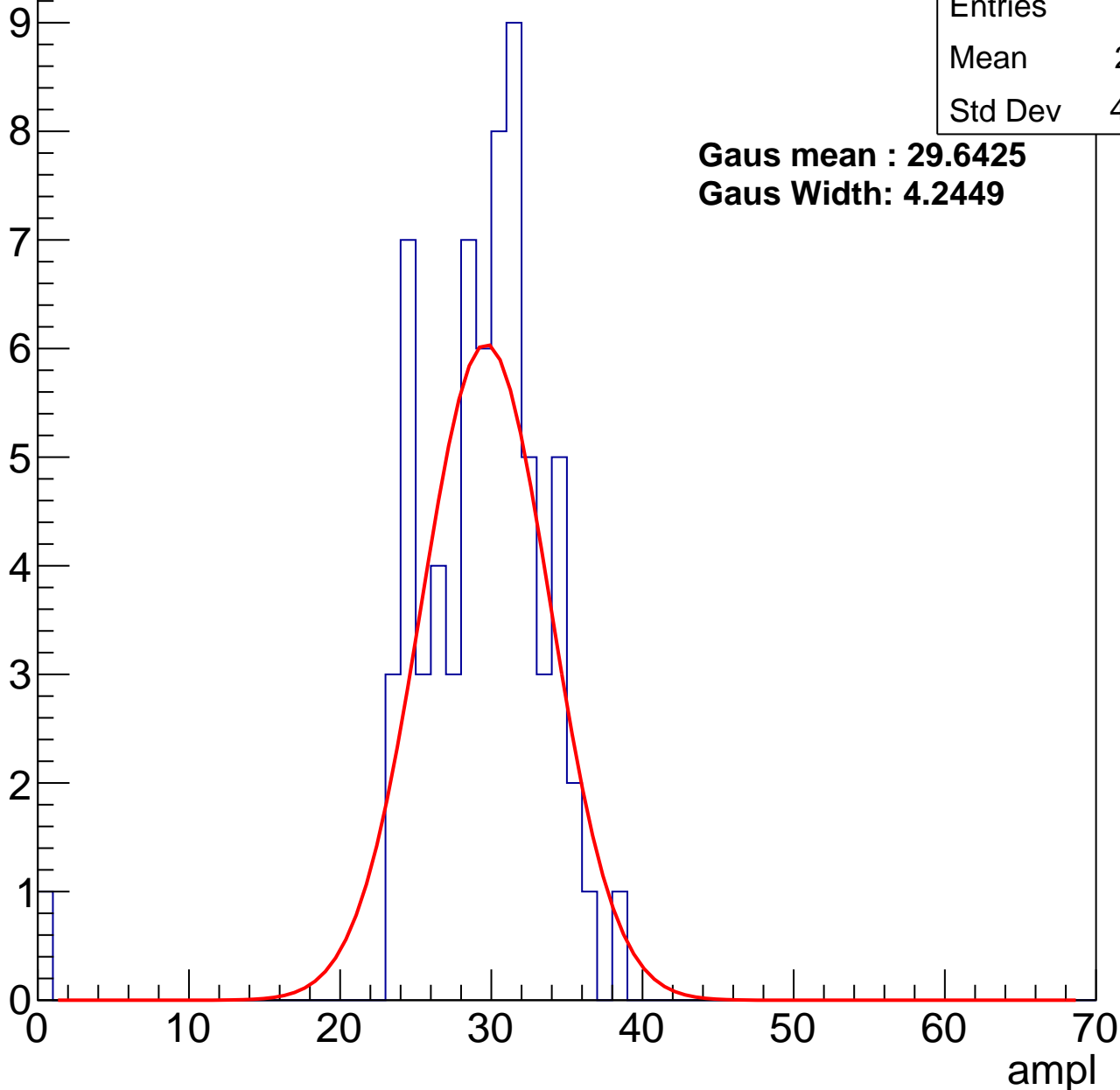
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.81
Std Dev	4.983

Gaus mean : 29.6425

Gaus Width: 4.2449



B1L101S, U14-ch83, adc1

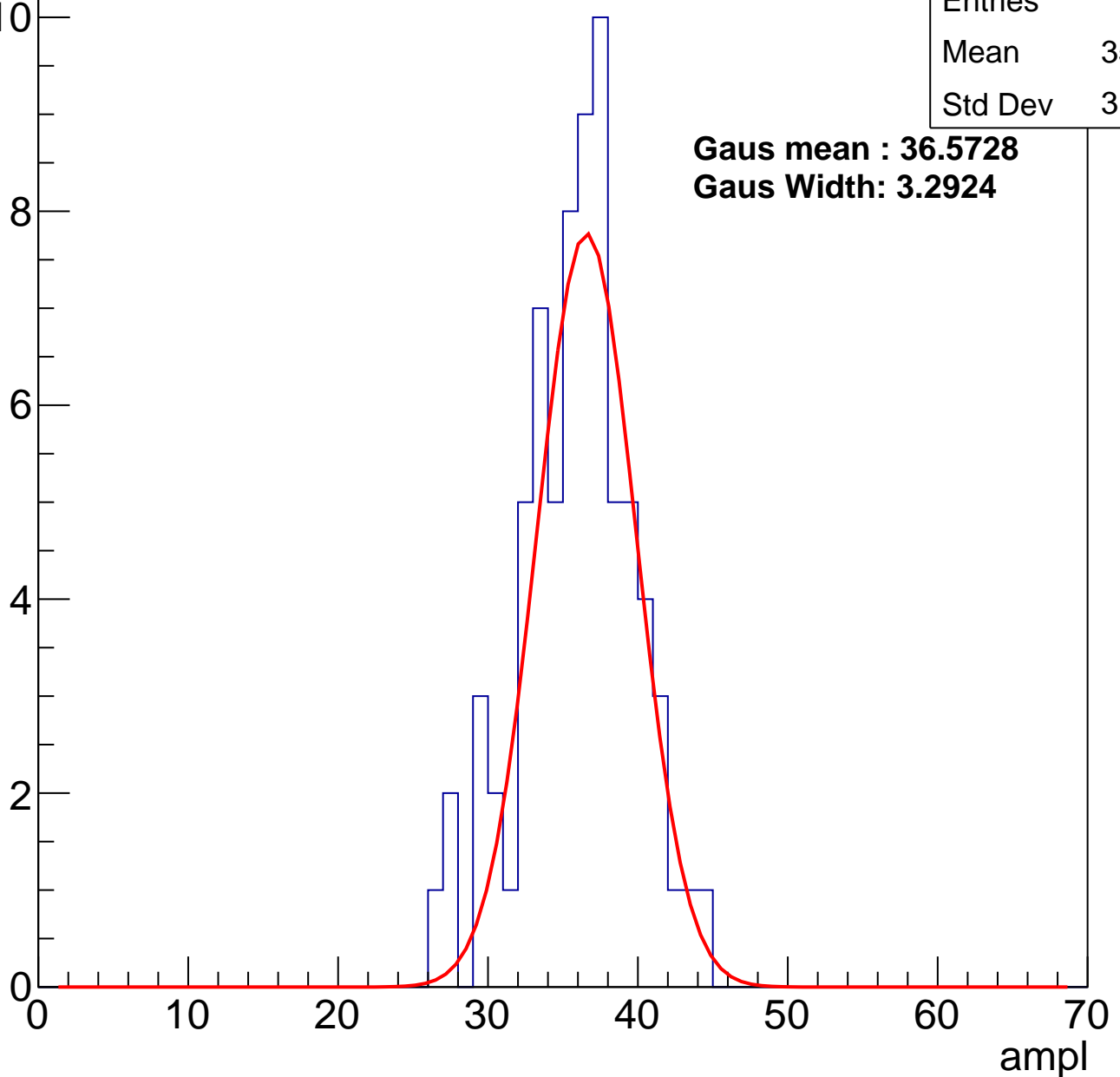
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.48
Std Dev	3.738

Gaus mean : 36.5728

Gaus Width: 3.2924



B1L101S, U14-ch83, adc2

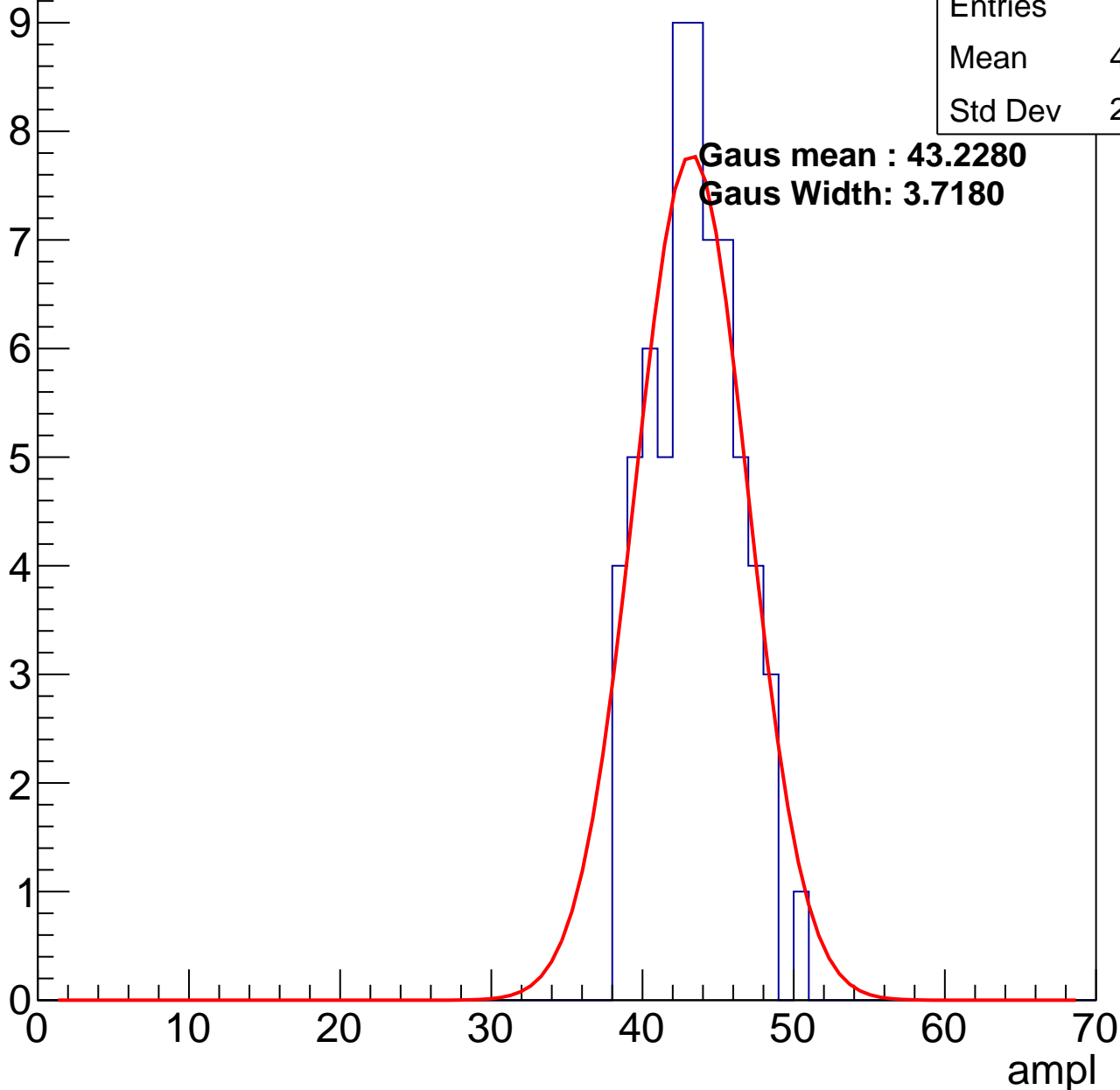
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.95
Std Dev	2.858

Gaus mean : 43.2280

Gaus Width: 3.7180

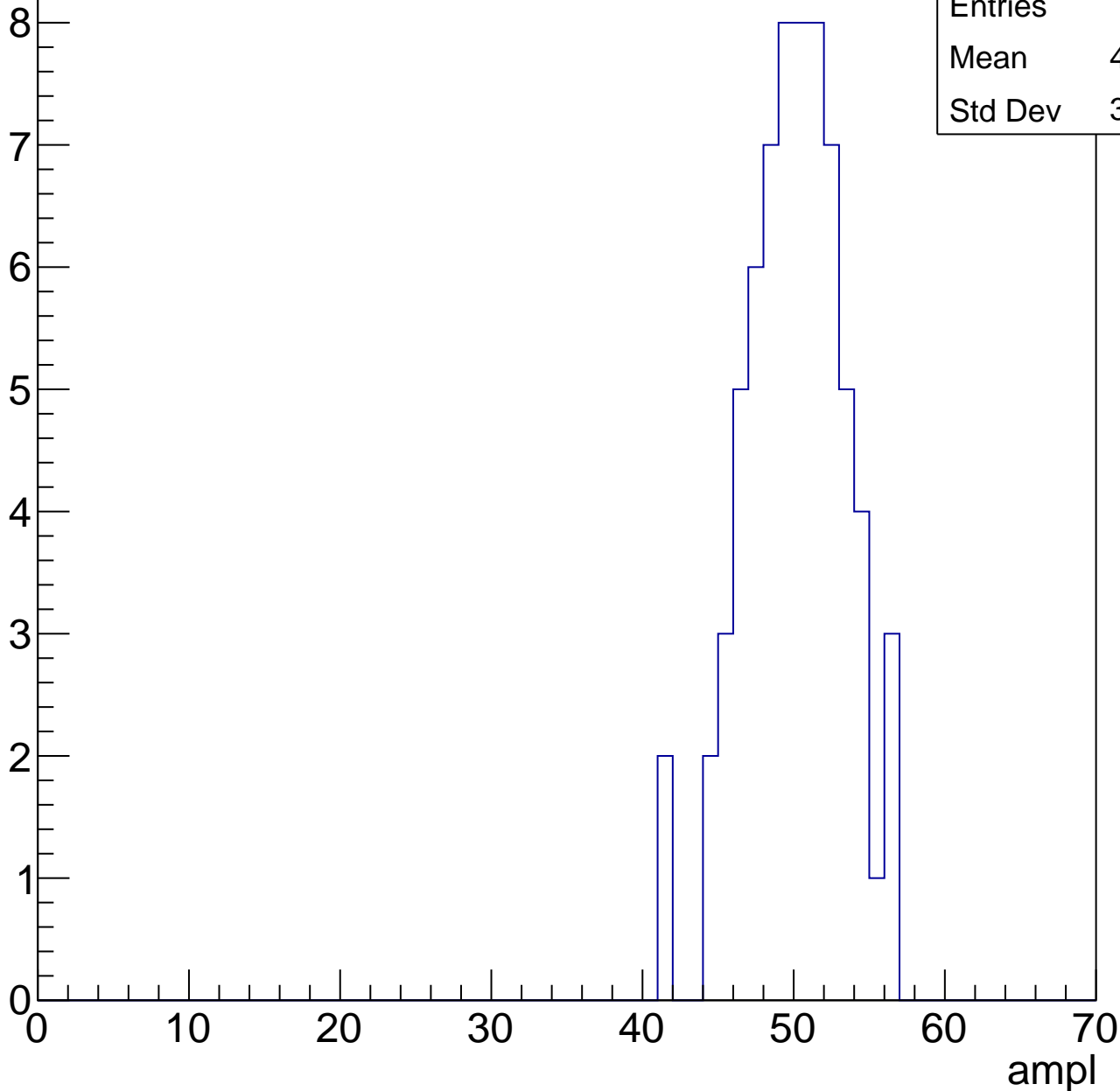


B1L101S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.58
Std Dev	3.285



B1L101S, U14-ch83, adc4

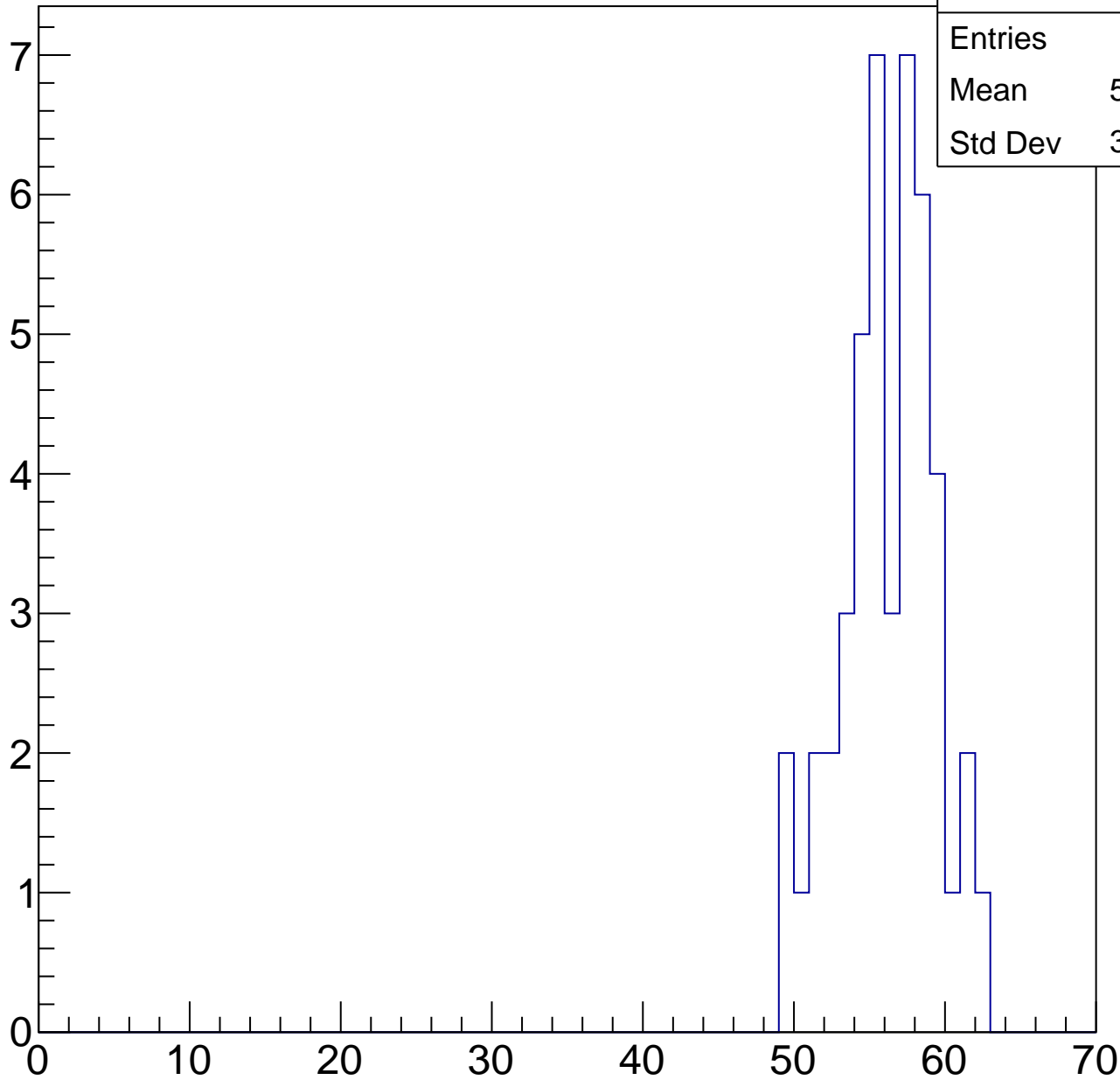
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	46
Mean	55.72
Std Dev	3.076

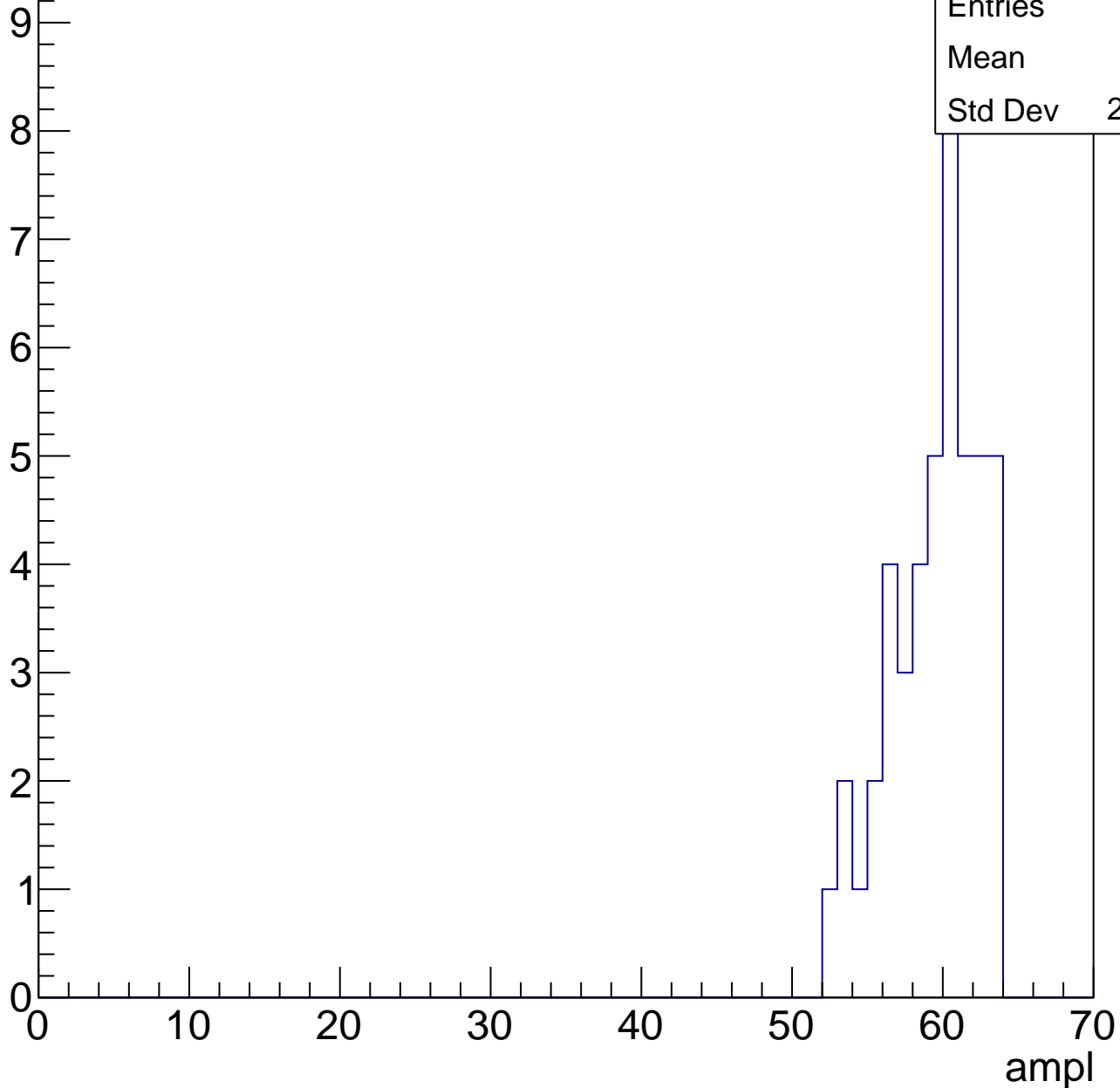
ampl



B1L101S, U14-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

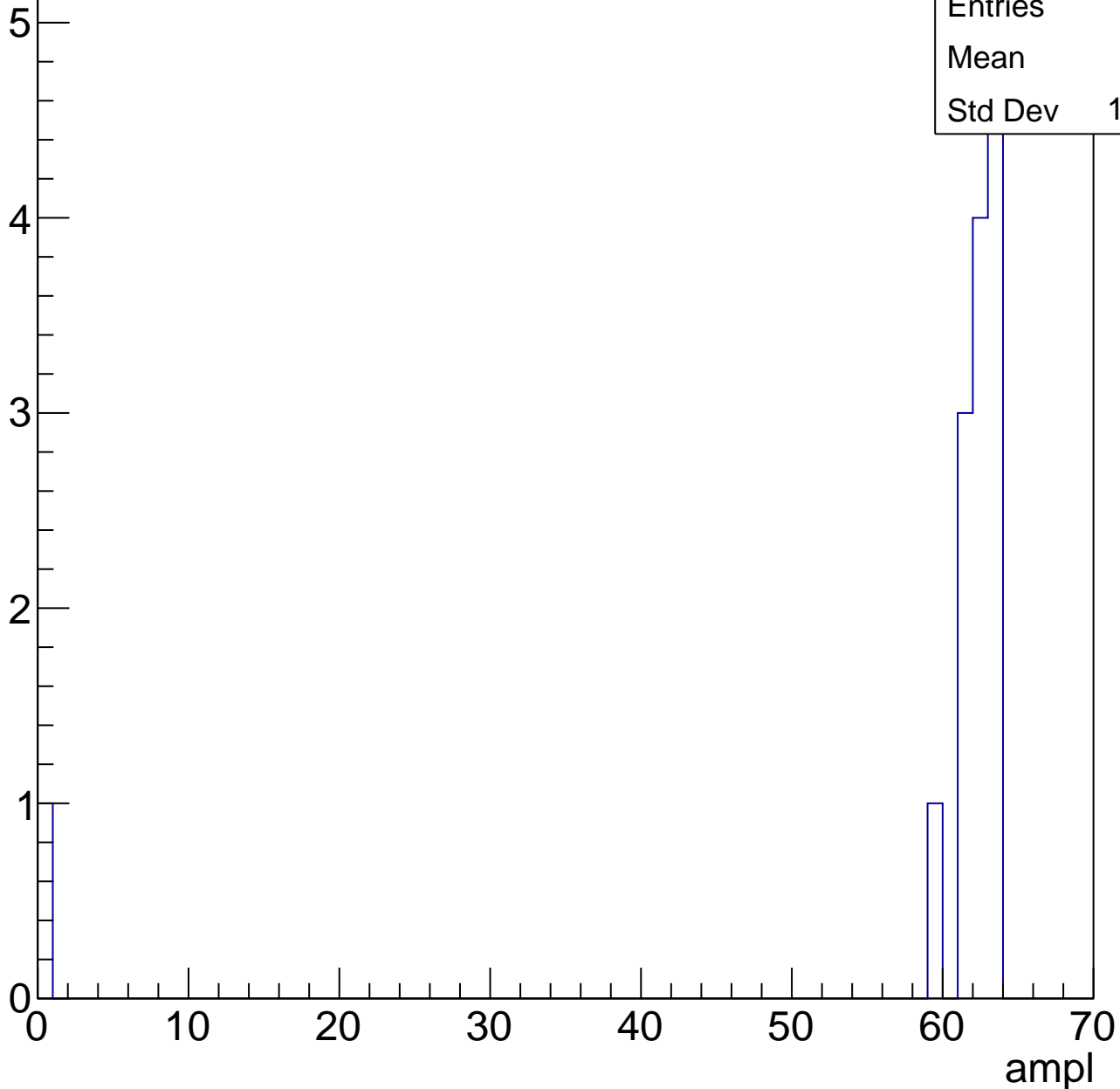


B1L101S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.5
Std Dev	15.99



B1L101S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch84, adc0

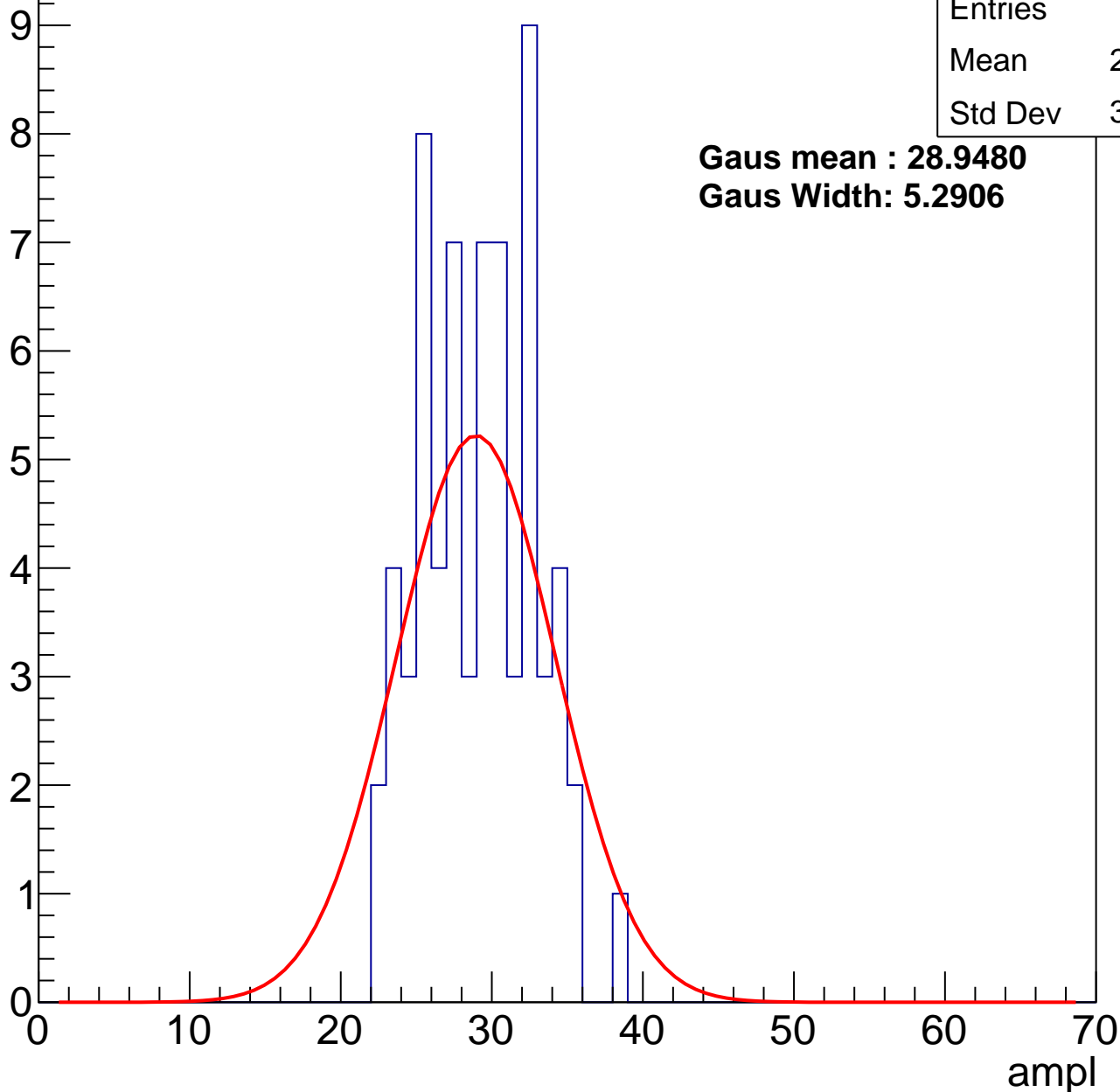
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.69
Std Dev	3.678

Gaus mean : 28.9480

Gaus Width: 5.2906



B1L101S, U14-ch84, adc1

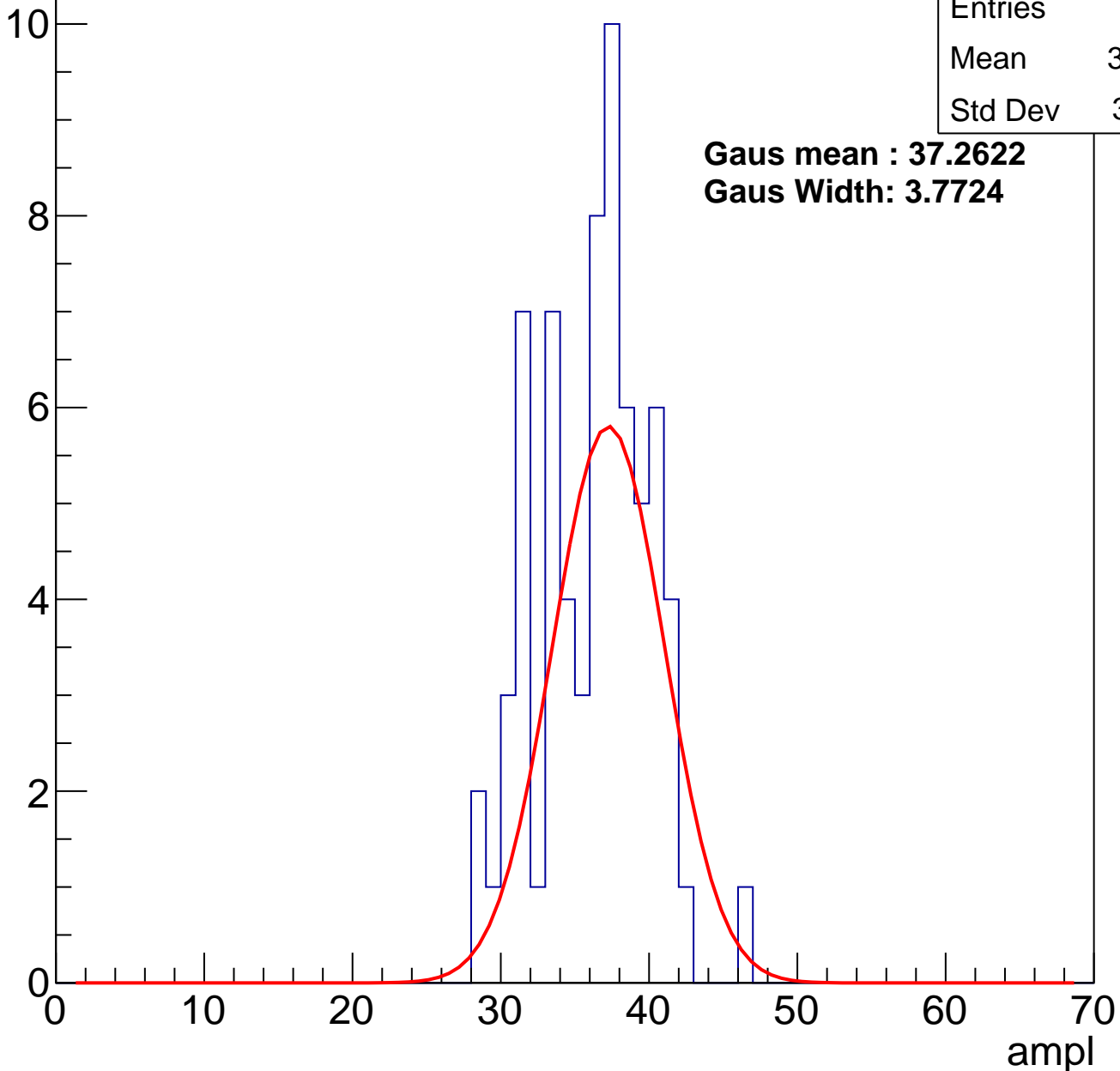
calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	35.78
Std Dev	3.741

Gaus mean : 37.2622

Gaus Width: 3.7724

Entry



B1L101S, U14-ch84, adc2

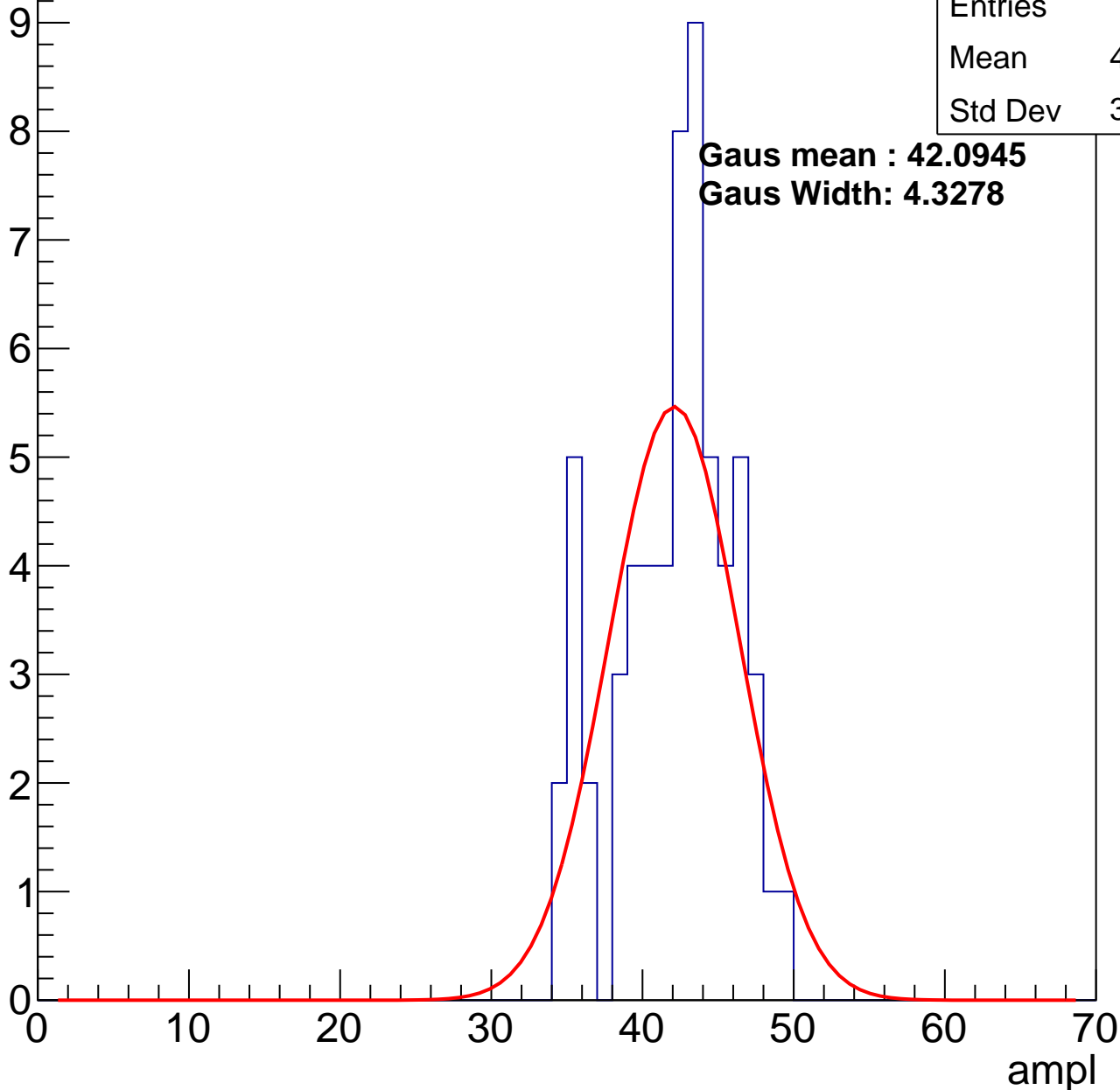
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.67
Std Dev	3.758

Gaus mean : 42.0945

Gaus Width: 4.3278

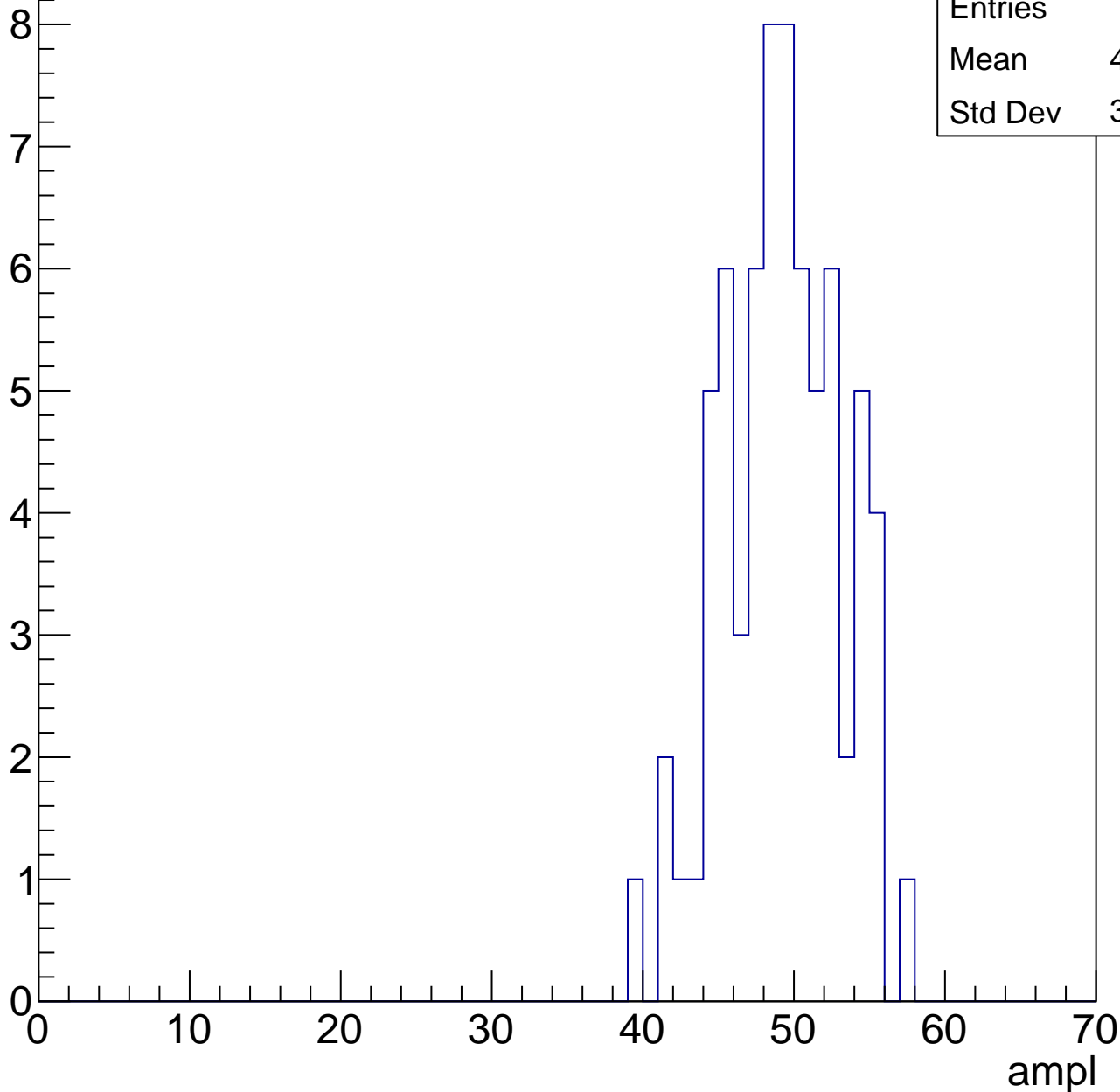


B1L101S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.74
Std Dev	3.842

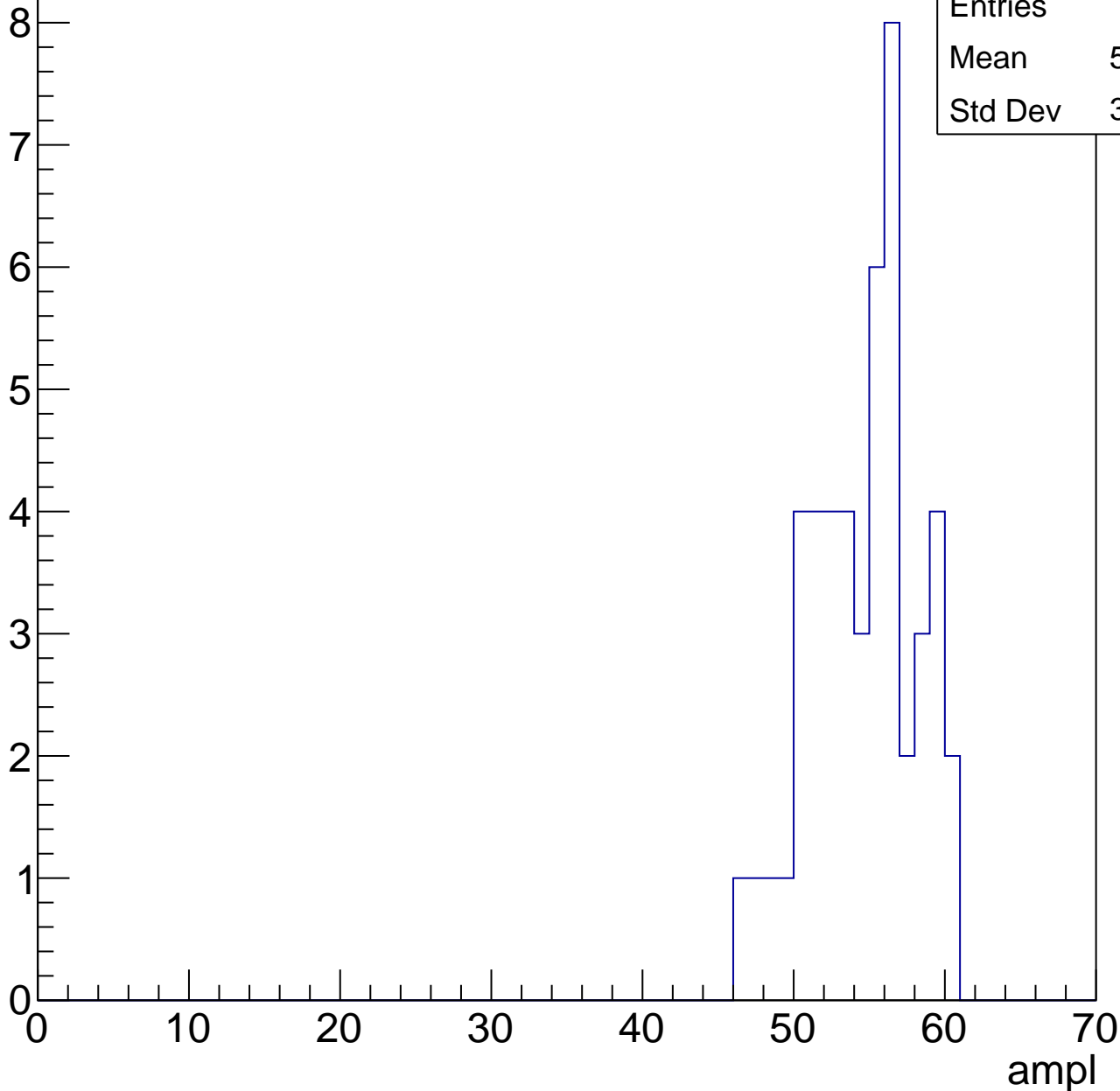


B1L101S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	54.12
Std Dev	3.444

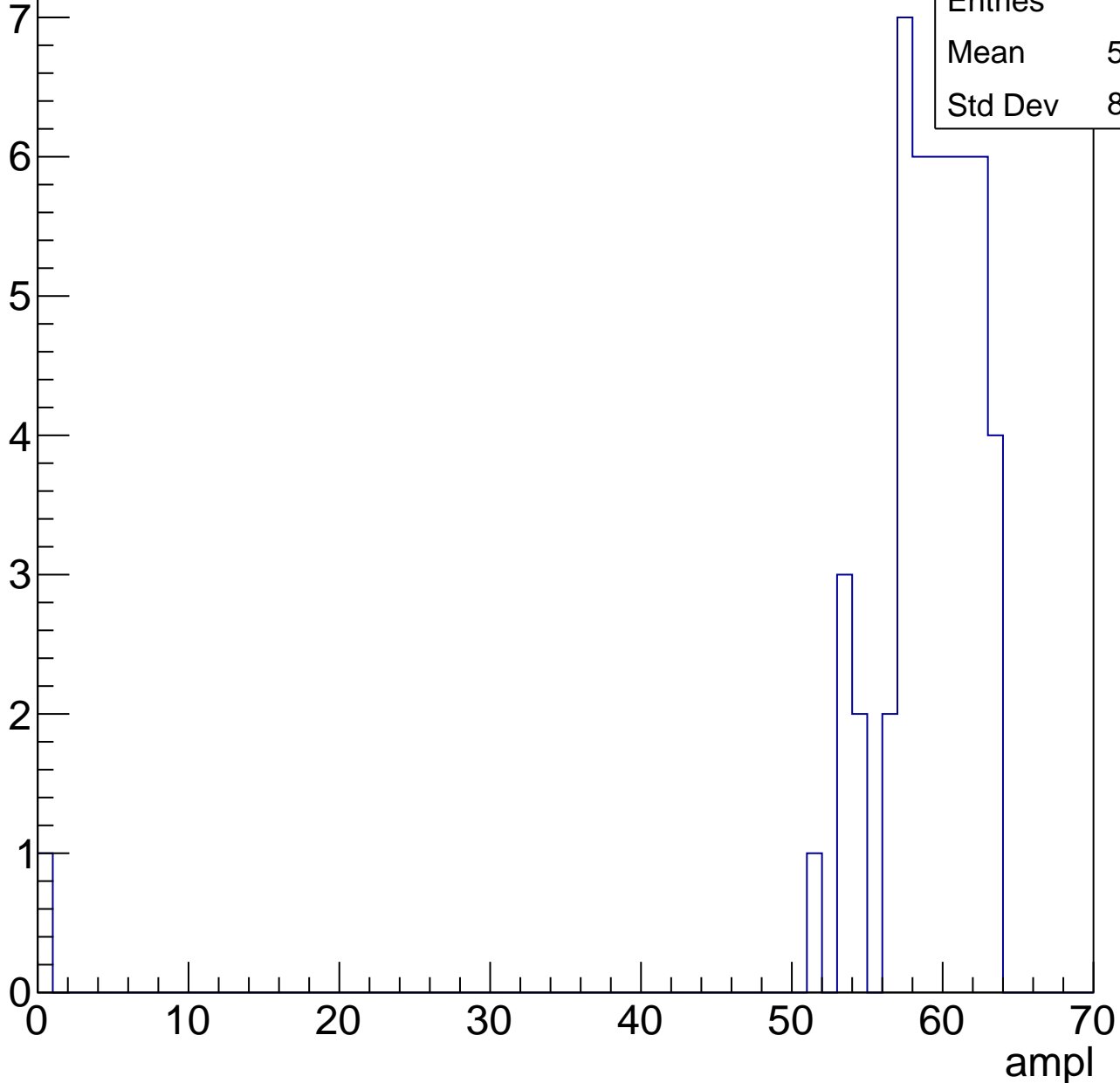


B1L101S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.62
Std Dev	8.727

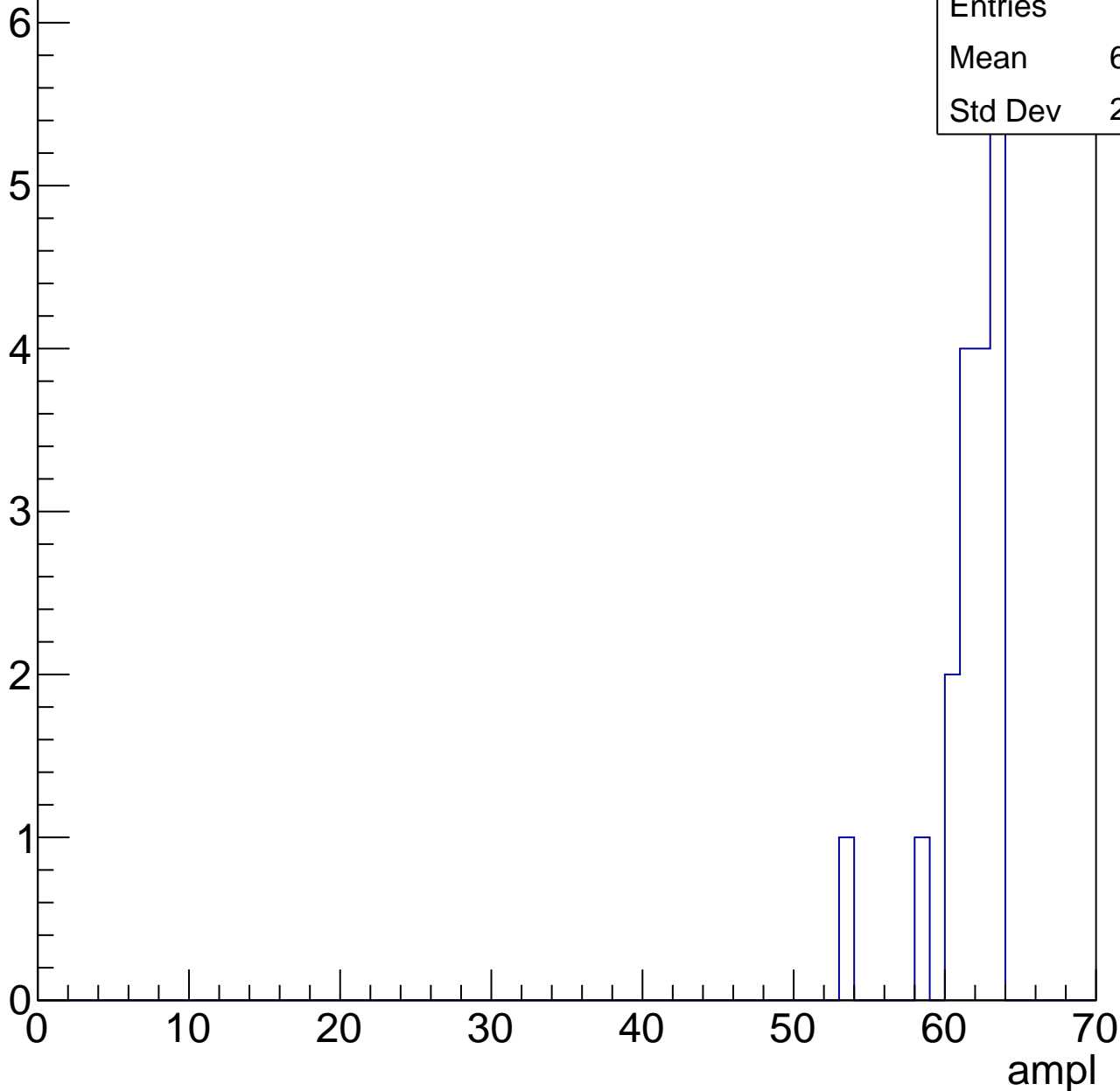


B1L101S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.17
Std Dev	2.386



B1L101S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch85, adc0

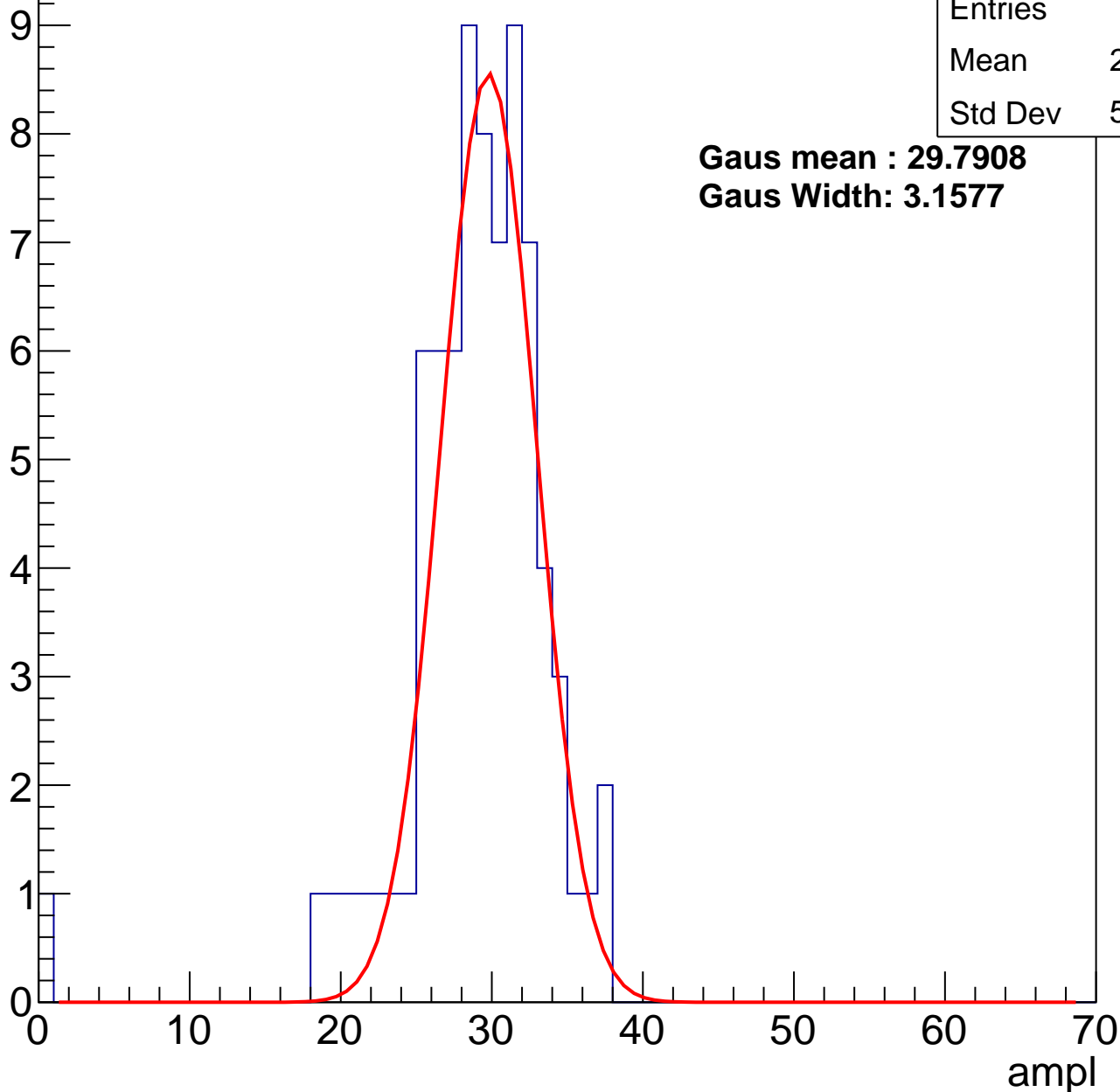
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.45
Std Dev	5.013

Gaus mean : 29.7908

Gaus Width: 3.1577



B1L101S, U14-ch85, adc1

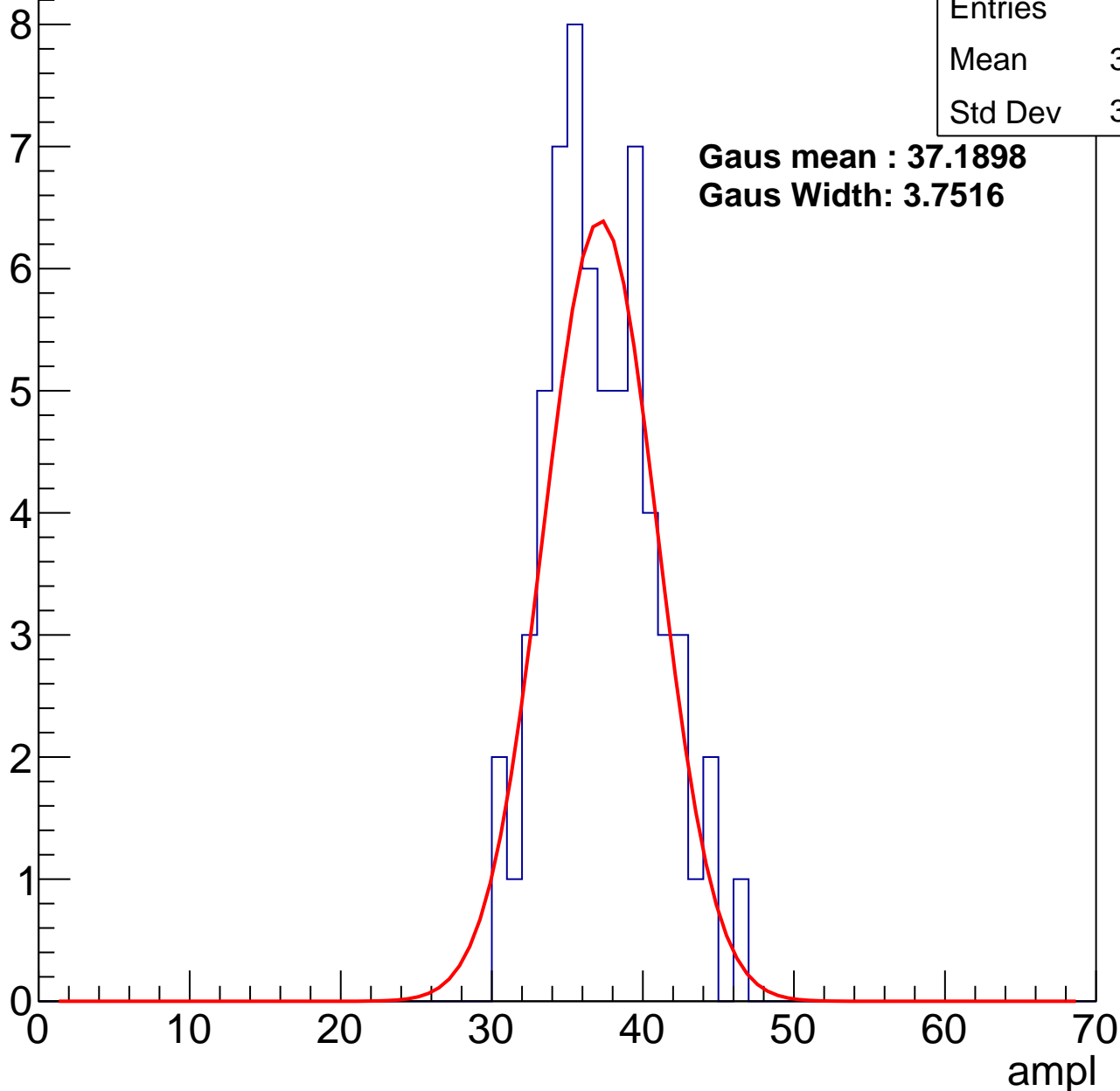
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.83
Std Dev	3.553

Gaus mean : 37.1898

Gaus Width: 3.7516



B1L101S, U14-ch85, adc2

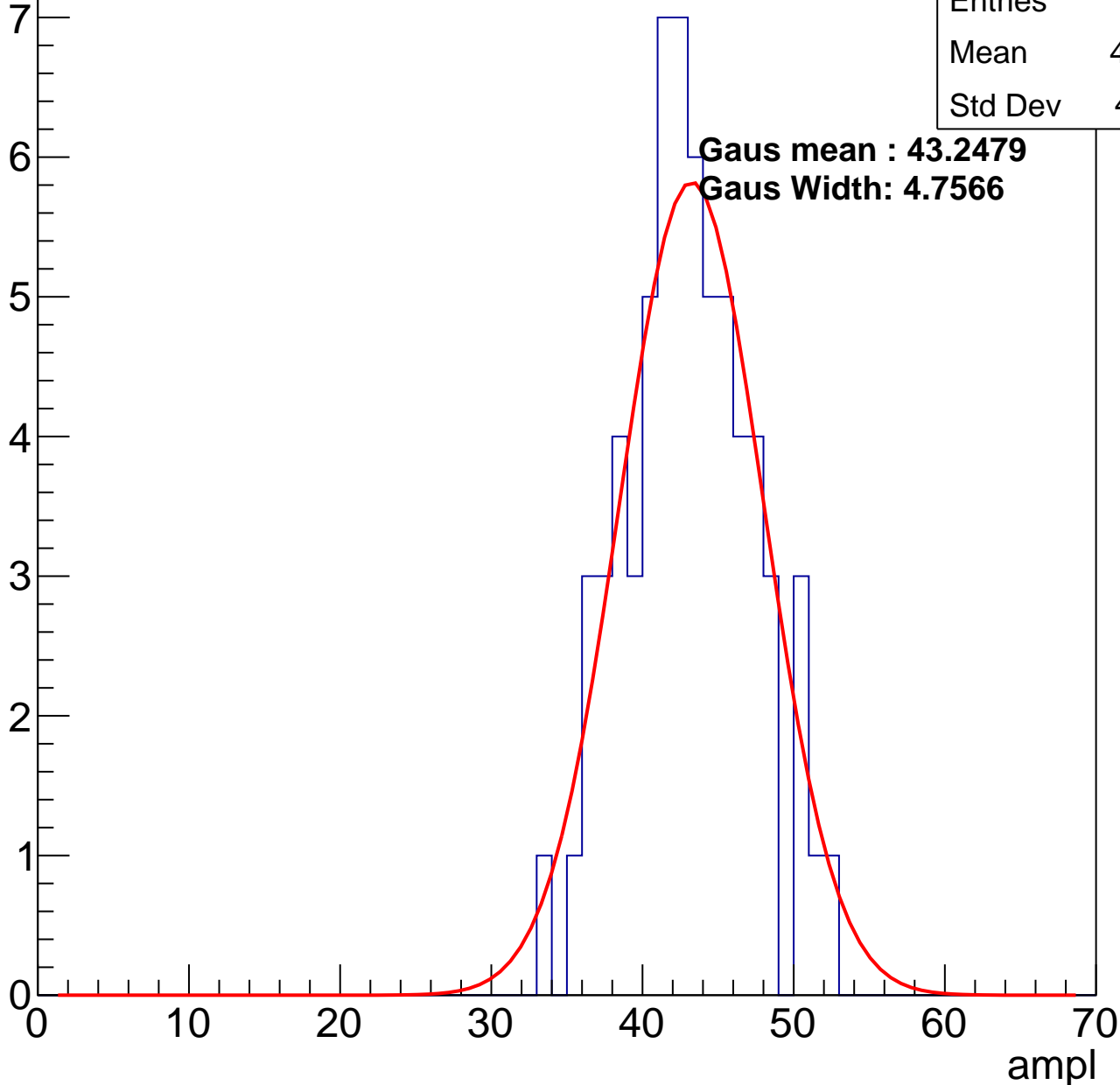
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.56
Std Dev	4.131

Gaus mean : 43.2479

Gaus Width: 4.7566

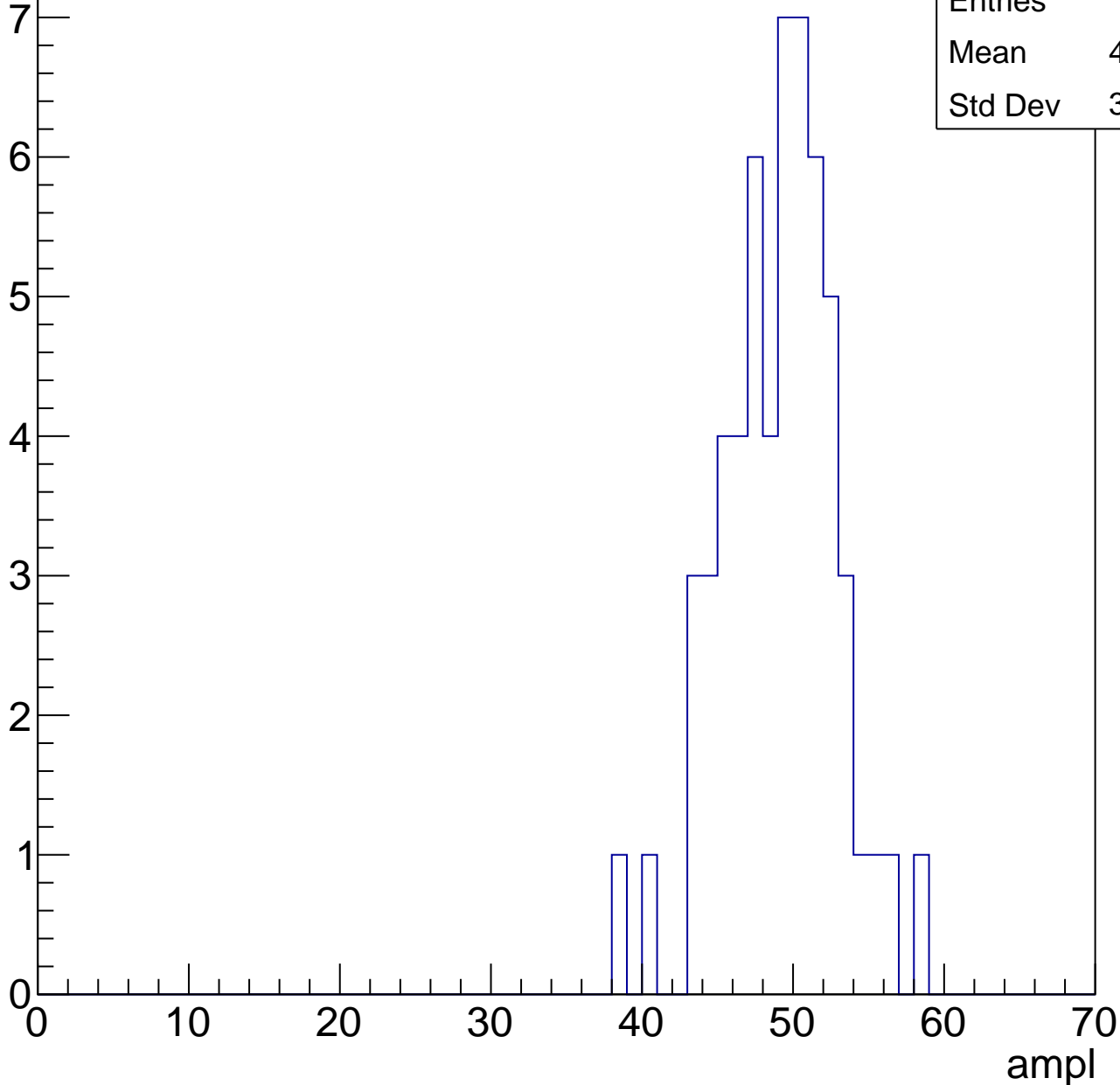


B1L101S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.59
Std Dev	3.756

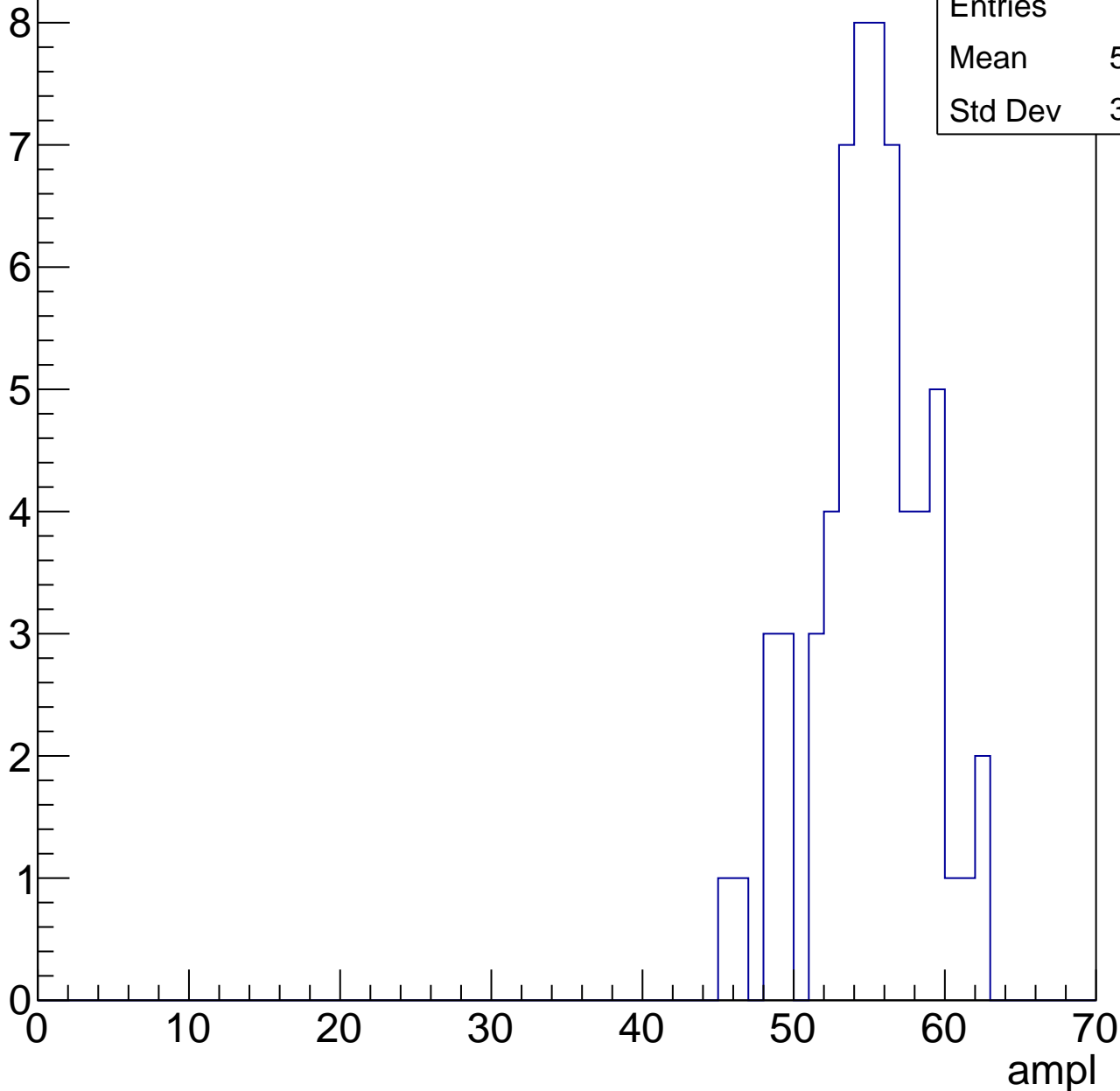


B1L101S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

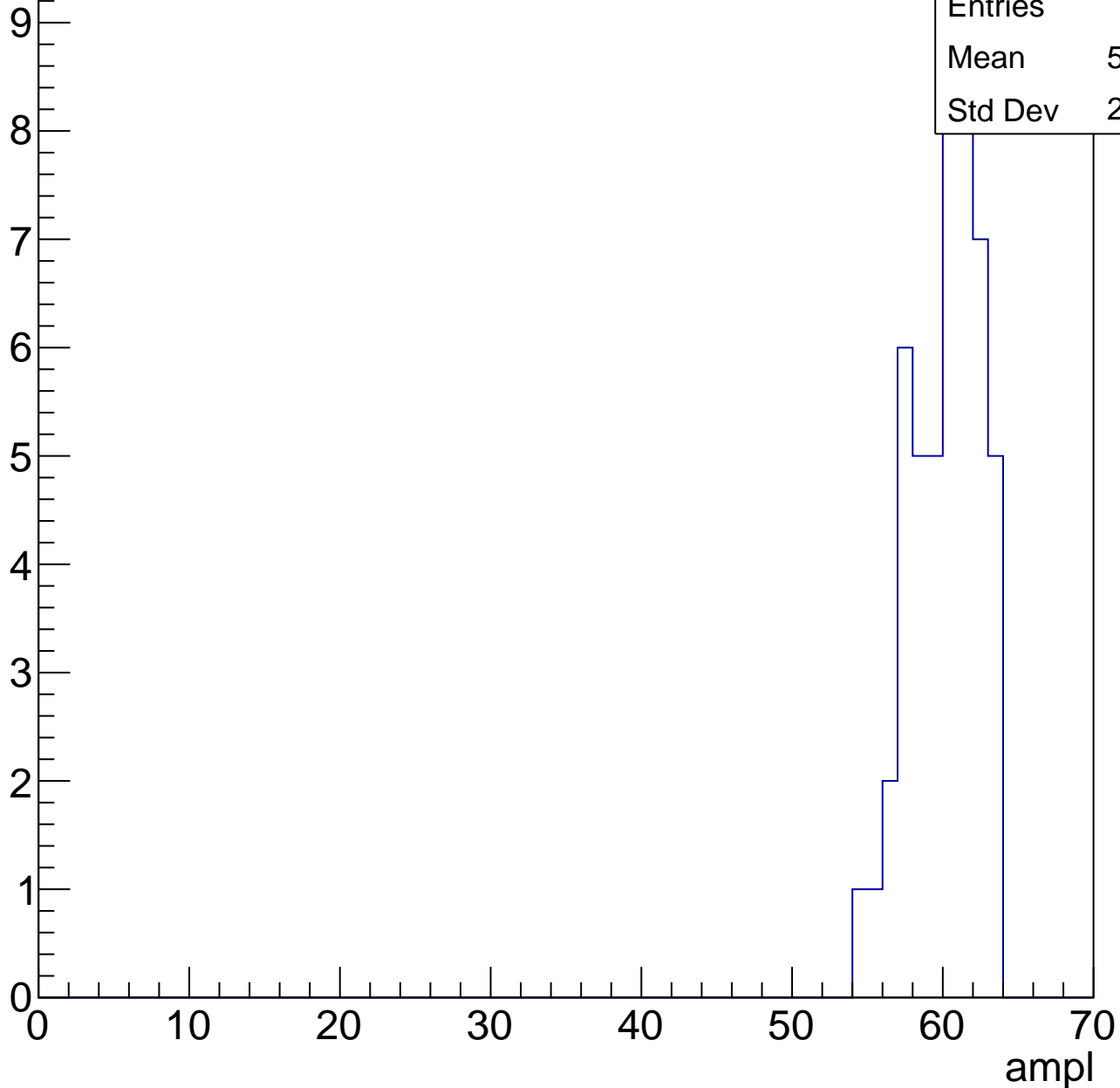
Entries	62
Mean	54.48
Std Dev	3.675



B1L101S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

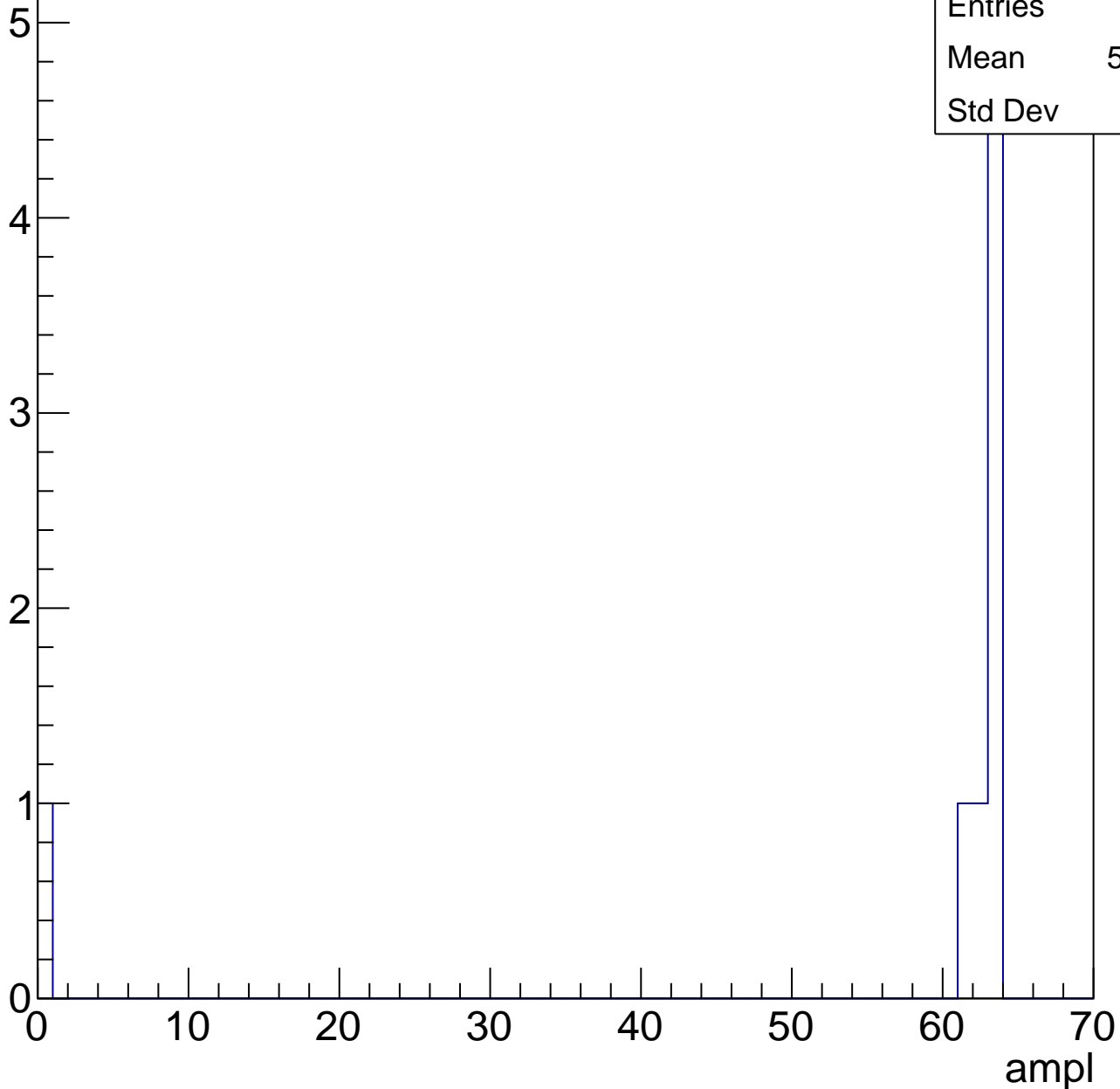


B1L101S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B1L101S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch86, adc0

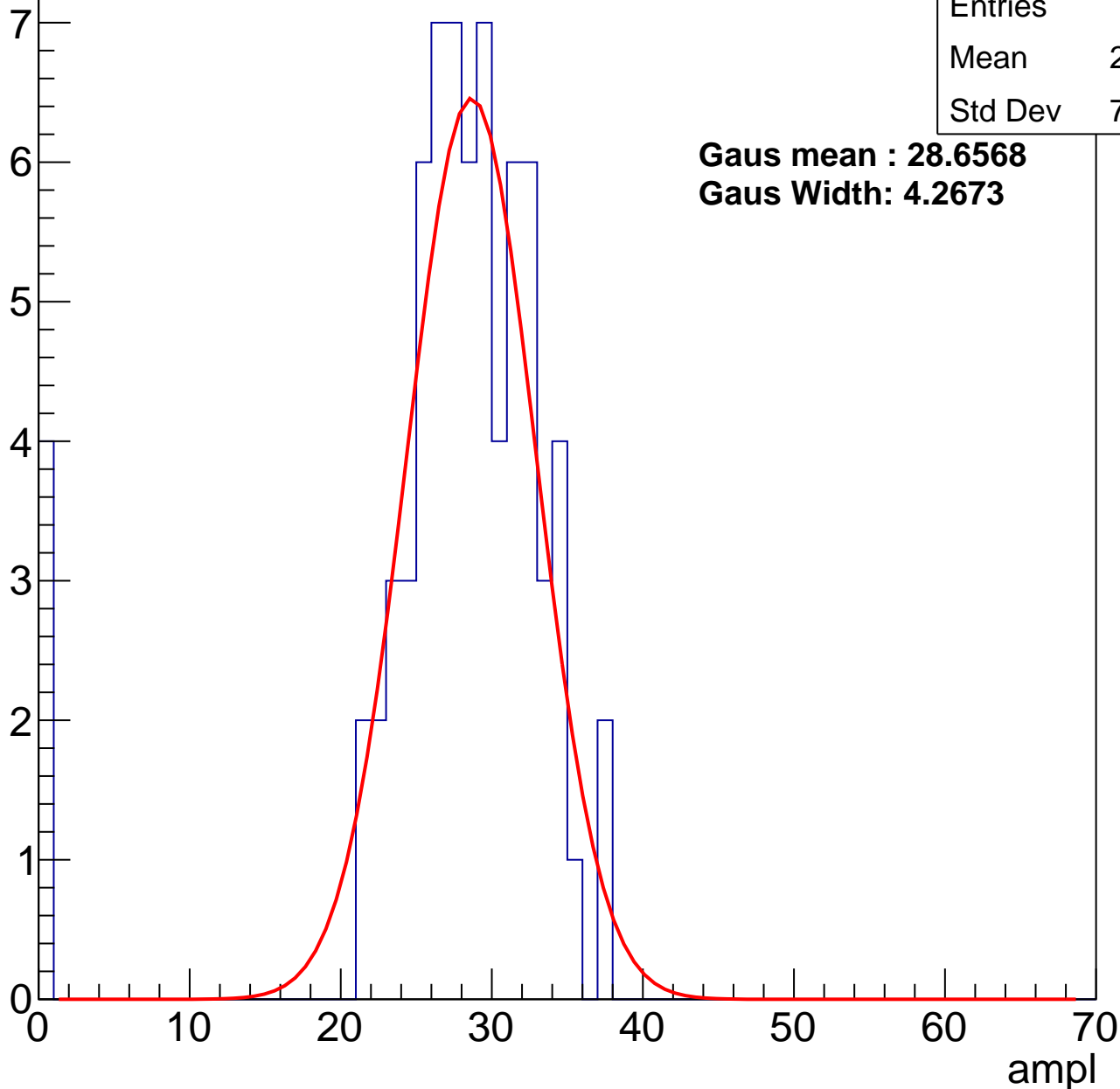
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	26.86
Std Dev	7.427

Gaus mean : 28.6568

Gaus Width: 4.2673



B1L101S, U14-ch86, adc1

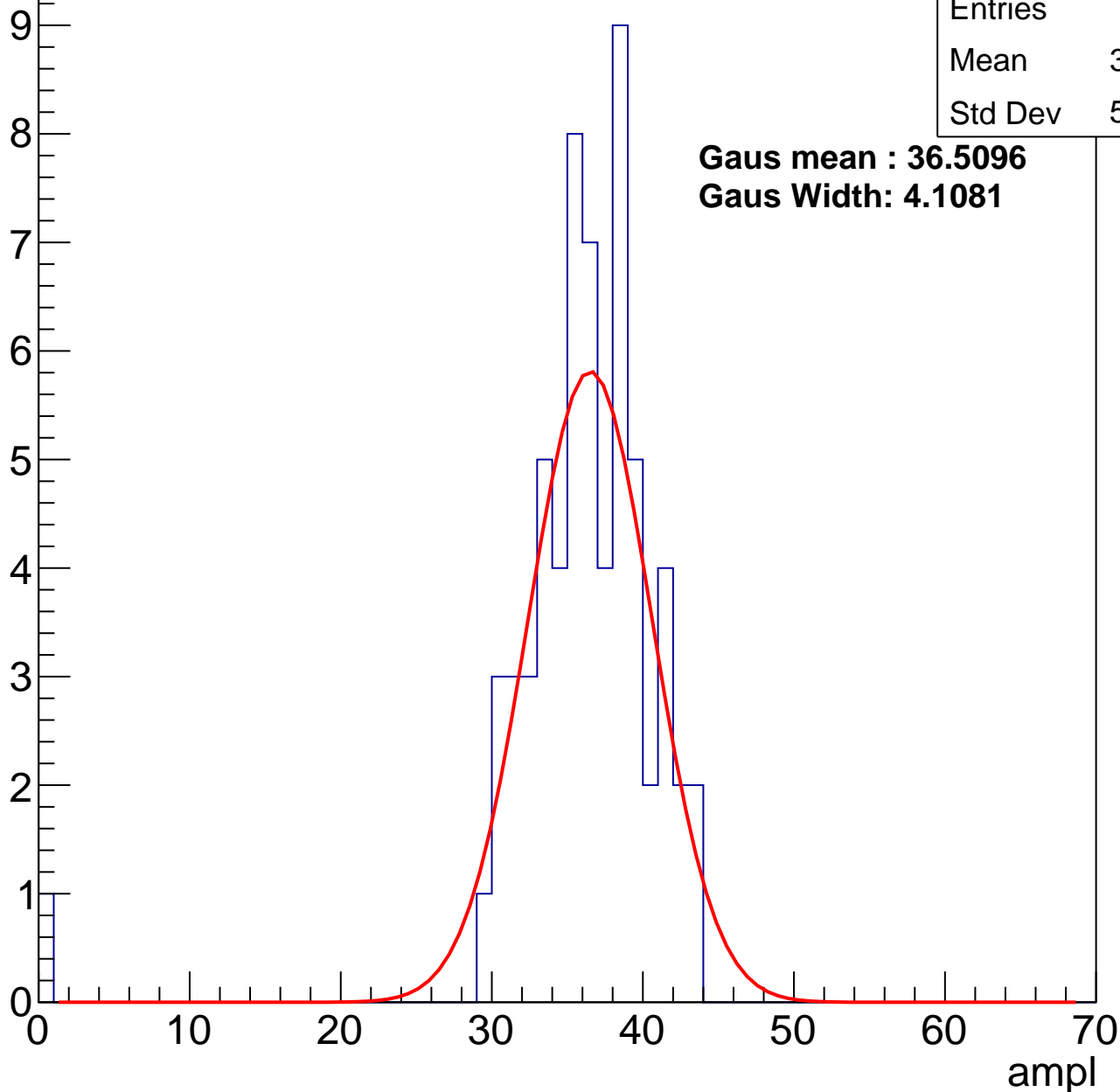
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.56
Std Dev	5.662

Gaus mean : 36.5096

Gaus Width: 4.1081



B1L101S, U14-ch86, adc2

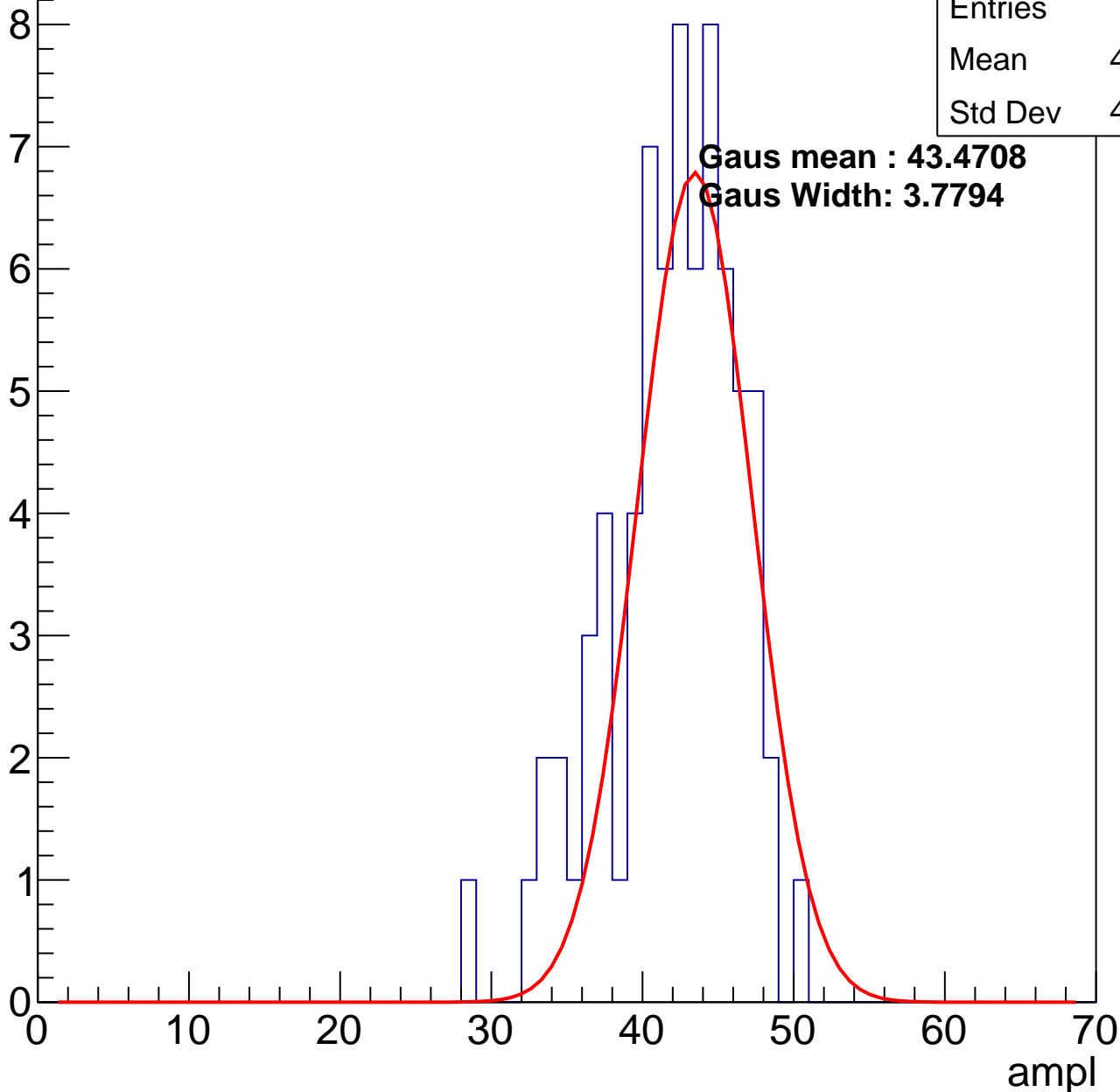
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.53
Std Dev	4.294

Gaus mean : 43.4708

Gaus Width: 3.7794

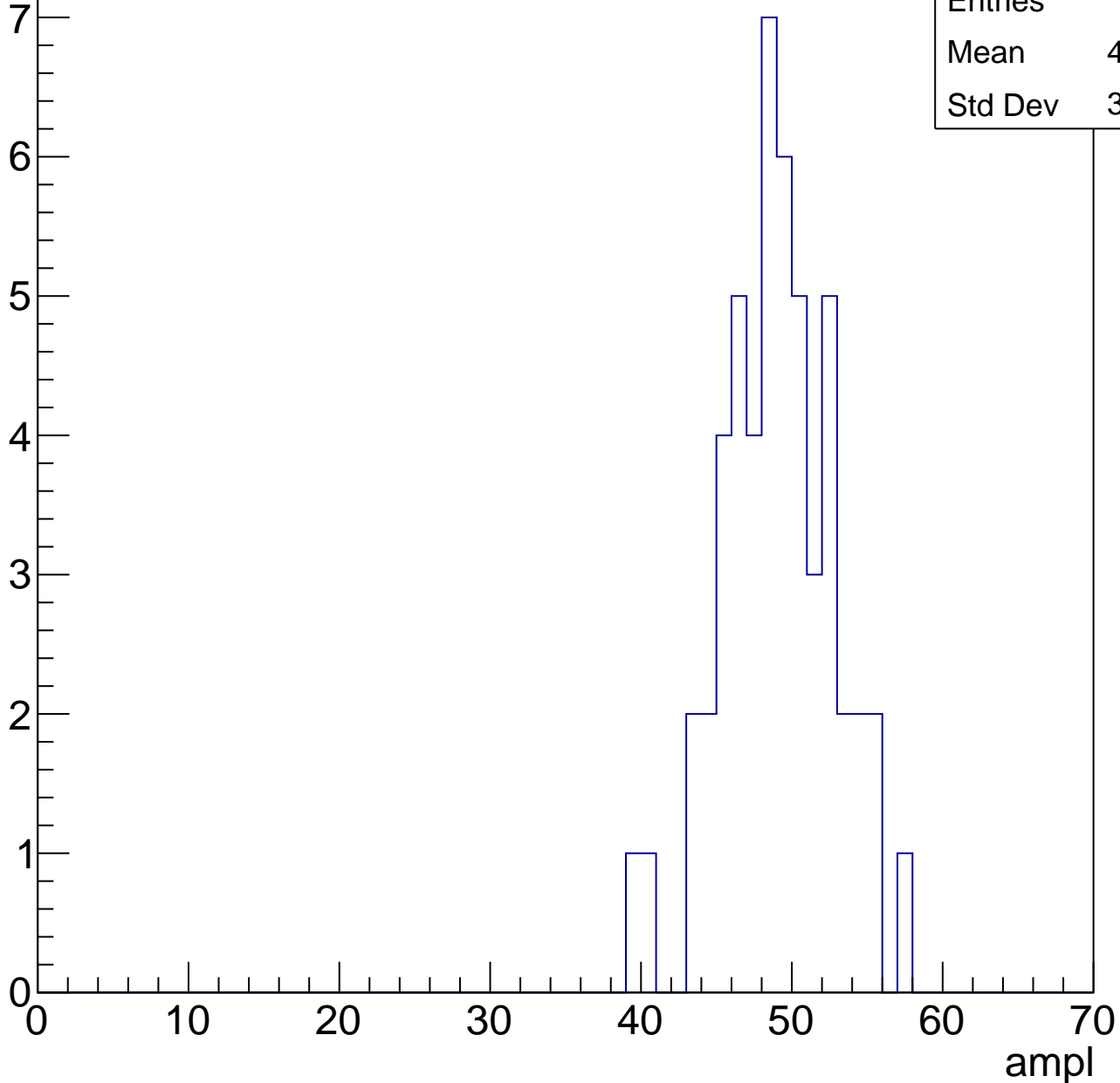


B1L101S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.56
Std Dev	3.682

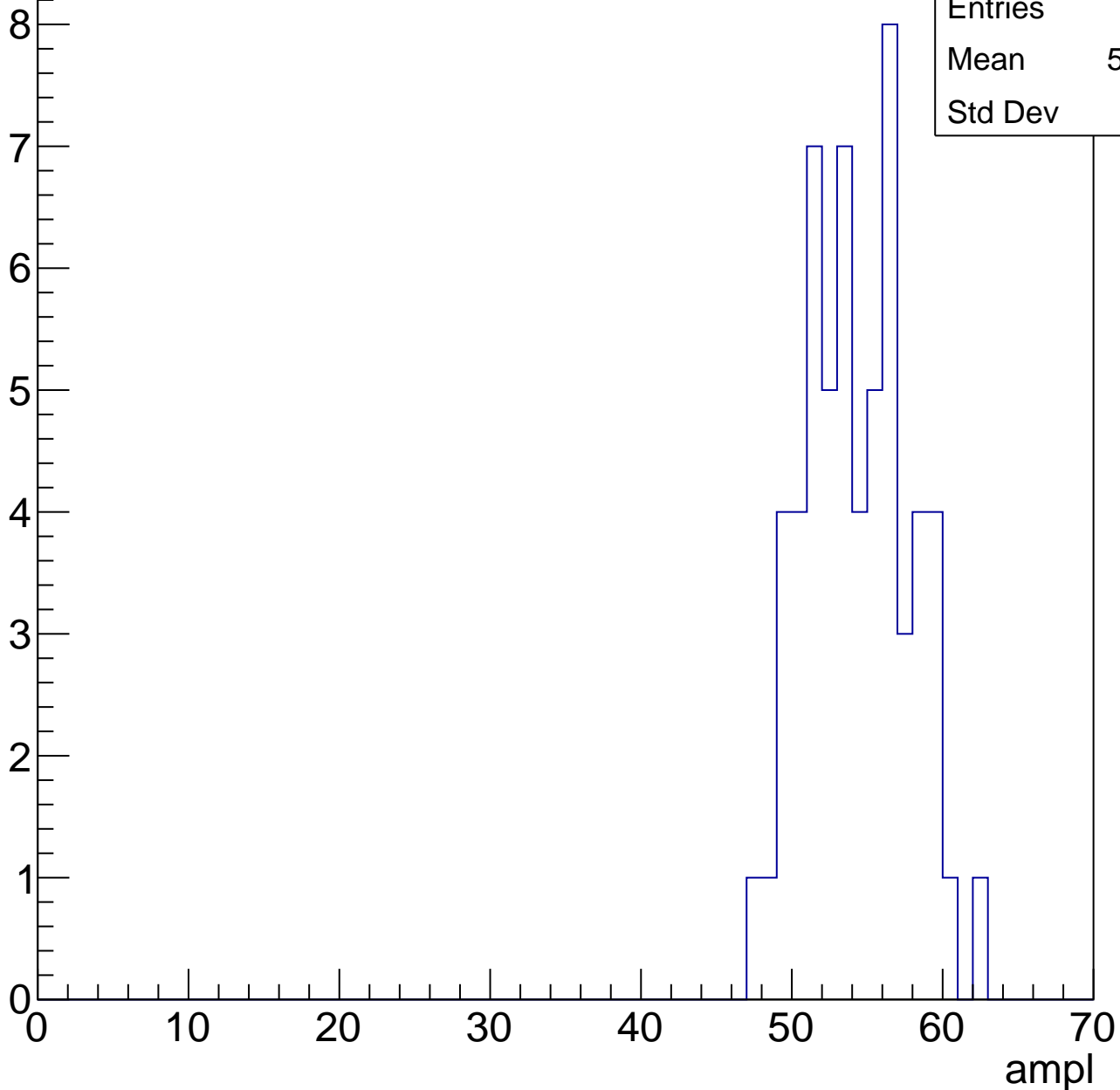


B1L101S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	53.88
Std Dev	3.36

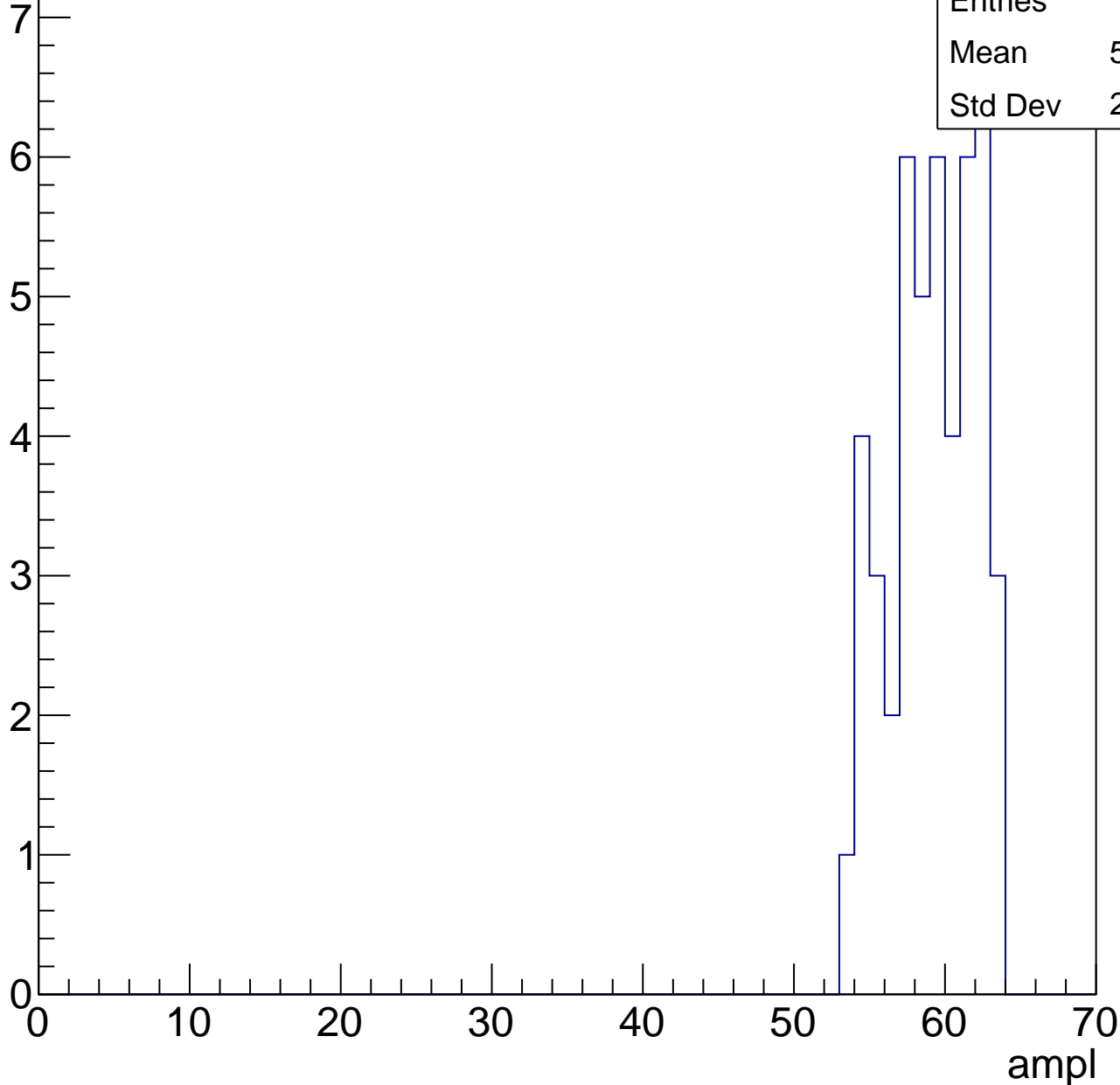


B1L101S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.74
Std Dev	2.794

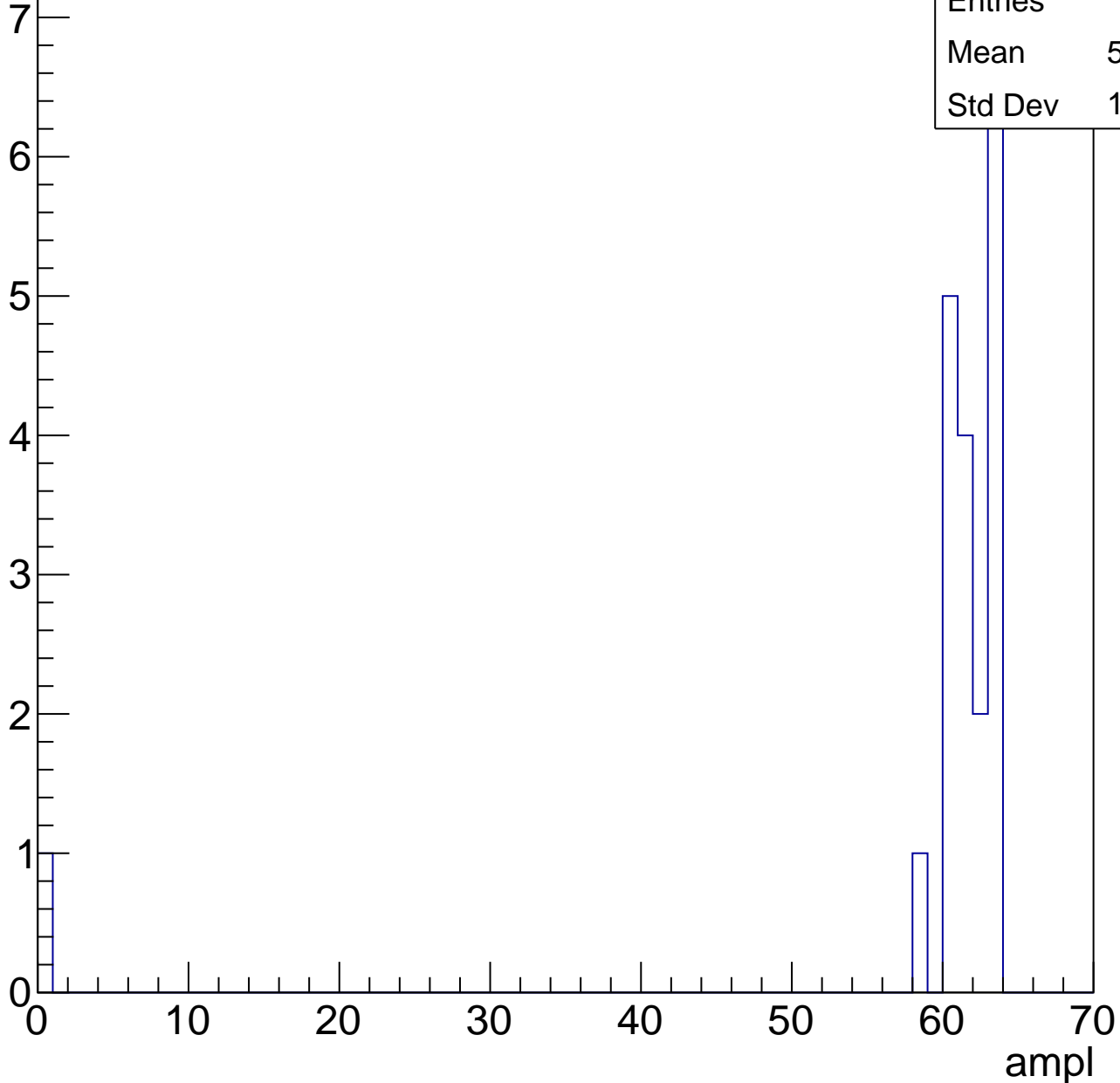


B1L101S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	58.35
Std Dev	13.46



B1L101S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch87, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	28.97
Std Dev	5.758

Gaus mean : 30.3302

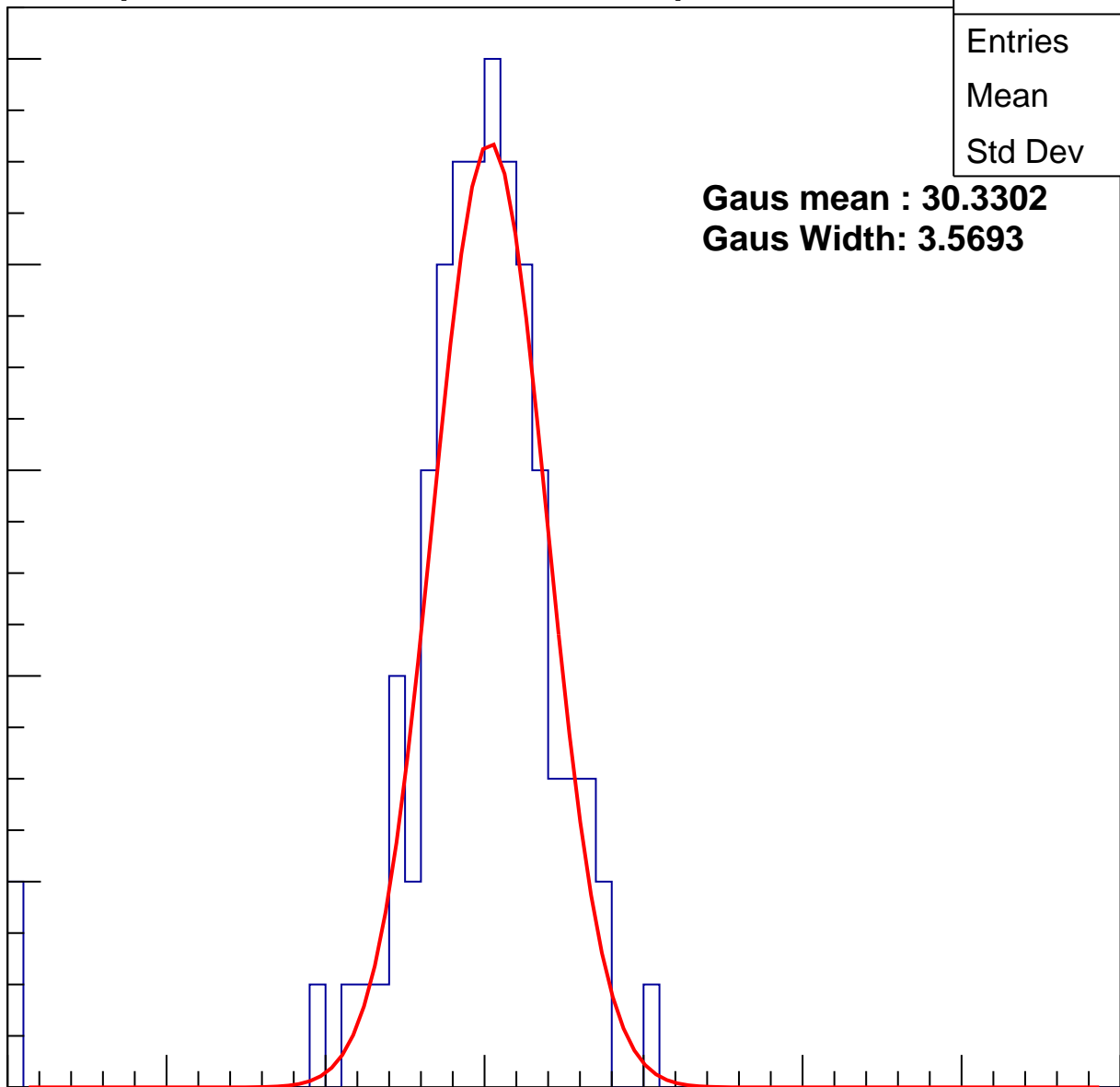
Gaus Width: 3.5693

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch87, adc1

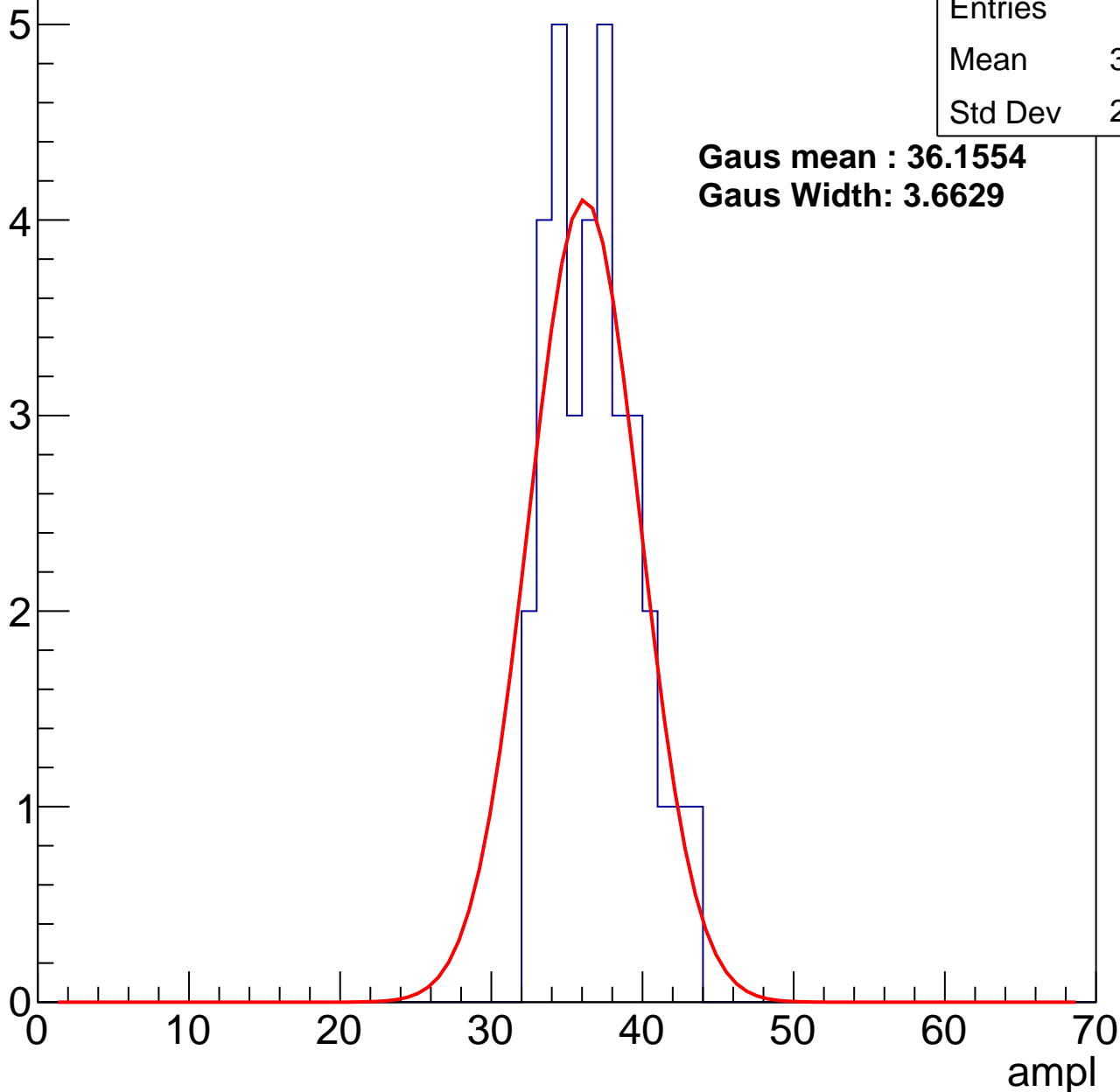
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	36.38
Std Dev	2.829

Gaus mean : 36.1554

Gaus Width: 3.6629



B1L101S, U14-ch87, adc2

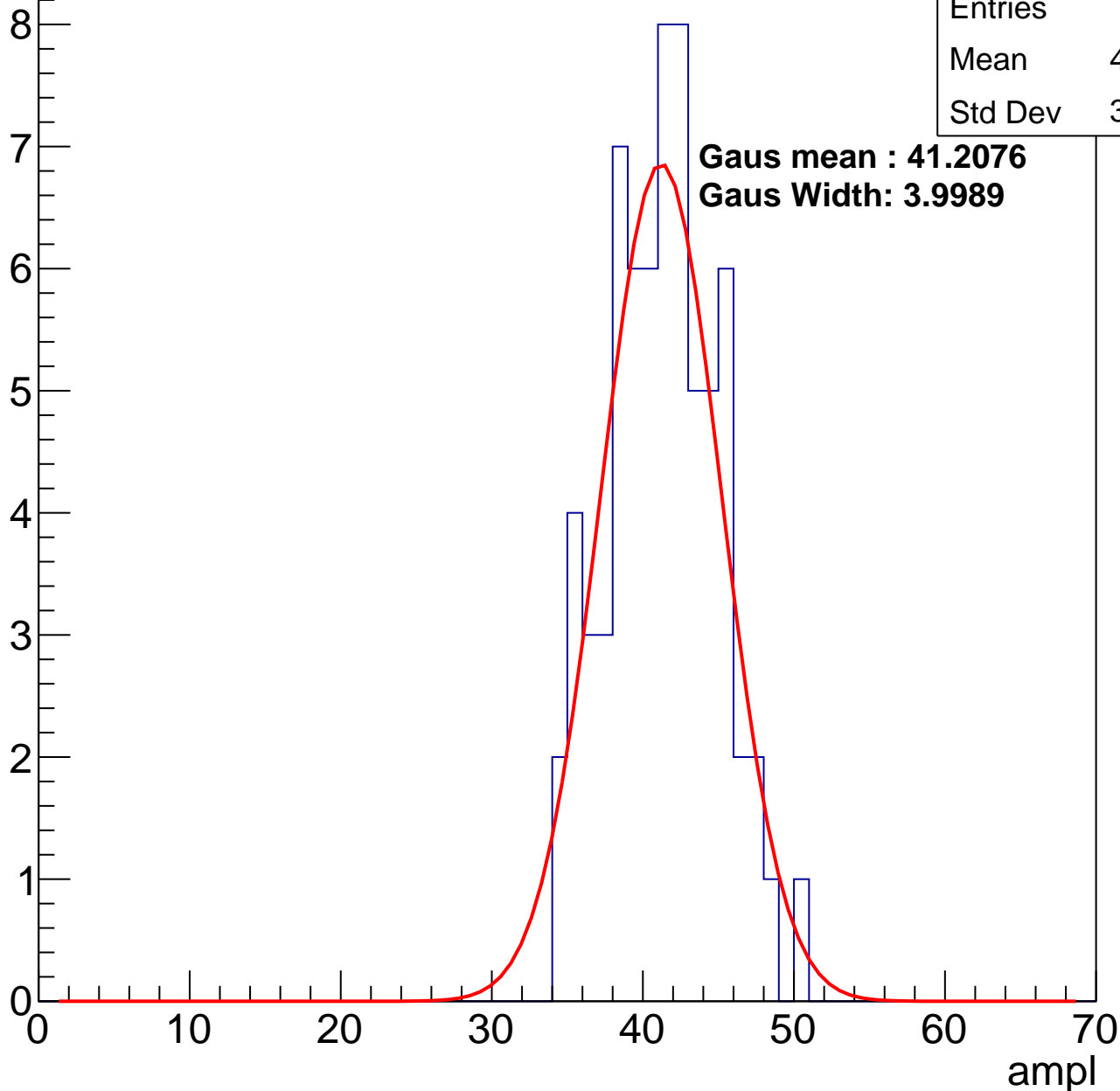
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	40.87
Std Dev	3.567

Gaus mean : 41.2076

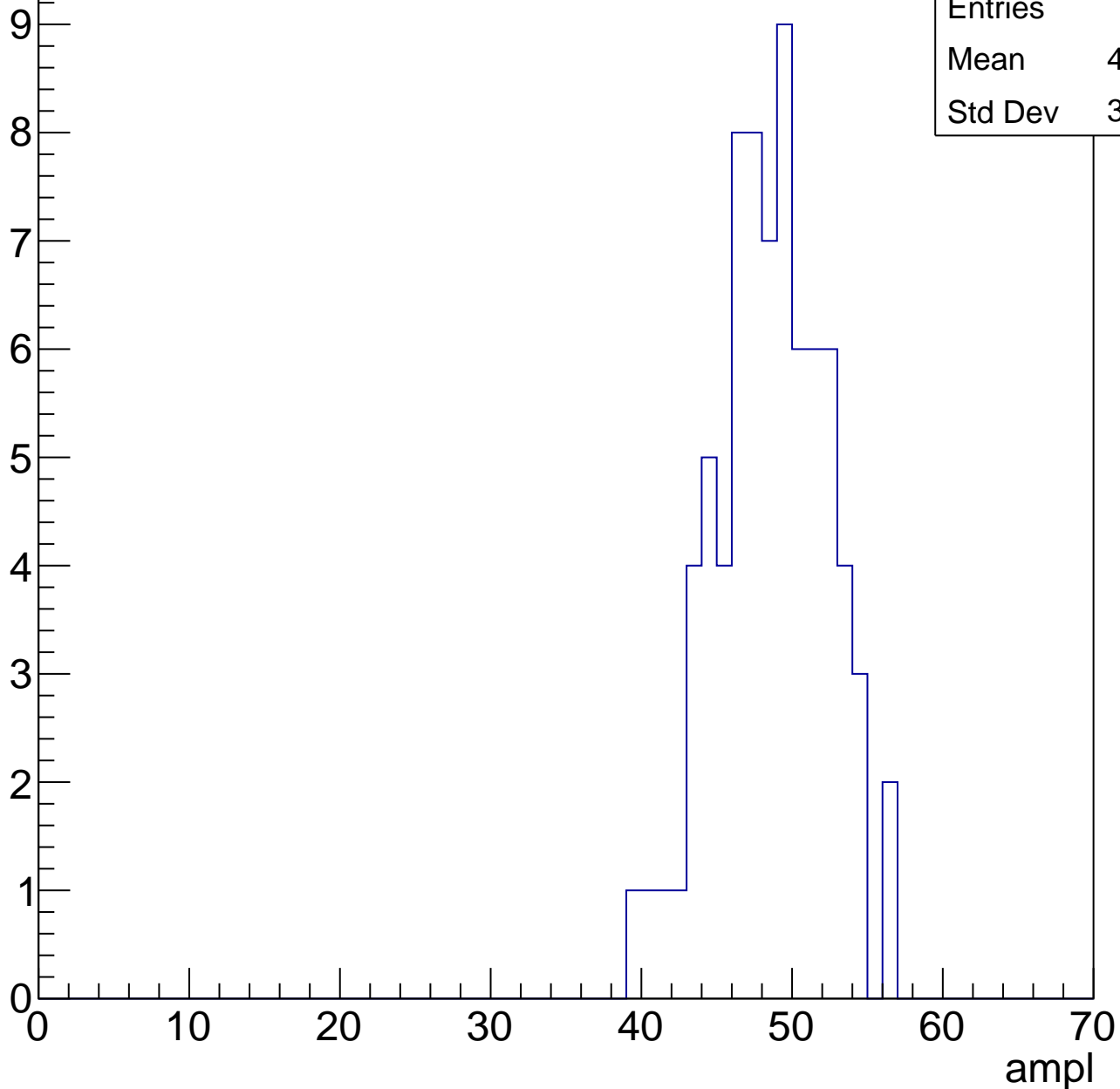
Gaus Width: 3.9989



B1L101S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 53.84

Std Dev 2.82

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

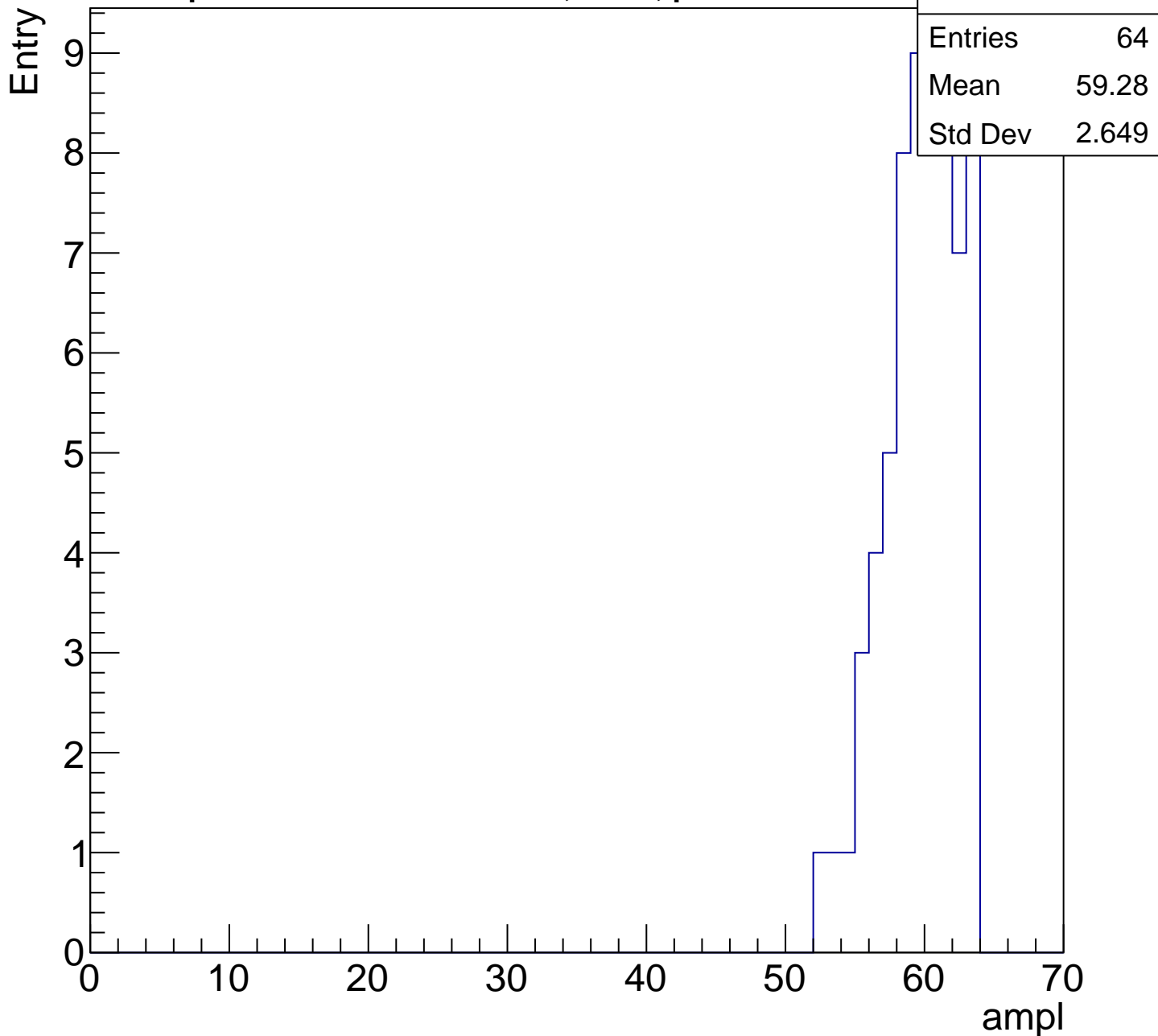
5

6

7

B1L101S, U14-ch87, adc5

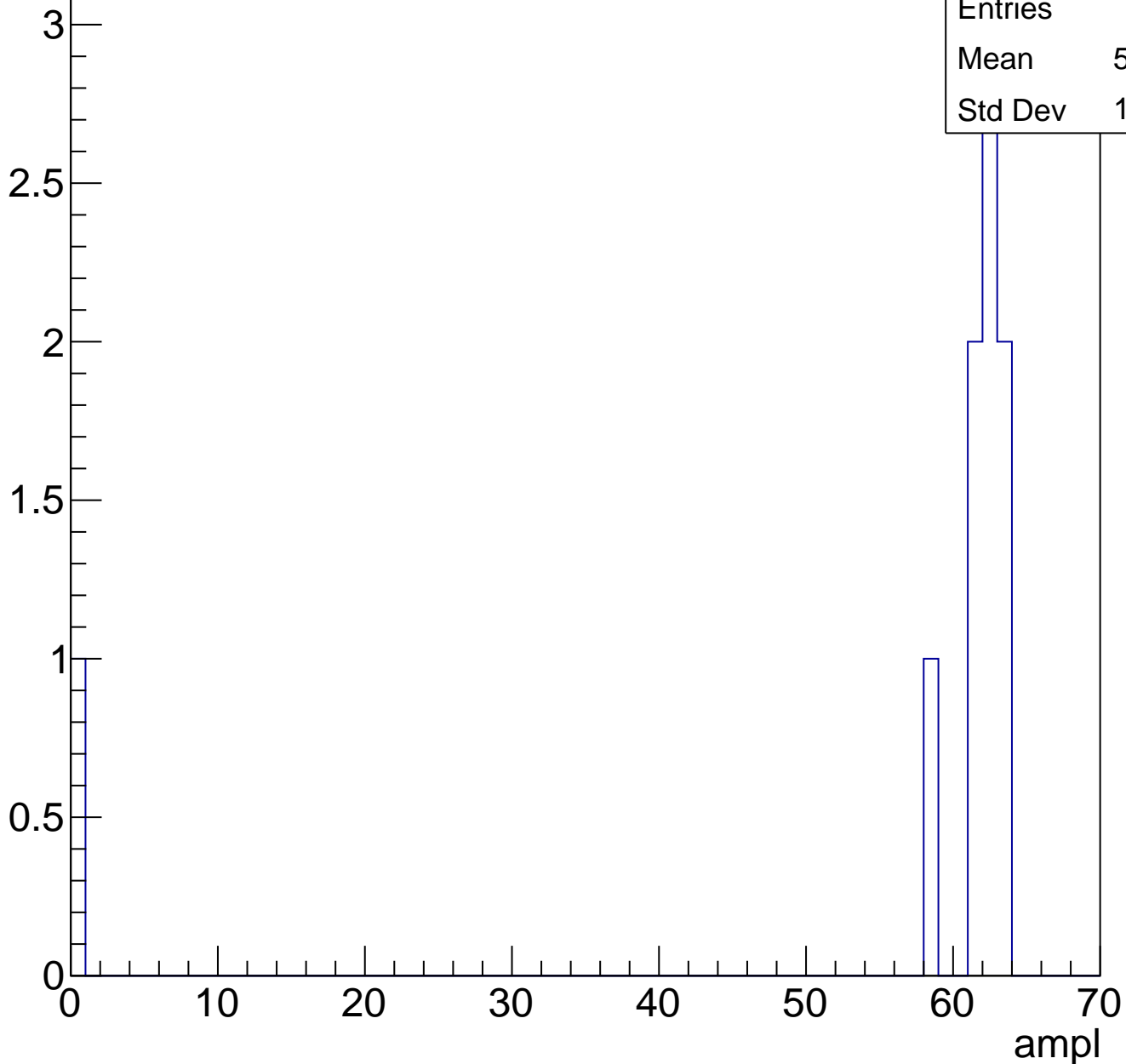
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch88, adc0

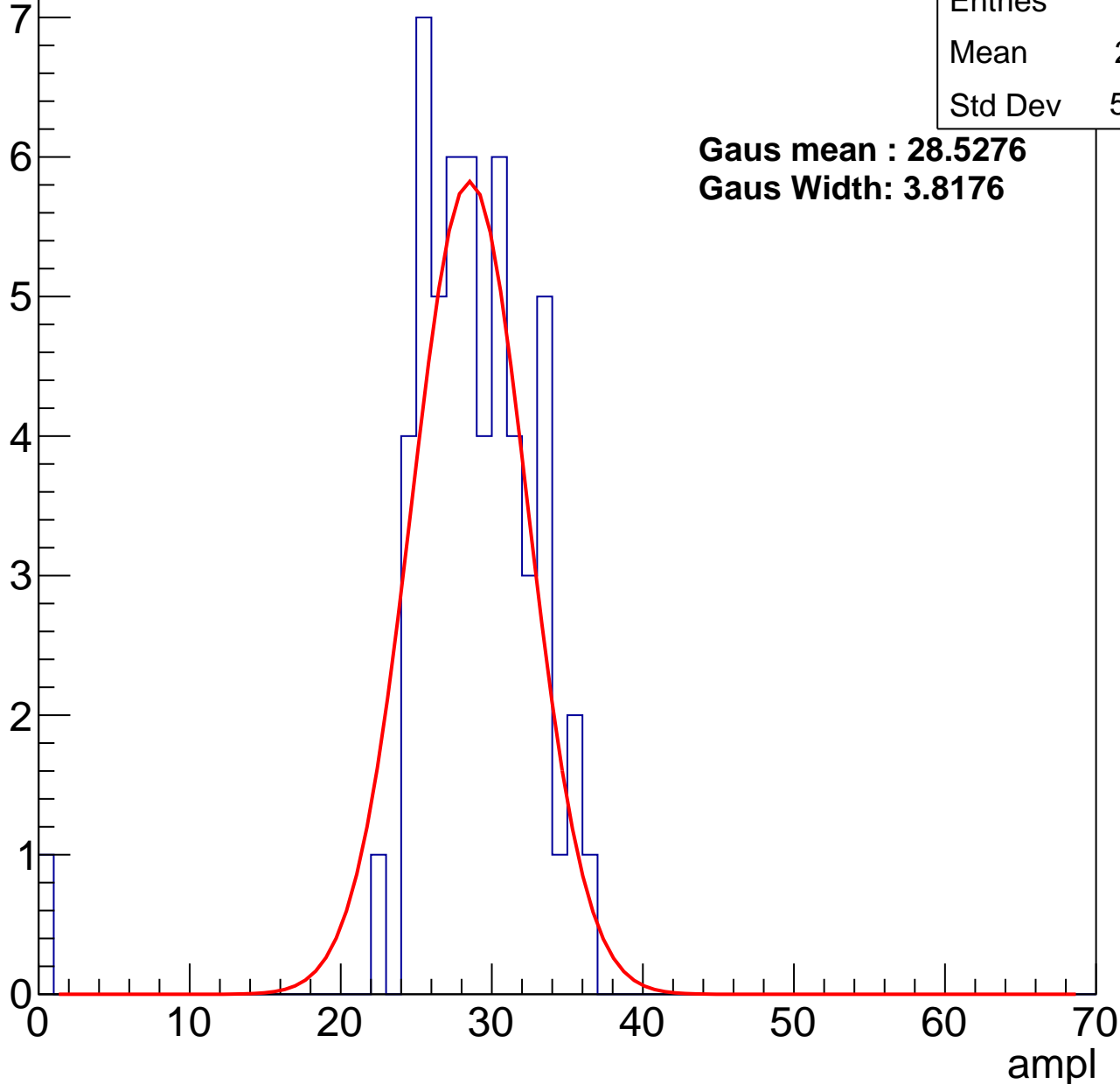
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.11
Std Dev	5.013

Gaus mean : 28.5276

Gaus Width: 3.8176



B1L101S, U14-ch88, adc1

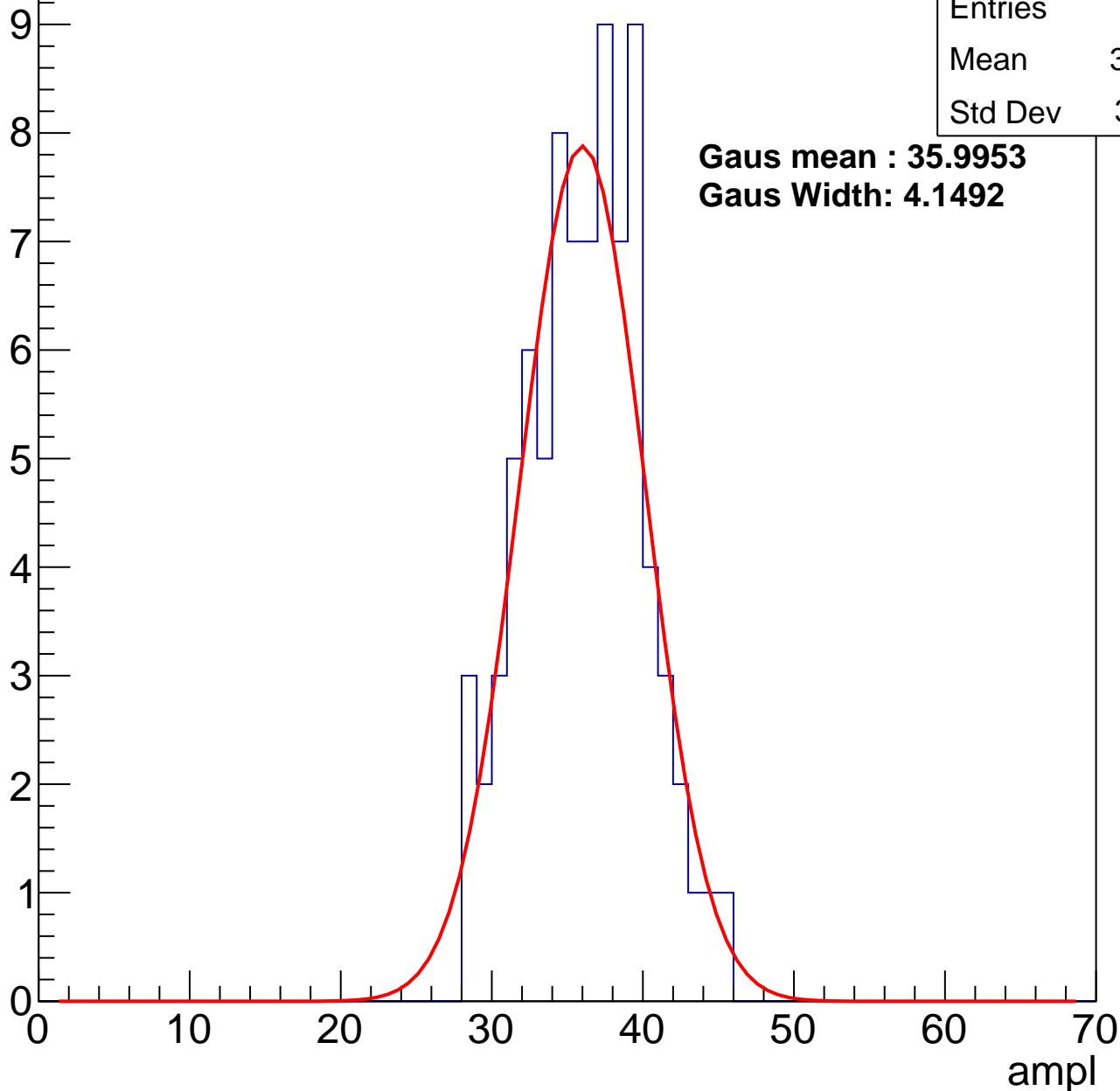
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	35.69
Std Dev	3.831

Gaus mean : 35.9953

Gaus Width: 4.1492



B1L101S, U14-ch88, adc2

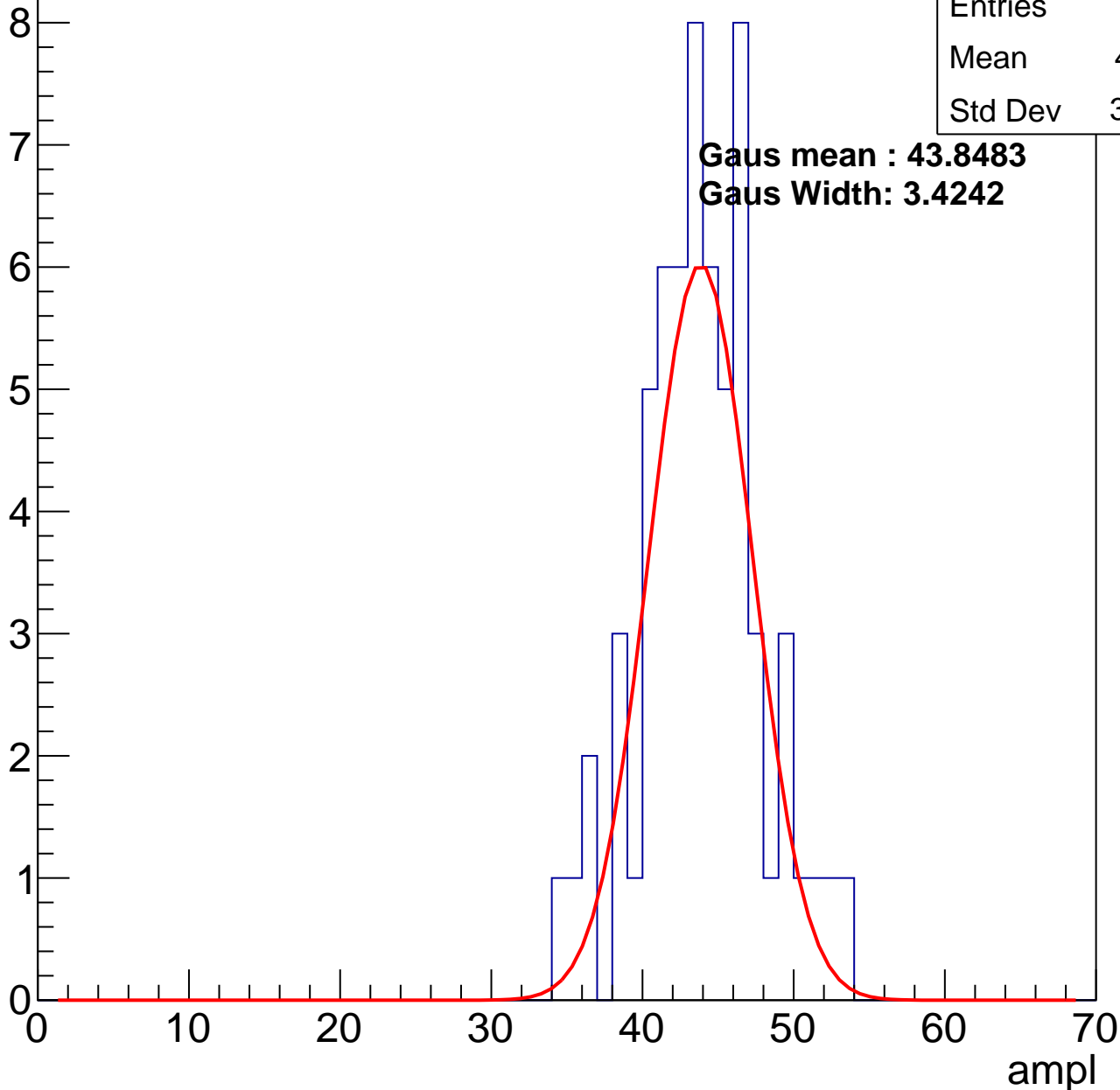
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.41
Std Dev	3.926

Gaus mean : 43.8483

Gaus Width: 3.4242

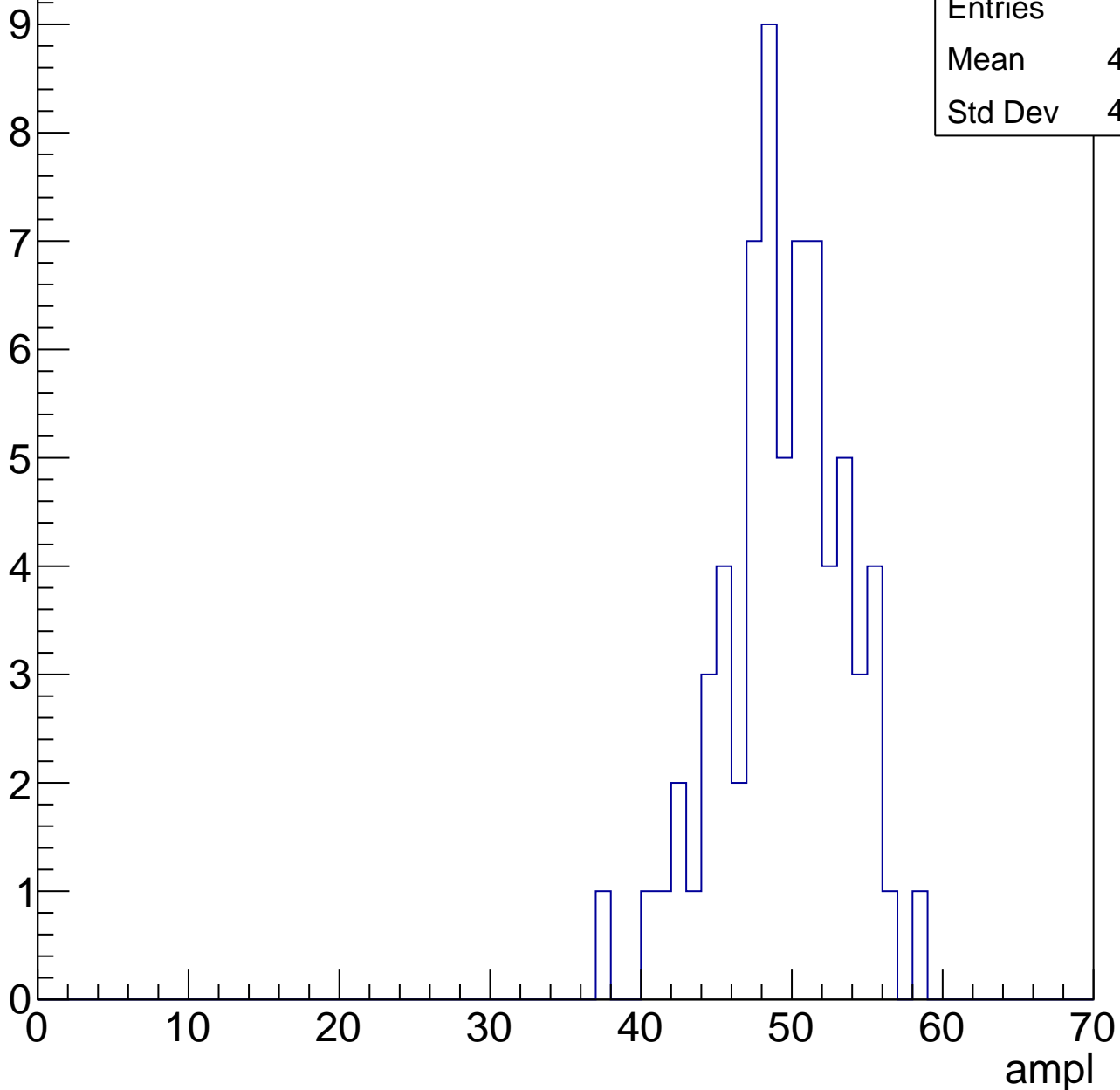


B1L101S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.99
Std Dev	4.053

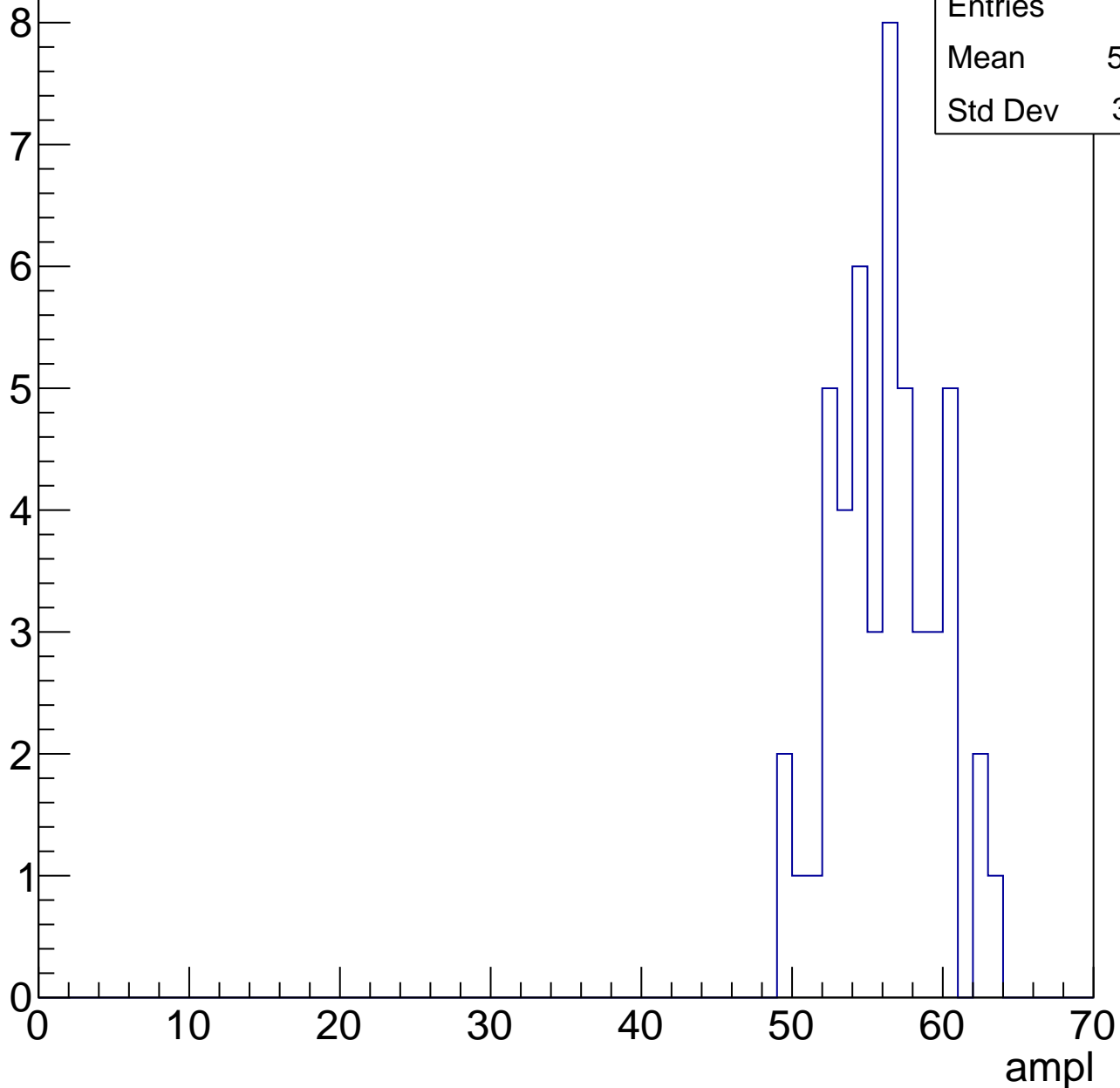


B1L101S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.73
Std Dev	3.331

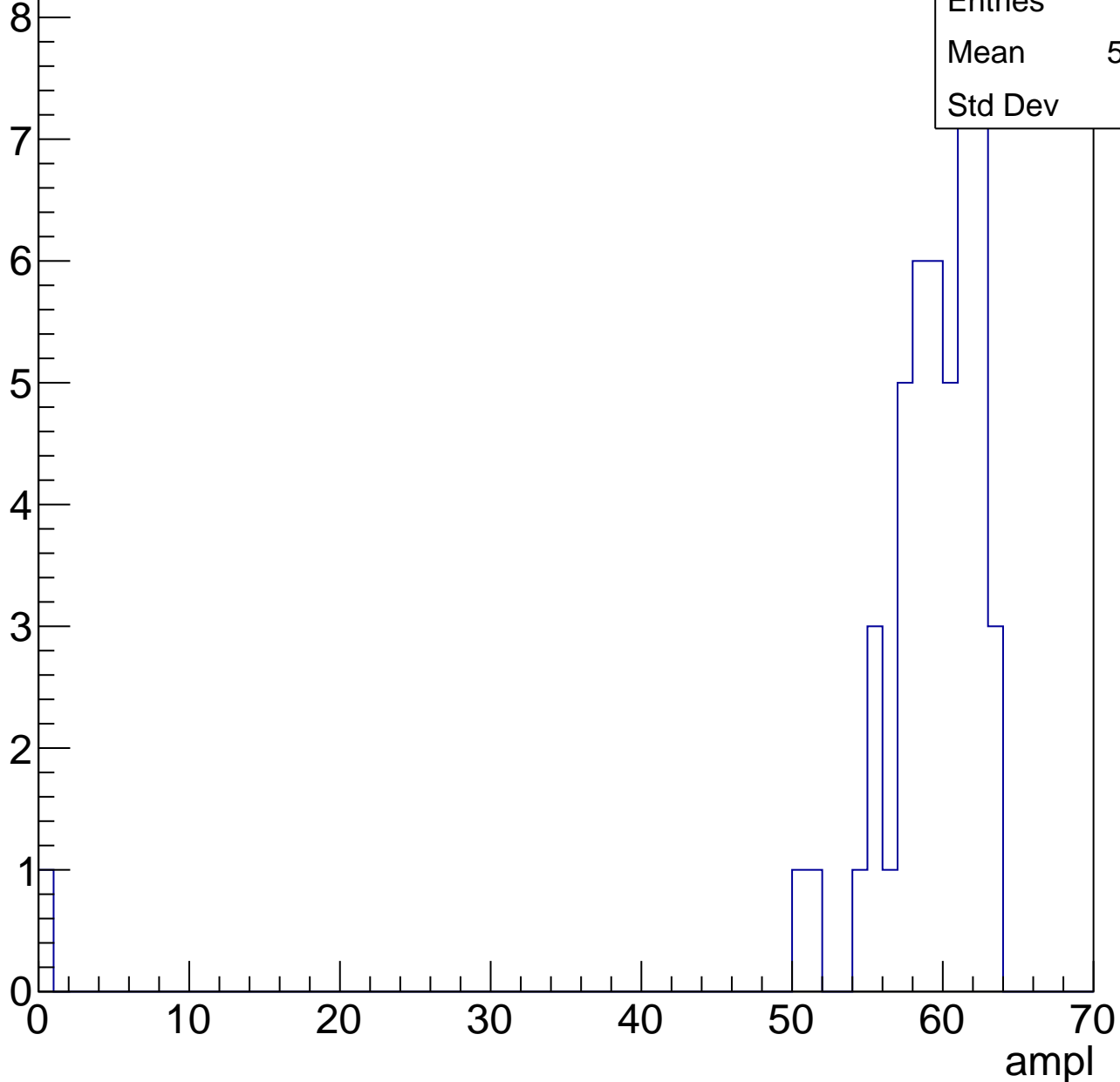


B1L101S, U14-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.88
Std Dev	8.84

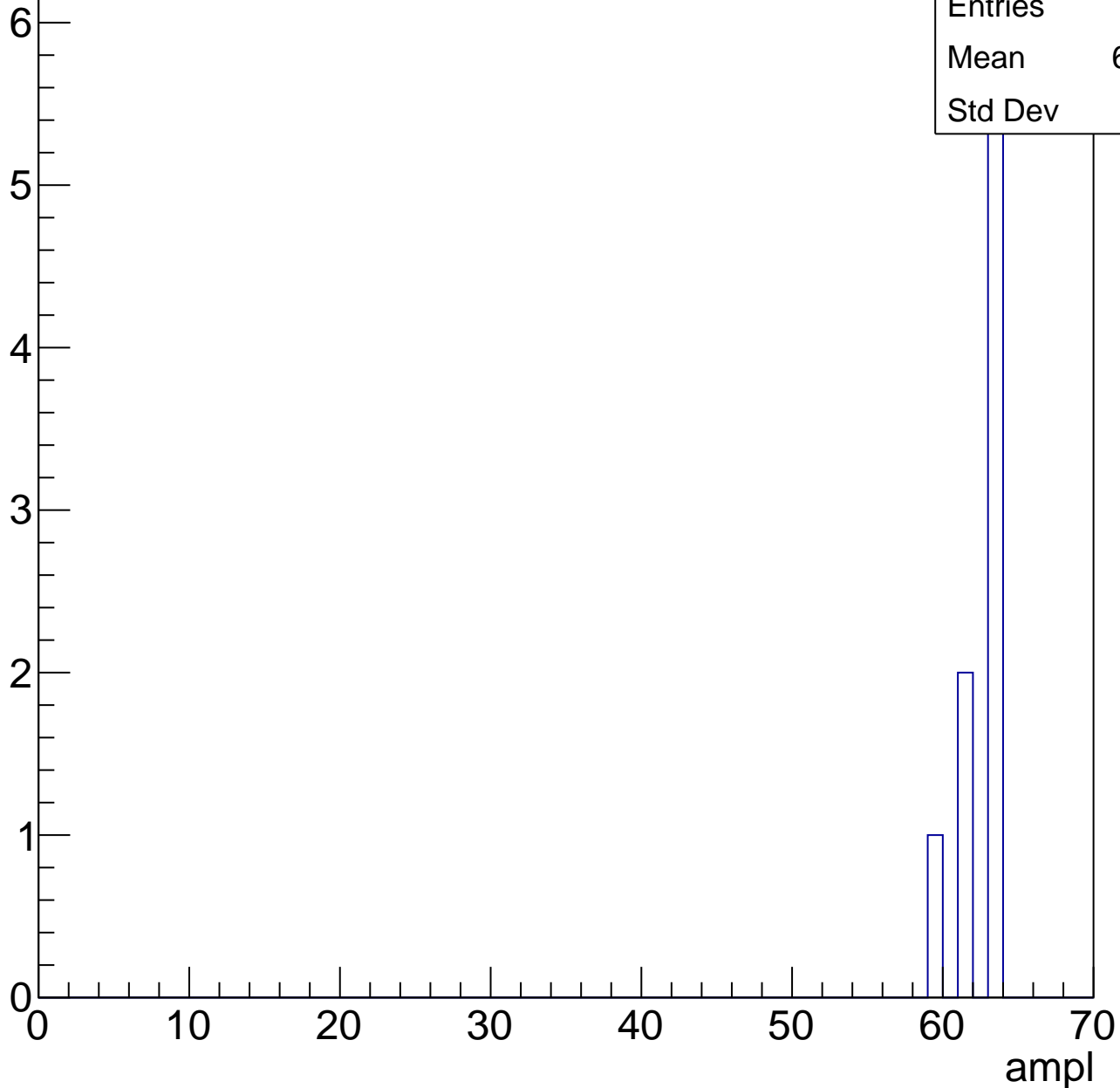


B1L101S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	62.11
Std Dev	1.37



B1L101S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch89, adc0

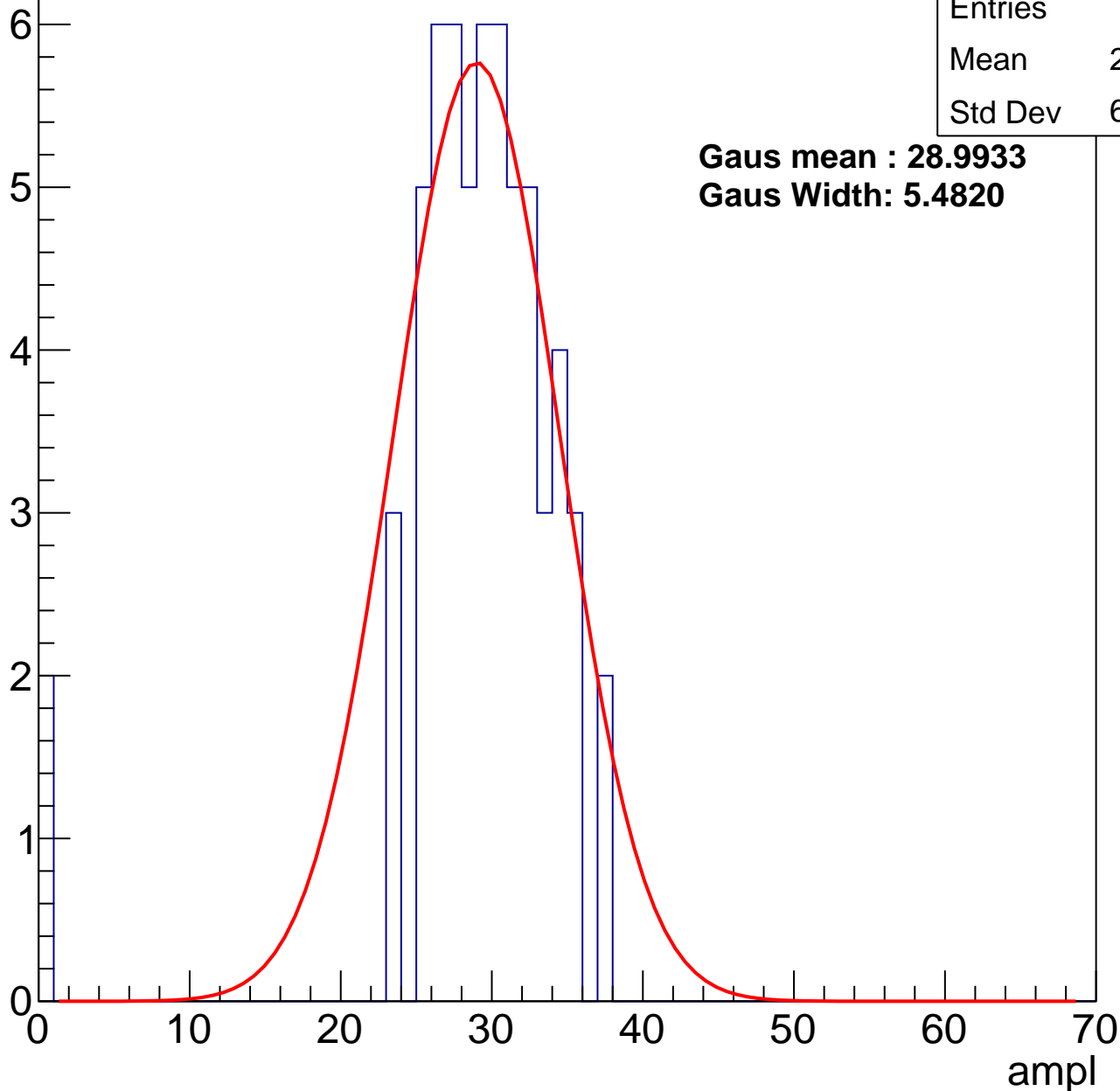
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	28.44
Std Dev	6.258

Gaus mean : 28.9933

Gaus Width: 5.4820



B1L101S, U14-ch89, adc1

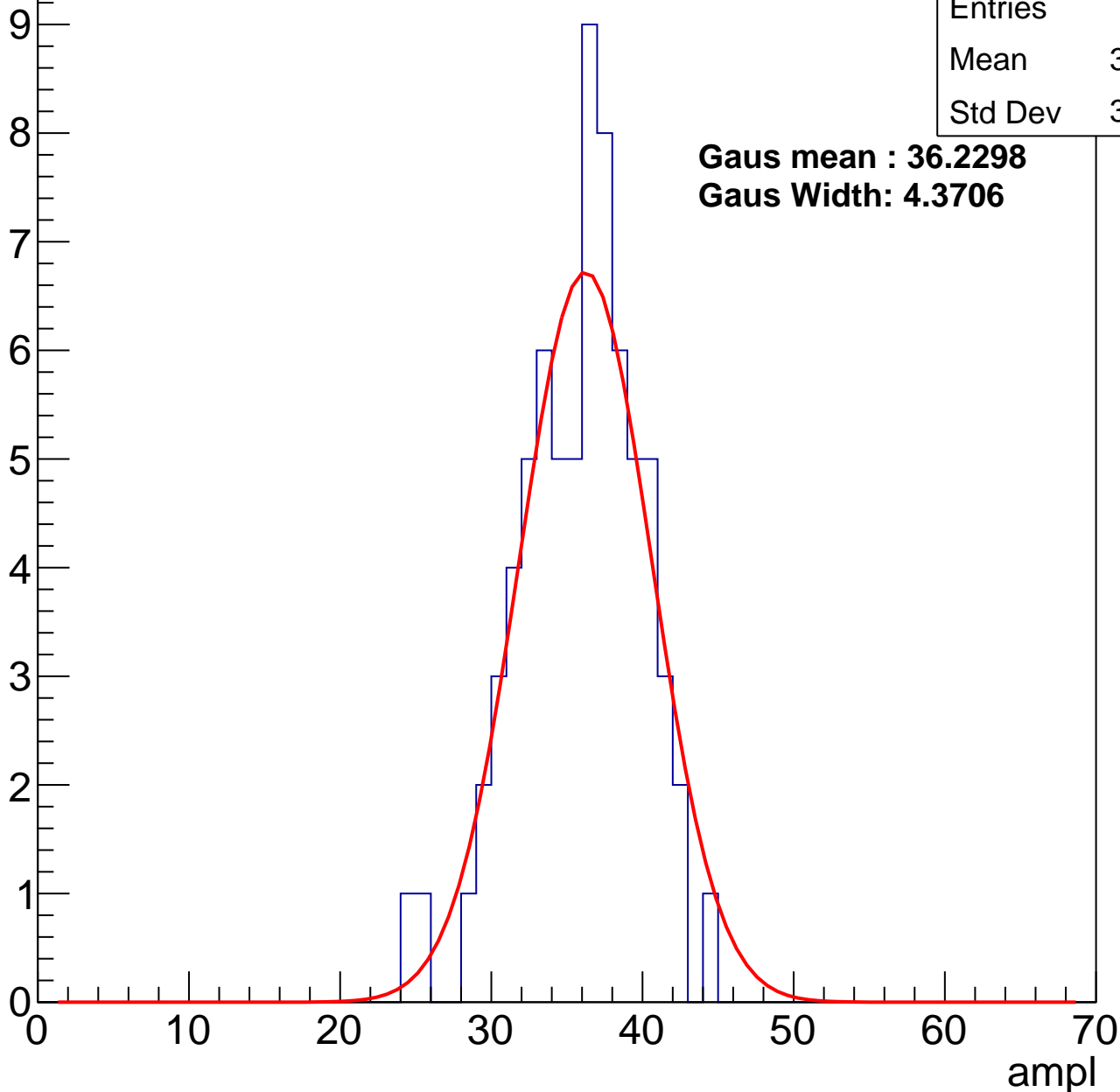
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	35.36
Std Dev	3.966

Gaus mean : 36.2298

Gaus Width: 4.3706



B1L101S, U14-ch89, adc2

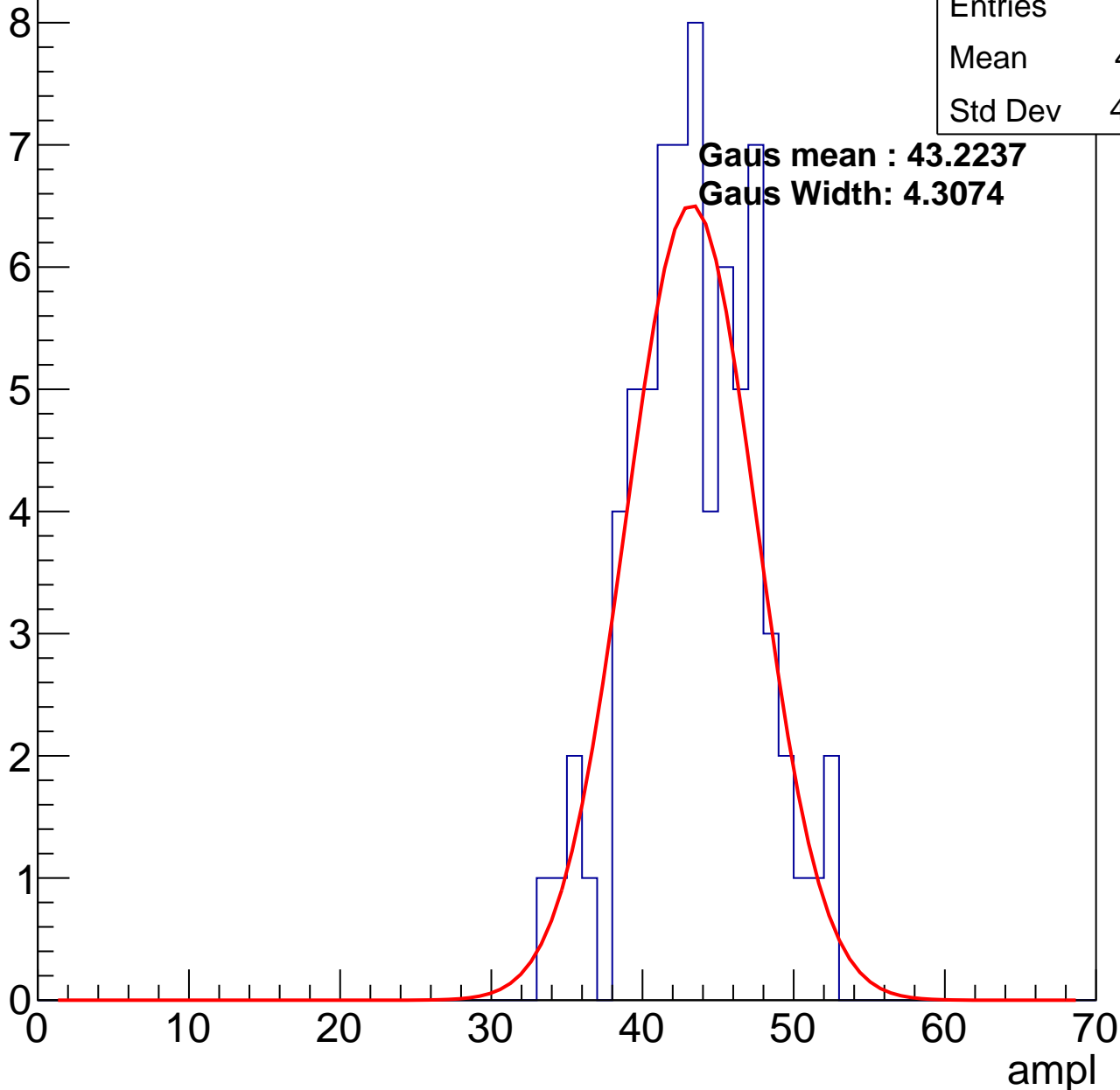
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.01
Std Dev	4.125

Gaus mean : 43.2237

Gaus Width: 4.3074

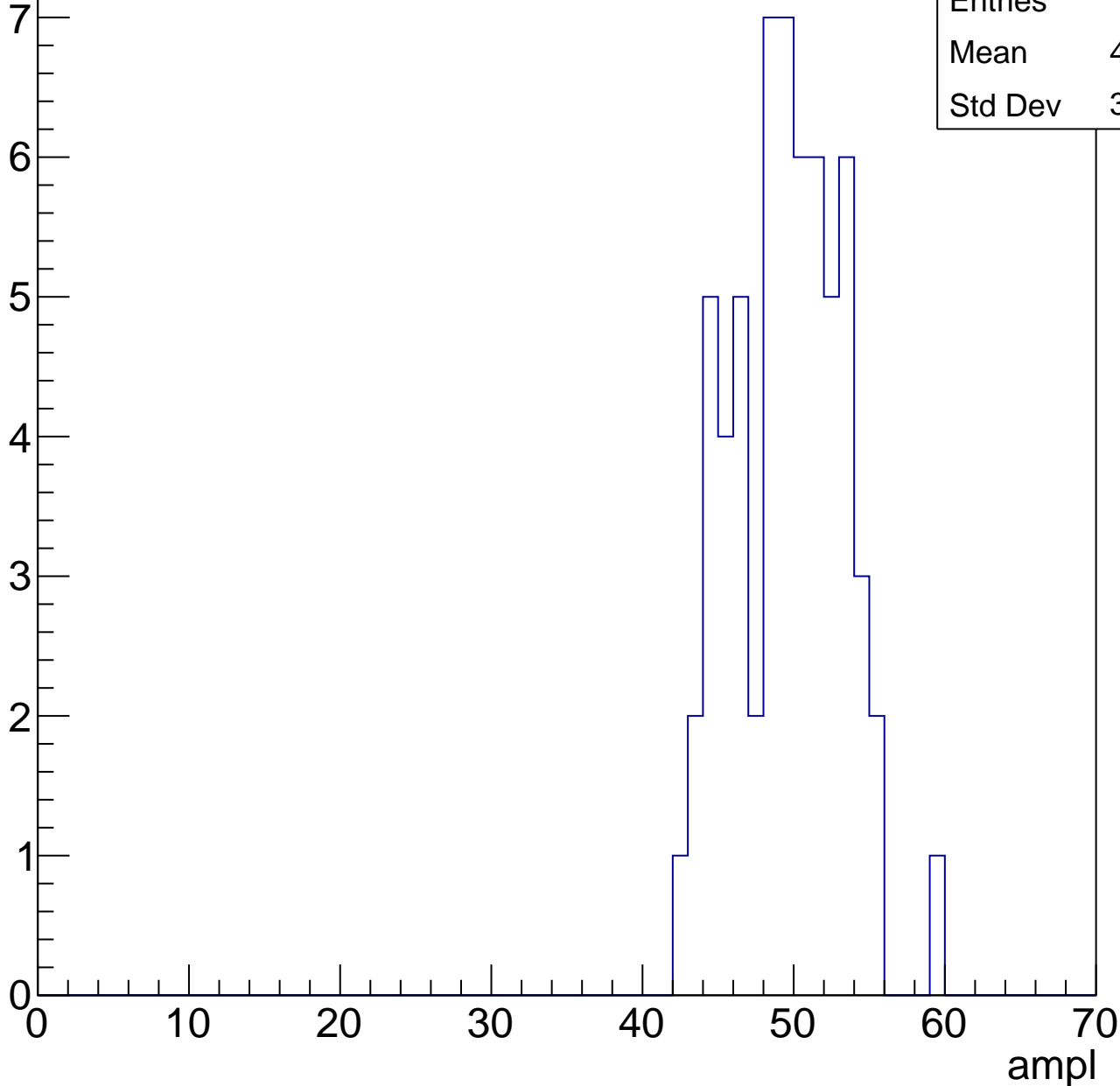


B1L101S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.13
Std Dev	3.563

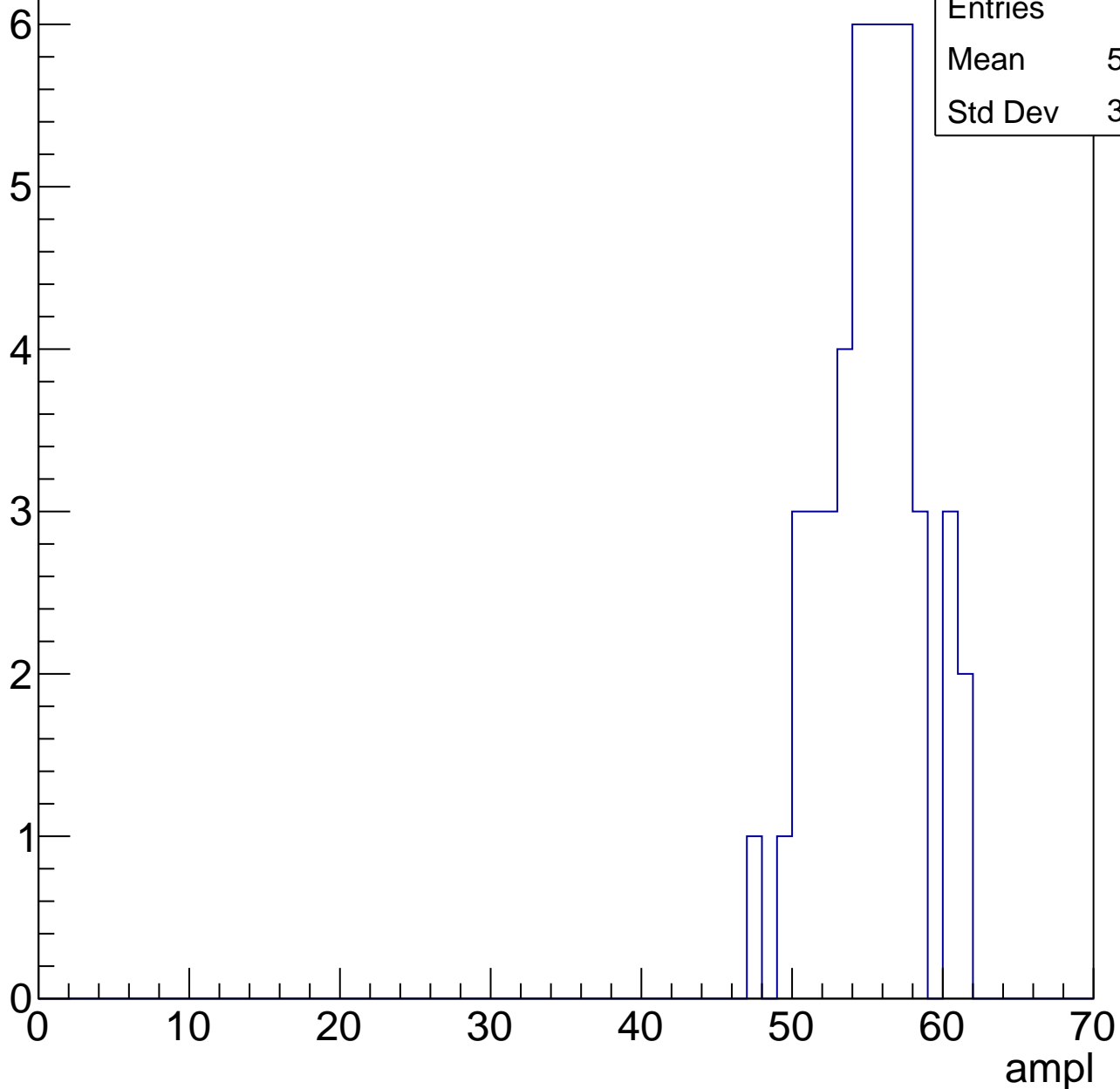


B1L101S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

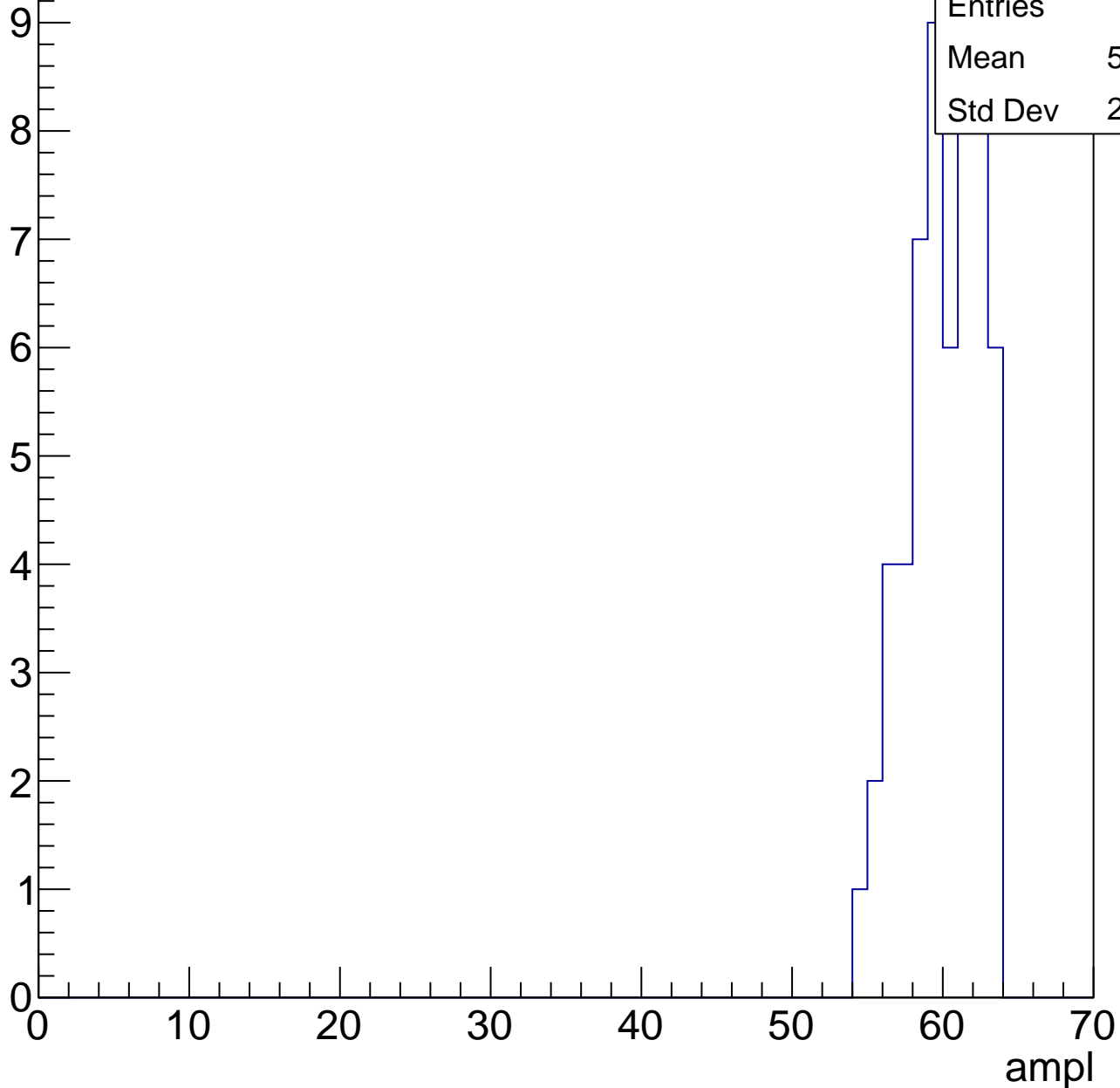
Entries	47
Mean	54.79
Std Dev	3.175



B1L101S, U14-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

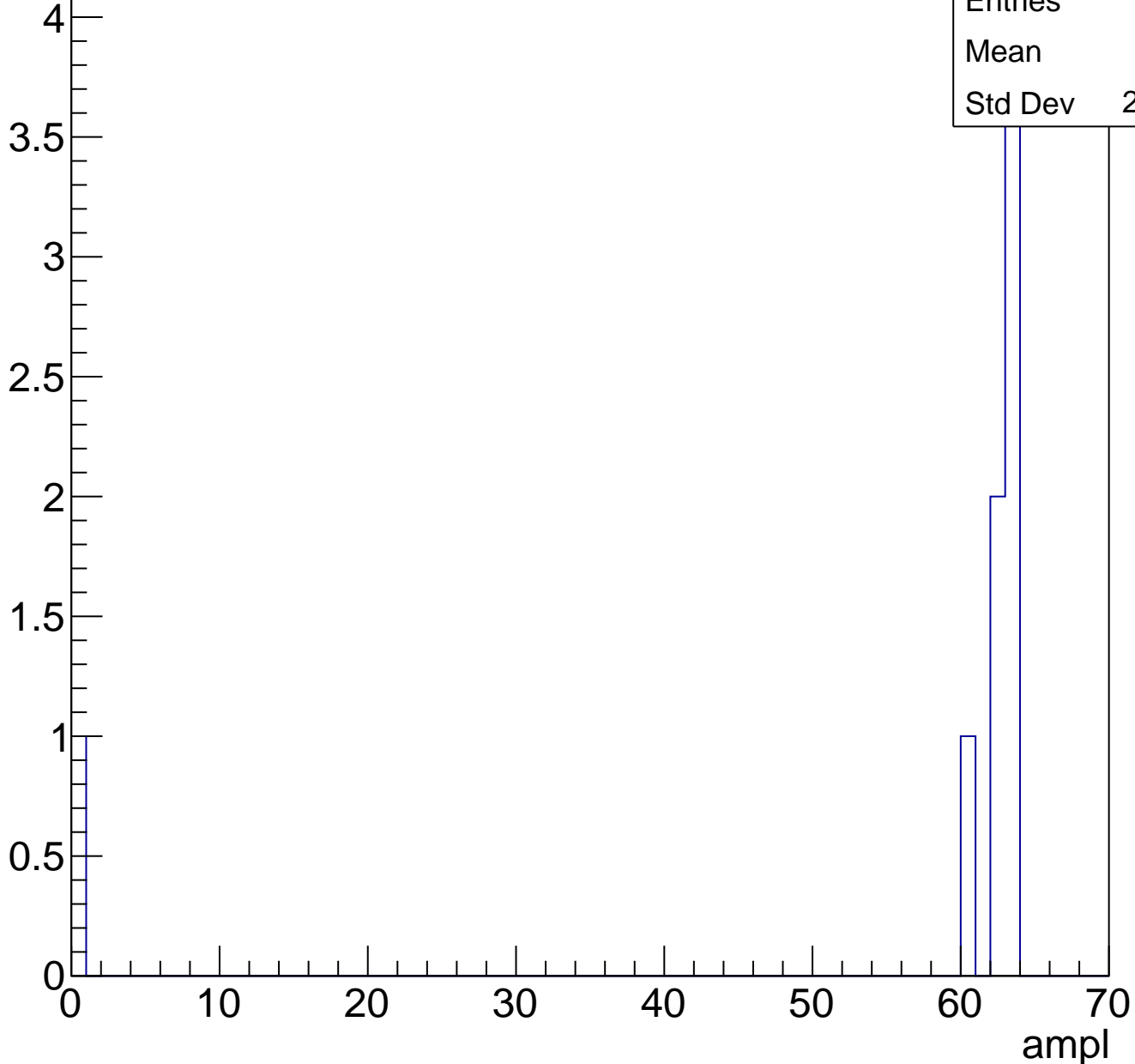


Entries	55
Mean	59.55
Std Dev	2.357

B1L101S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U14-ch90, adc0

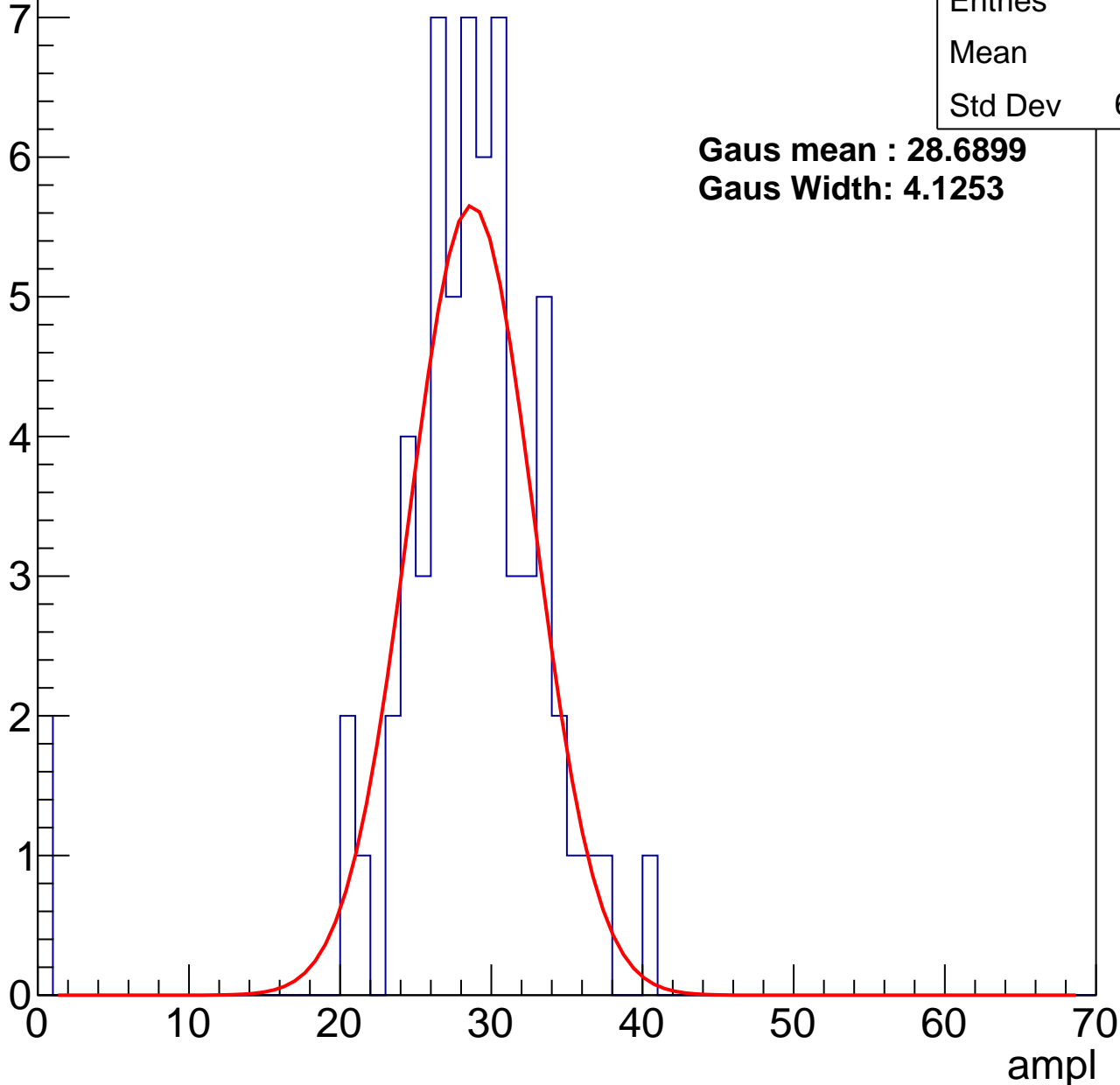
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.7
Std Dev	6.371

Gaus mean : 28.6899

Gaus Width: 4.1253



B1L101S, U14-ch90, adc1

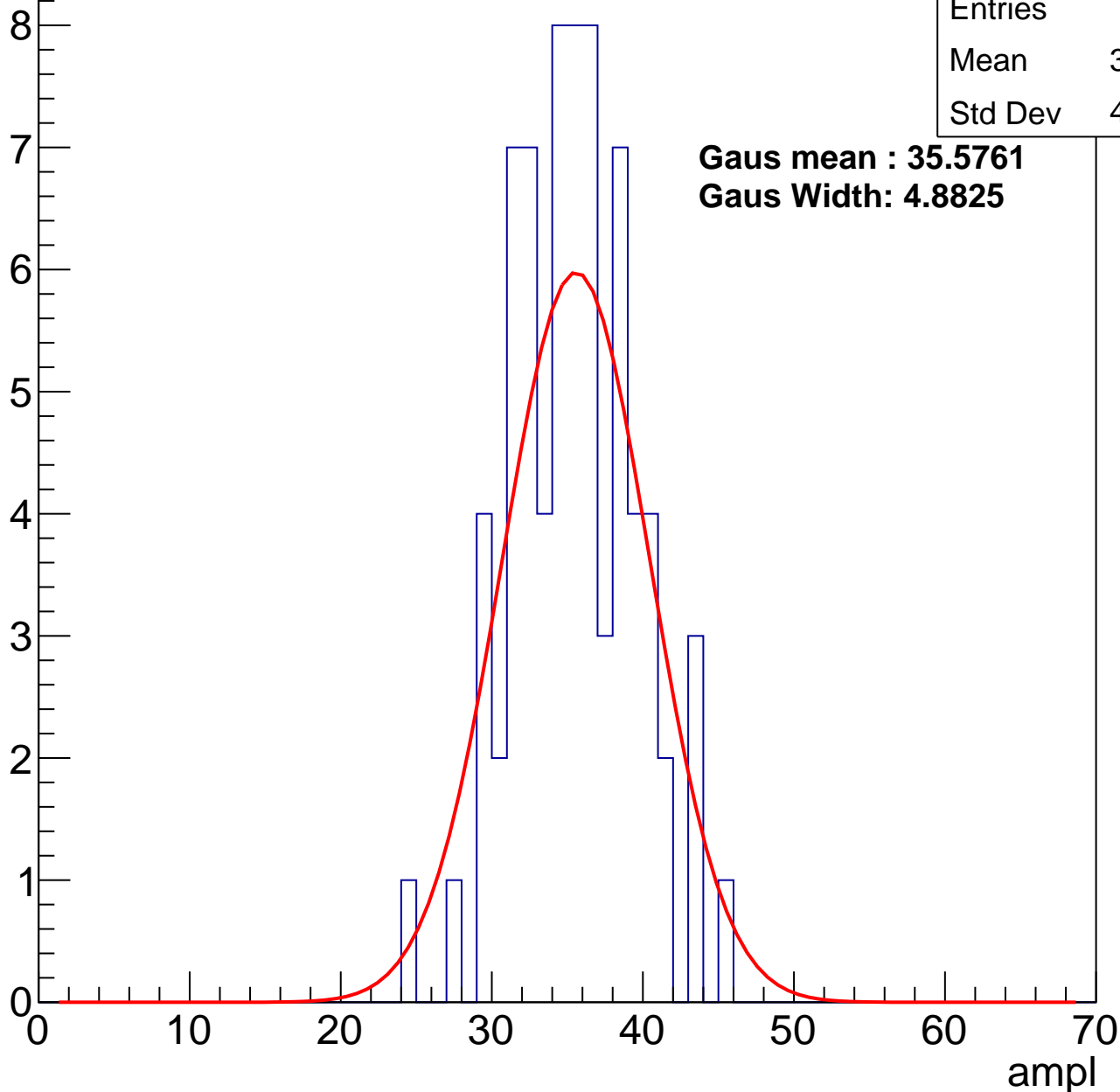
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.99
Std Dev	4.008

Gaus mean : 35.5761

Gaus Width: 4.8825



B1L101S, U14-ch90, adc2

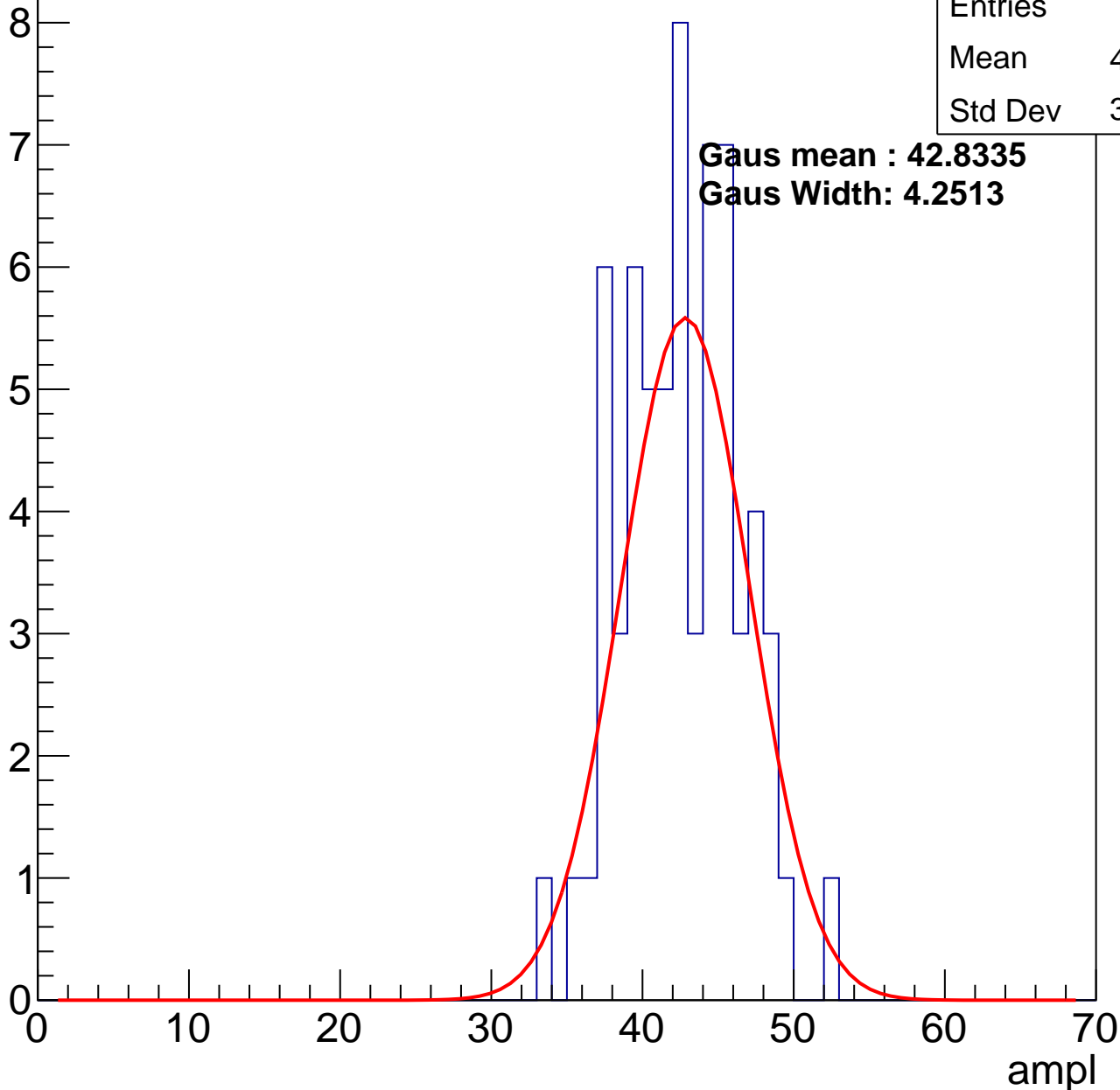
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.12
Std Dev	3.805

Gaus mean : 42.8335

Gaus Width: 4.2513

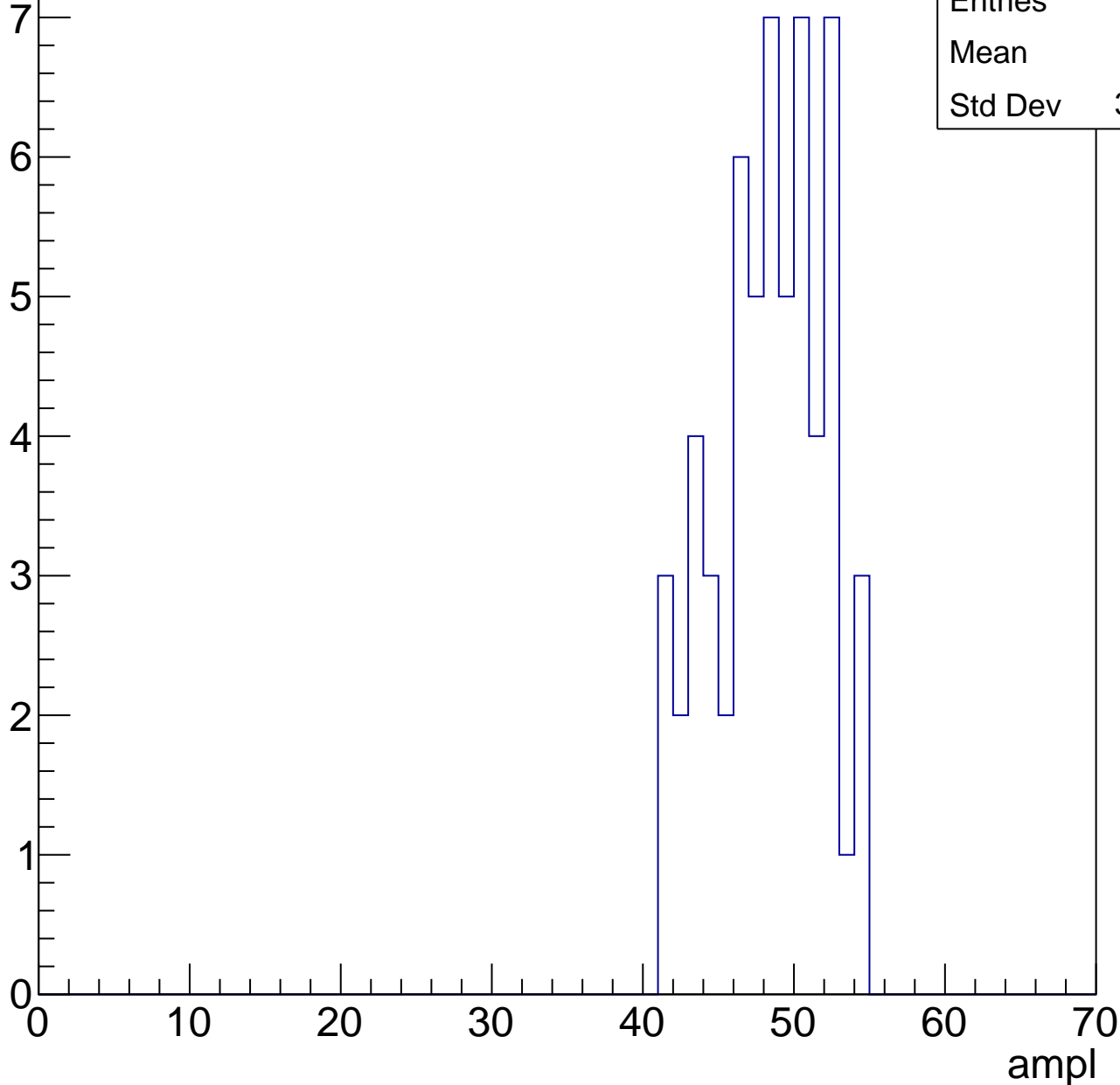


B1L101S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	47.9
Std Dev	3.511

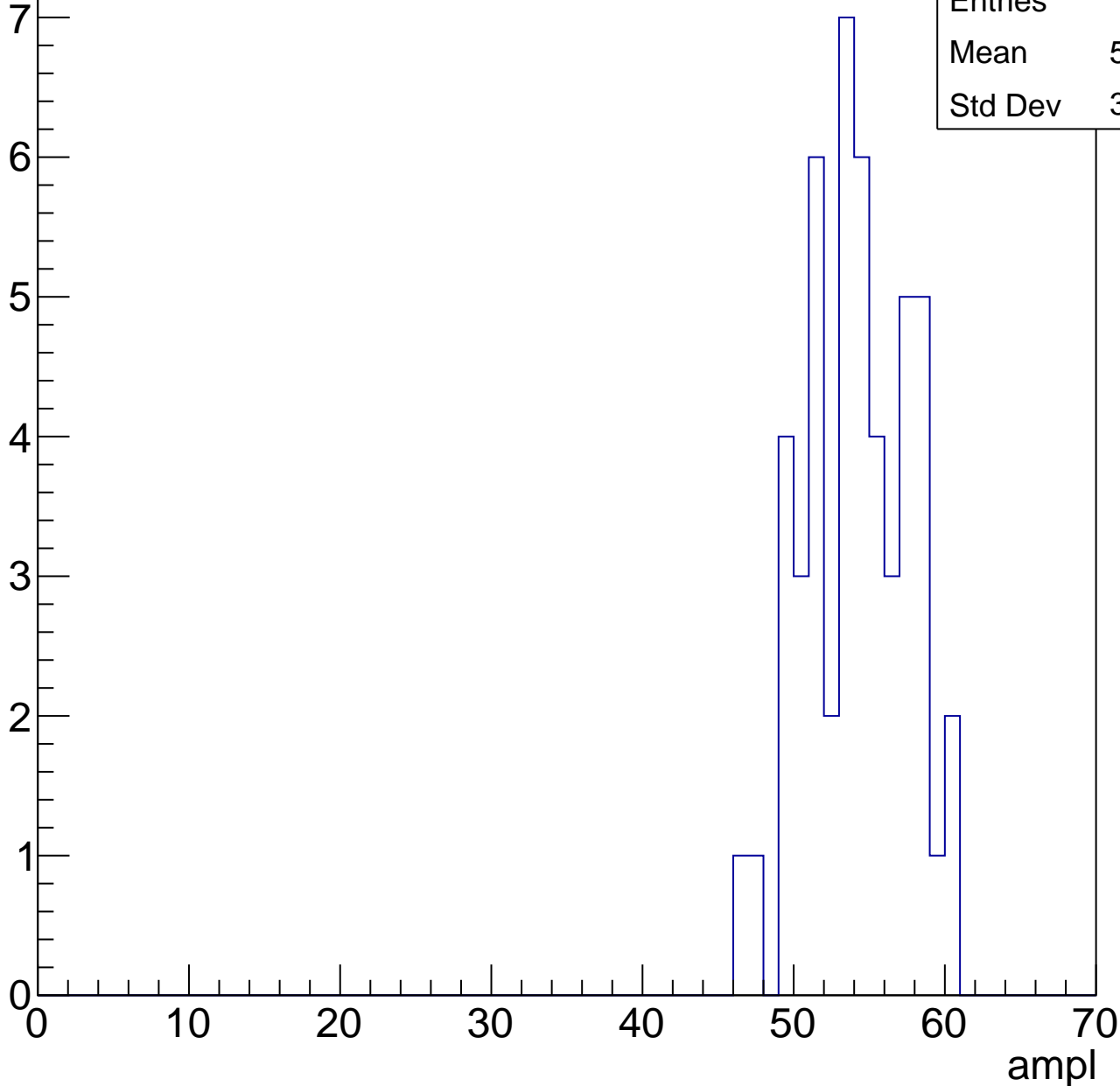


B1L101S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

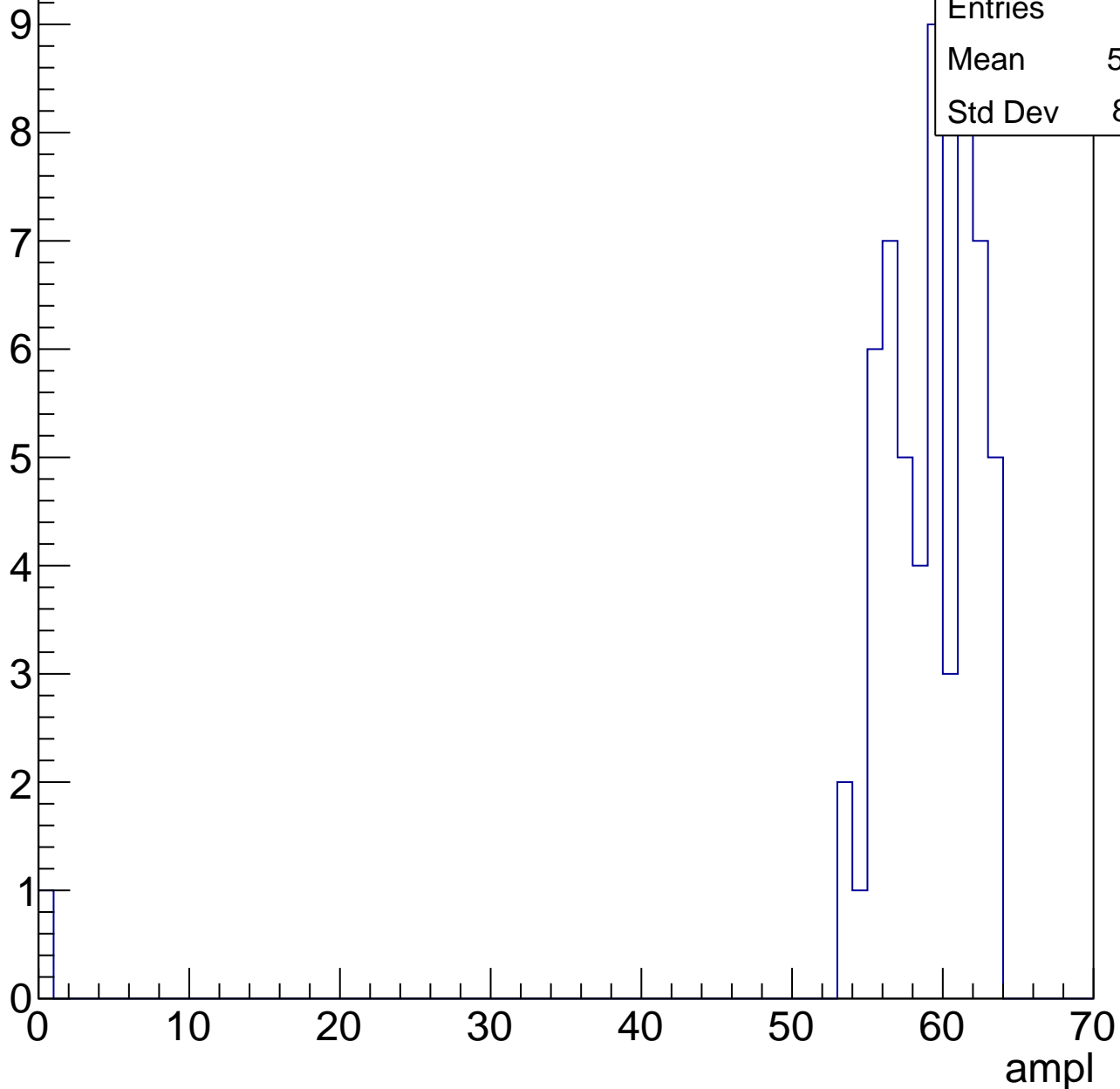
Entries	50
Mean	53.72
Std Dev	3.359



B1L101S, U14-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

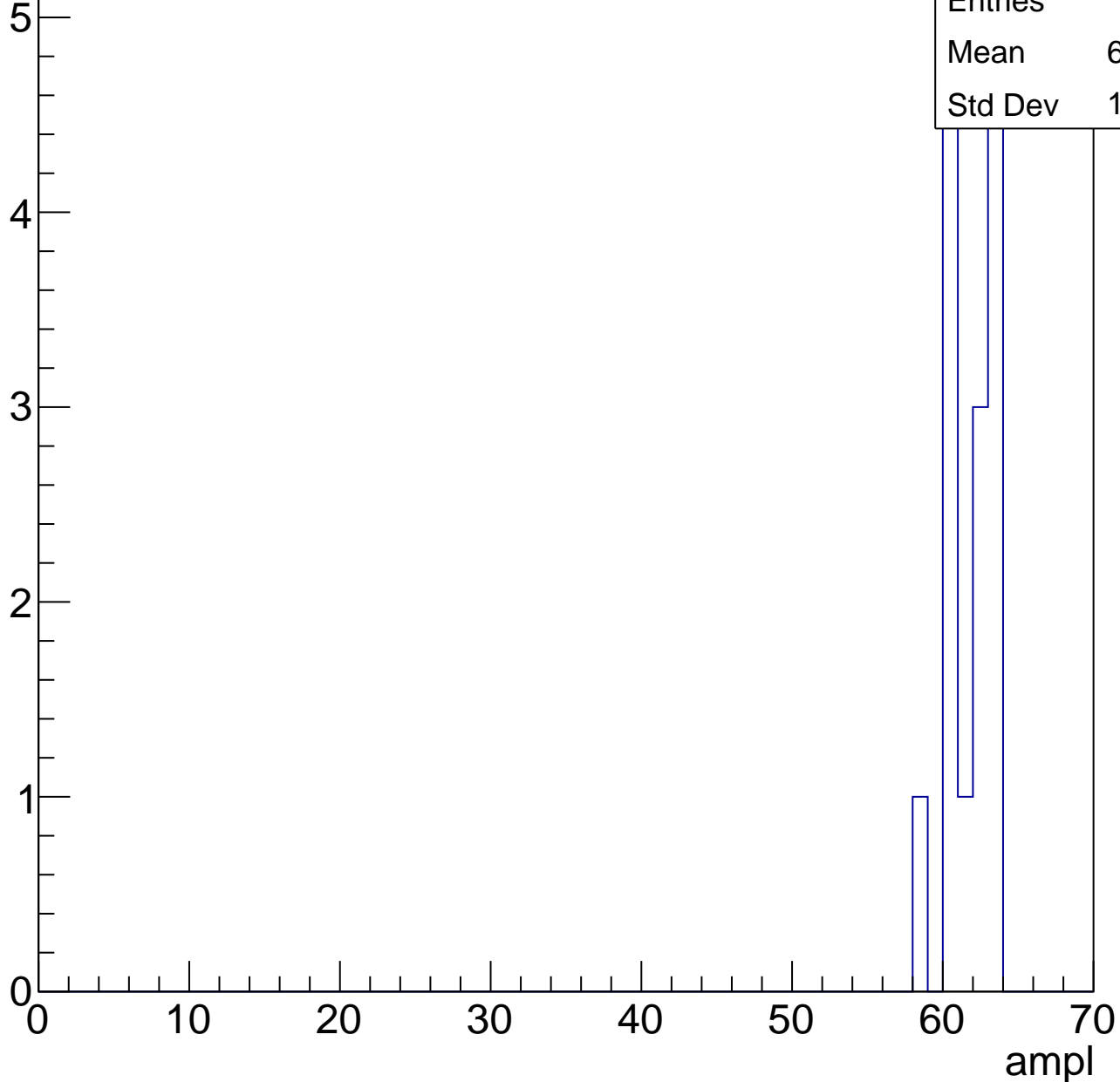
Entry



B1L101S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch91, adc0

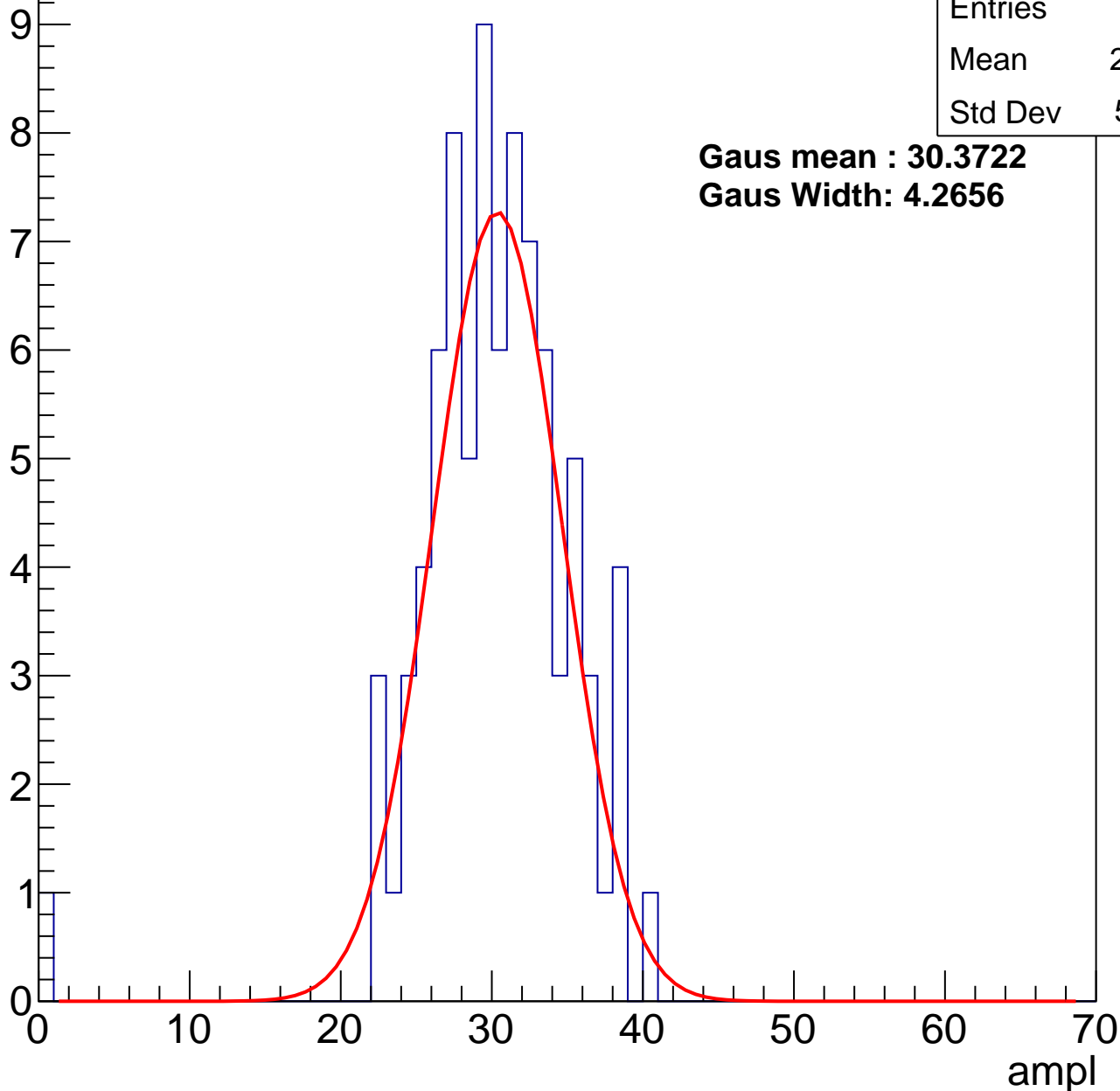
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.74
Std Dev	5.251

Gaus mean : 30.3722

Gaus Width: 4.2656



B1L101S, U14-ch91, adc1

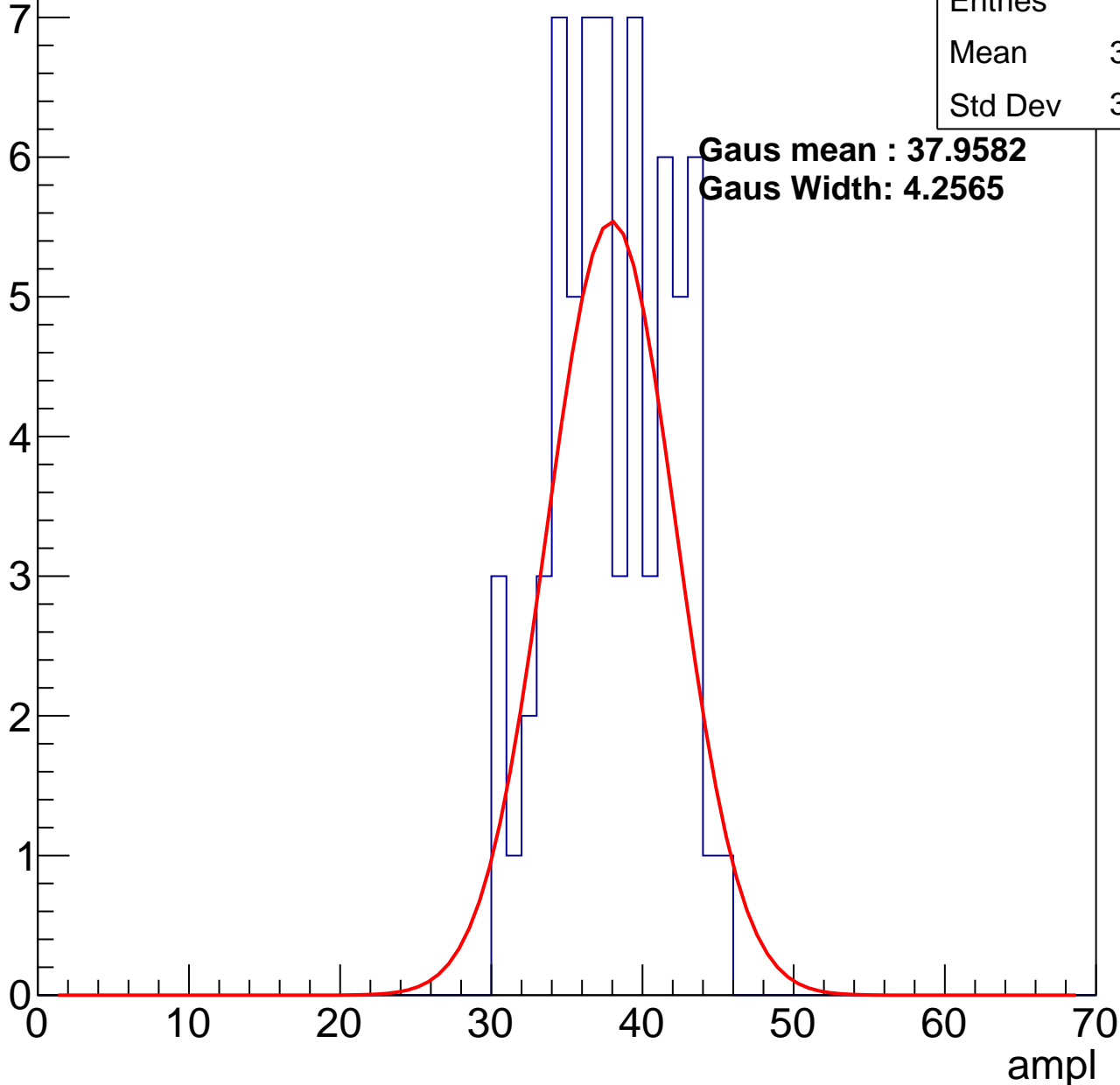
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.58
Std Dev	3.778

Gaus mean : 37.9582

Gaus Width: 4.2565



B1L101S, U14-ch91, adc2

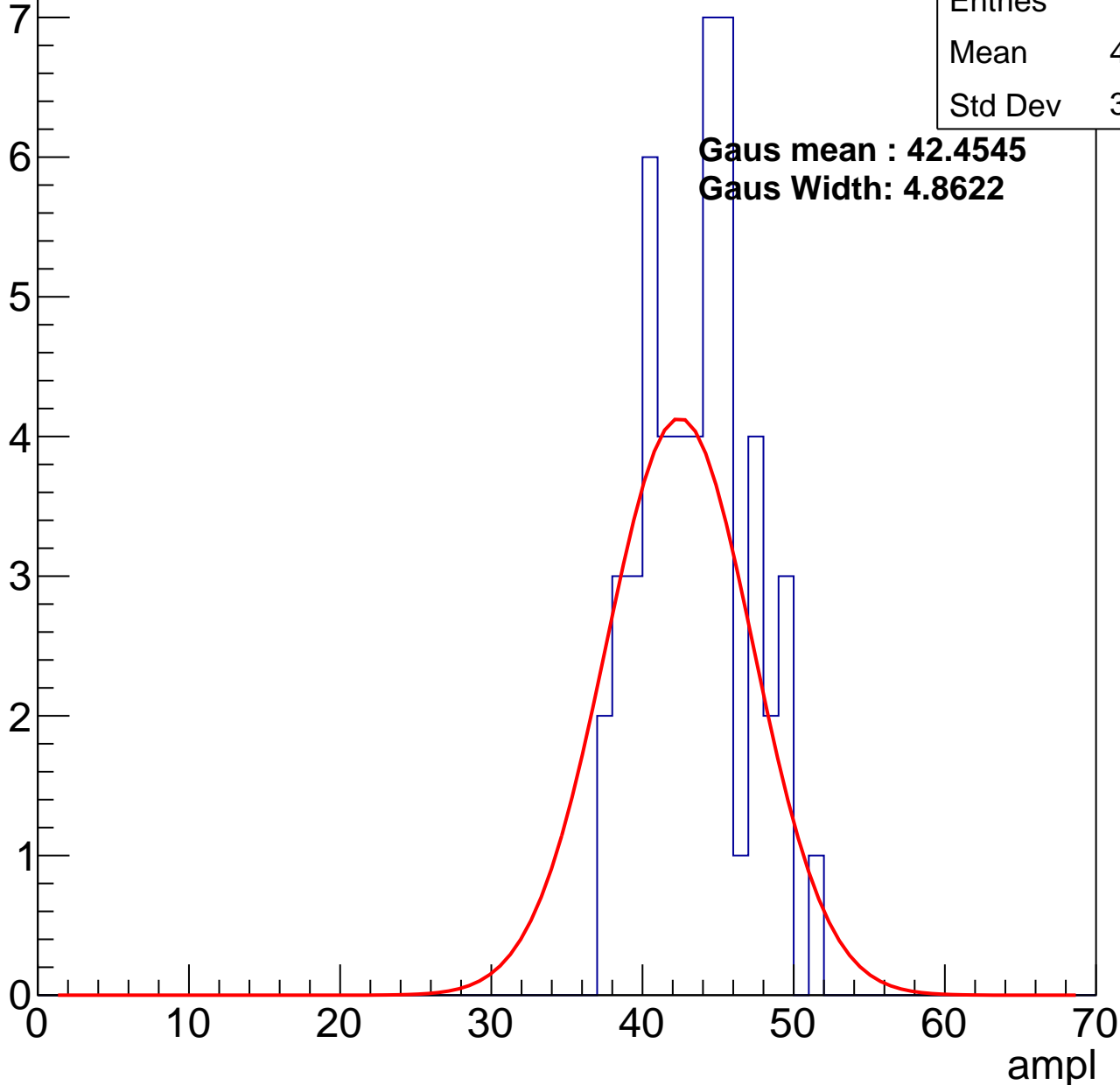
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	43.14
Std Dev	3.424

Gaus mean : 42.4545

Gaus Width: 4.8622

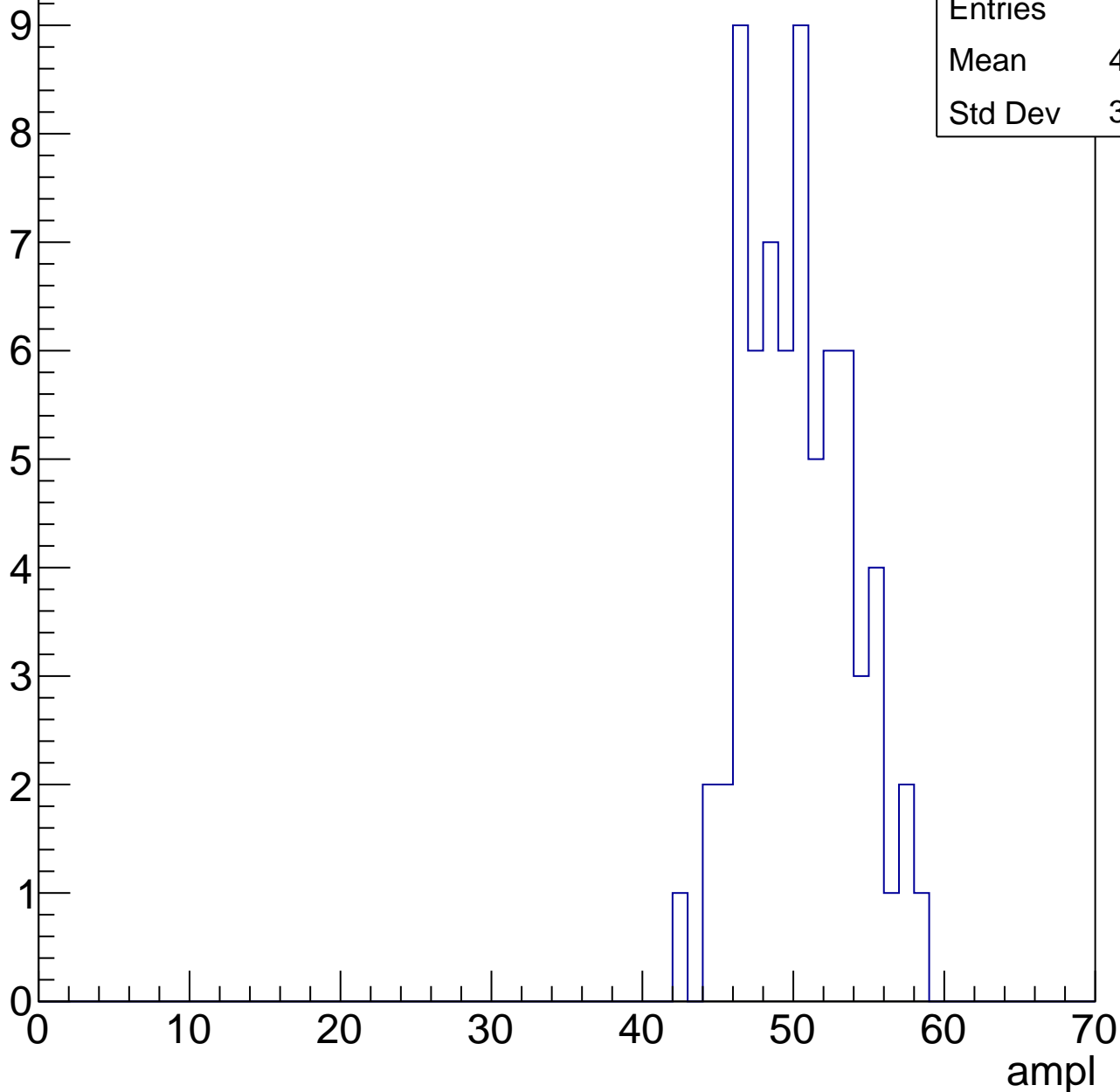


B1L101S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.87
Std Dev	3.476

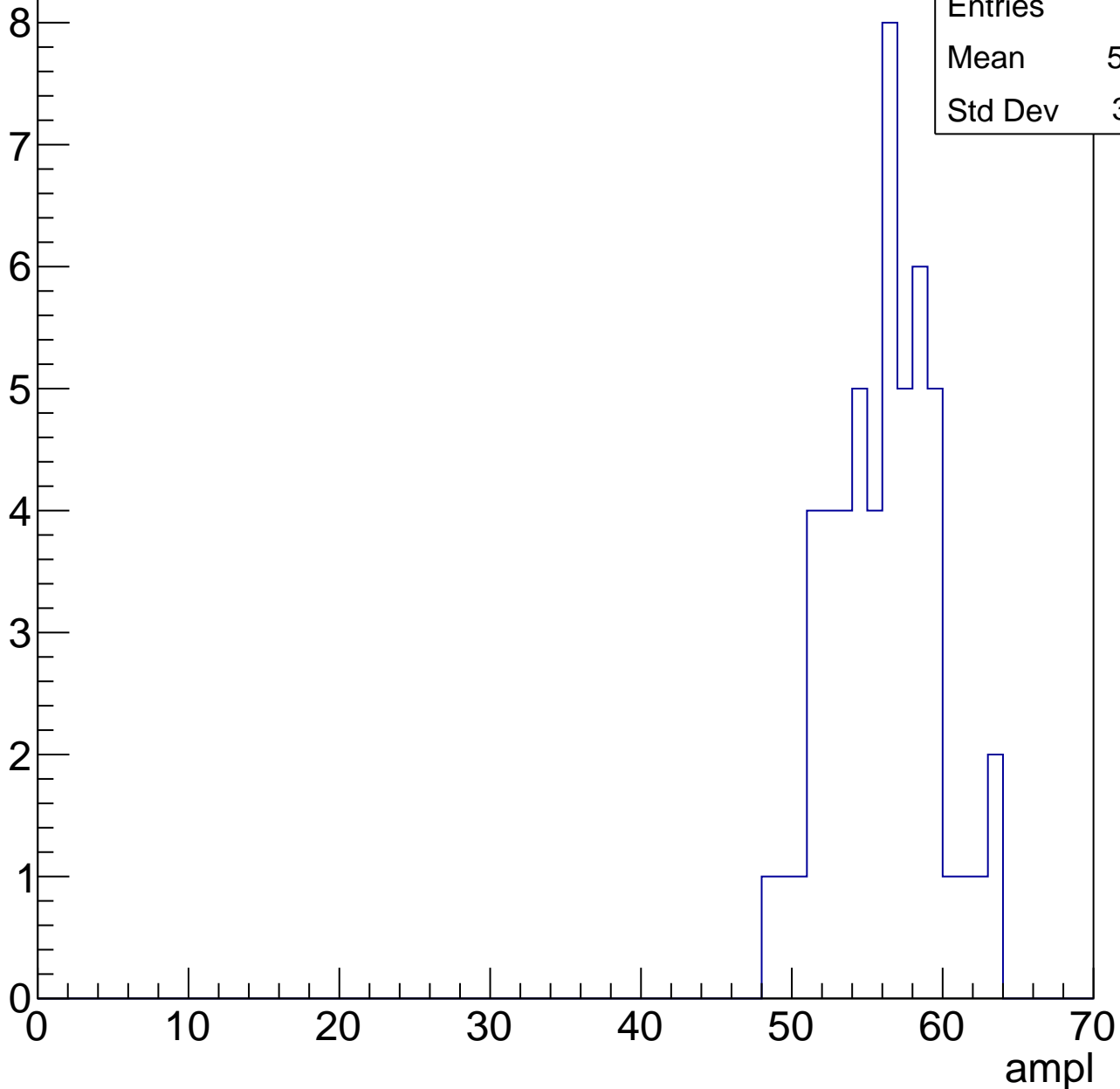


B1L101S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

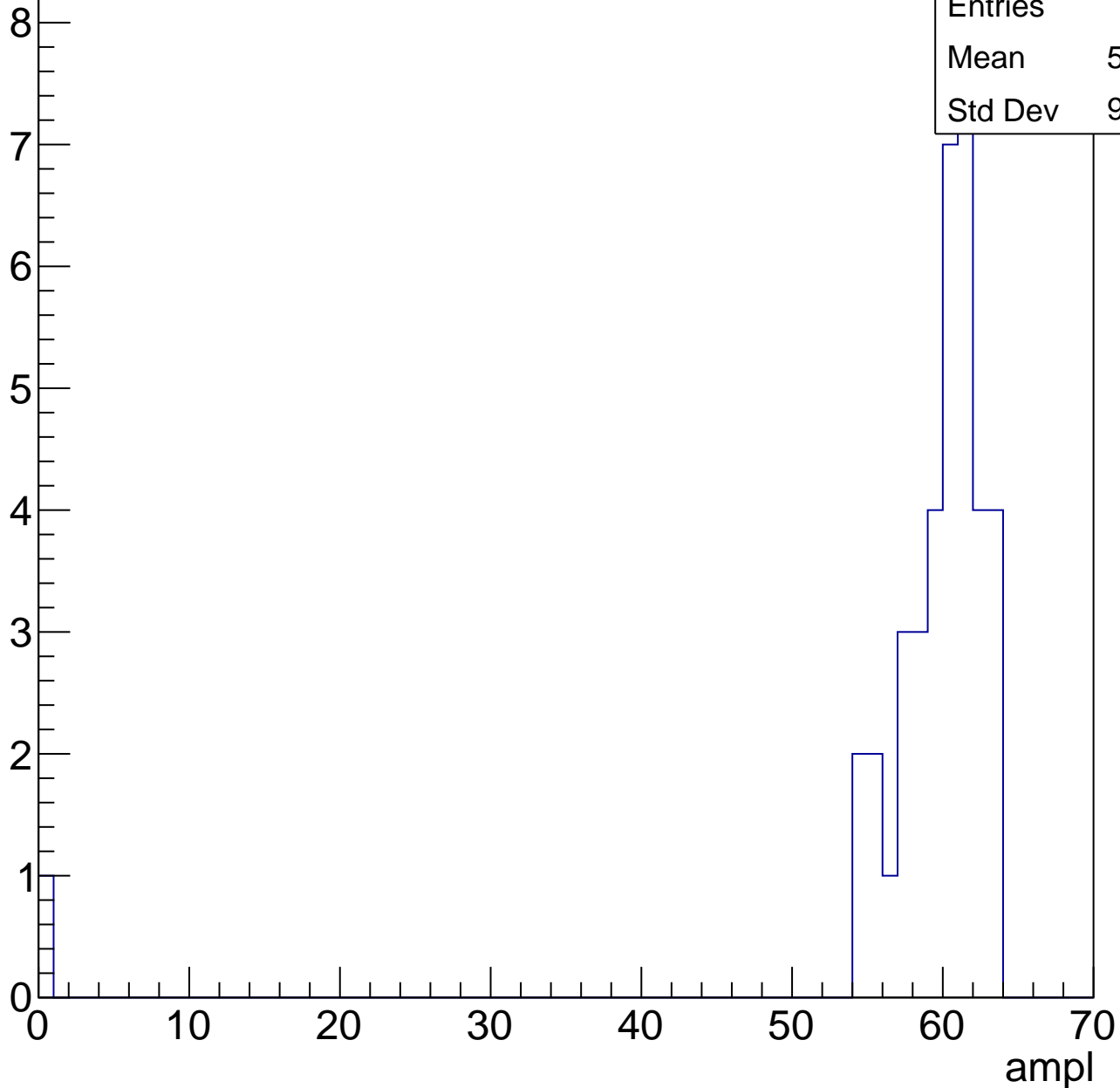
Entries	53
Mean	55.58
Std Dev	3.401



B1L101S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

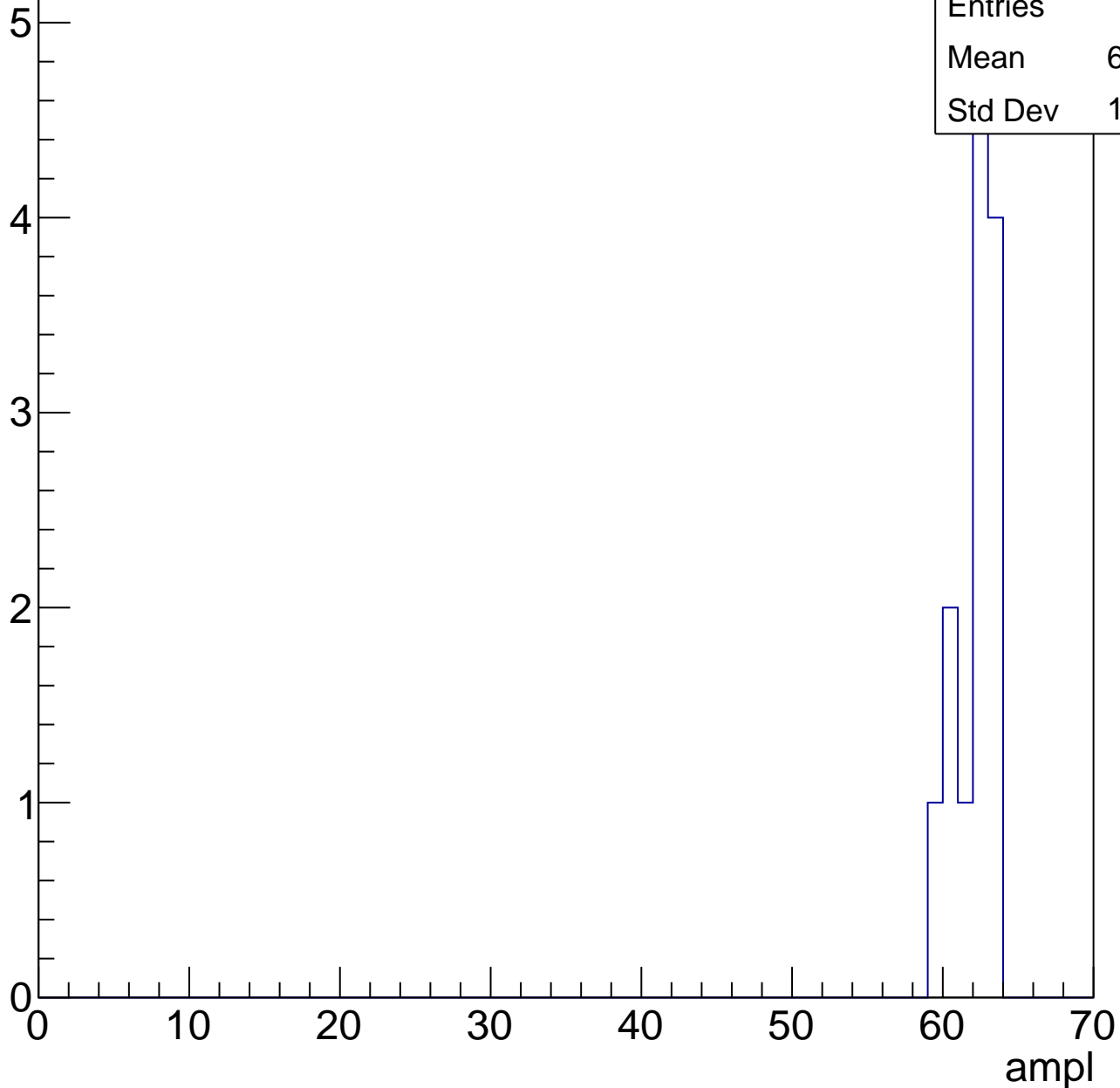


B1L101S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.69
Std Dev	1.264



B1L101S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch92, adc0

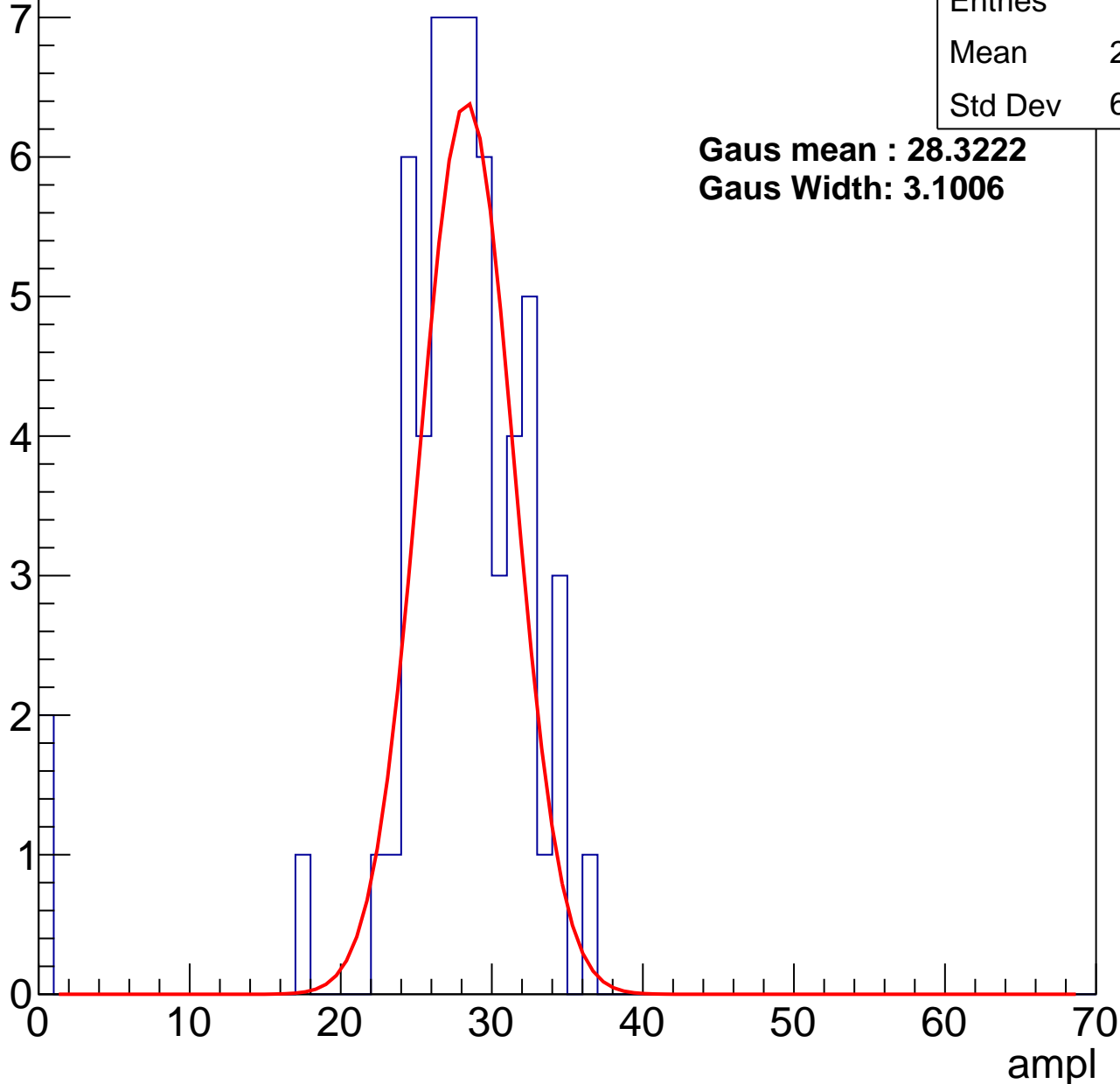
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	26.98
Std Dev	6.088

Gaus mean : 28.3222

Gaus Width: 3.1006



B1L101S, U14-ch92, adc1

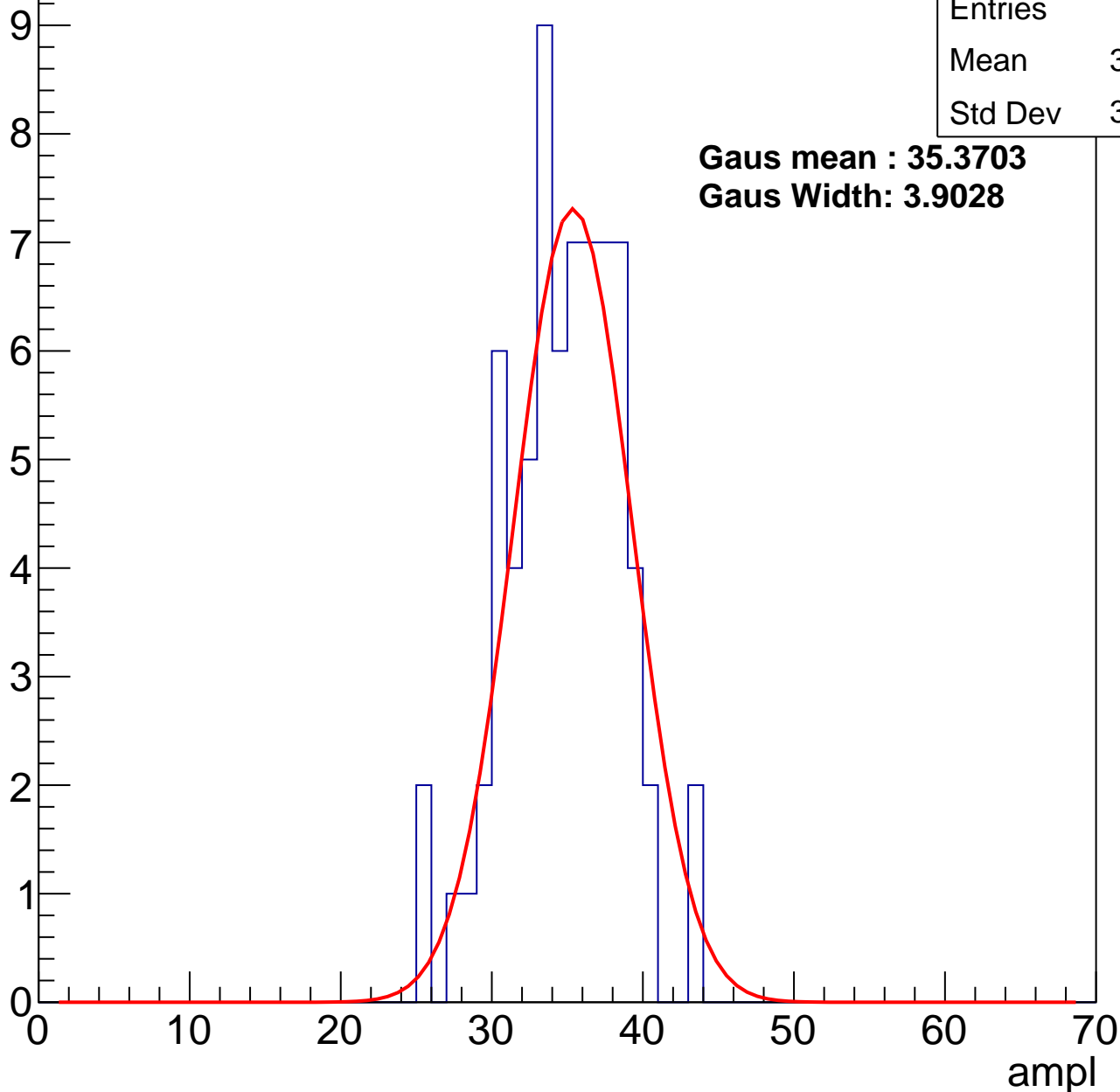
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	34.33
Std Dev	3.716

Gaus mean : 35.3703

Gaus Width: 3.9028



B1L101S, U14-ch92, adc2

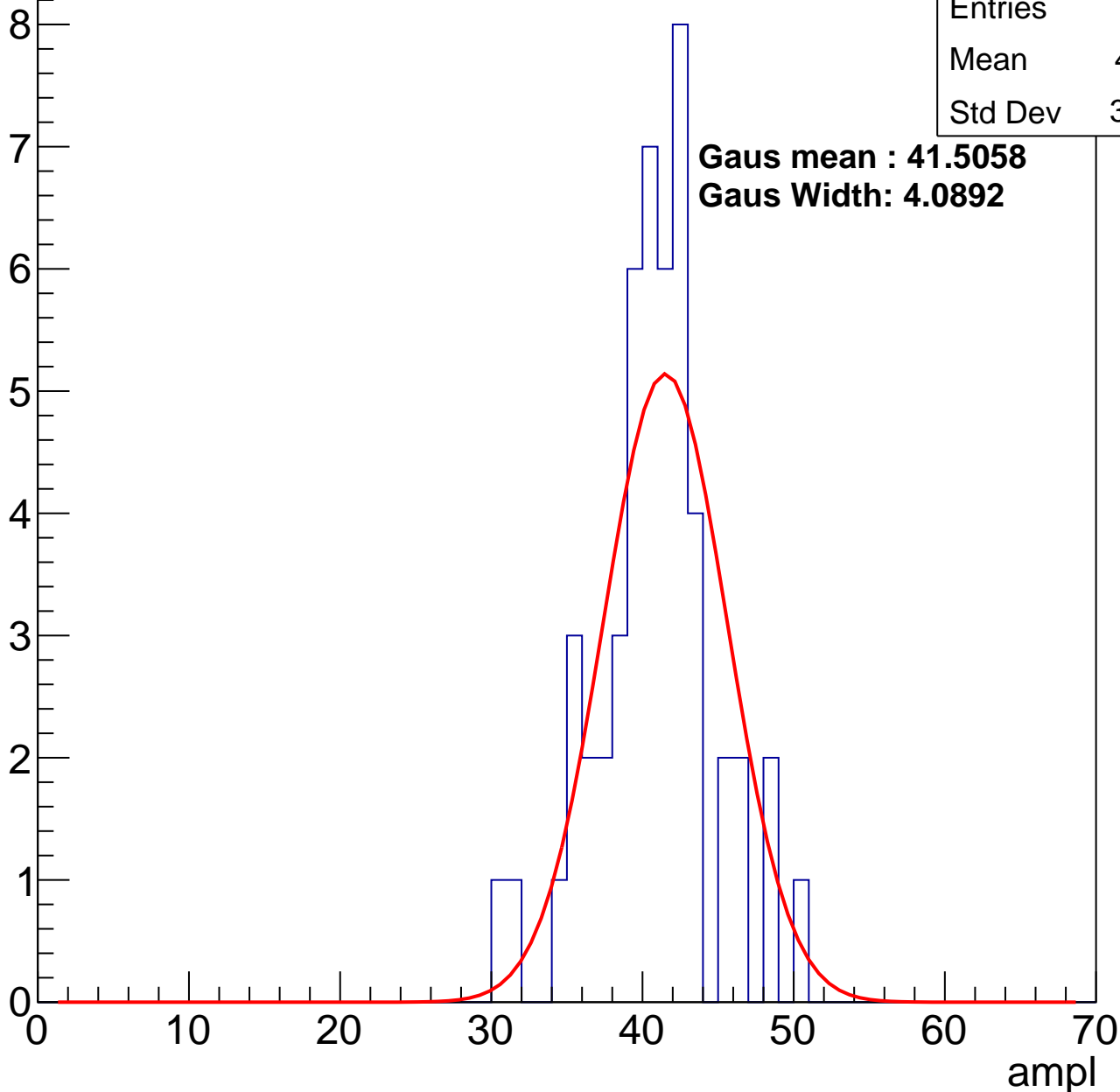
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	40.31
Std Dev	3.908

Gaus mean : 41.5058

Gaus Width: 4.0892



B1L101S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	46.69
Std Dev	3.533

Entry

10

8

6

4

2

0

0

10

20

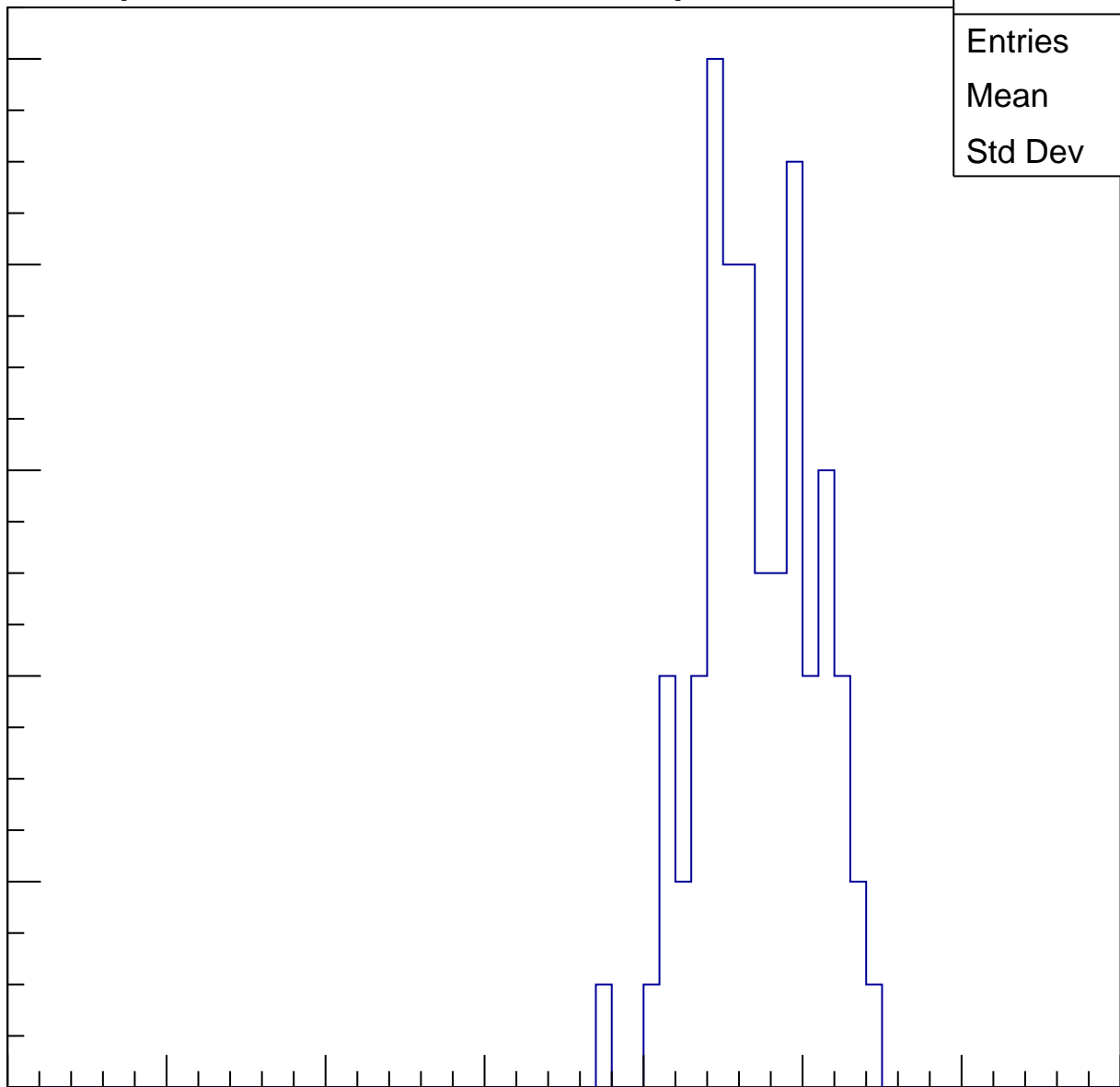
30

40

50

60

ampl



B1L101S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

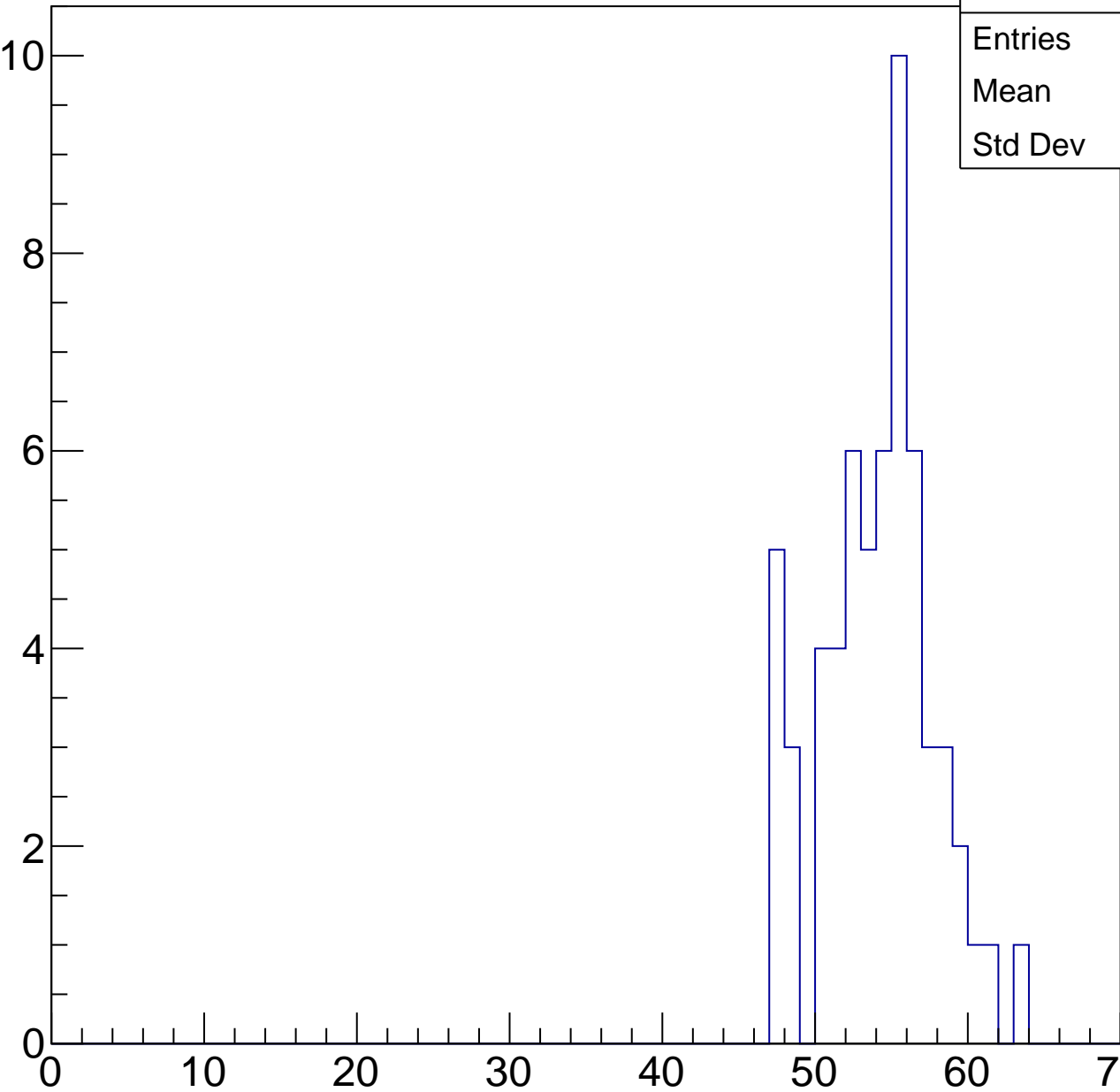
Entries	60
Mean	53.62
Std Dev	3.647

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

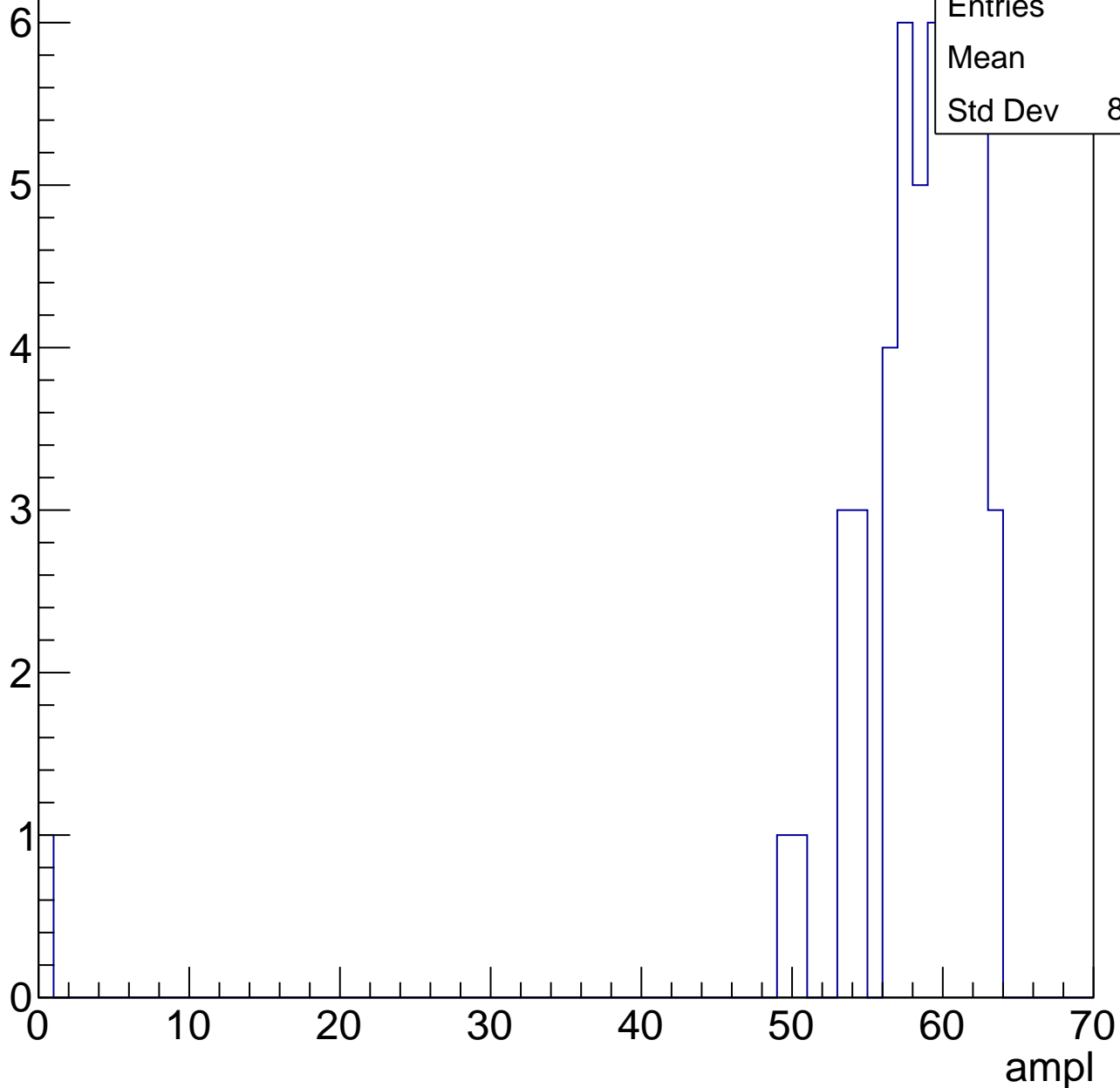


B1L101S, U14-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.2
Std Dev	8.718

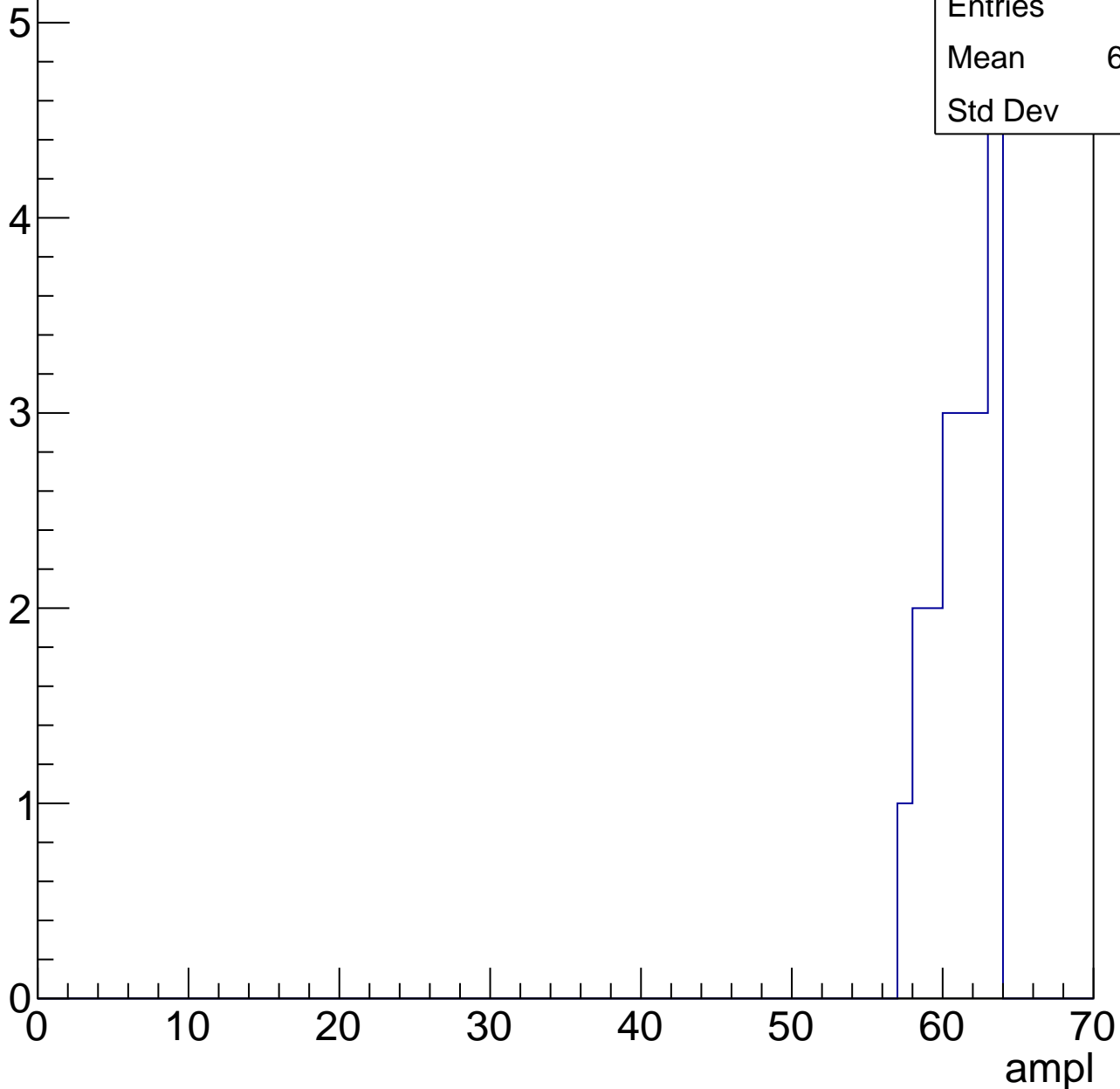


B1L101S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	60.79
Std Dev	1.88



B1L101S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch93, adc0

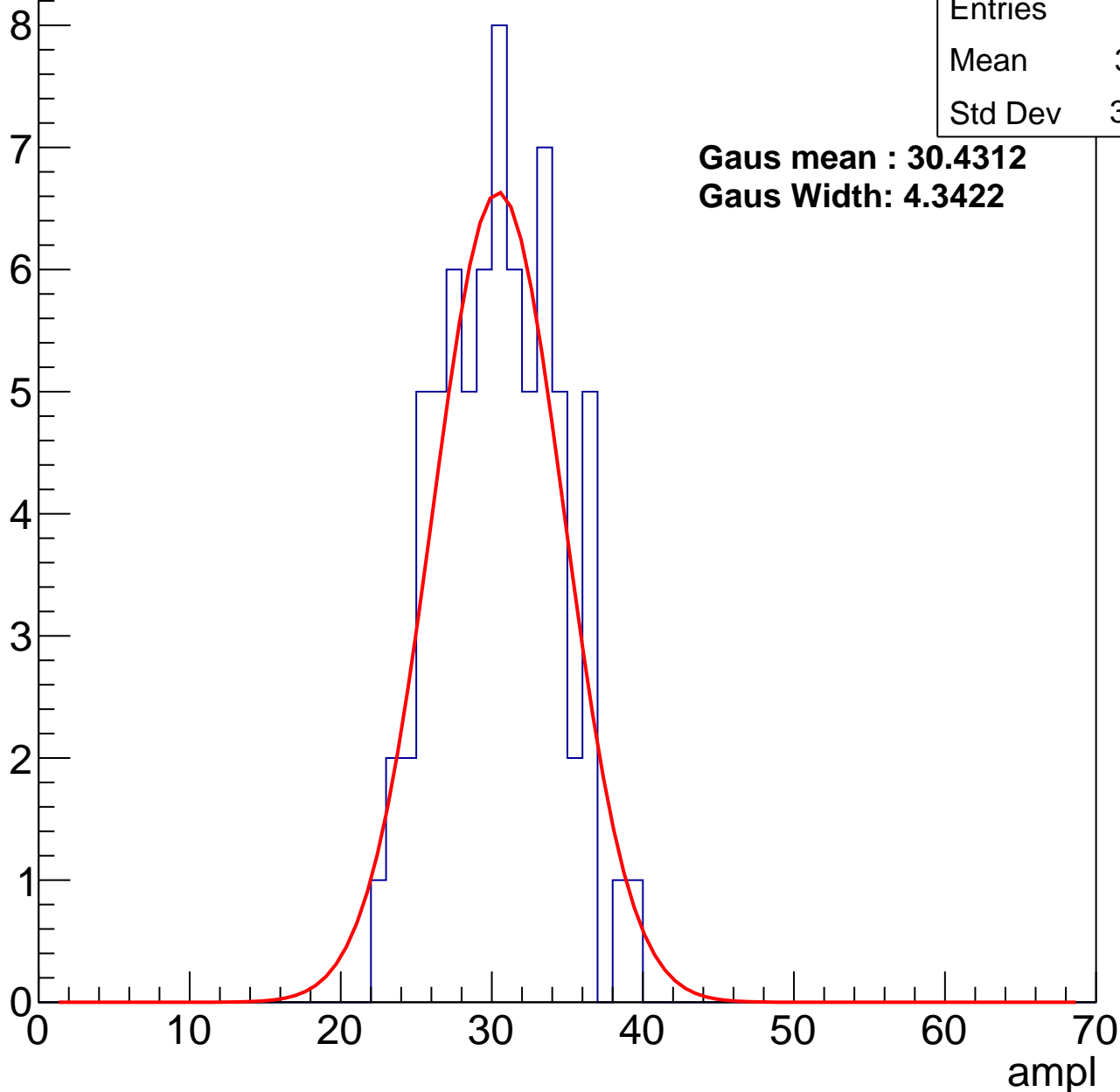
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.01
Std Dev	3.842

Gaus mean : 30.4312

Gaus Width: 4.3422



B1L101S, U14-ch93, adc1

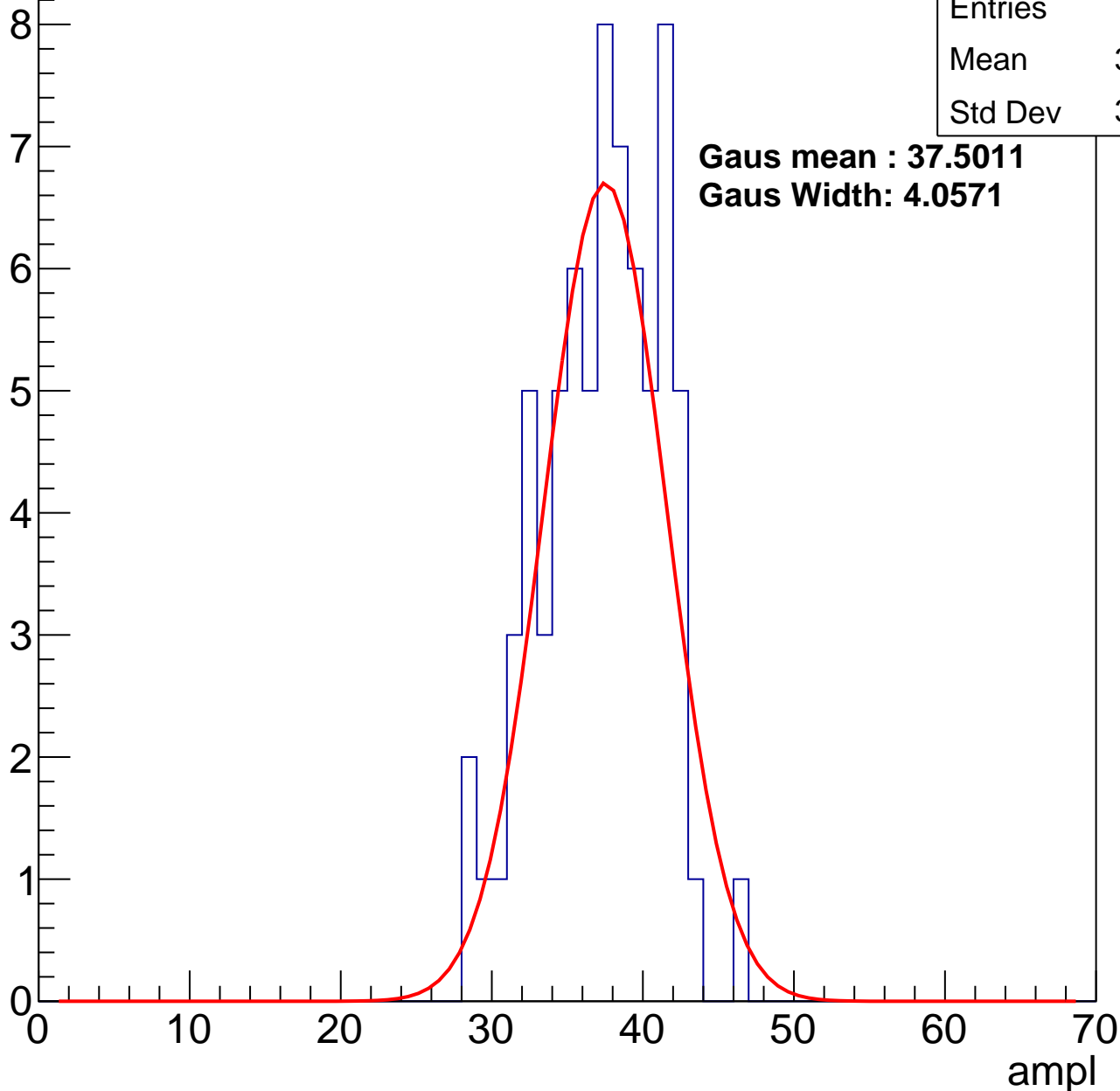
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.81
Std Dev	3.861

Gaus mean : 37.5011

Gaus Width: 4.0571



B1L101S, U14-ch93, adc2

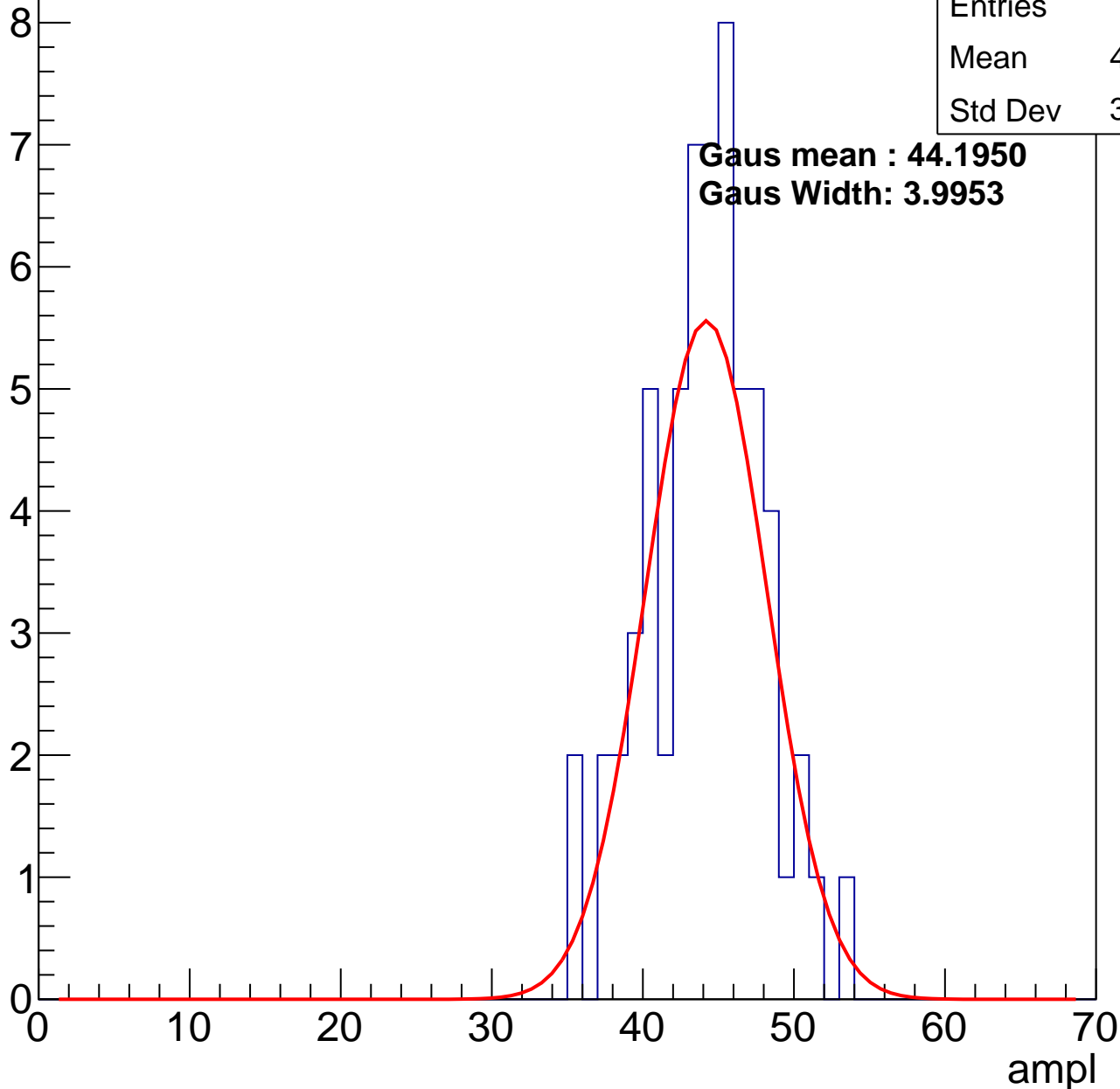
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.68
Std Dev	3.788

Gaus mean : 44.1950

Gaus Width: 3.9953

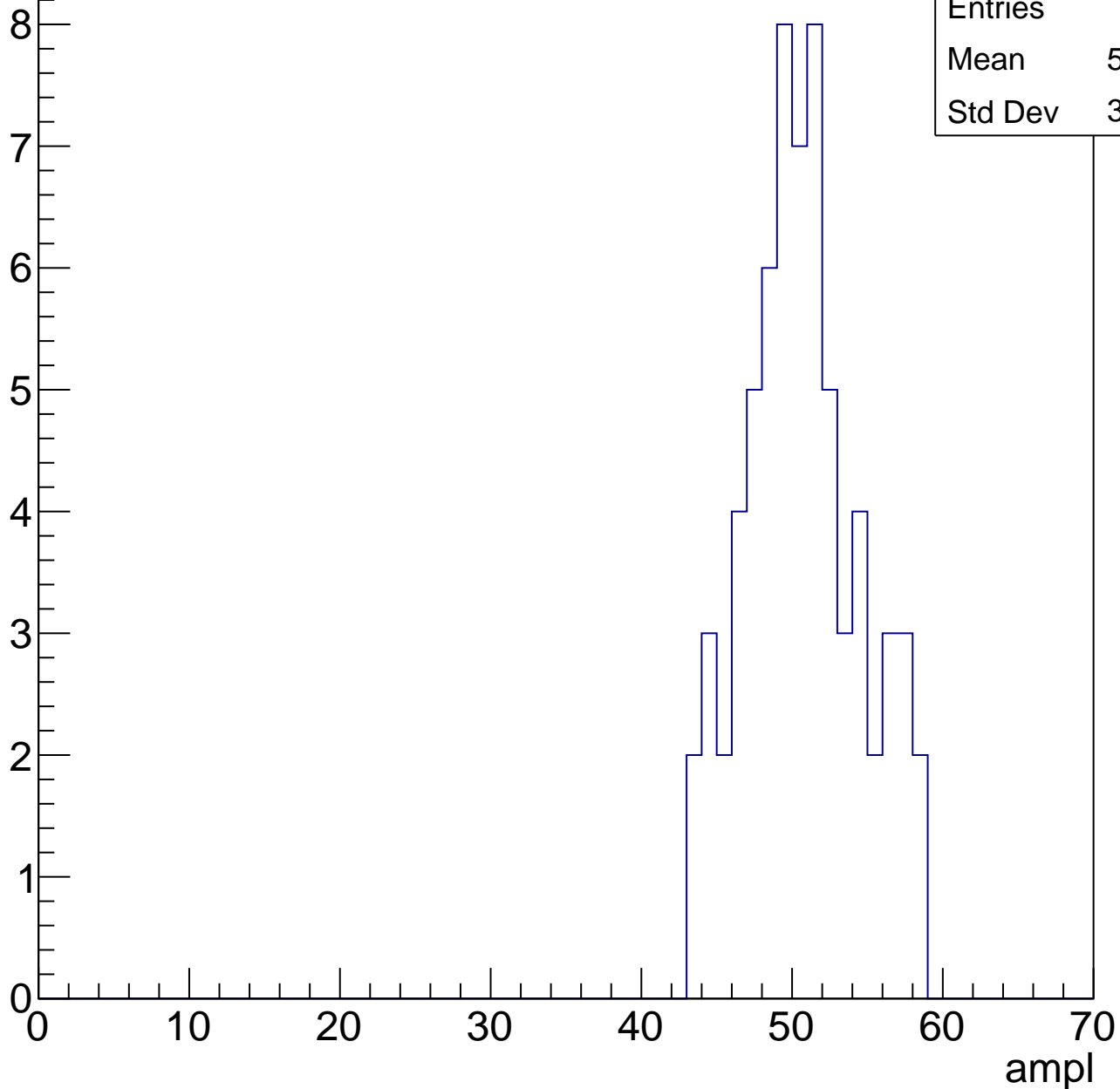


B1L101S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.22
Std Dev	3.757

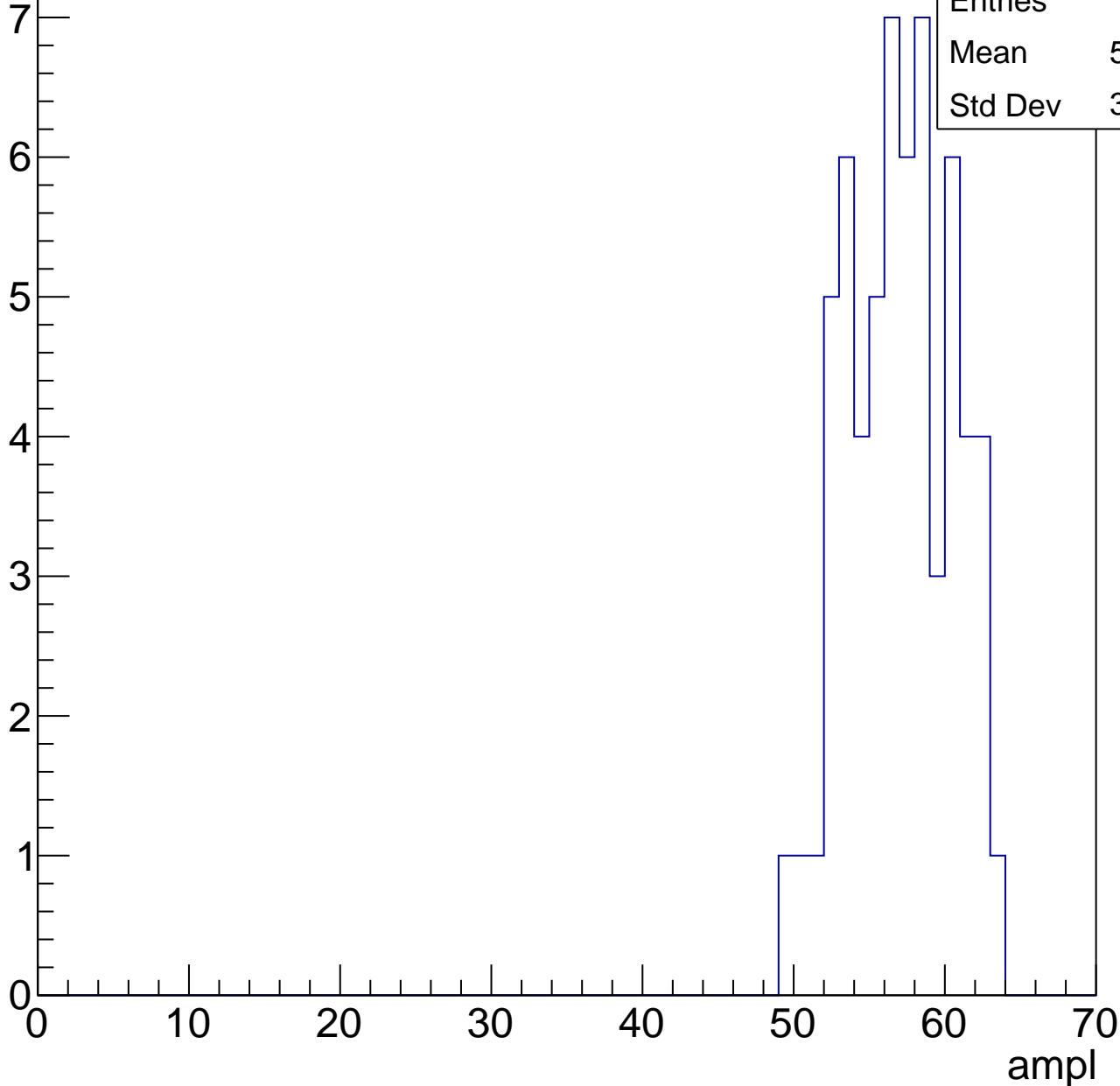


B1L101S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

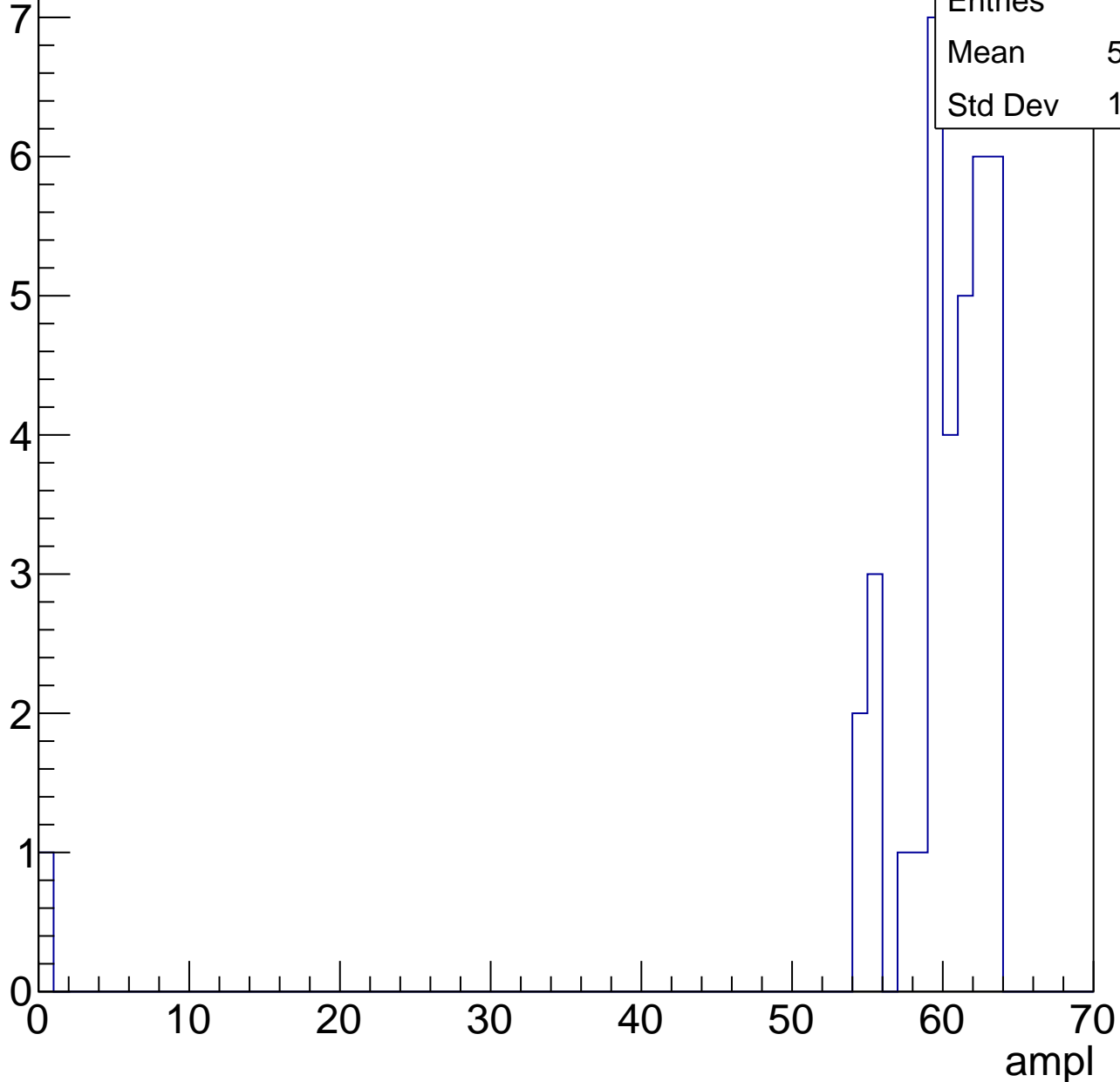
Entries	61
Mean	56.57
Std Dev	3.375



B1L101S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch94, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	29.33
Std Dev	5.045

Gaus mean : 30.4144

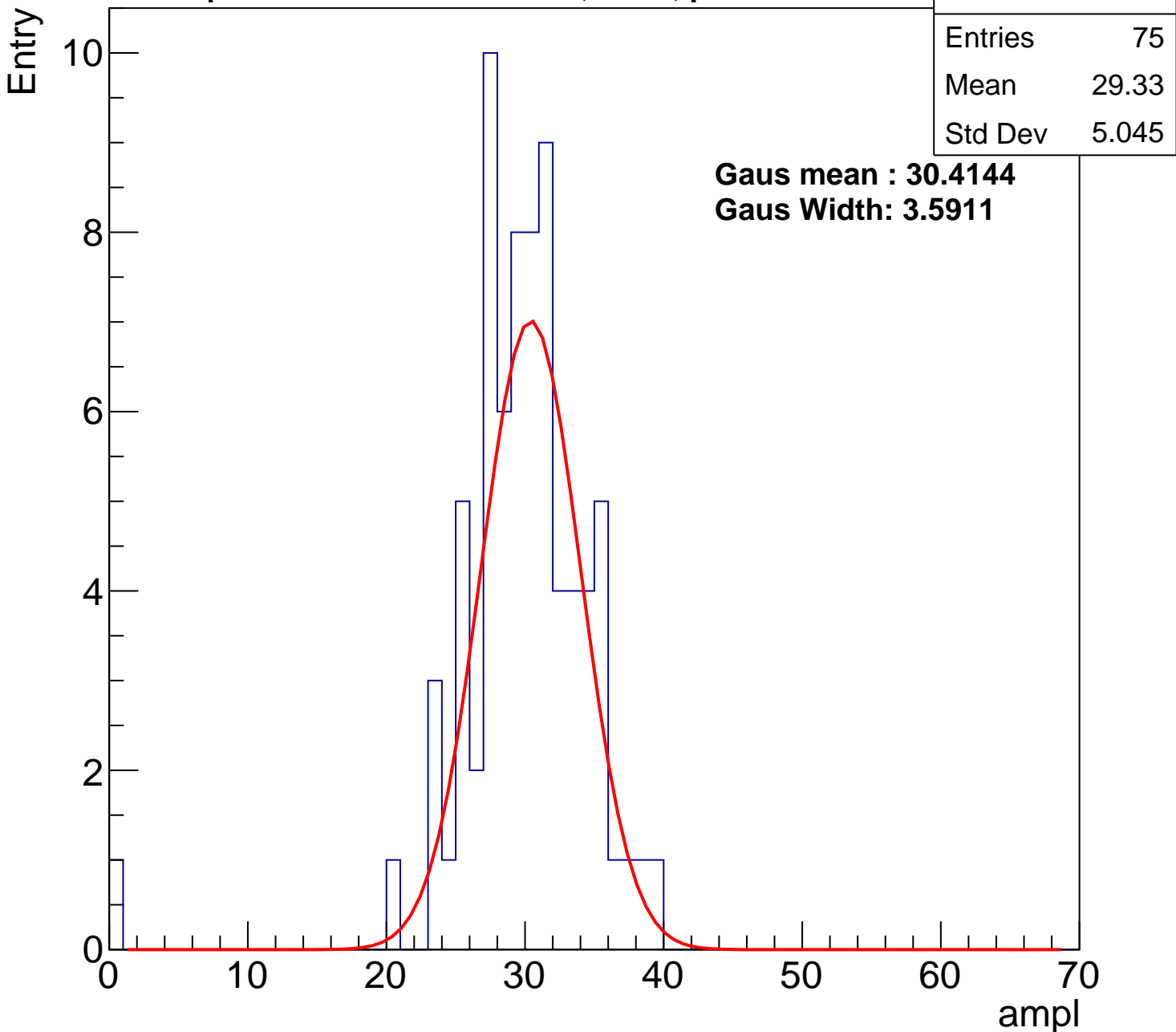
Gaus Width: 3.5911

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch94, adc1

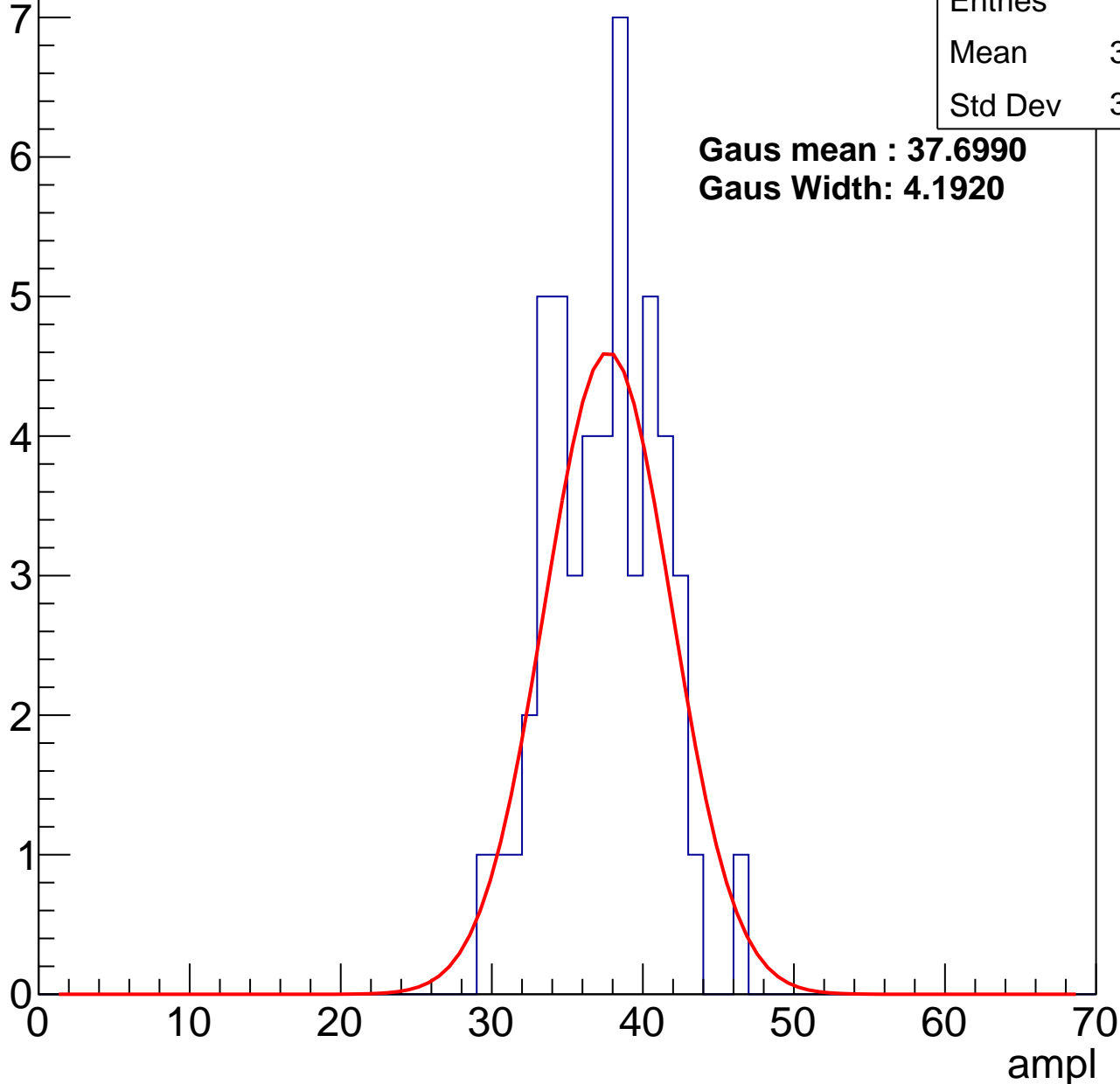
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	36.96
Std Dev	3.633

Gaus mean : 37.6990

Gaus Width: 4.1920



B1L101S, U14-ch94, adc2

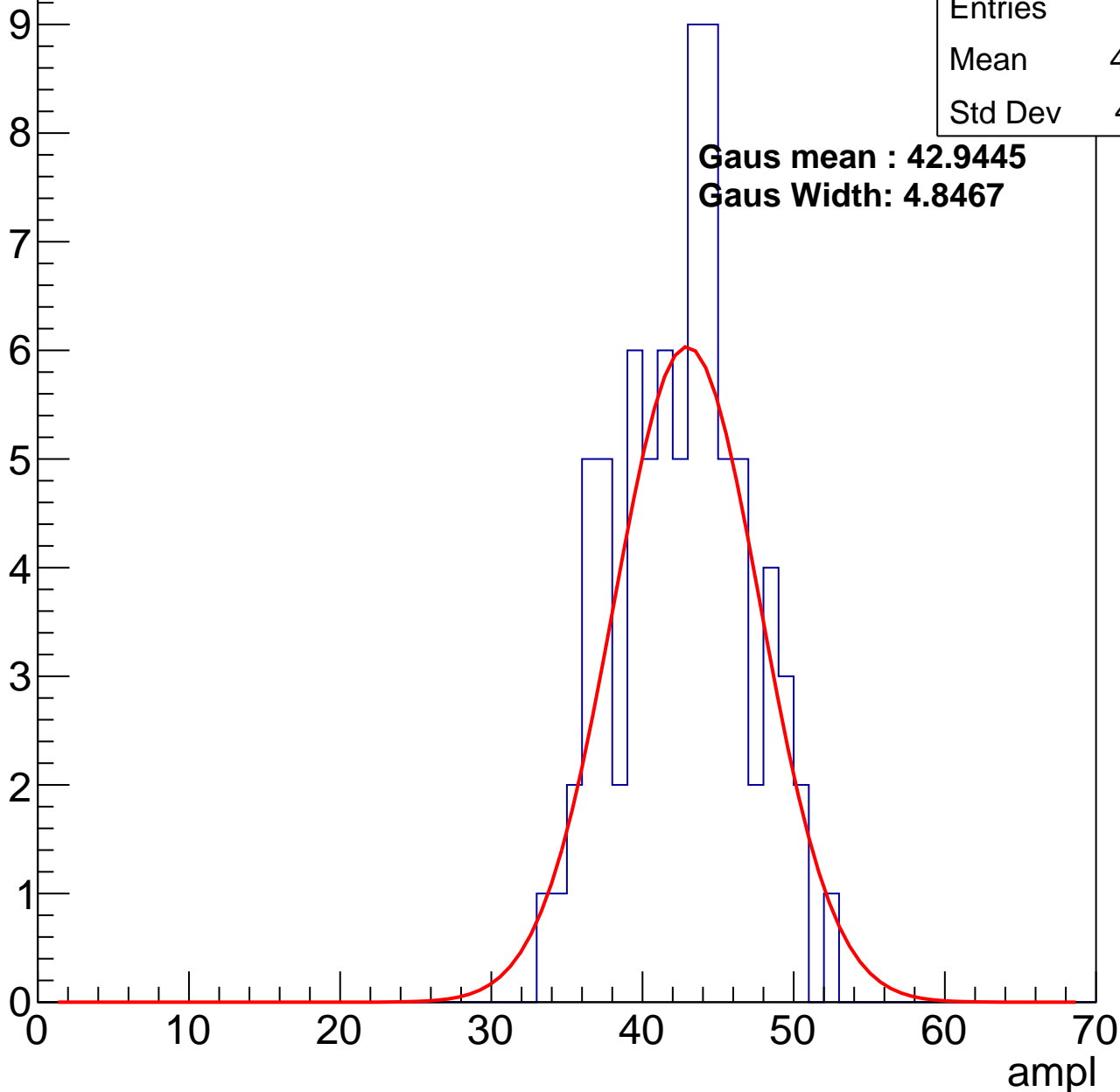
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.19
Std Dev	4.231

Gaus mean : 42.9445

Gaus Width: 4.8467

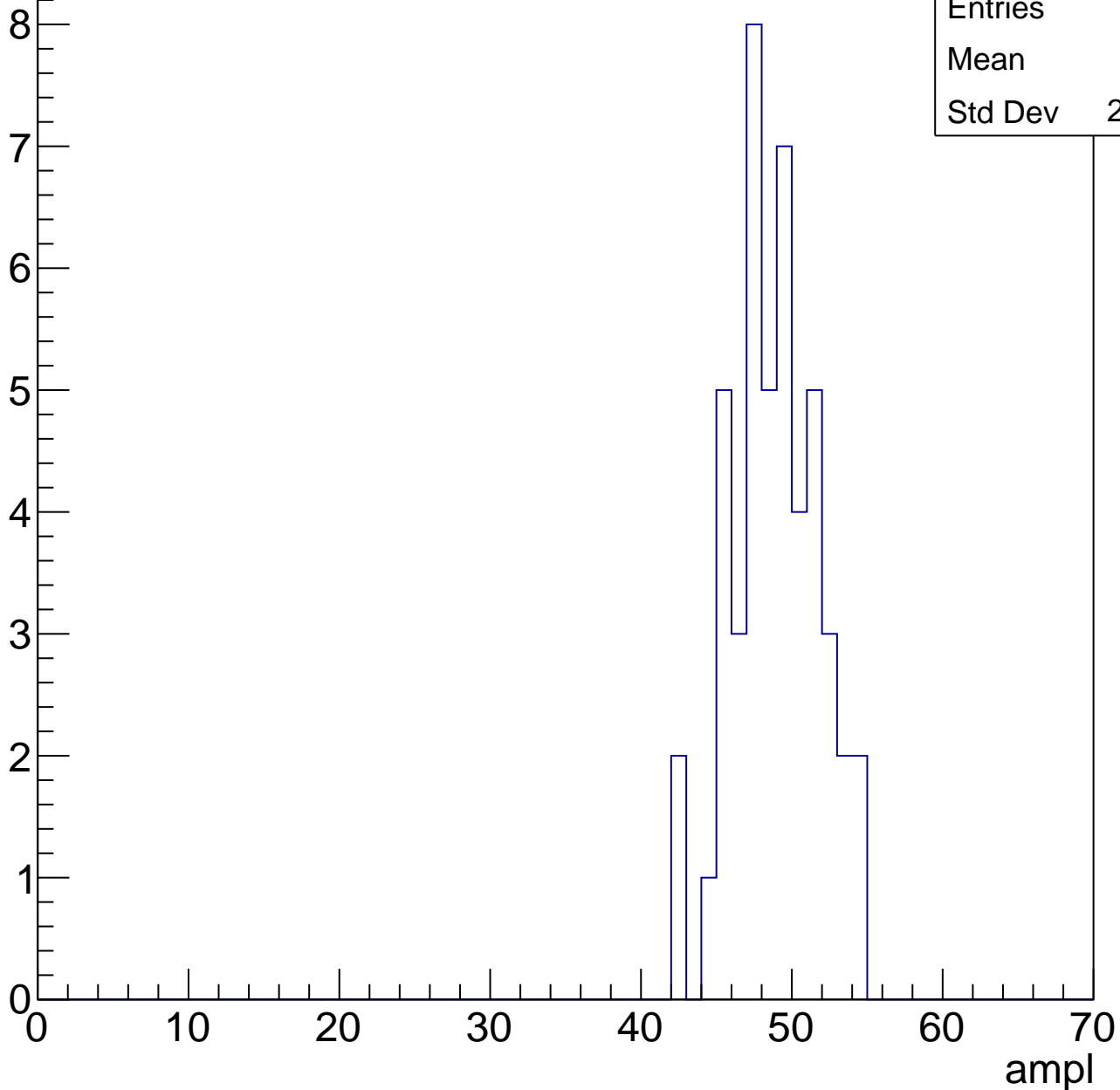


B1L101S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	48.4
Std Dev	2.856

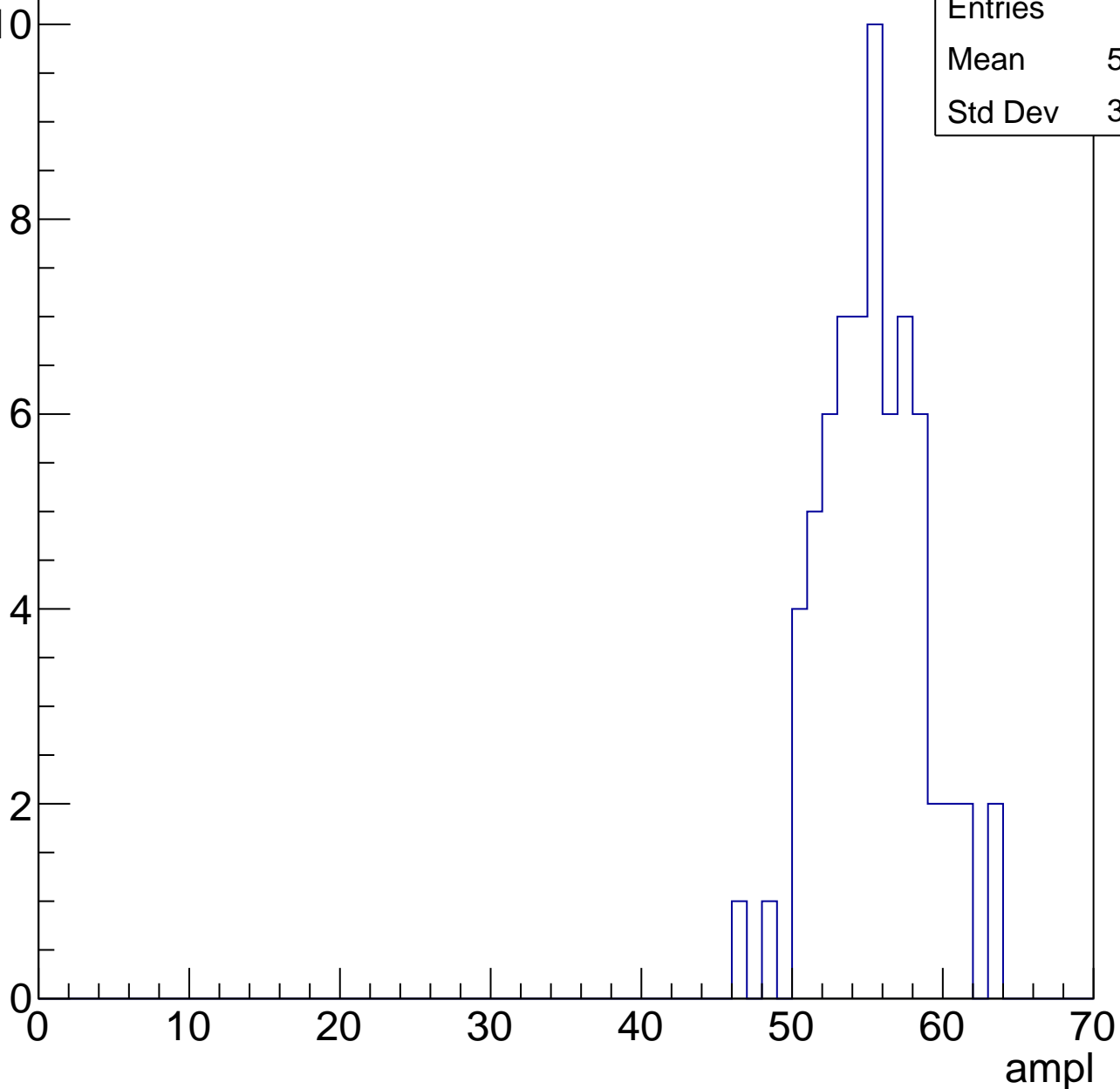


B1L101S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	54.84
Std Dev	3.359



B1L101S, U14-ch94, adc5

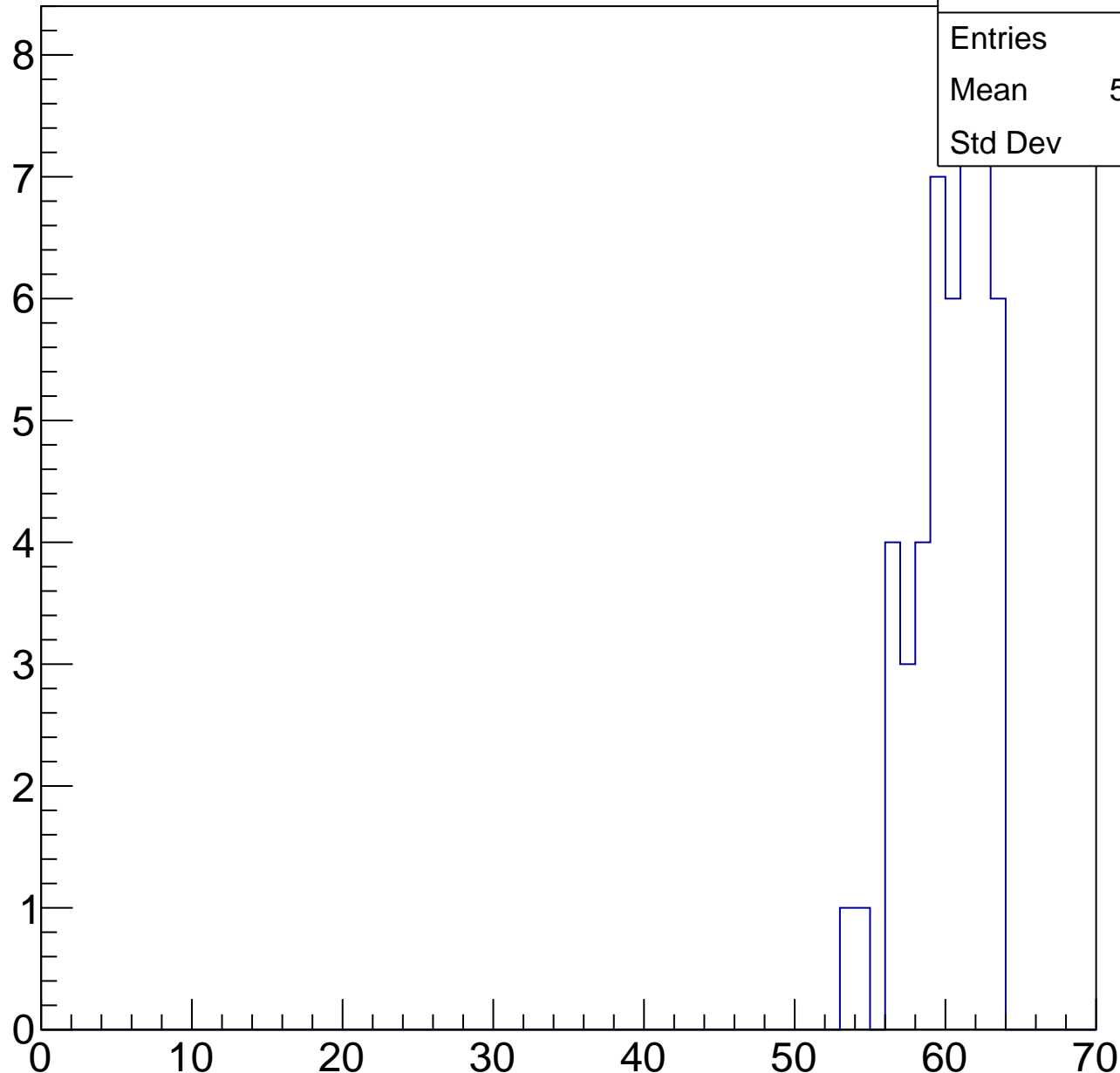
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.77
Std Dev	2.46

ampl



B1L101S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

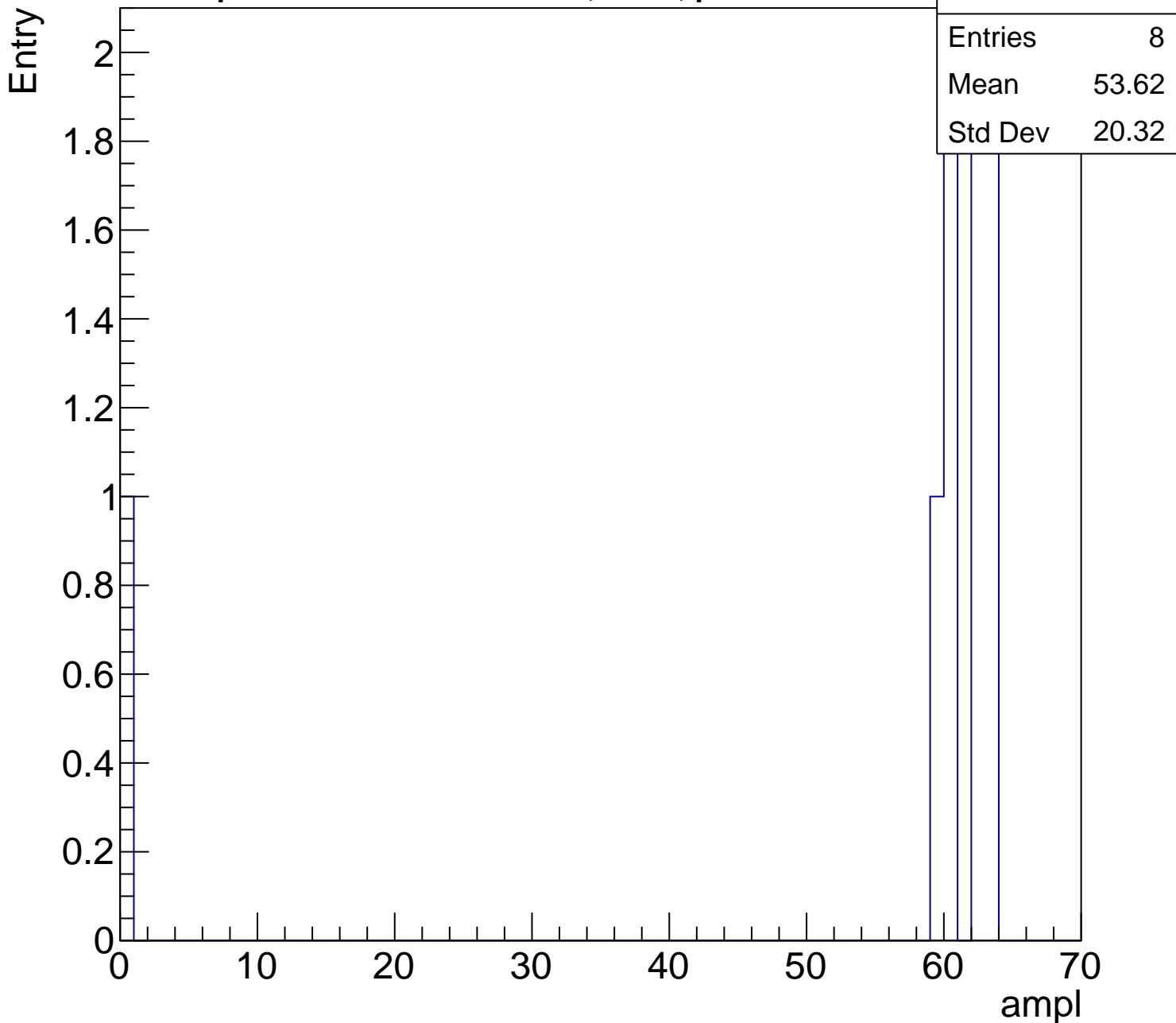
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.62
Std Dev	20.32

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch95, adc0

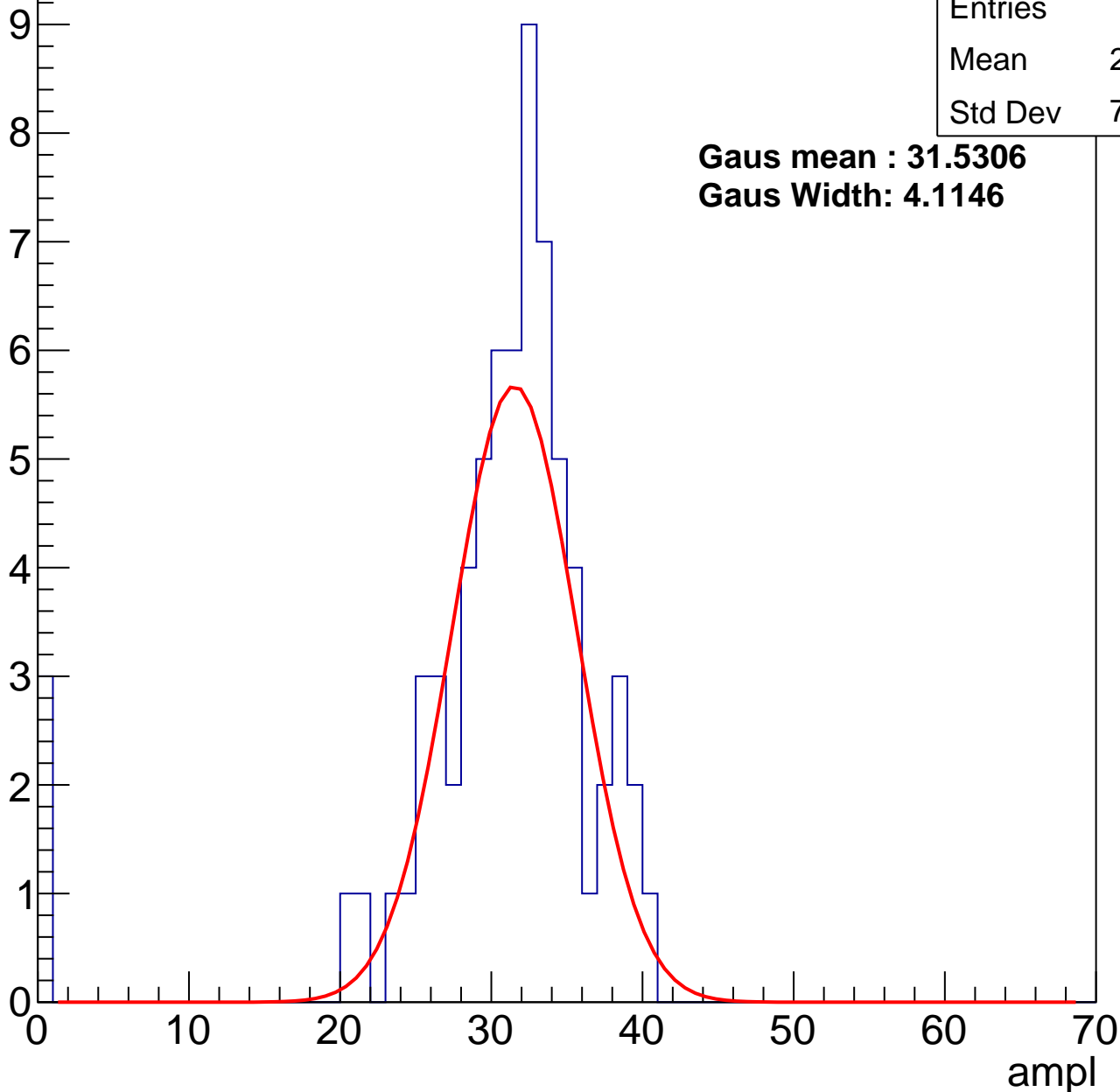
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.84
Std Dev	7.559

Gaus mean : 31.5306

Gaus Width: 4.1146



B1L101S, U14-ch95, adc1

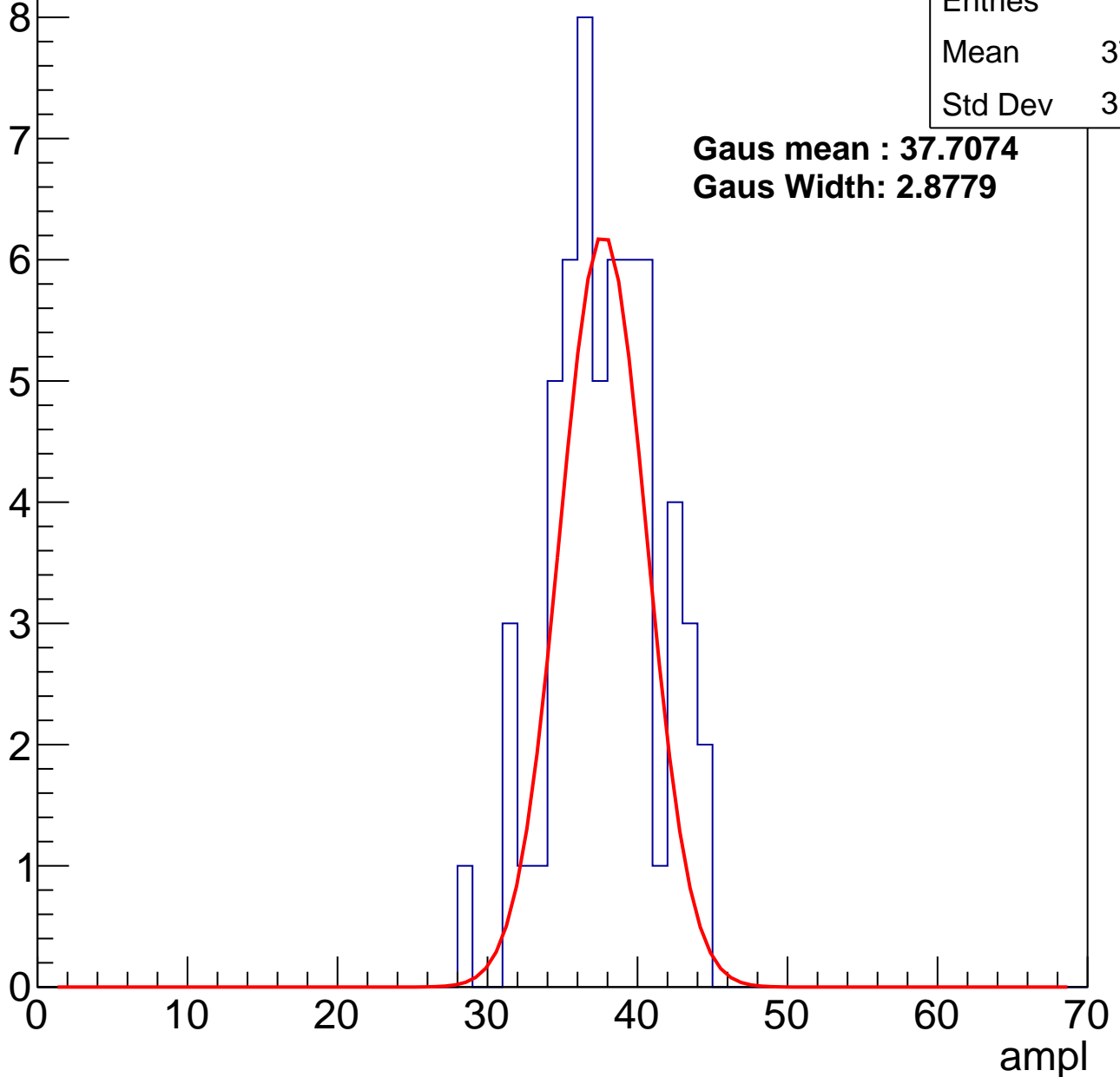
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.36
Std Dev	3.487

Gaus mean : 37.7074

Gaus Width: 2.8779



B1L101S, U14-ch95, adc2

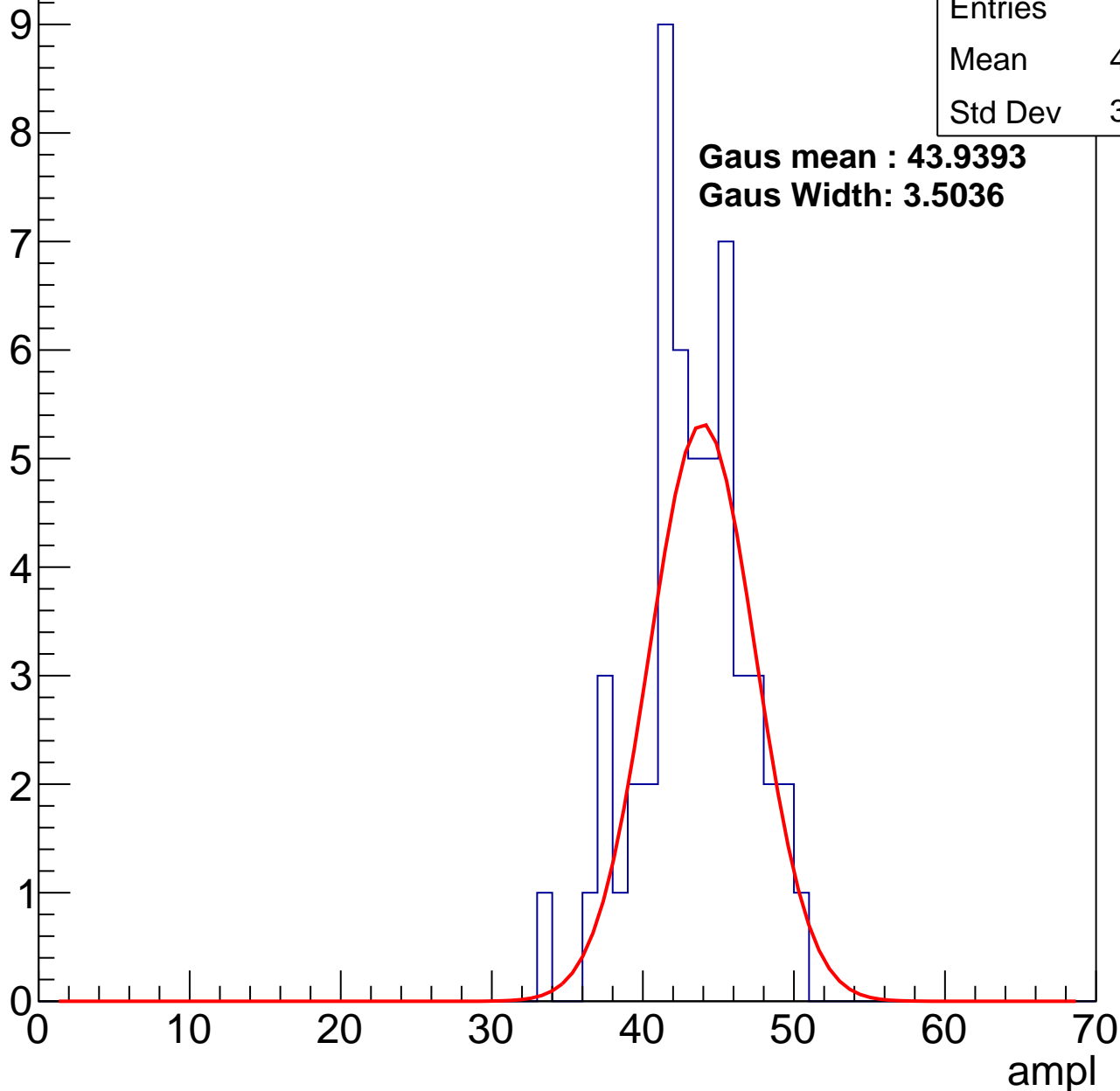
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.83
Std Dev	3.506

Gaus mean : 43.9393

Gaus Width: 3.5036

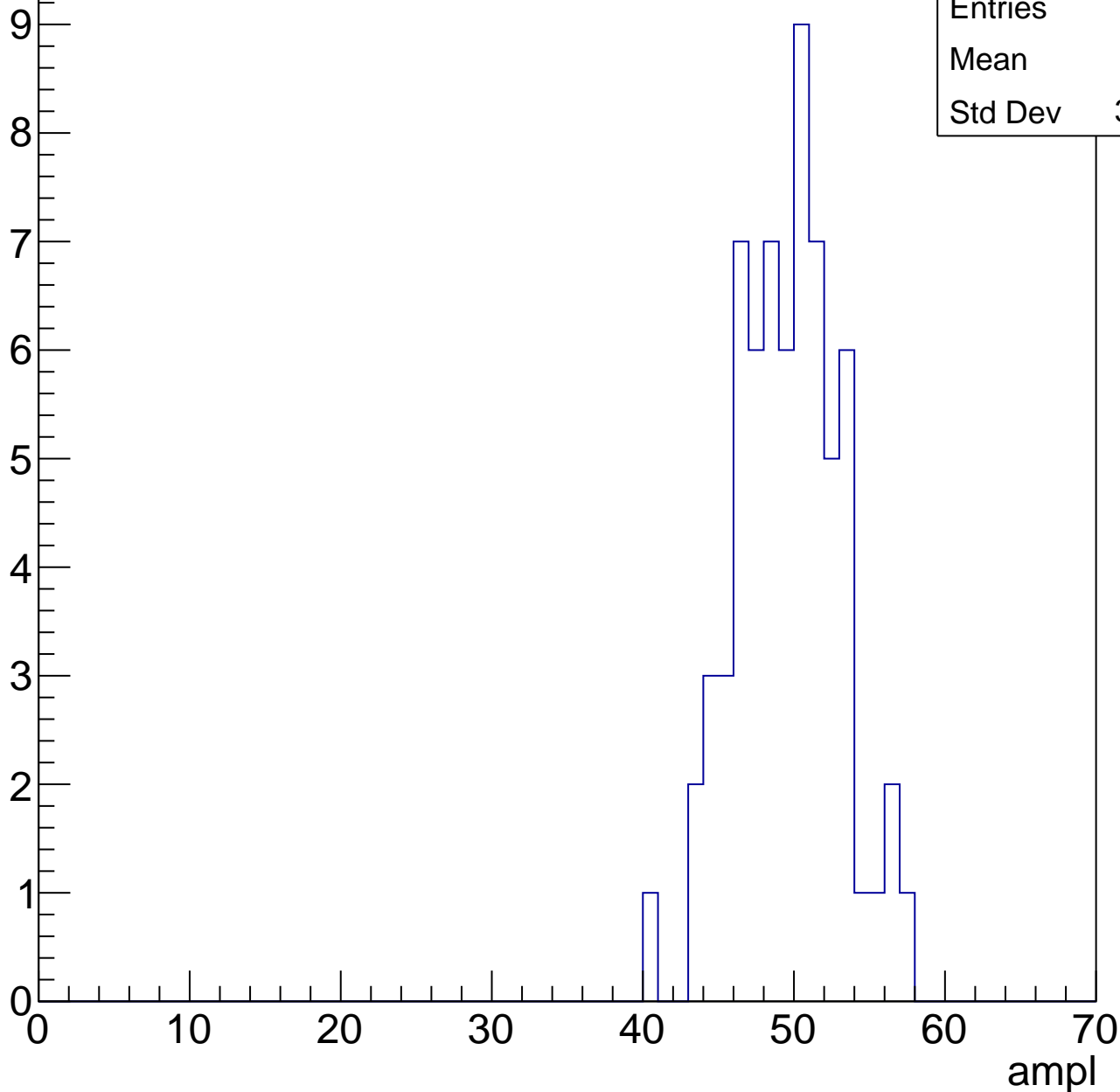


B1L101S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

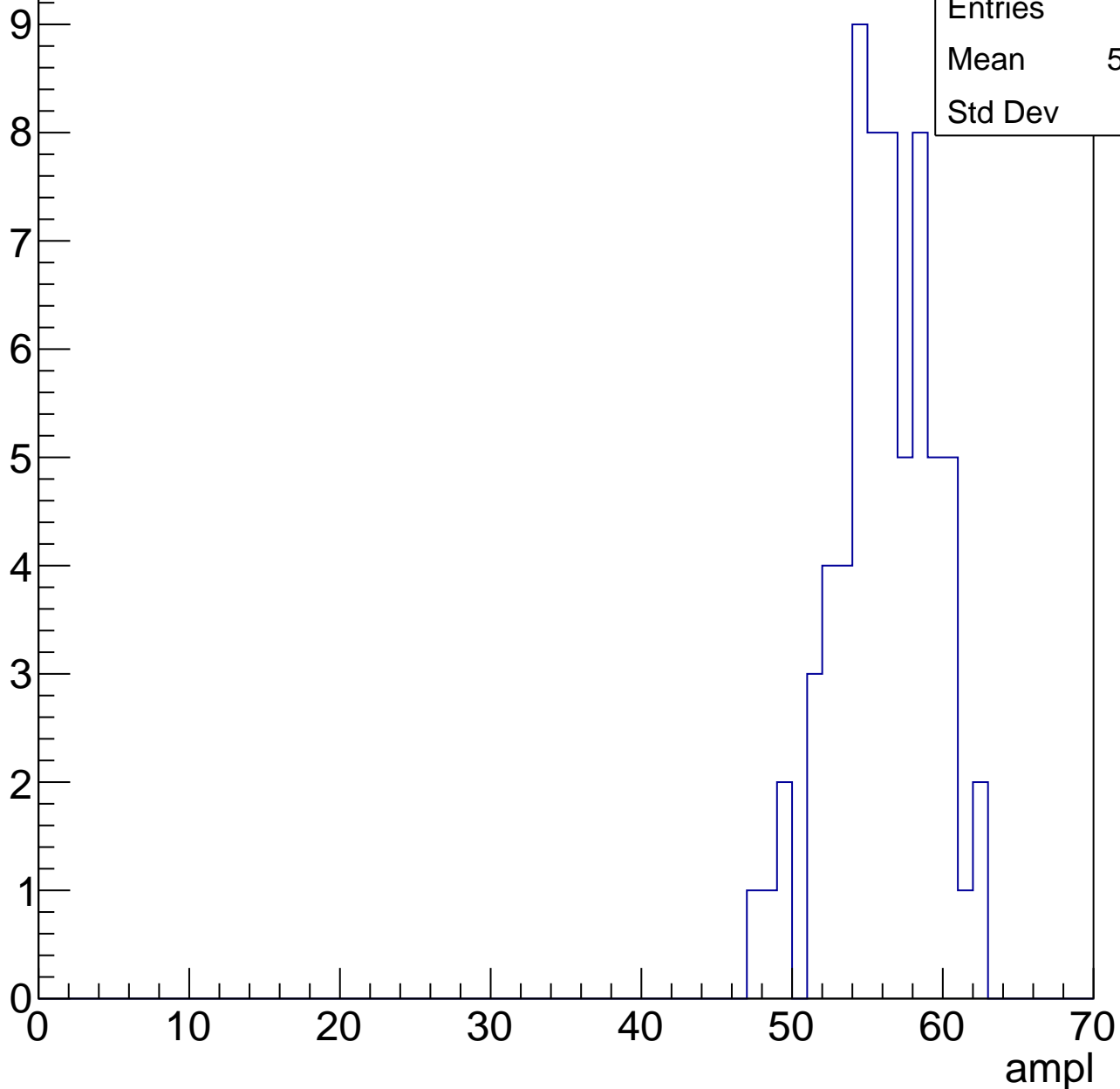
Entries	67
Mean	49.1
Std Dev	3.391



B1L101S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



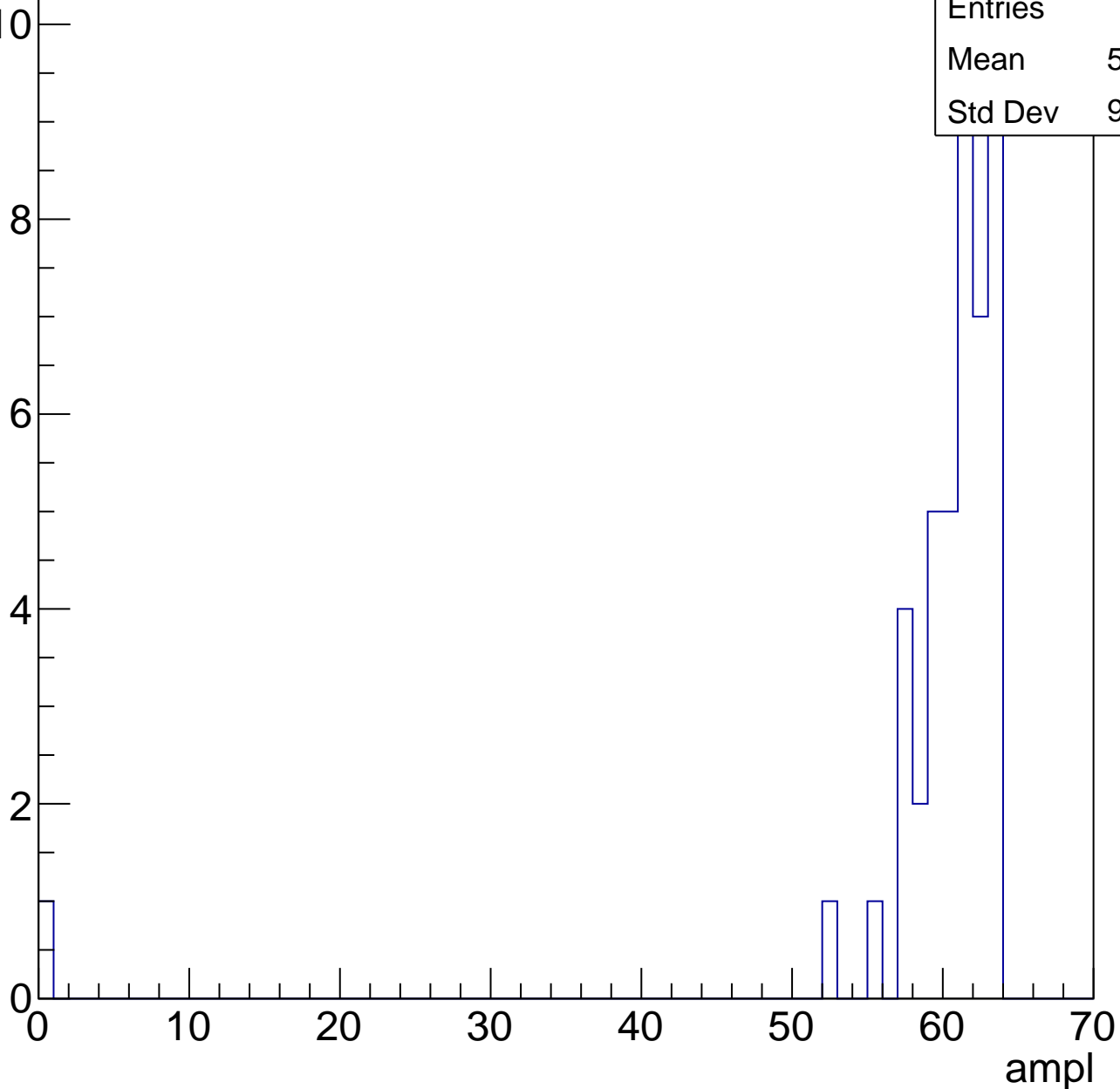
Entries	66
Mean	55.59
Std Dev	3.28

B1L101S, U14-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.09
Std Dev	9.223



B1L101S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch96, adc0

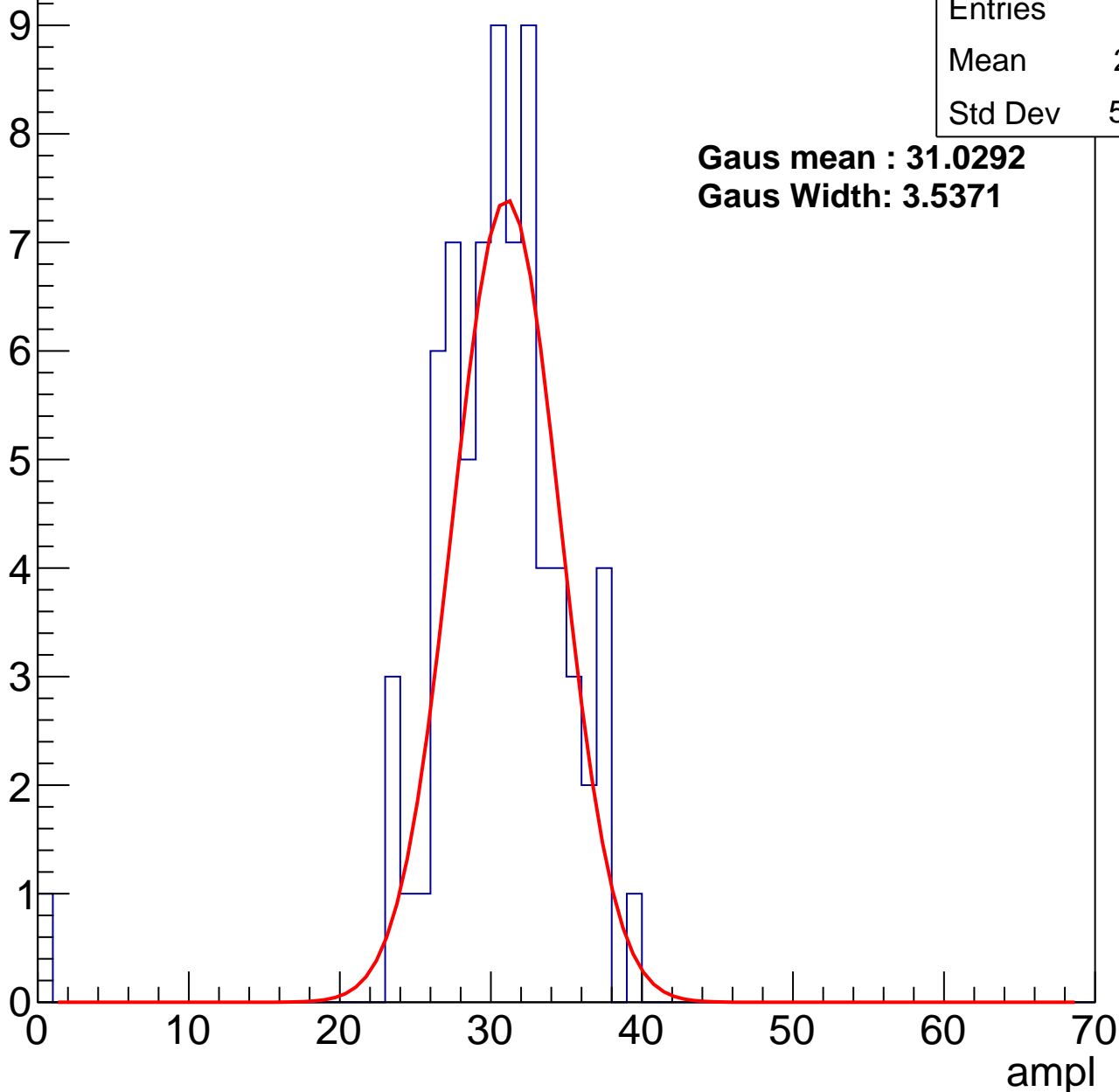
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.91
Std Dev	5.014

Gaus mean : 31.0292

Gaus Width: 3.5371



B1L101S, U14-ch96, adc1

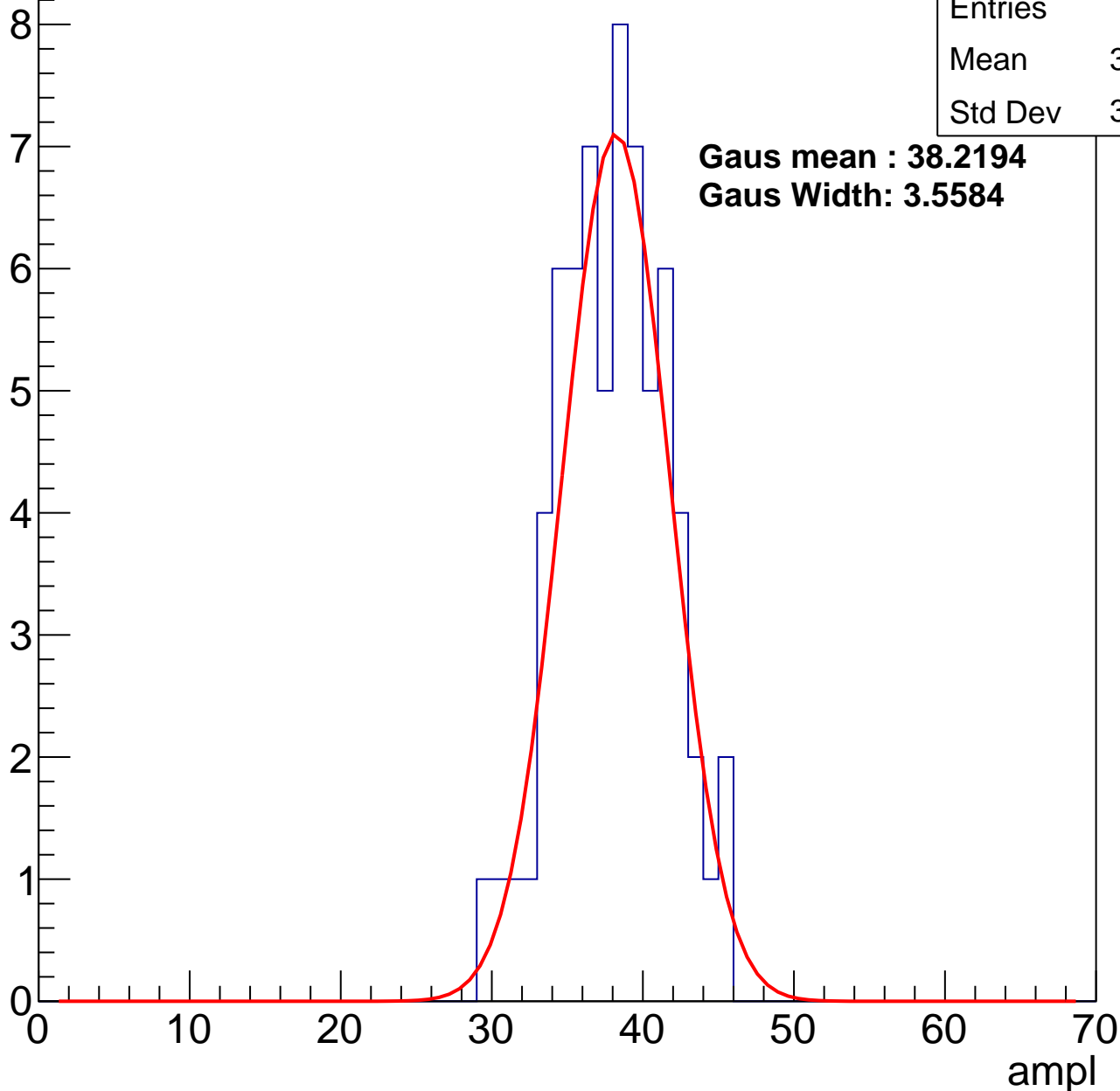
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.55
Std Dev	3.517

Gaus mean : 38.2194

Gaus Width: 3.5584



B1L101S, U14-ch96, adc2

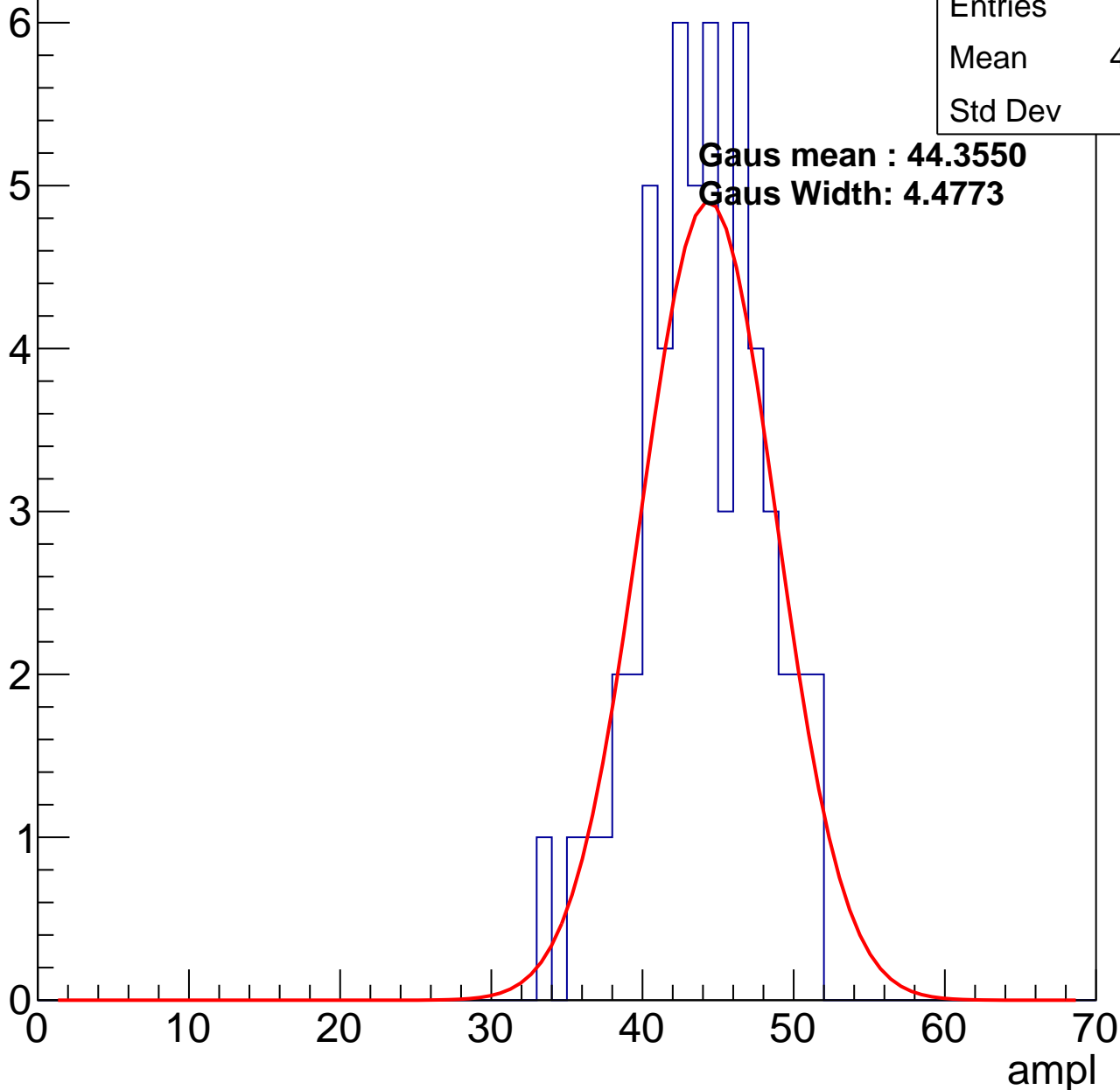
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.45
Std Dev	4

Gaus mean : 44.3550

Gaus Width: 4.4773

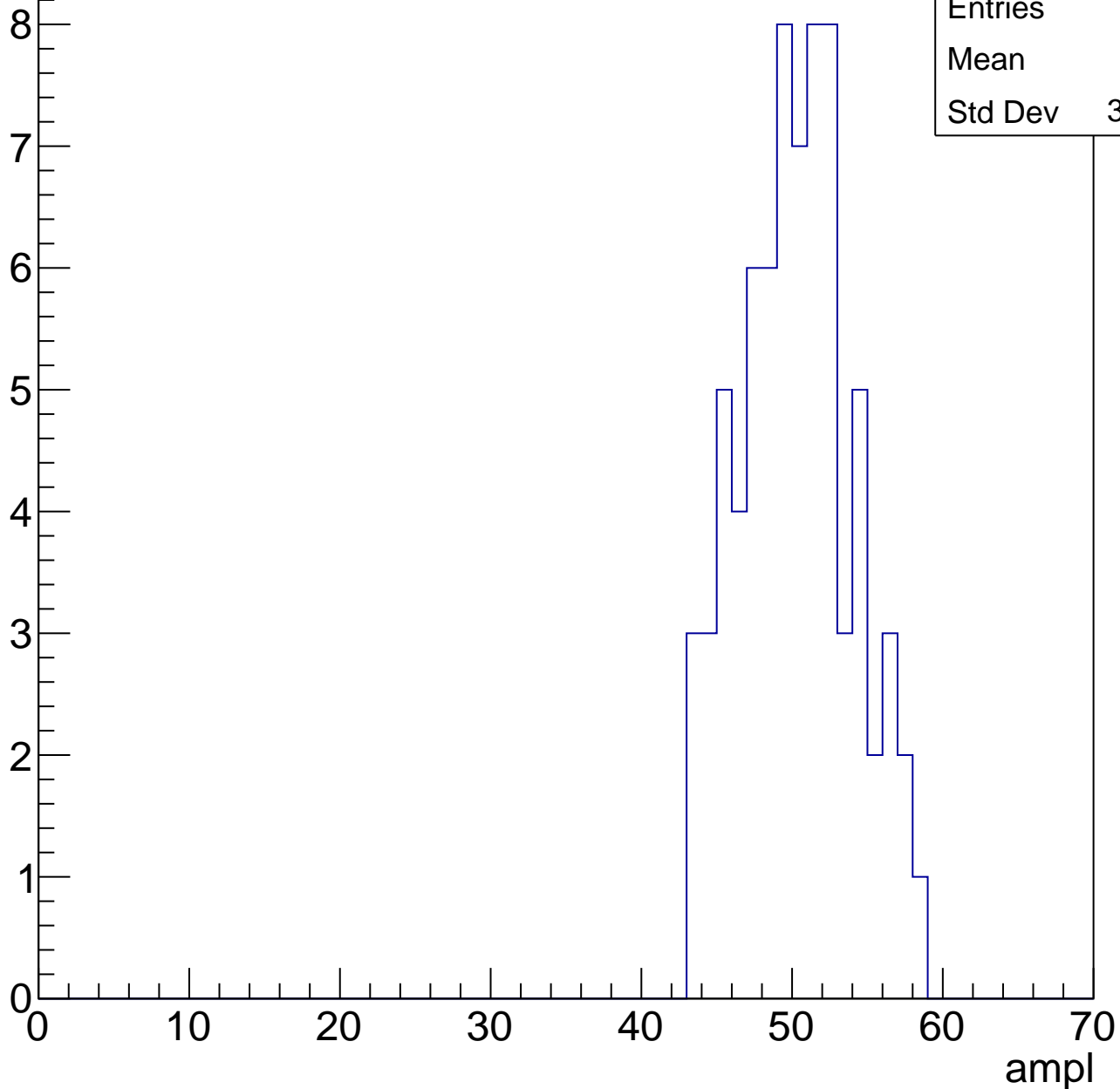


B1L101S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	49.8
Std Dev	3.665

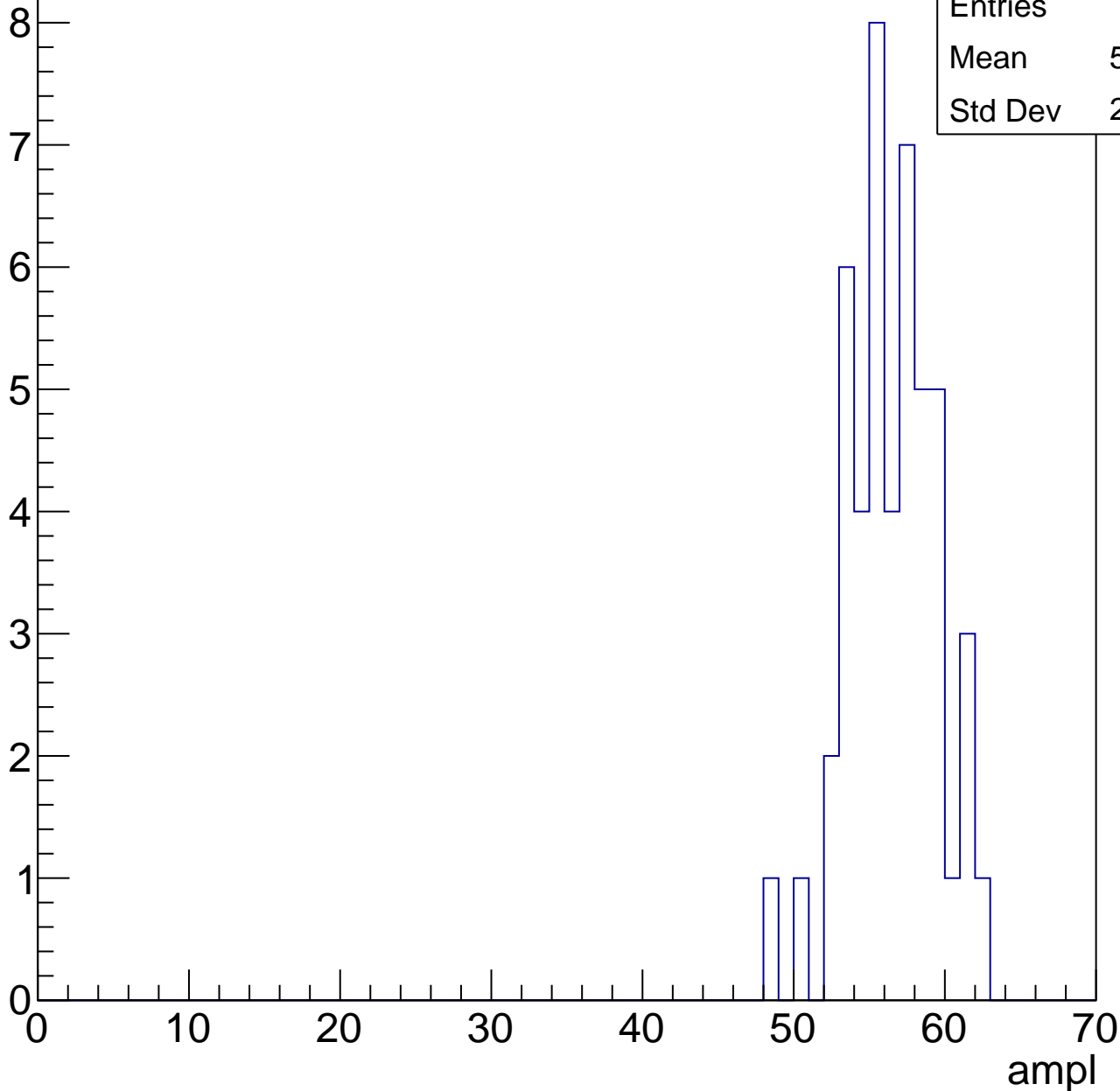


B1L101S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

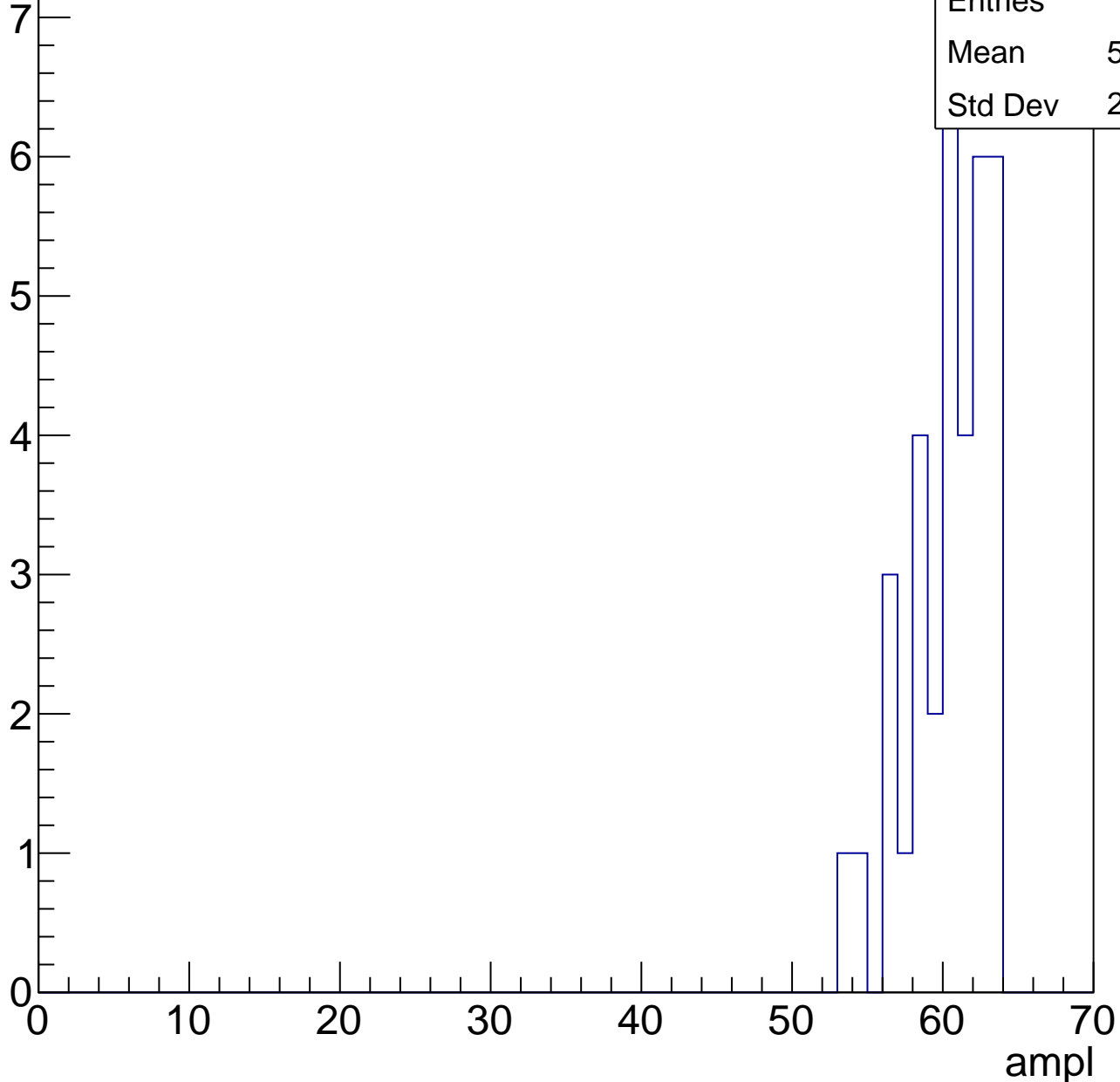
Entries	48
Mean	56.02
Std Dev	2.919



B1L101S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

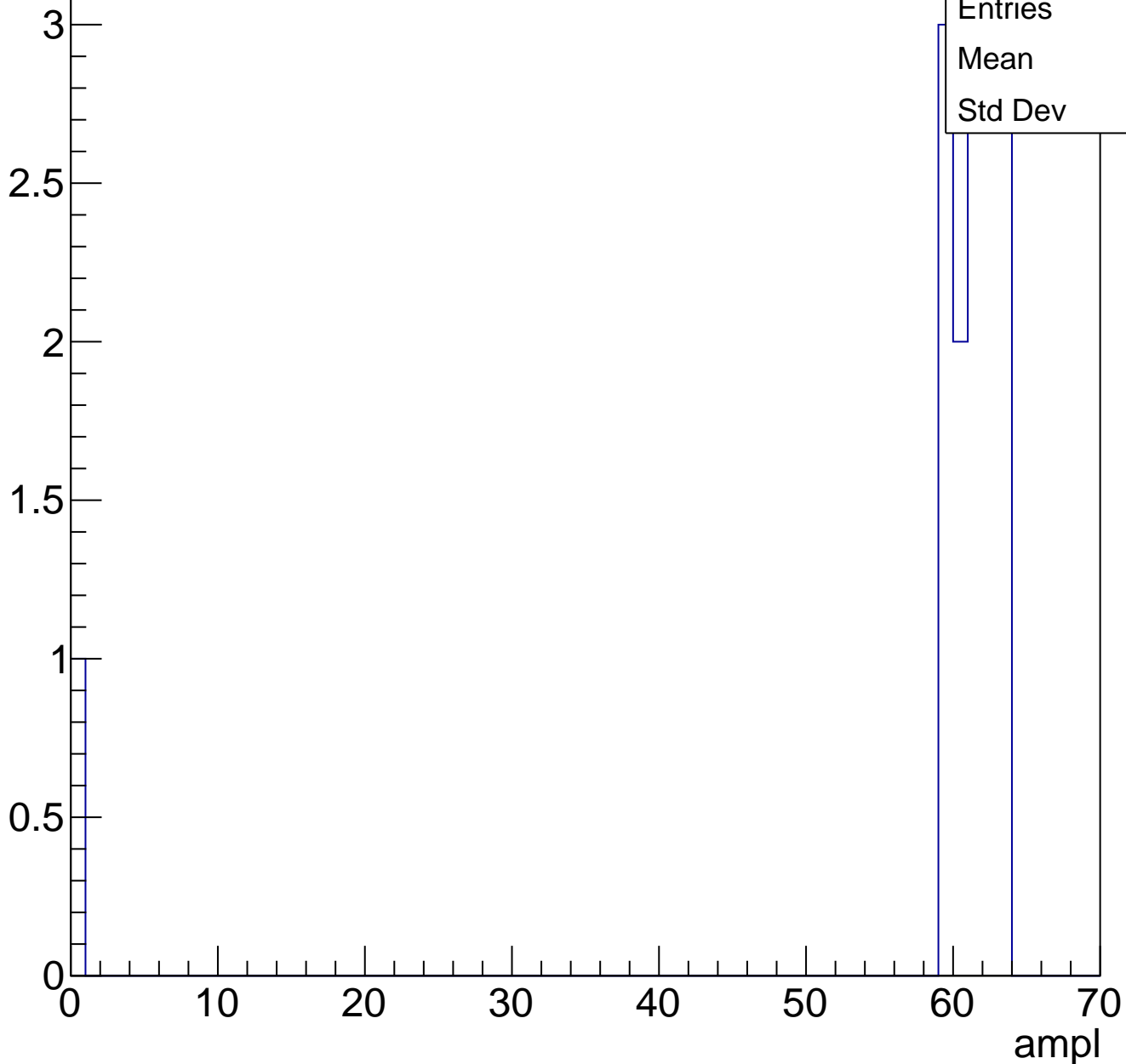
Entry



B1L101S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch97, adc0

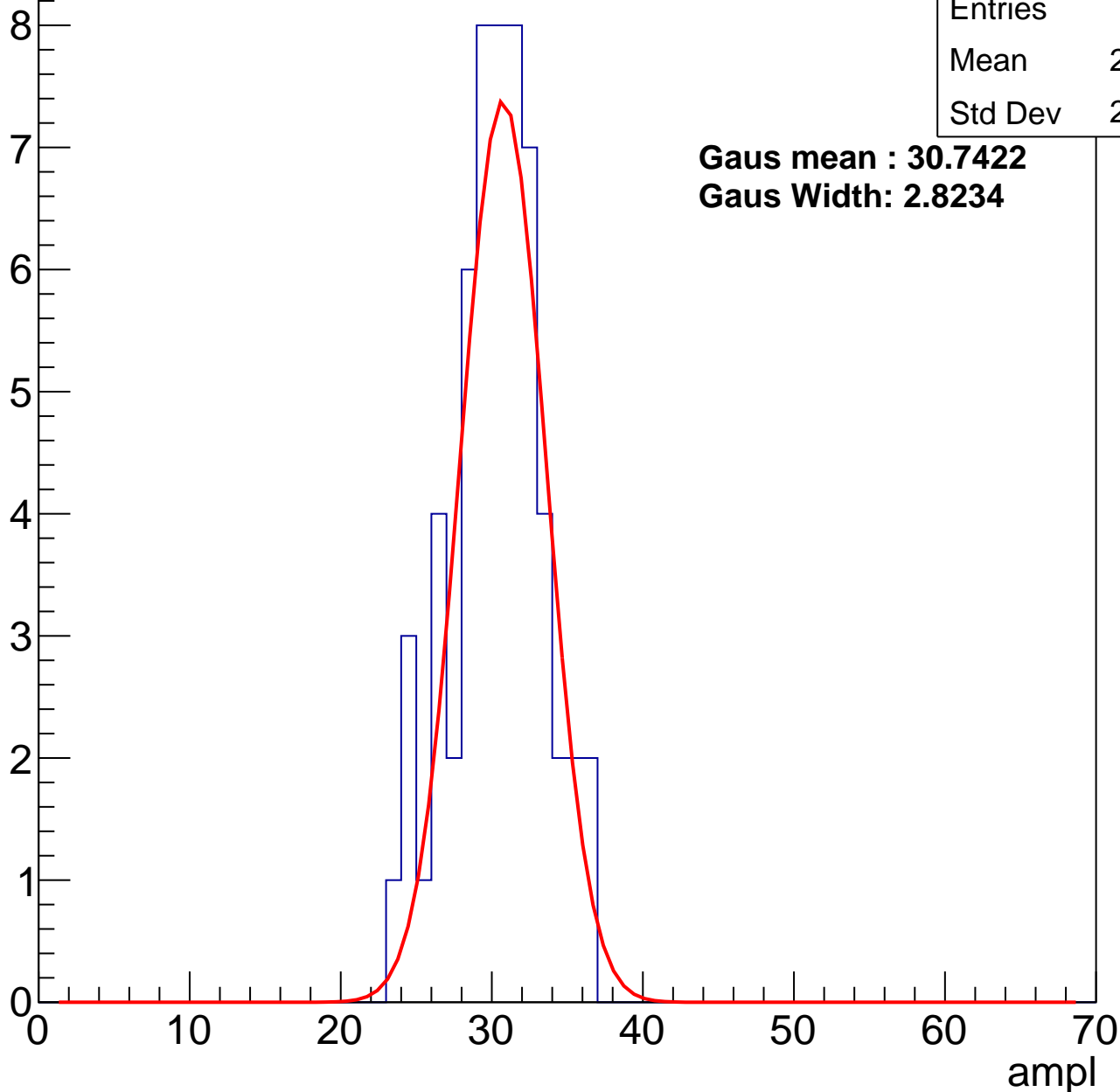
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.86
Std Dev	2.997

Gaus mean : 30.7422

Gaus Width: 2.8234



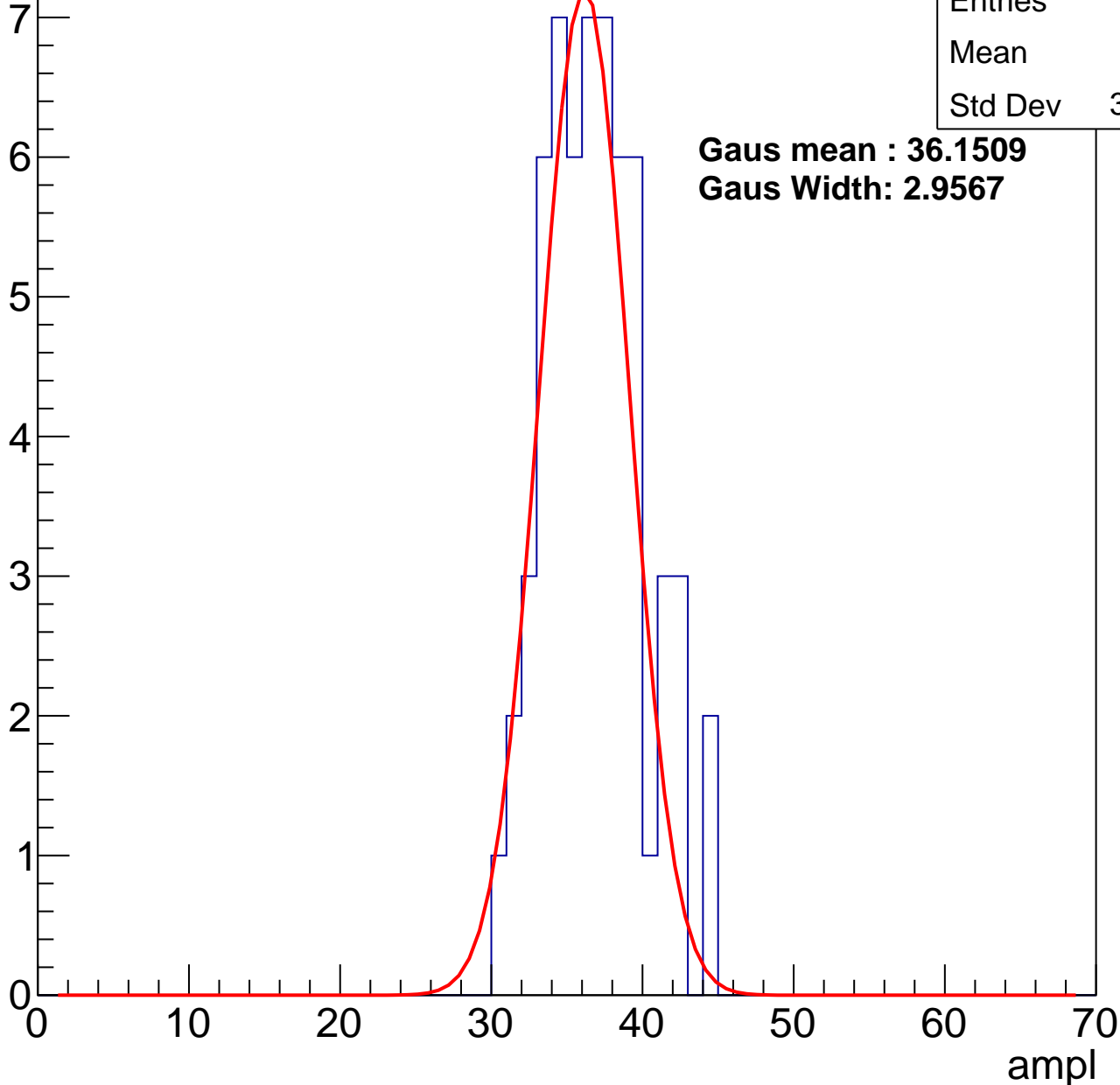
B1L101S, U14-ch97, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.4
Std Dev	3.226

Gaus mean : 36.1509
Gaus Width: 2.9567



B1L101S, U14-ch97, adc2

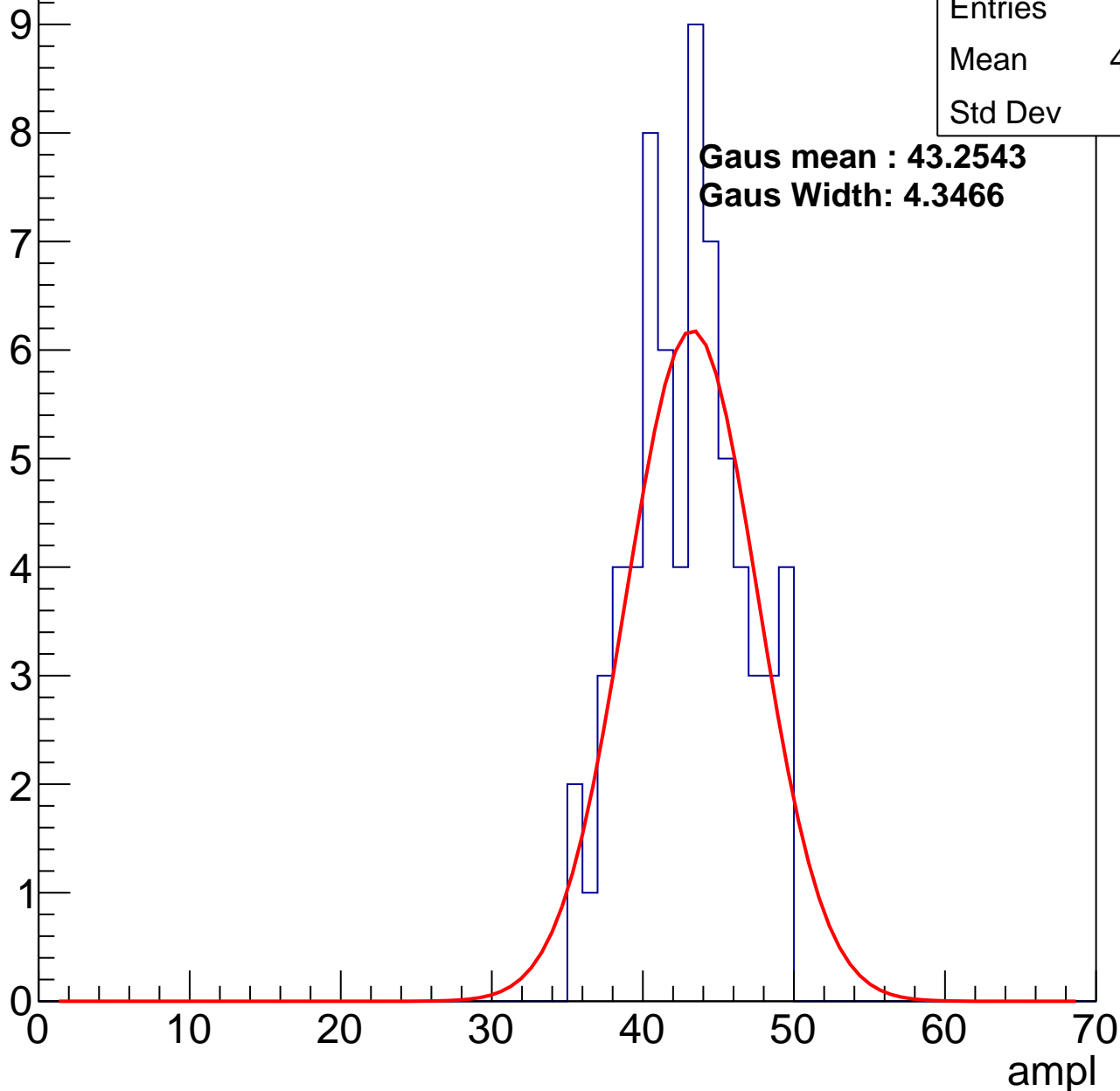
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.45
Std Dev	3.58

Gaus mean : 43.2543

Gaus Width: 4.3466

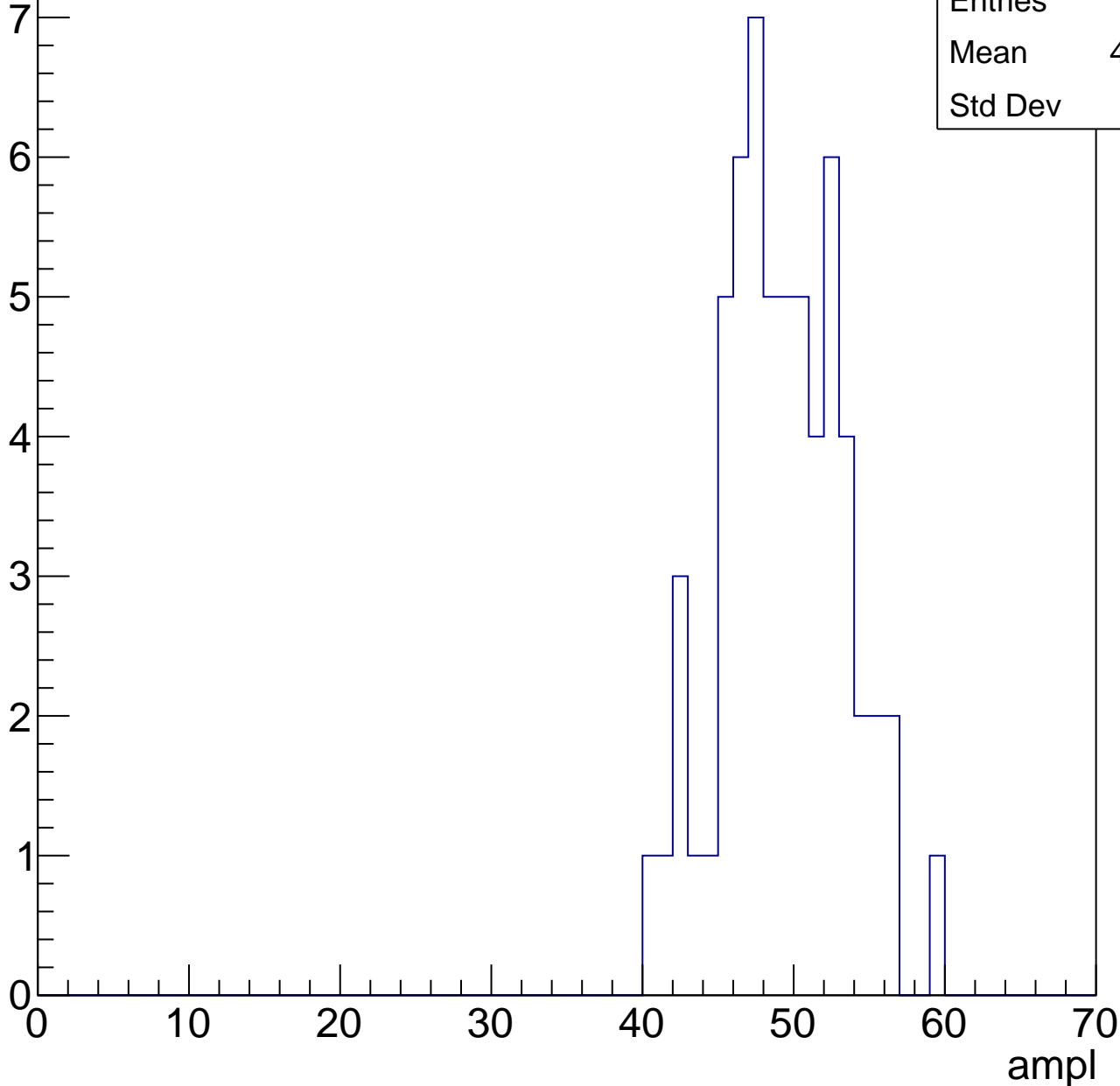


B1L101S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.79
Std Dev	4

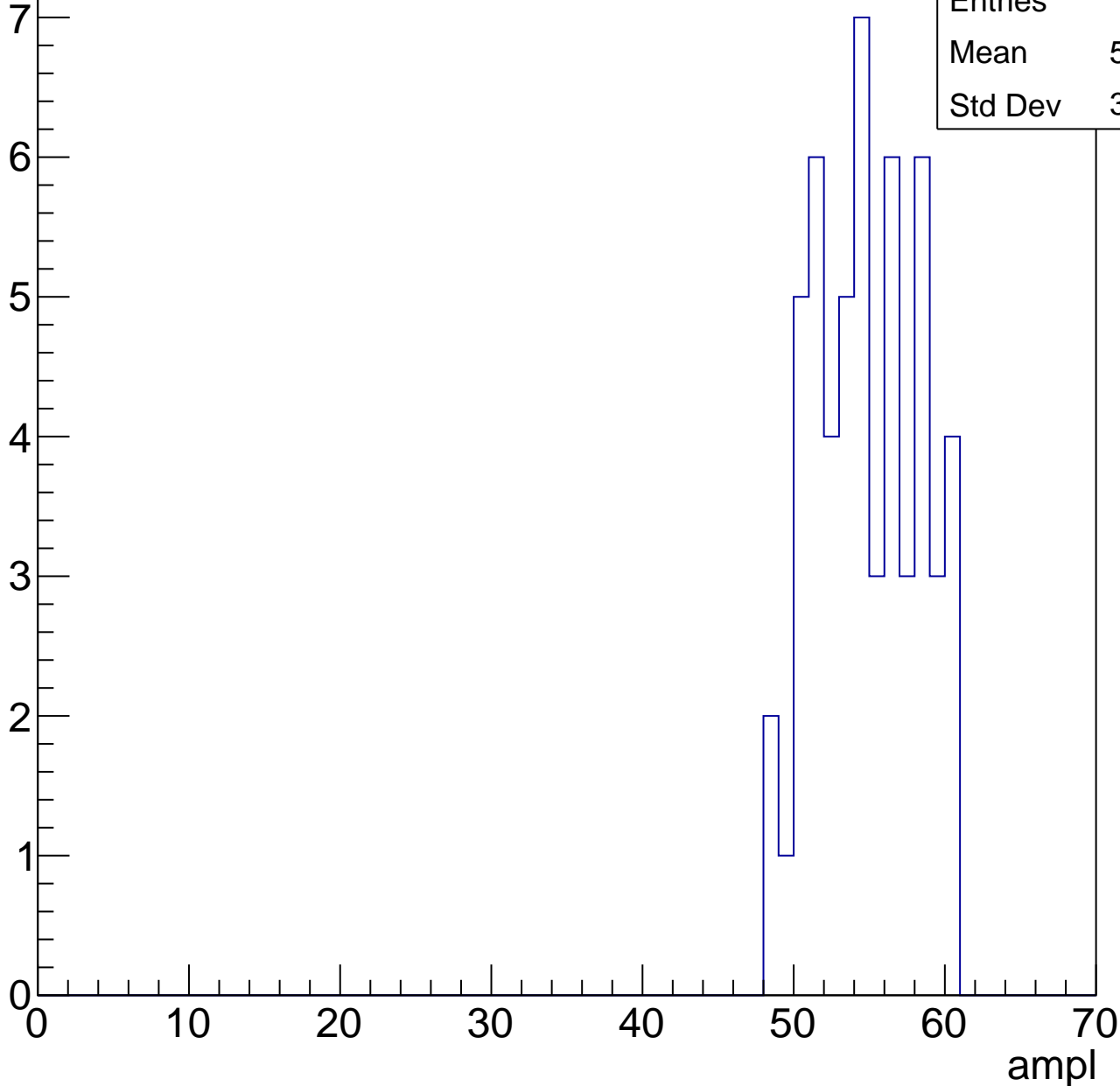


B1L101S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.35
Std Dev	3.342

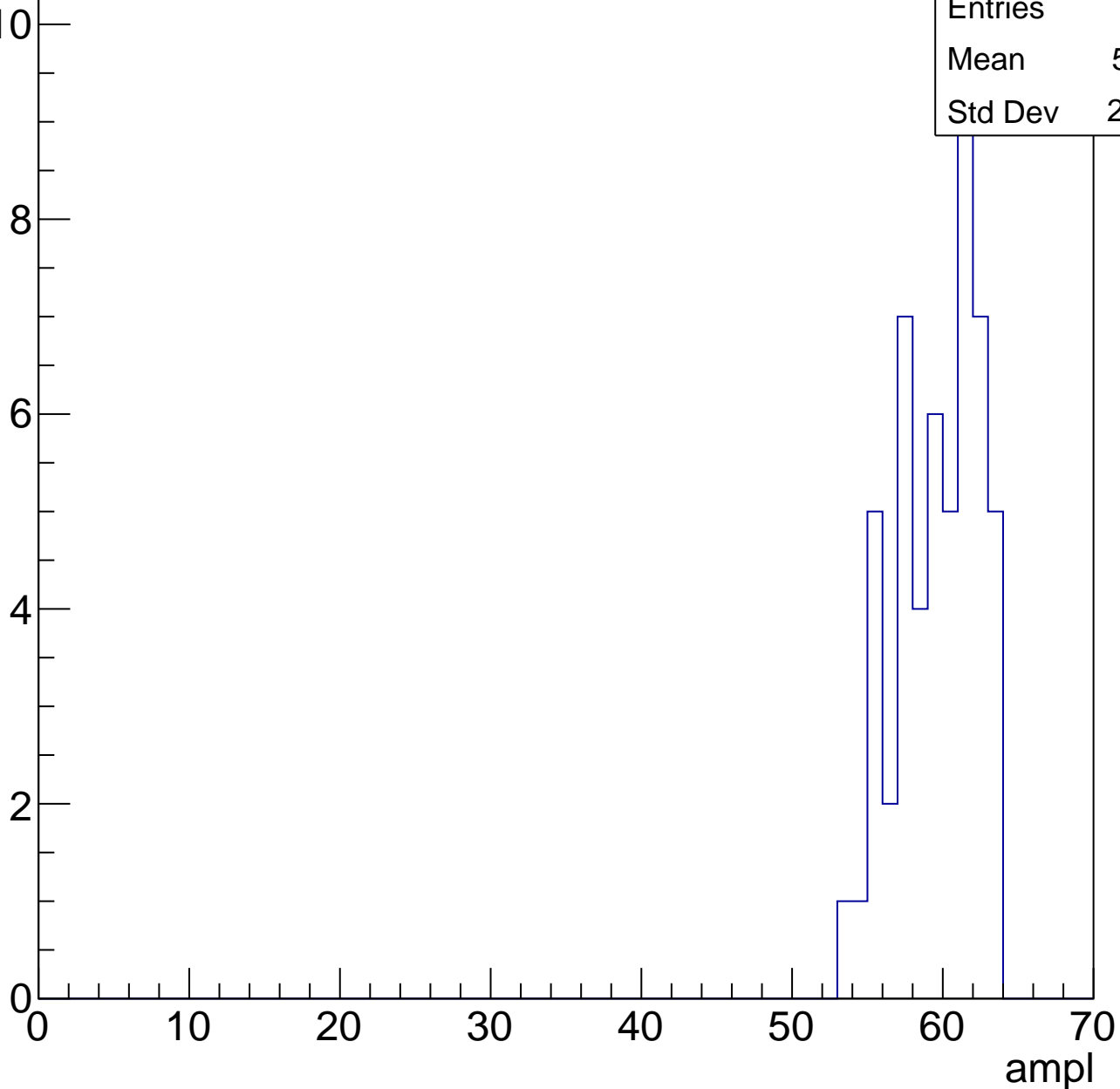


B1L101S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	59.21
Std Dev	2.666

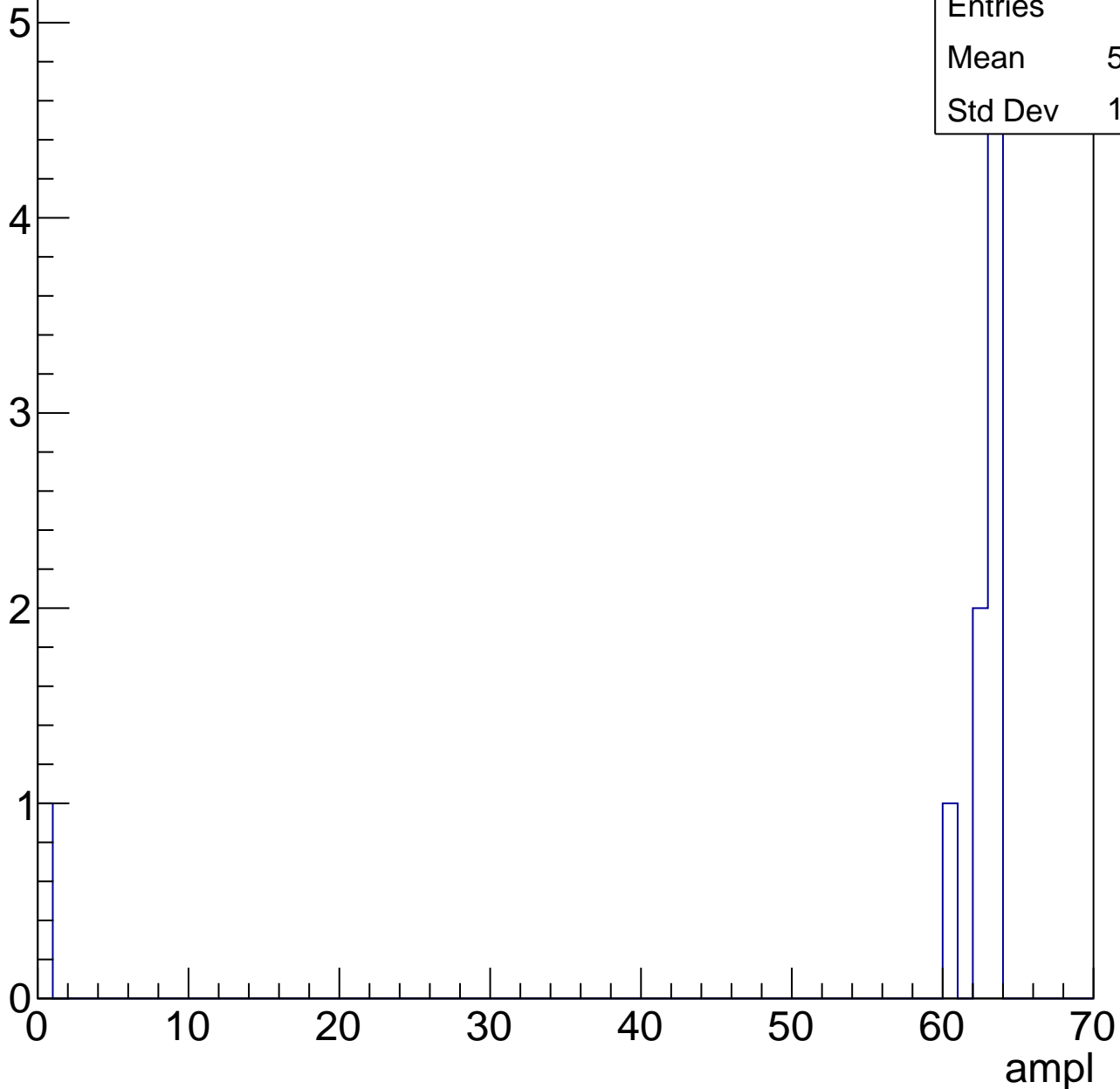


B1L101S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

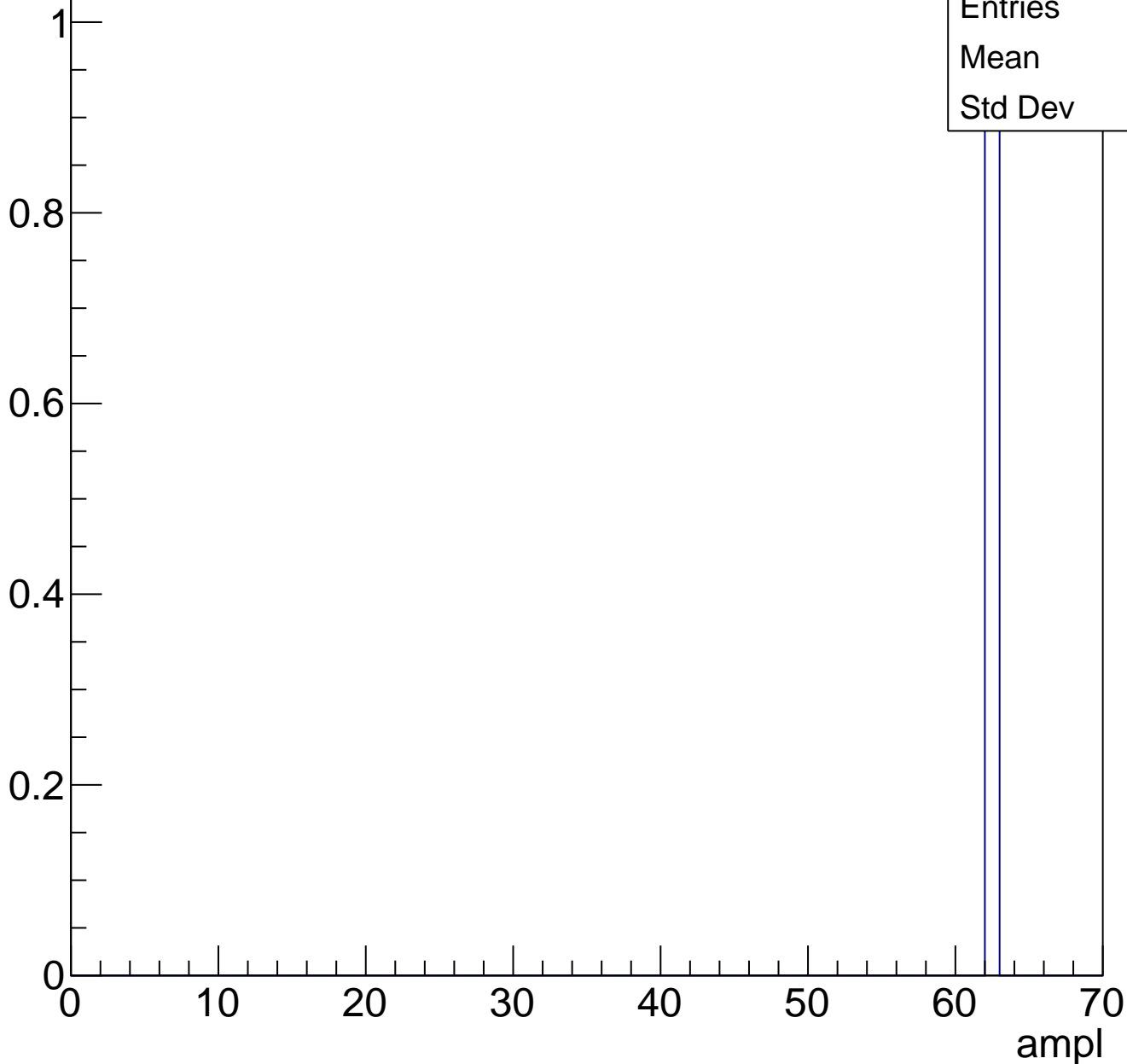
Entries	9
Mean	55.44
Std Dev	19.62



B1L101S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch98, adc0

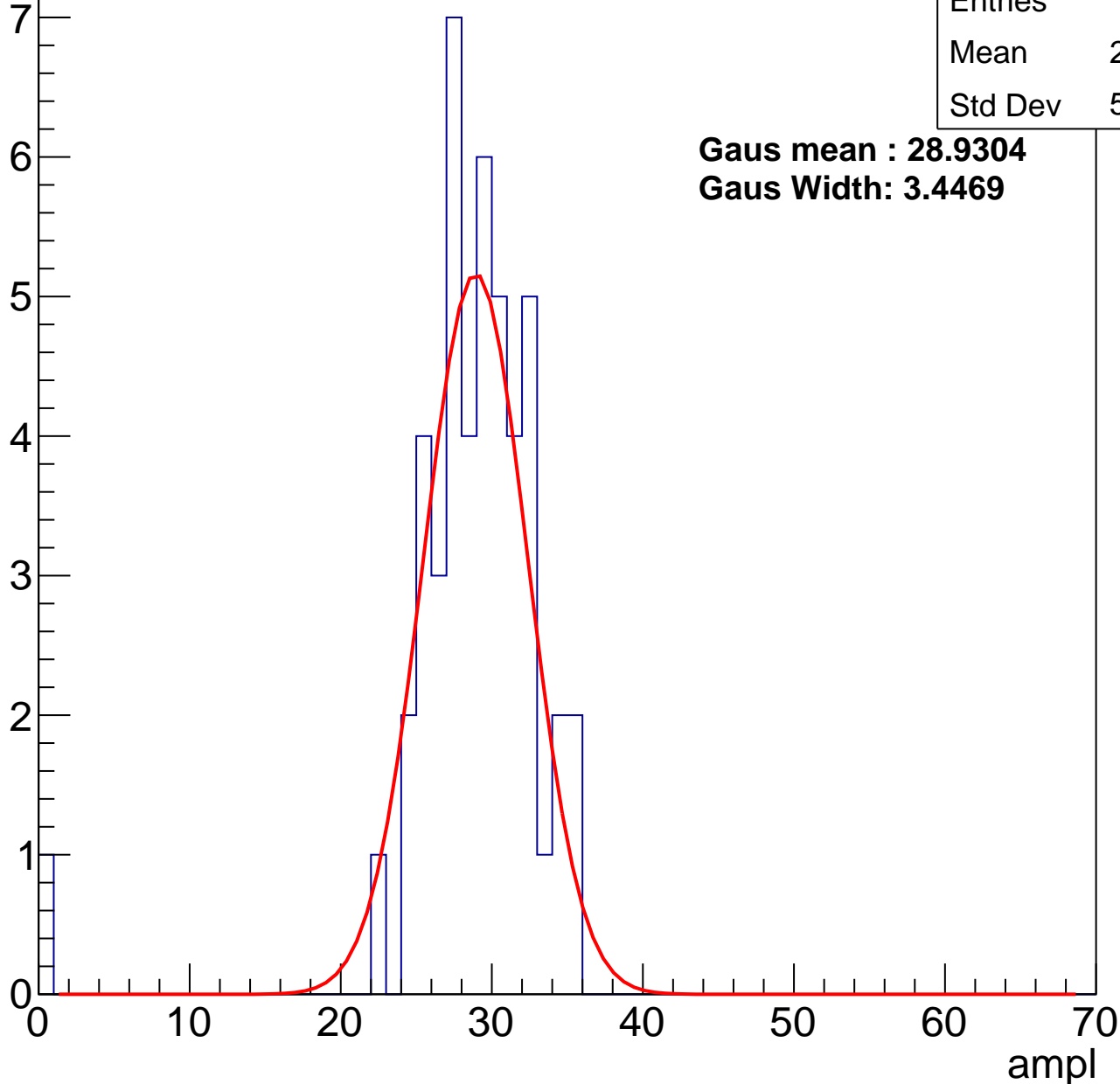
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	28.26
Std Dev	5.138

Gaus mean : 28.9304

Gaus Width: 3.4469



B1L101S, U14-ch98, adc1

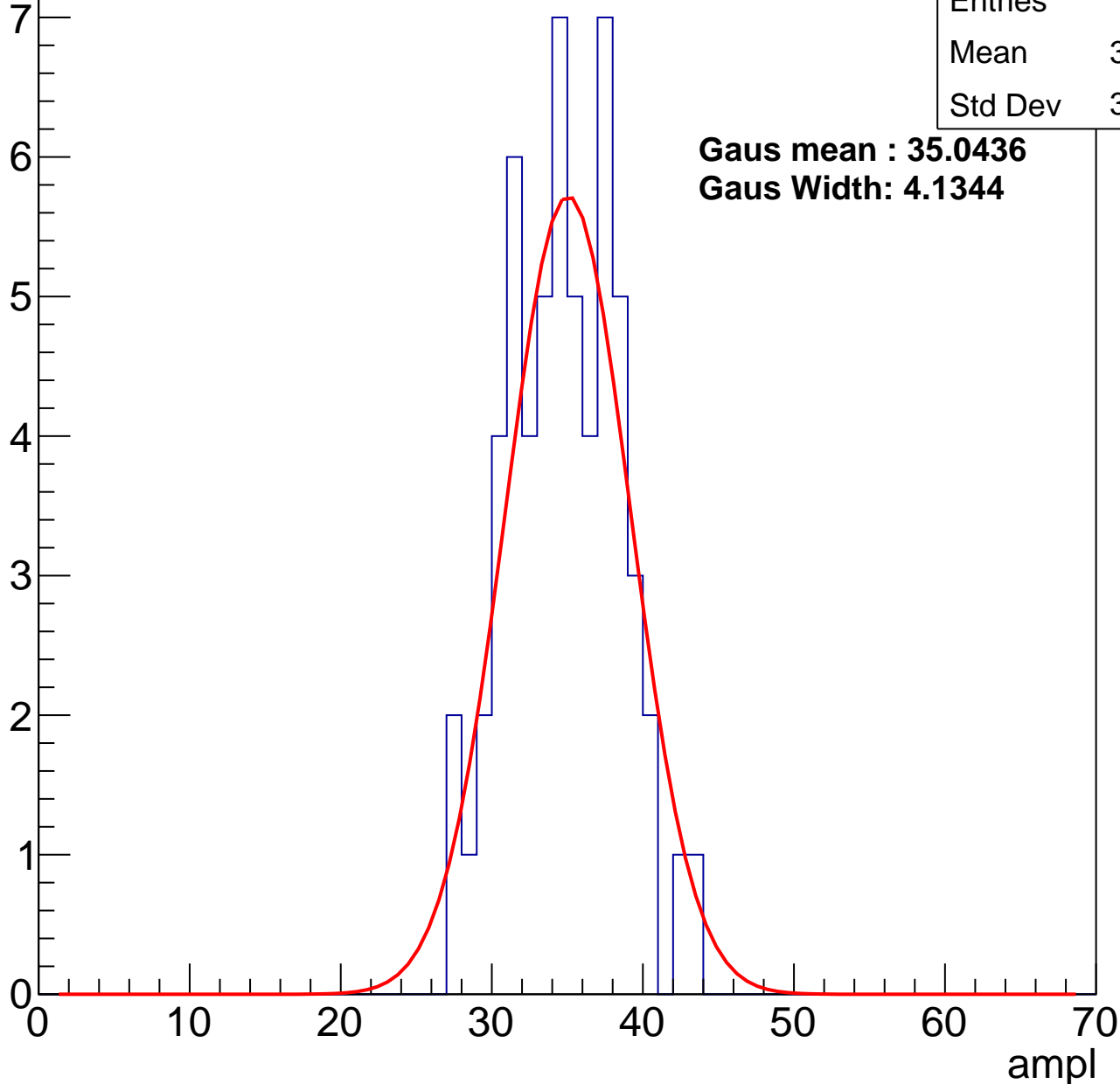
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.36
Std Dev	3.616

Gaus mean : 35.0436

Gaus Width: 4.1344



B1L101S, U14-ch98, adc2

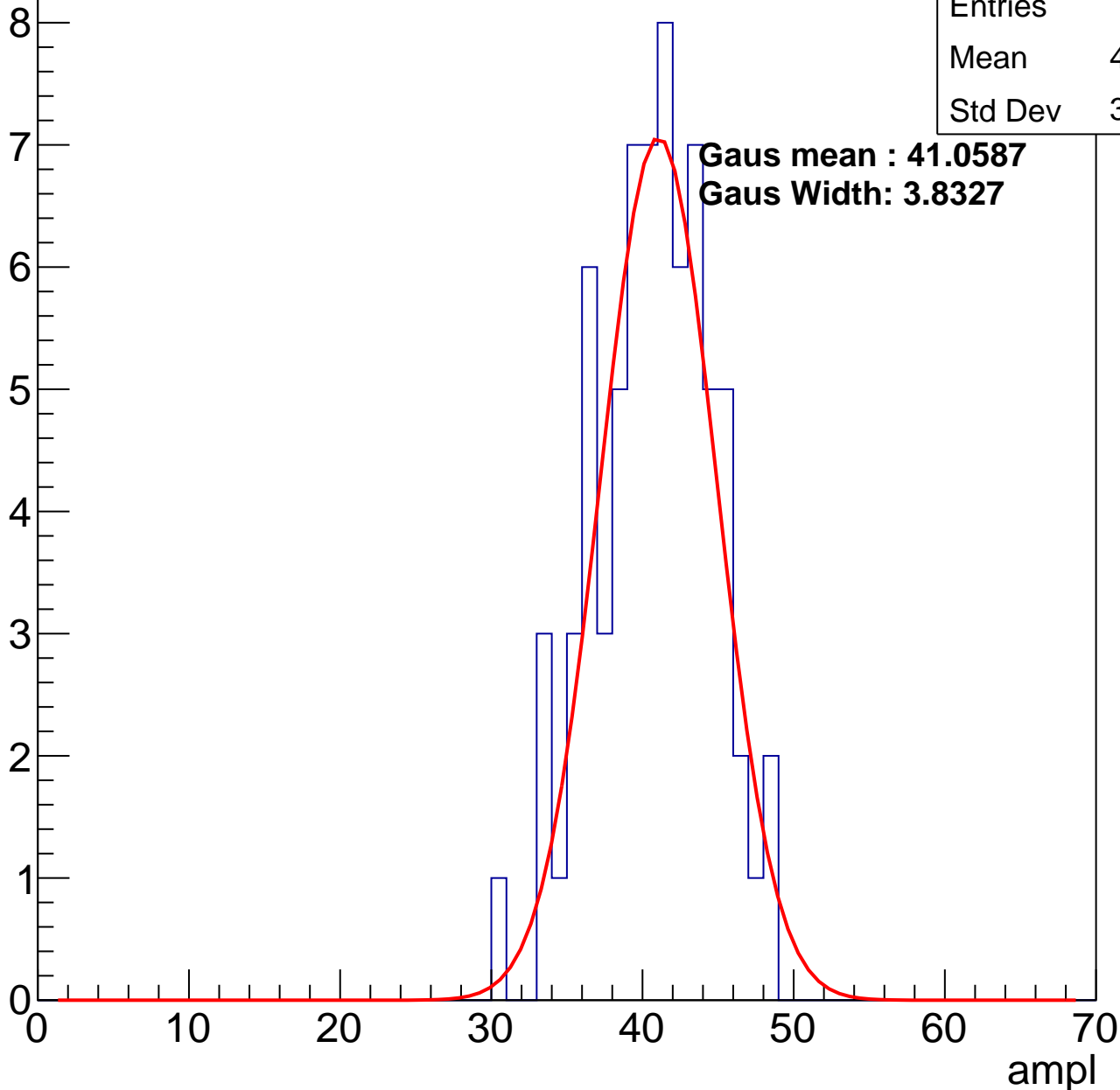
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	40.26
Std Dev	3.822

Gaus mean : 41.0587

Gaus Width: 3.8327

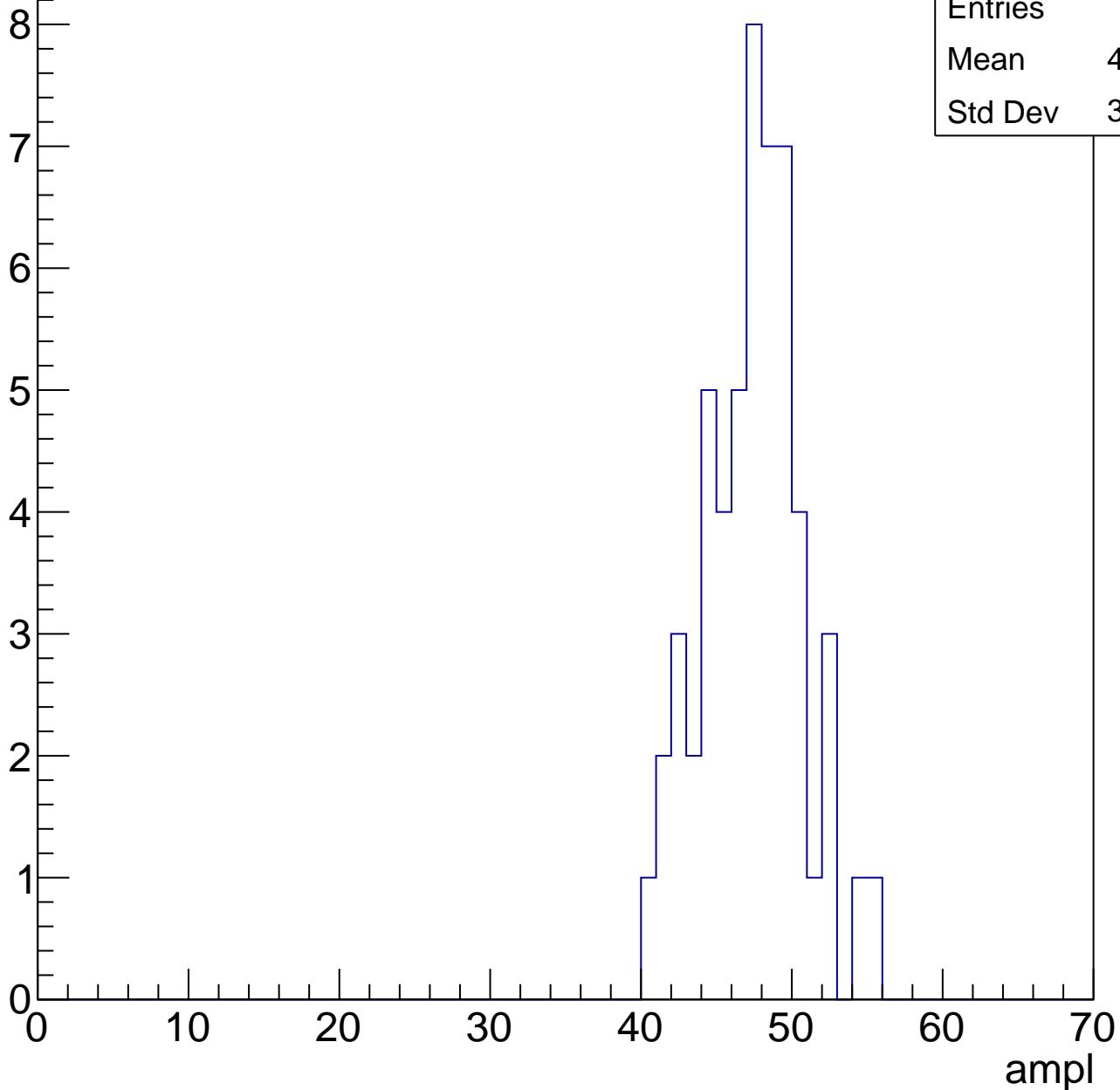


B1L101S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	46.94
Std Dev	3.246

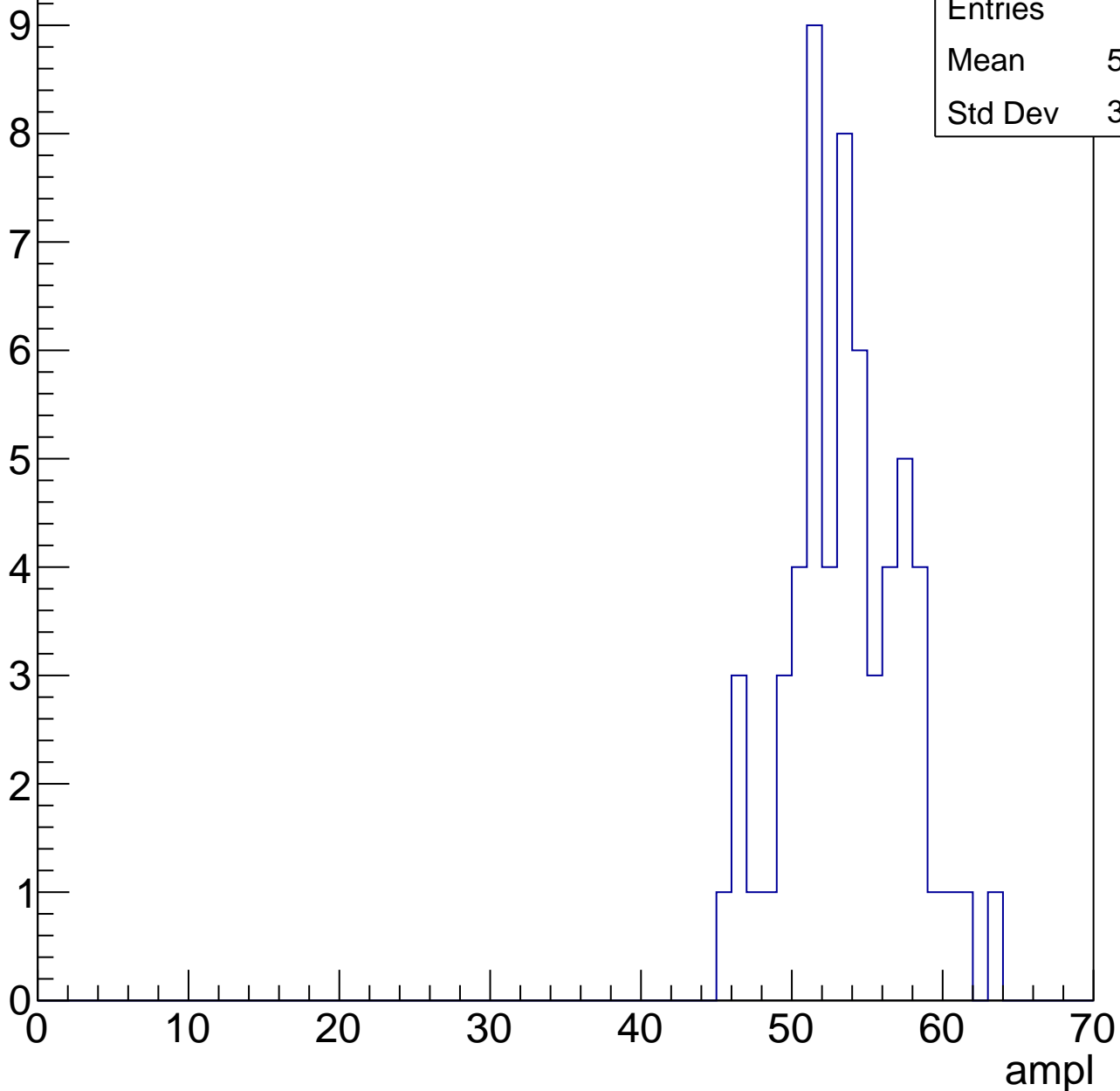


B1L101S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	53.15
Std Dev	3.825



B1L101S, U14-ch98, adc5

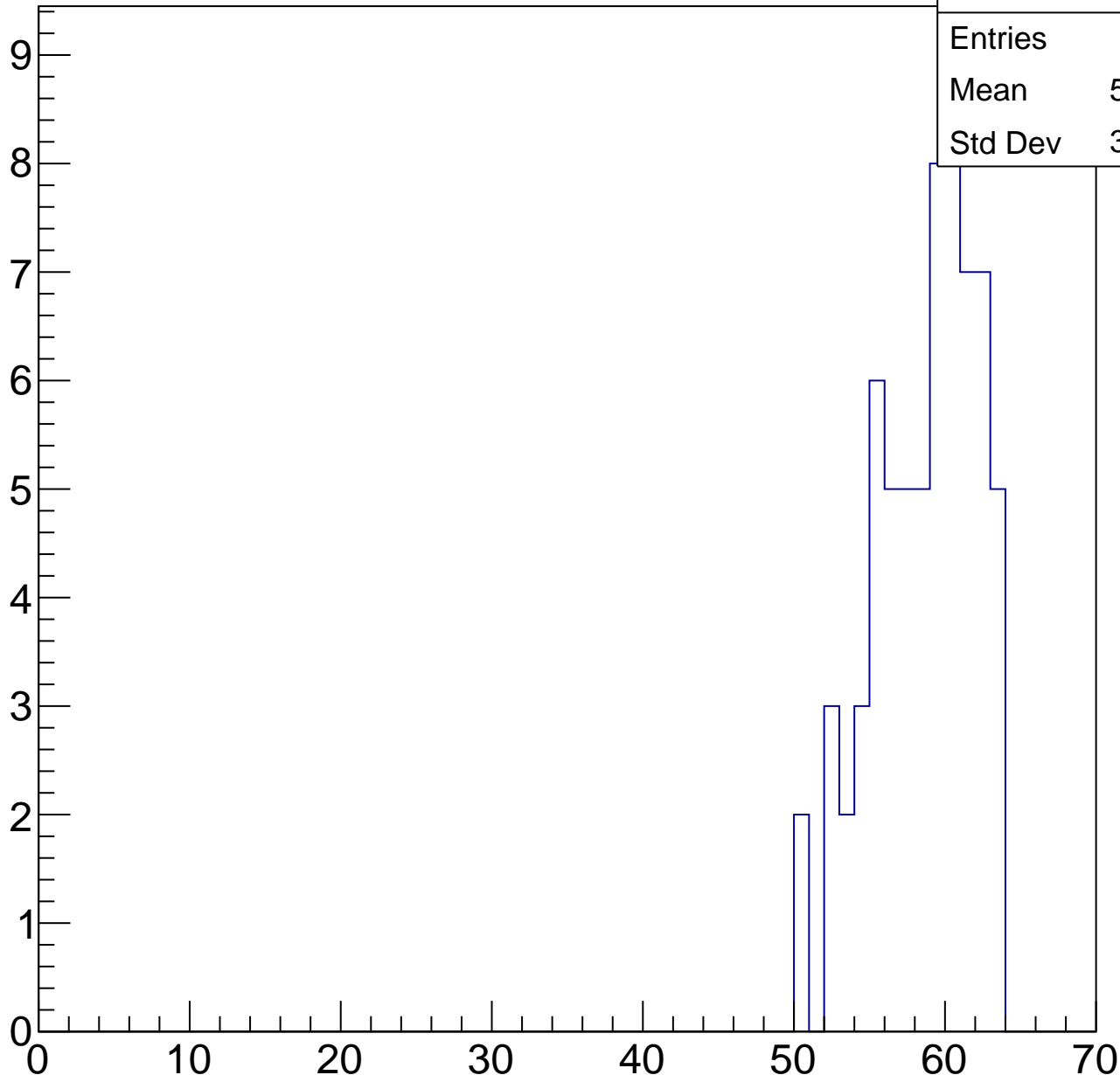
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	58.16
Std Dev	3.357

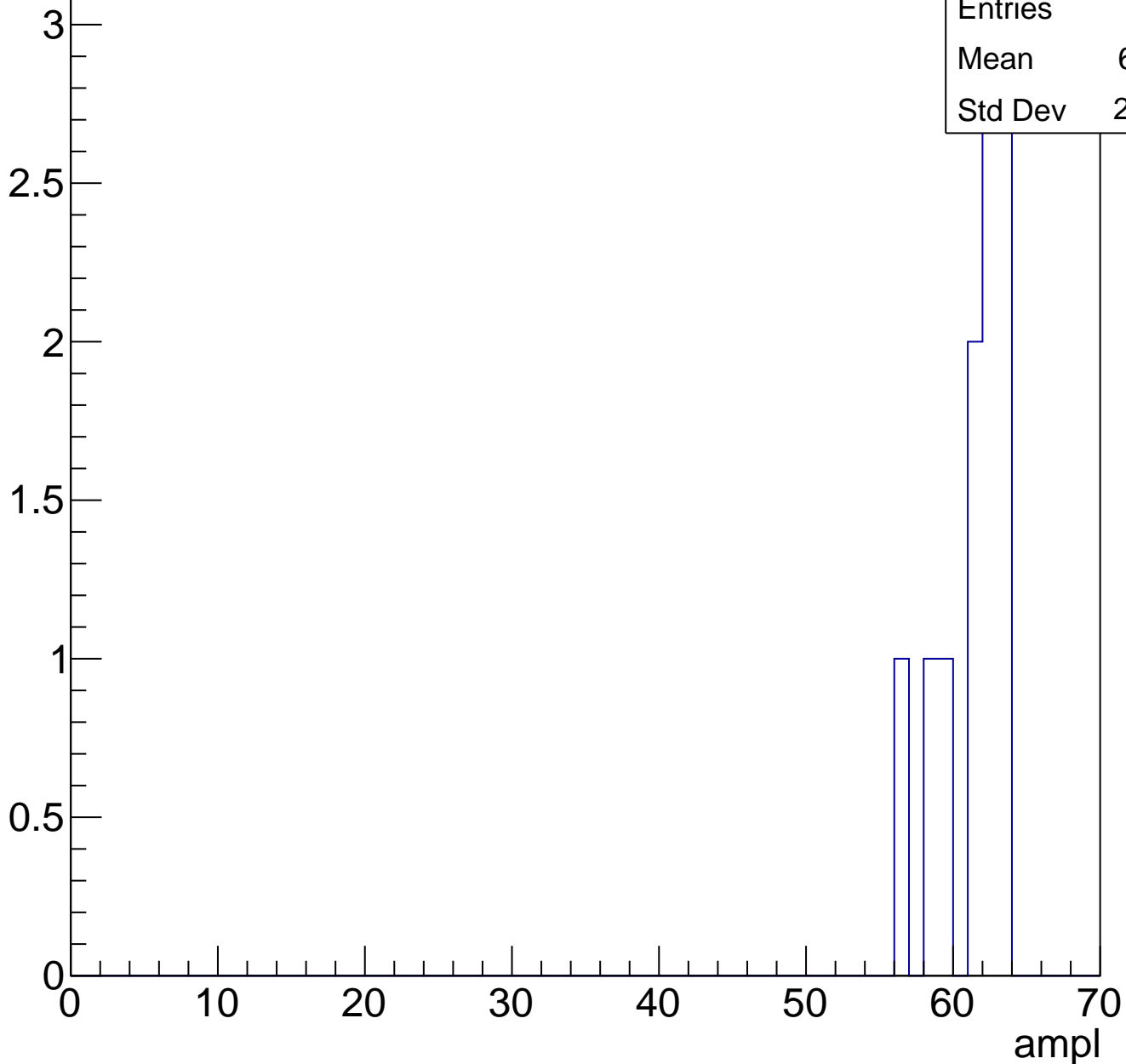
ampl



B1L101S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch99, adc0

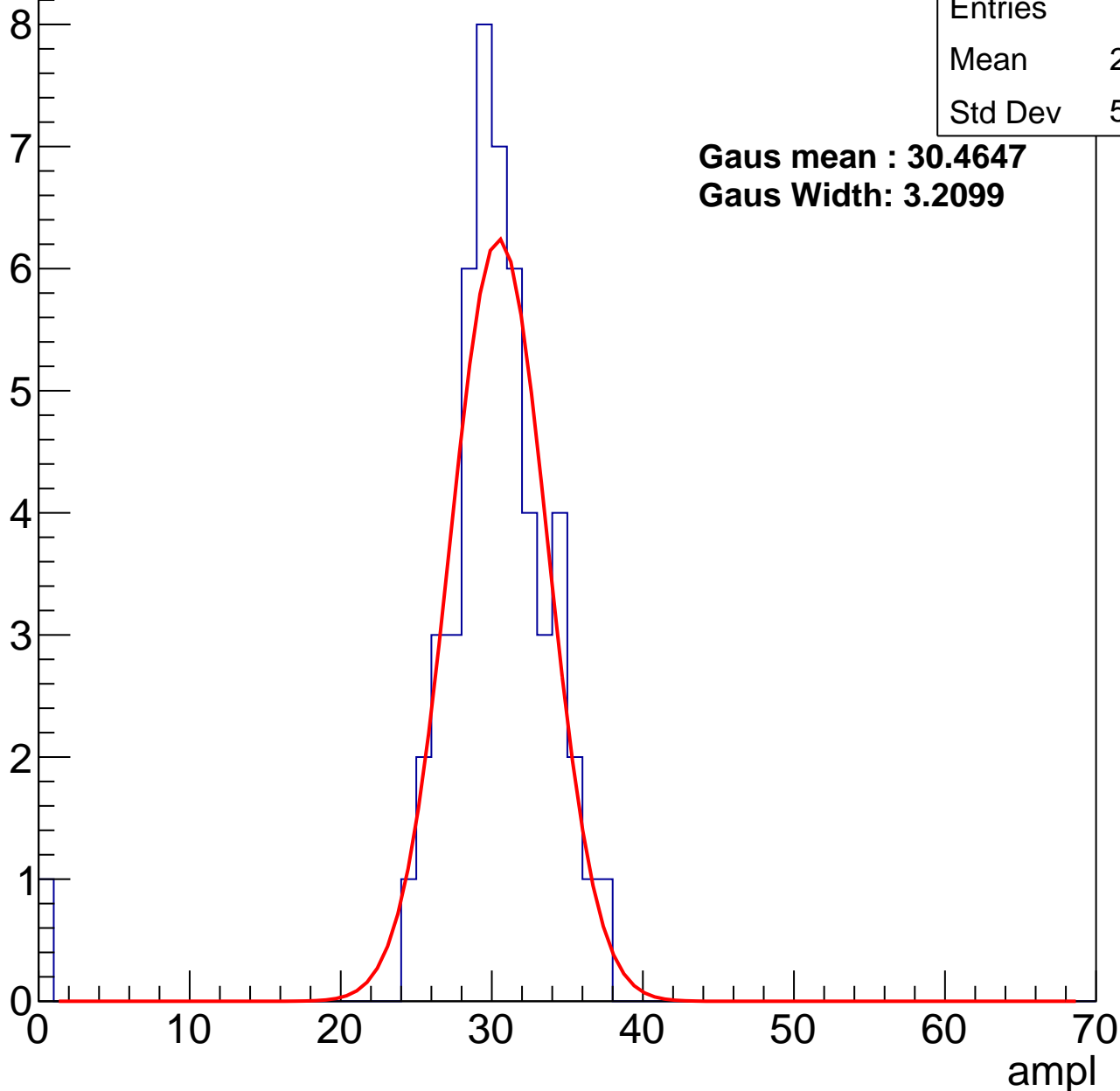
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	29.52
Std Dev	5.055

Gaus mean : 30.4647

Gaus Width: 3.2099



B1L101S, U14-ch99, adc1

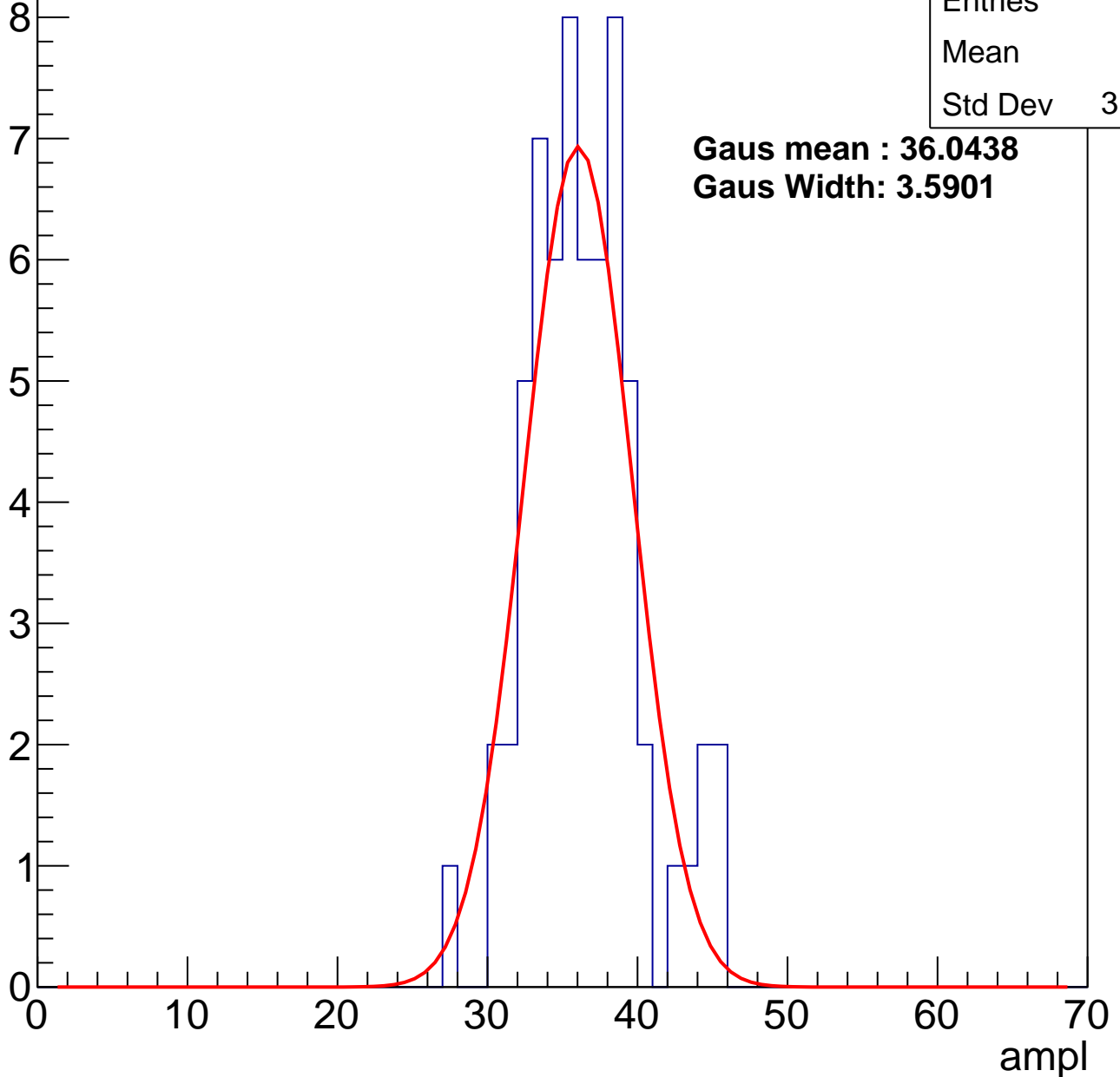
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36
Std Dev	3.683

Gaus mean : 36.0438

Gaus Width: 3.5901



B1L101S, U14-ch99, adc2

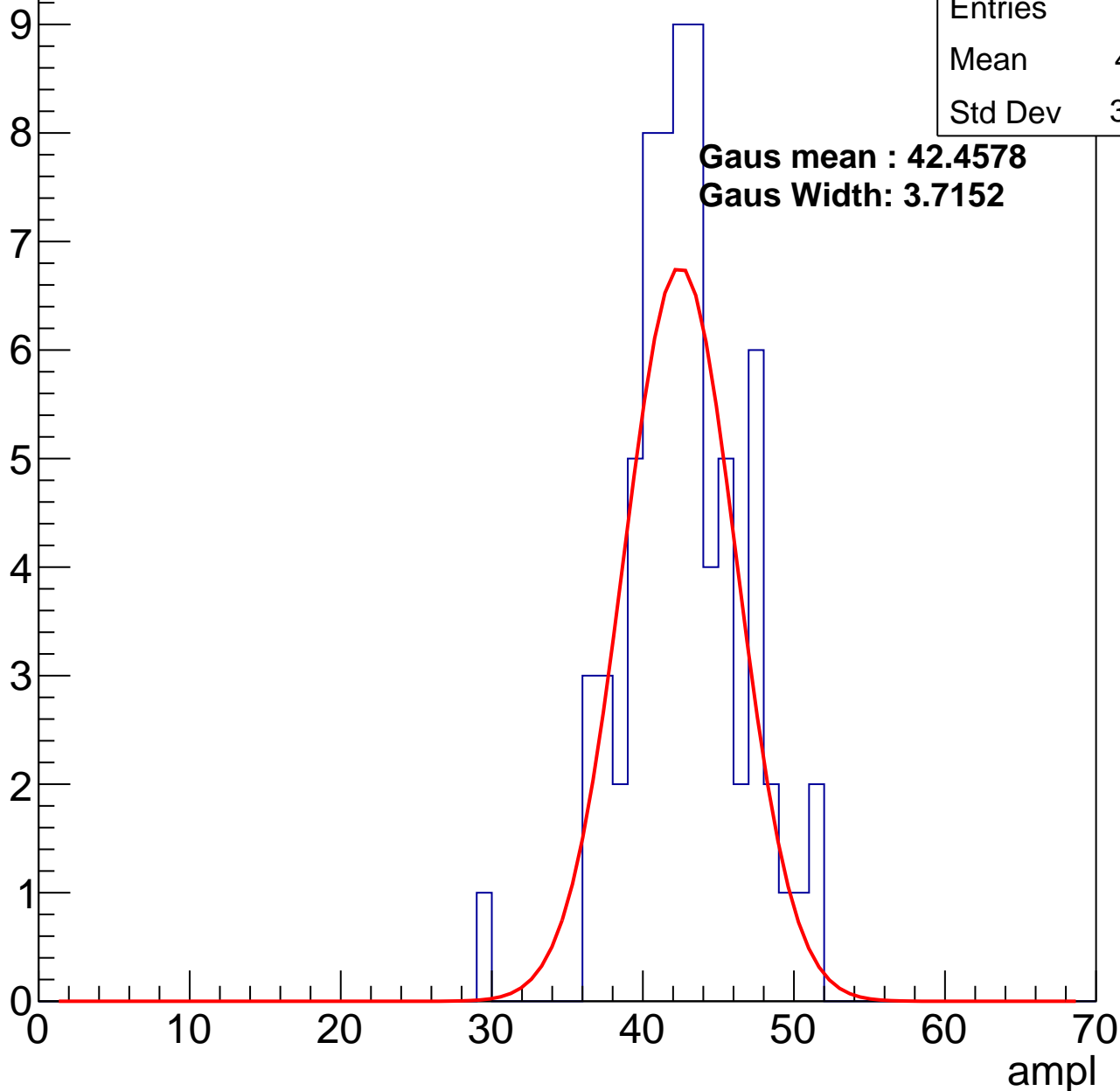
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.31
Std Dev	3.866

Gaus mean : 42.4578

Gaus Width: 3.7152

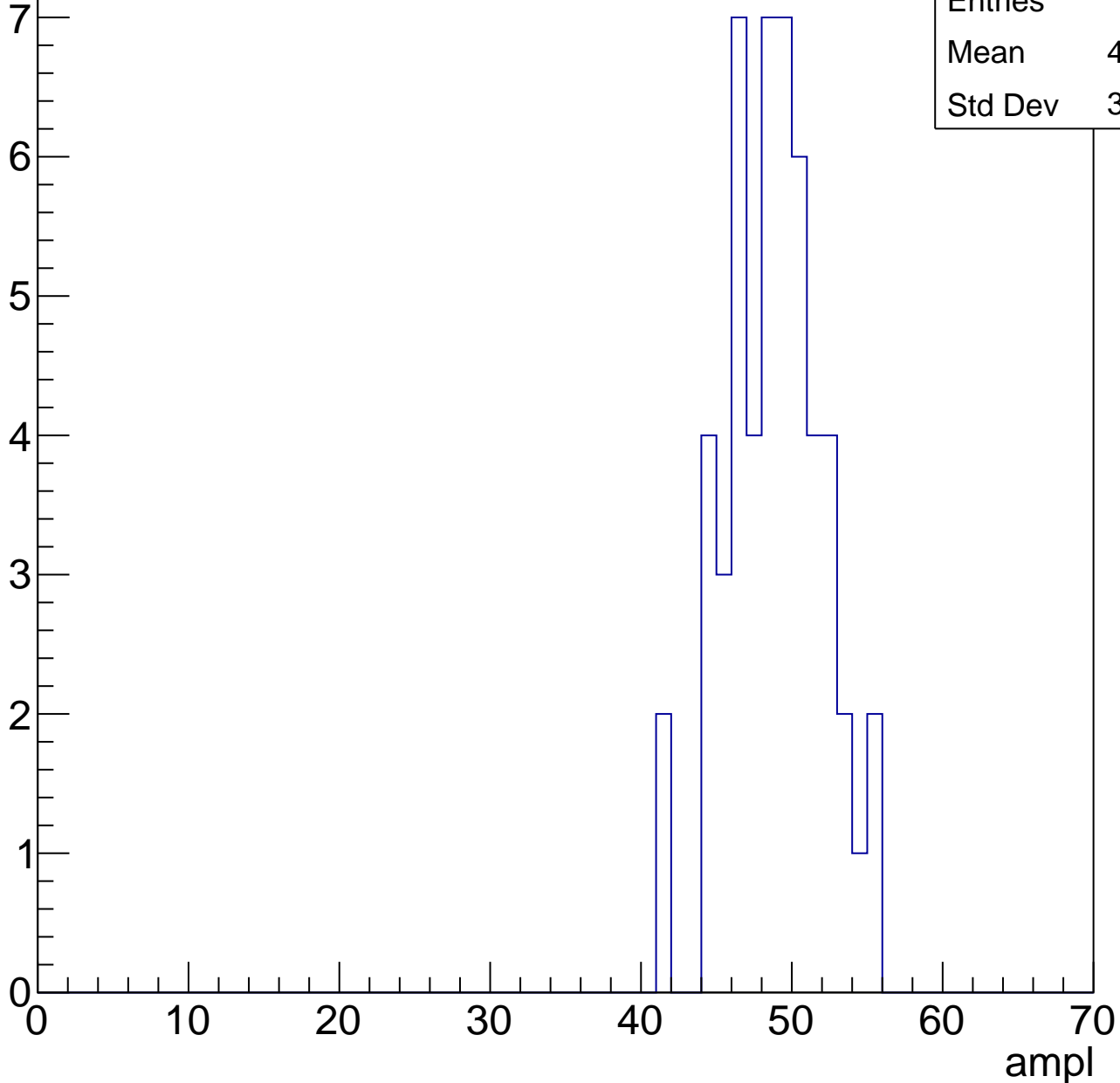


B1L101S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	48.38
Std Dev	3.158

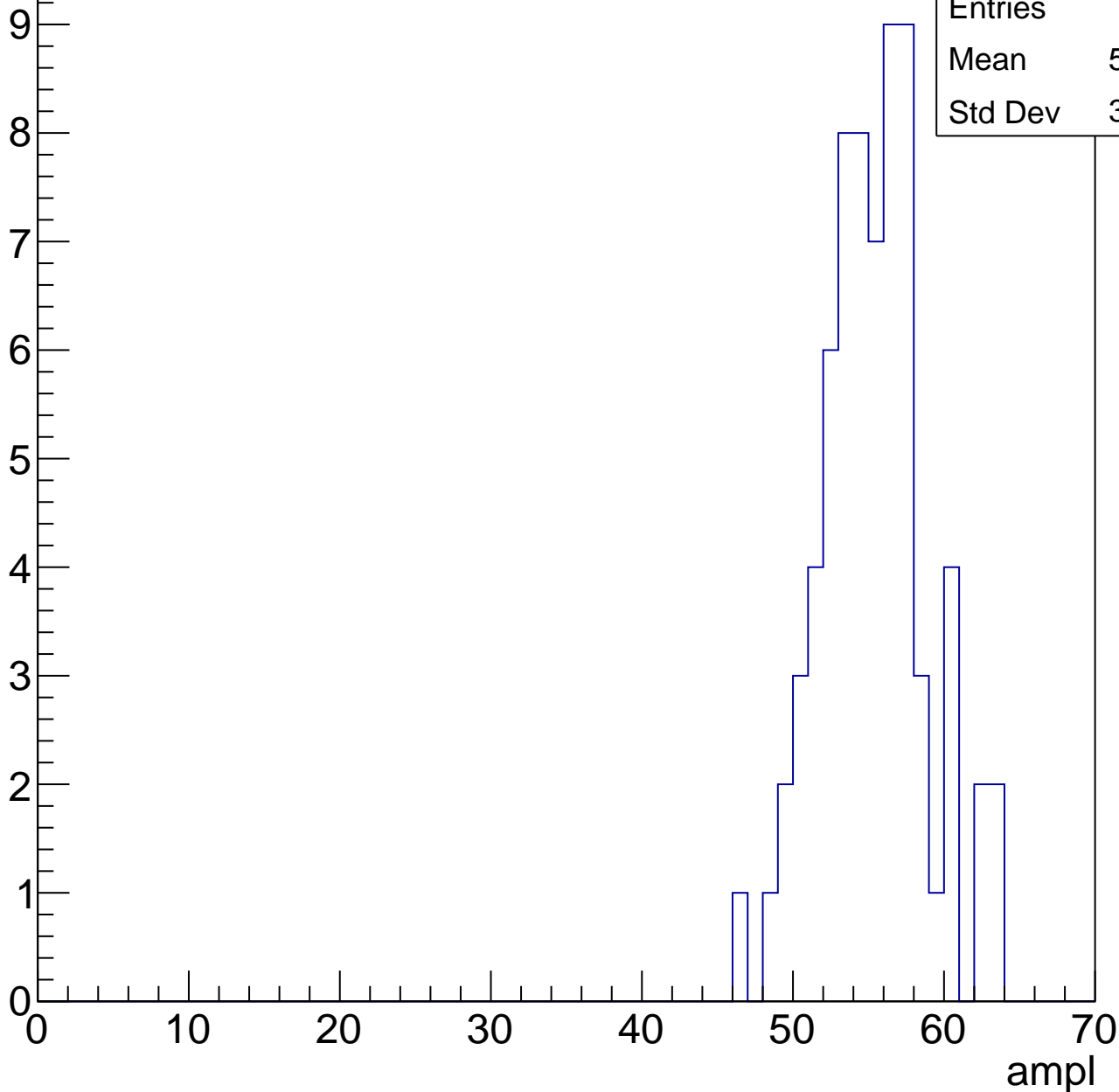


B1L101S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	54.84
Std Dev	3.479



B1L101S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

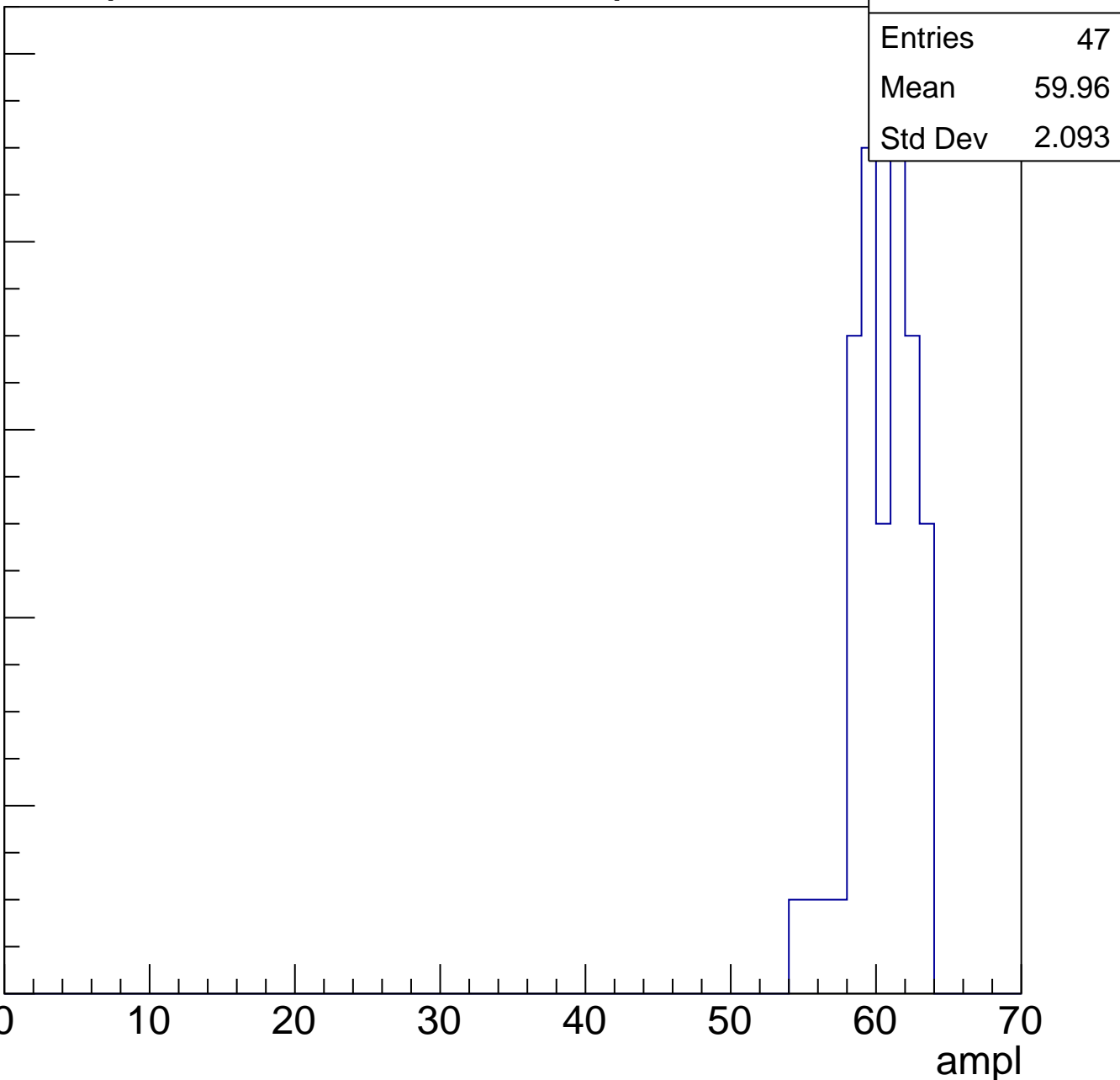
47

Mean

59.96

Std Dev

2.093



B1L101S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

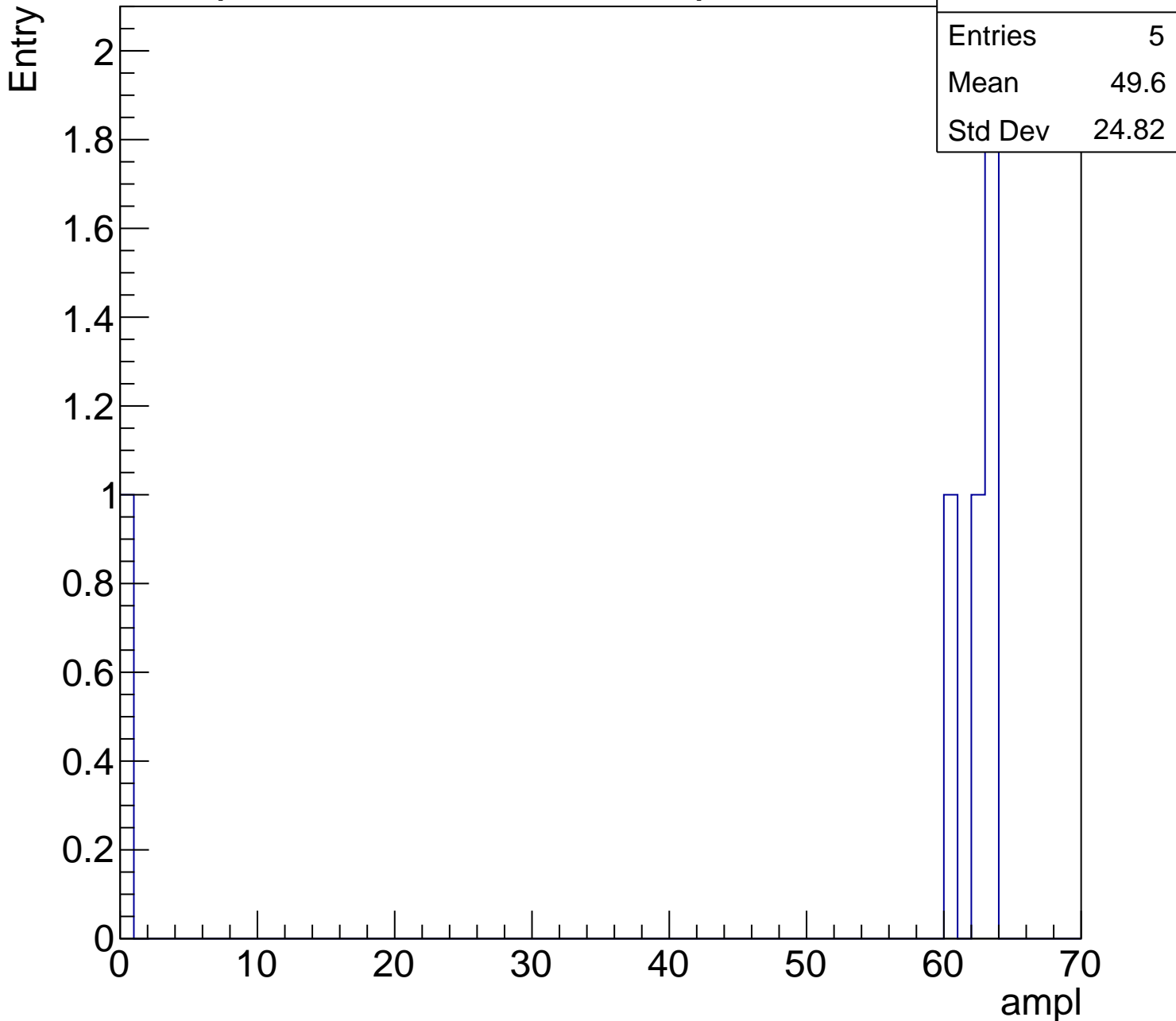
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch100, adc0

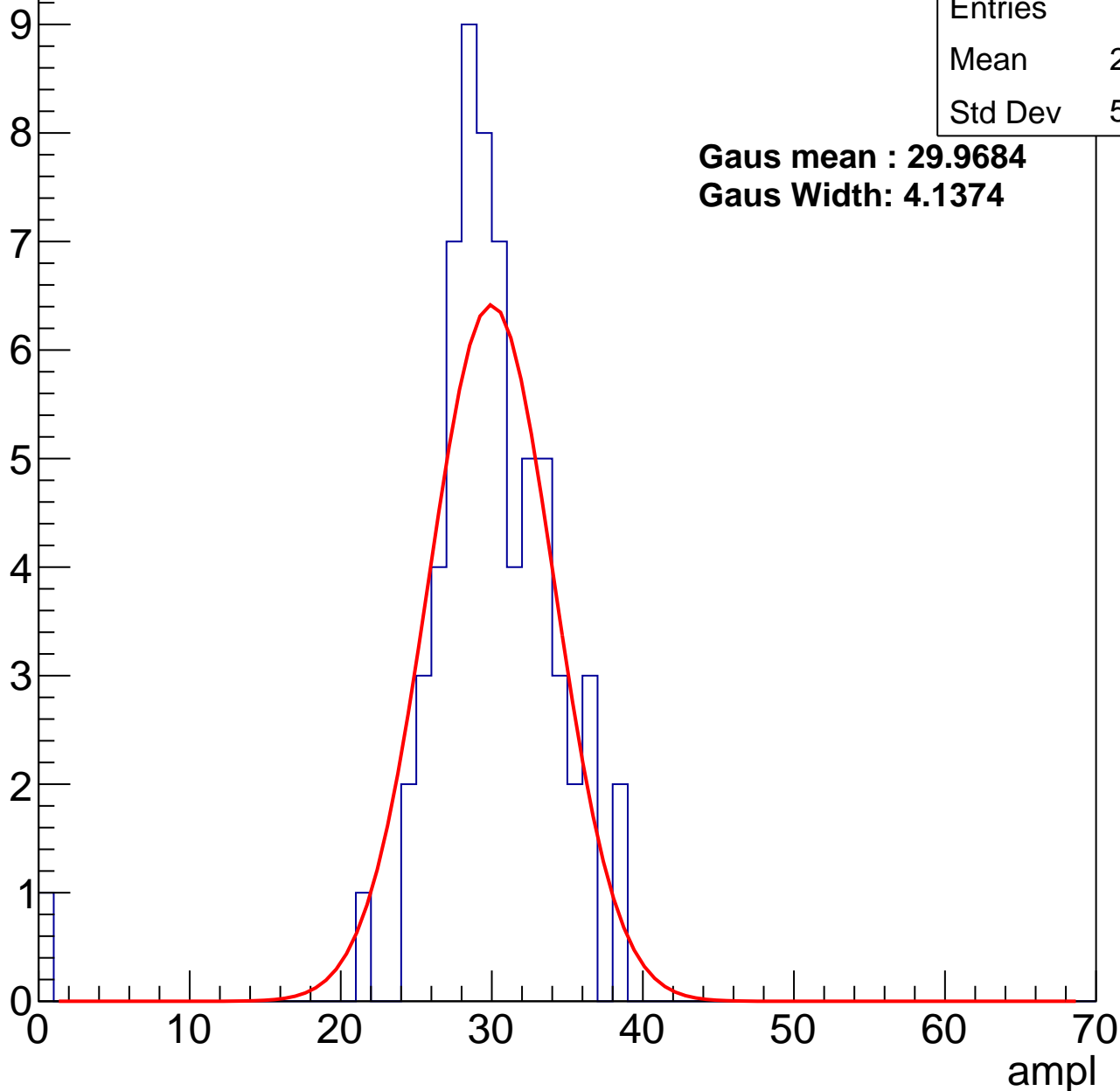
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.33
Std Dev	5.034

Gaus mean : 29.9684

Gaus Width: 4.1374



B1L101S, U14-ch100, adc1

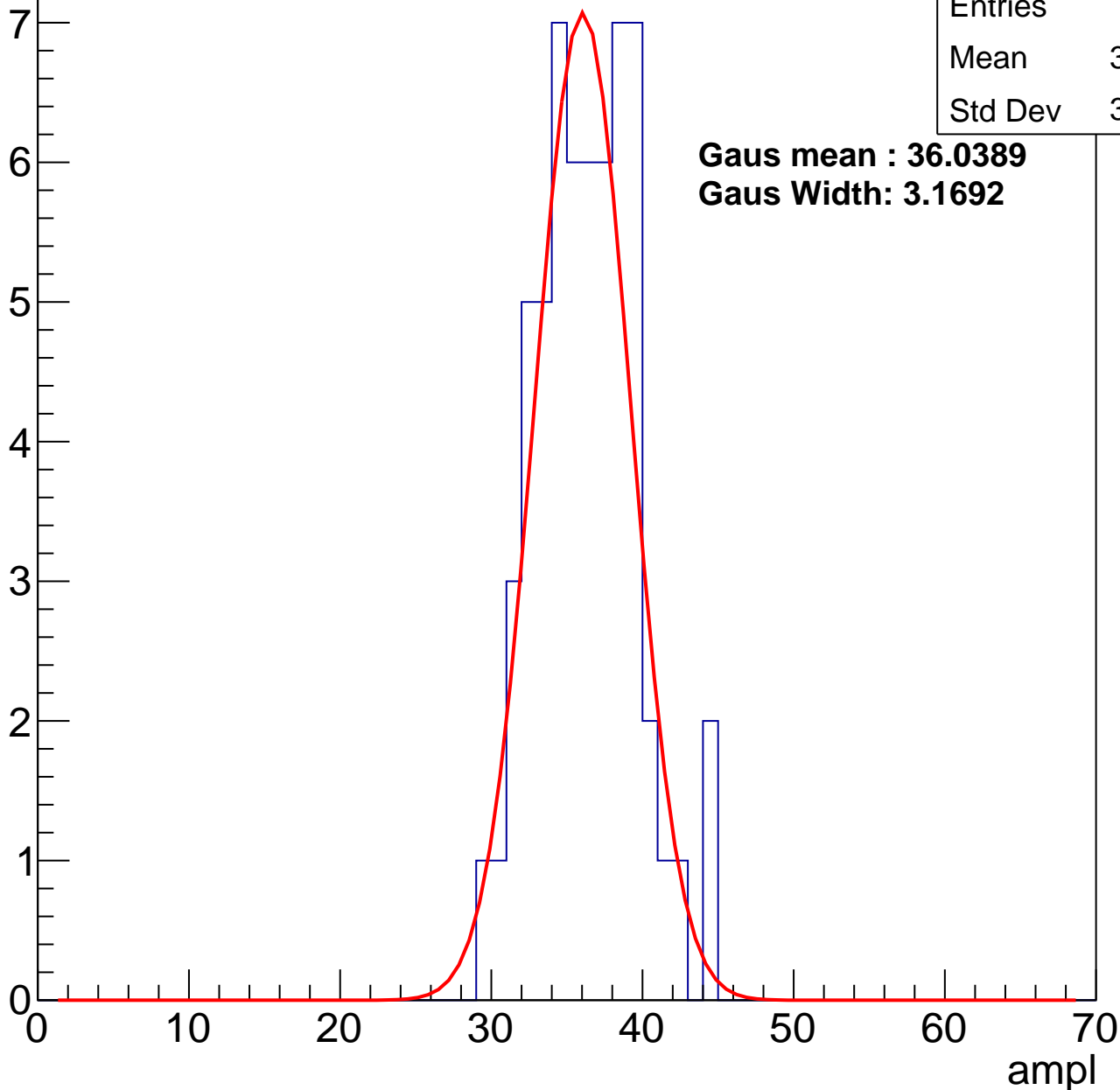
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.88
Std Dev	3.256

Gaus mean : 36.0389

Gaus Width: 3.1692



B1L101S, U14-ch100, adc2

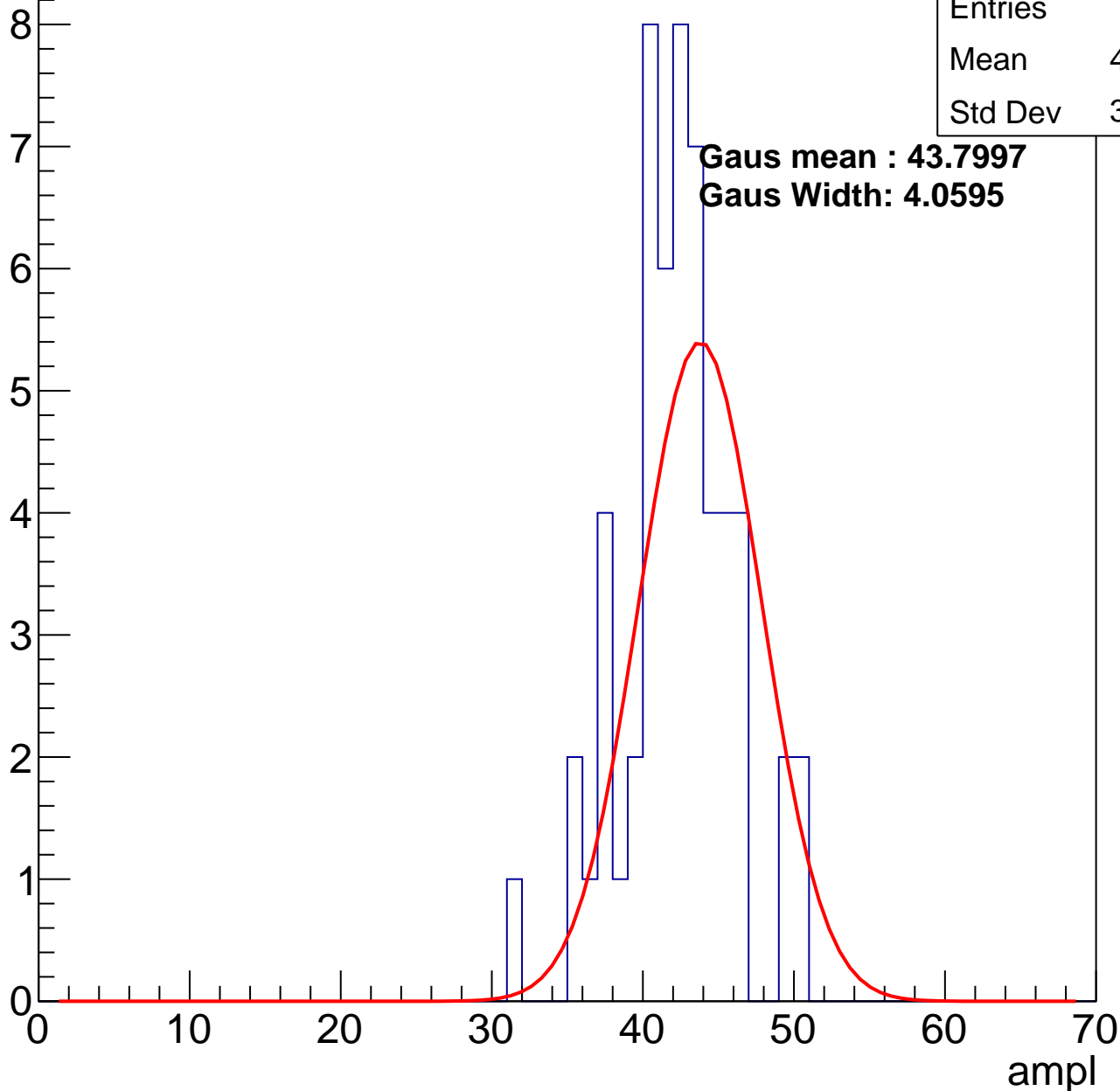
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.82
Std Dev	3.718

Gaus mean : 43.7997

Gaus Width: 4.0595

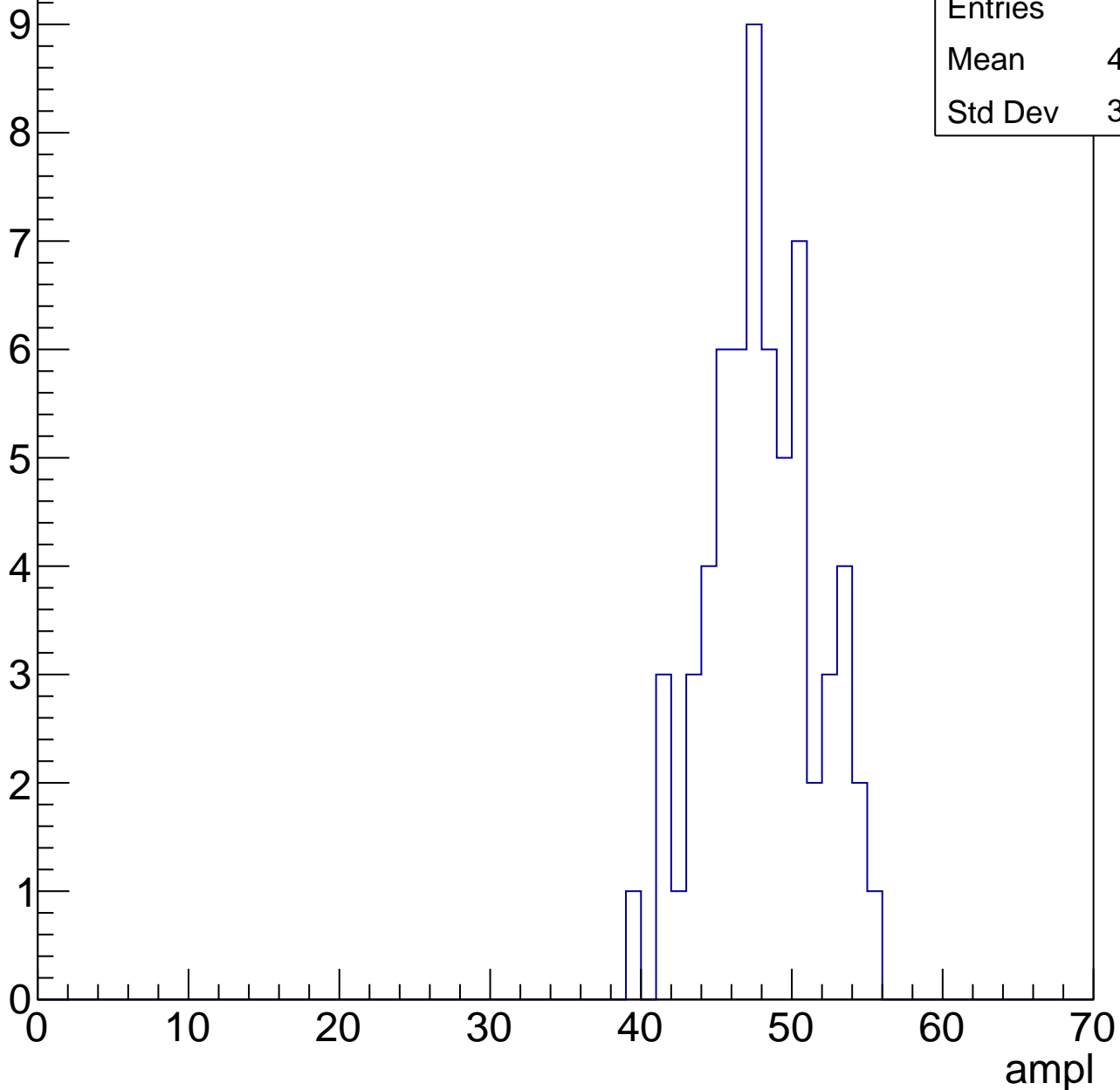


B1L101S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	47.52
Std Dev	3.554

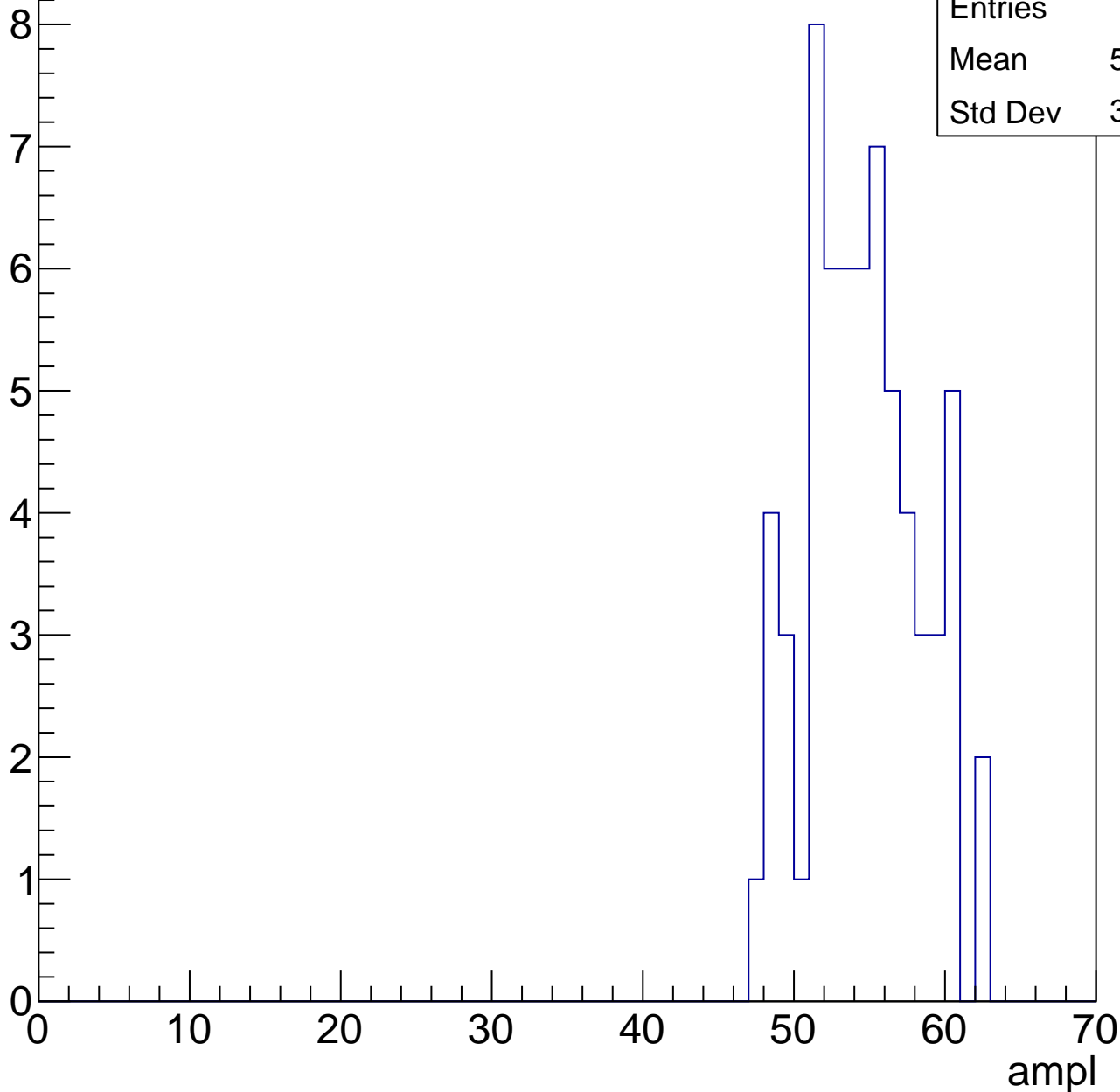


B1L101S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	54.16
Std Dev	3.705

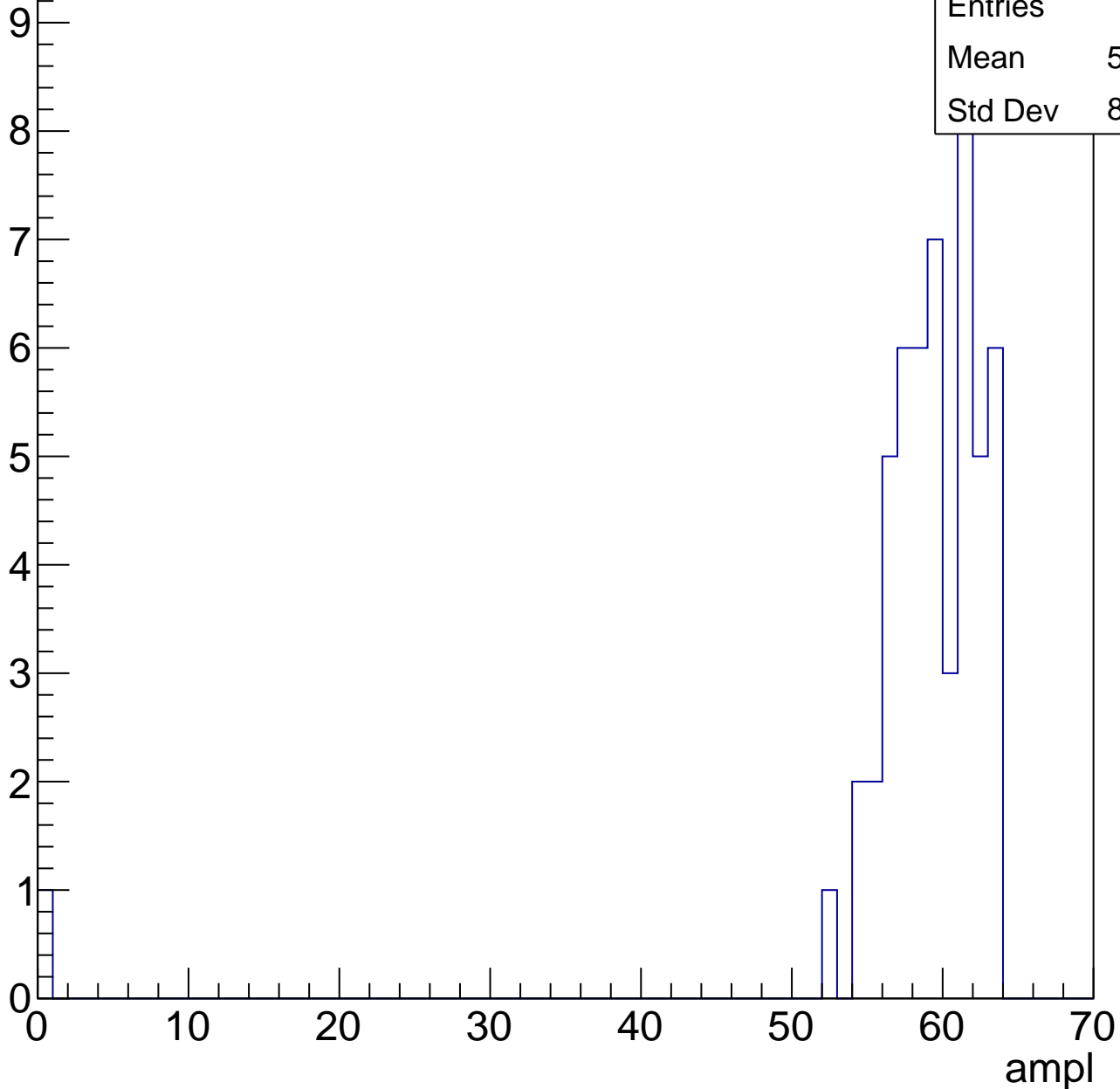


B1L101S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

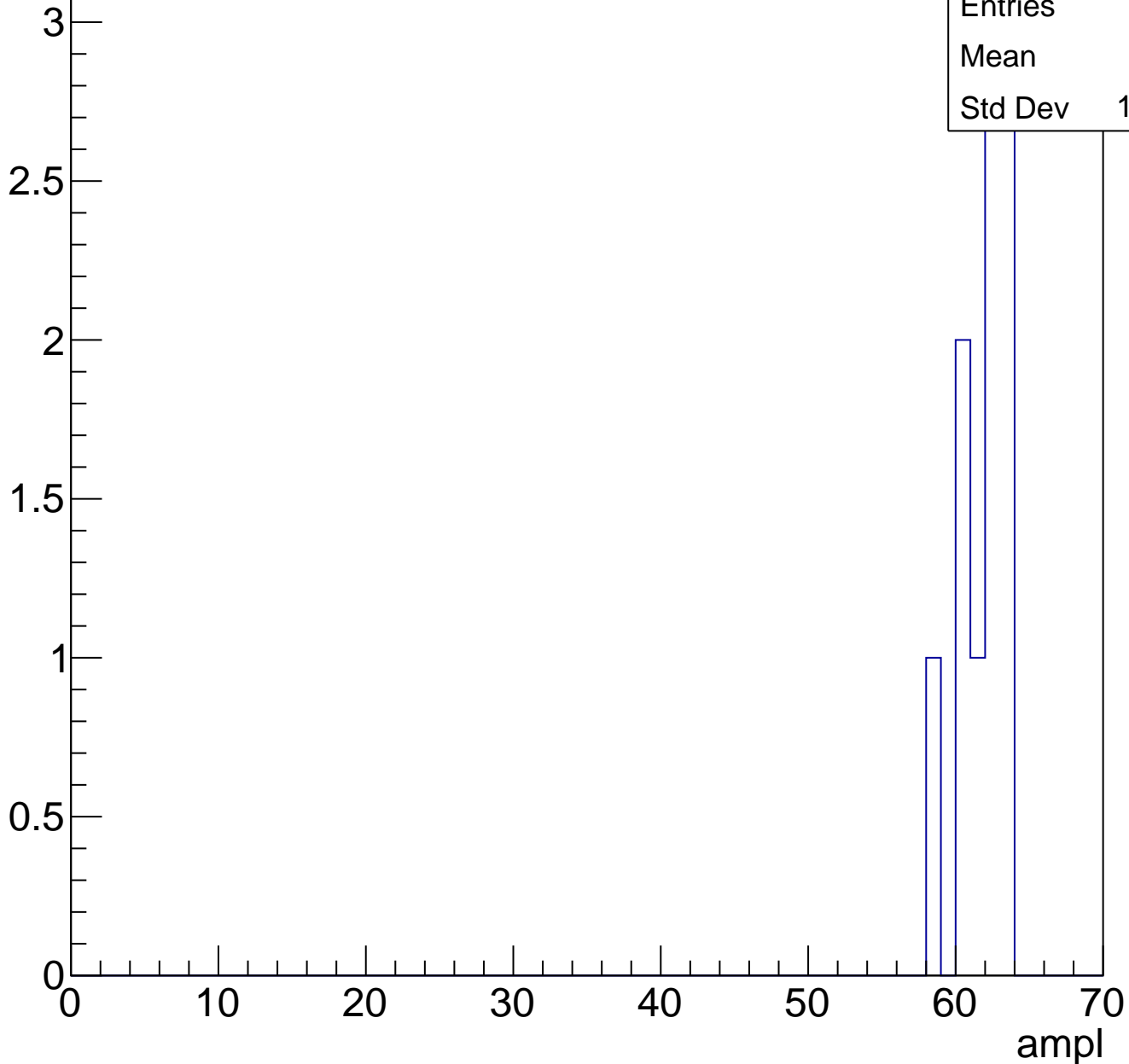
Entries	53
Mean	57.92
Std Dev	8.474



B1L101S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch101, adc0

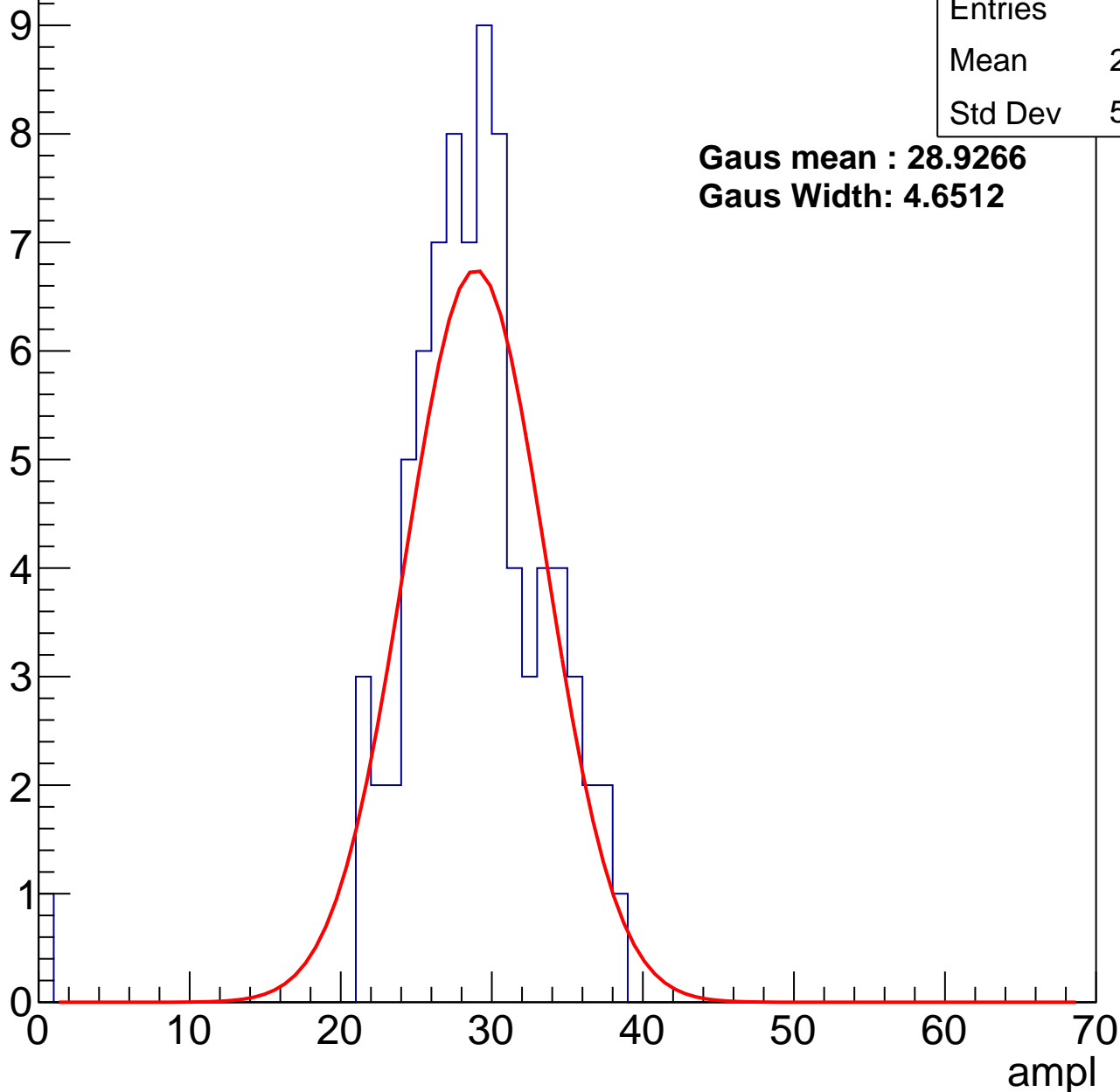
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.33
Std Dev	5.116

Gaus mean : 28.9266

Gaus Width: 4.6512



B1L101S, U14-ch101, adc1

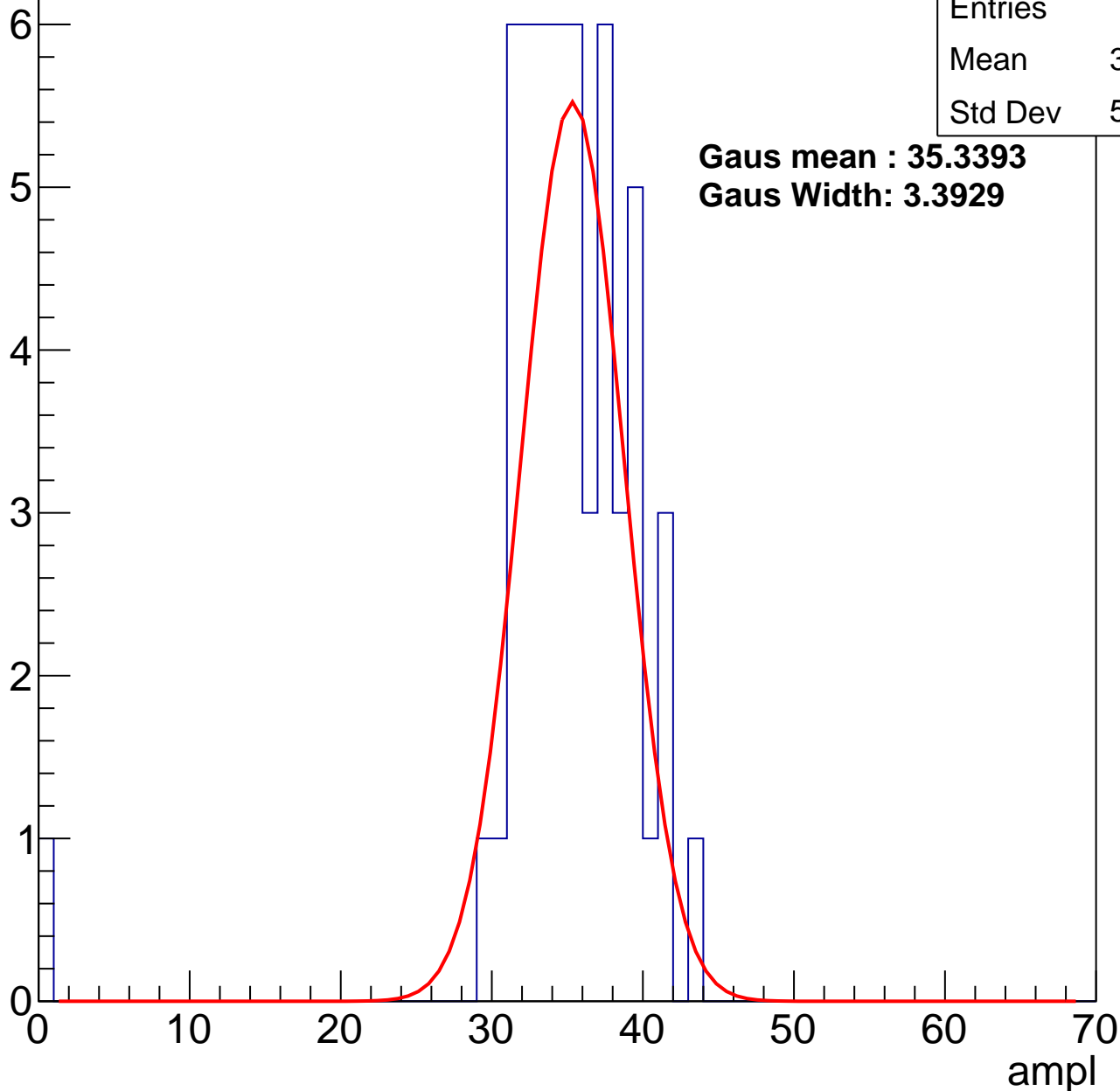
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	34.44
Std Dev	5.688

Gaus mean : 35.3393

Gaus Width: 3.3929



B1L101S, U14-ch101, adc2

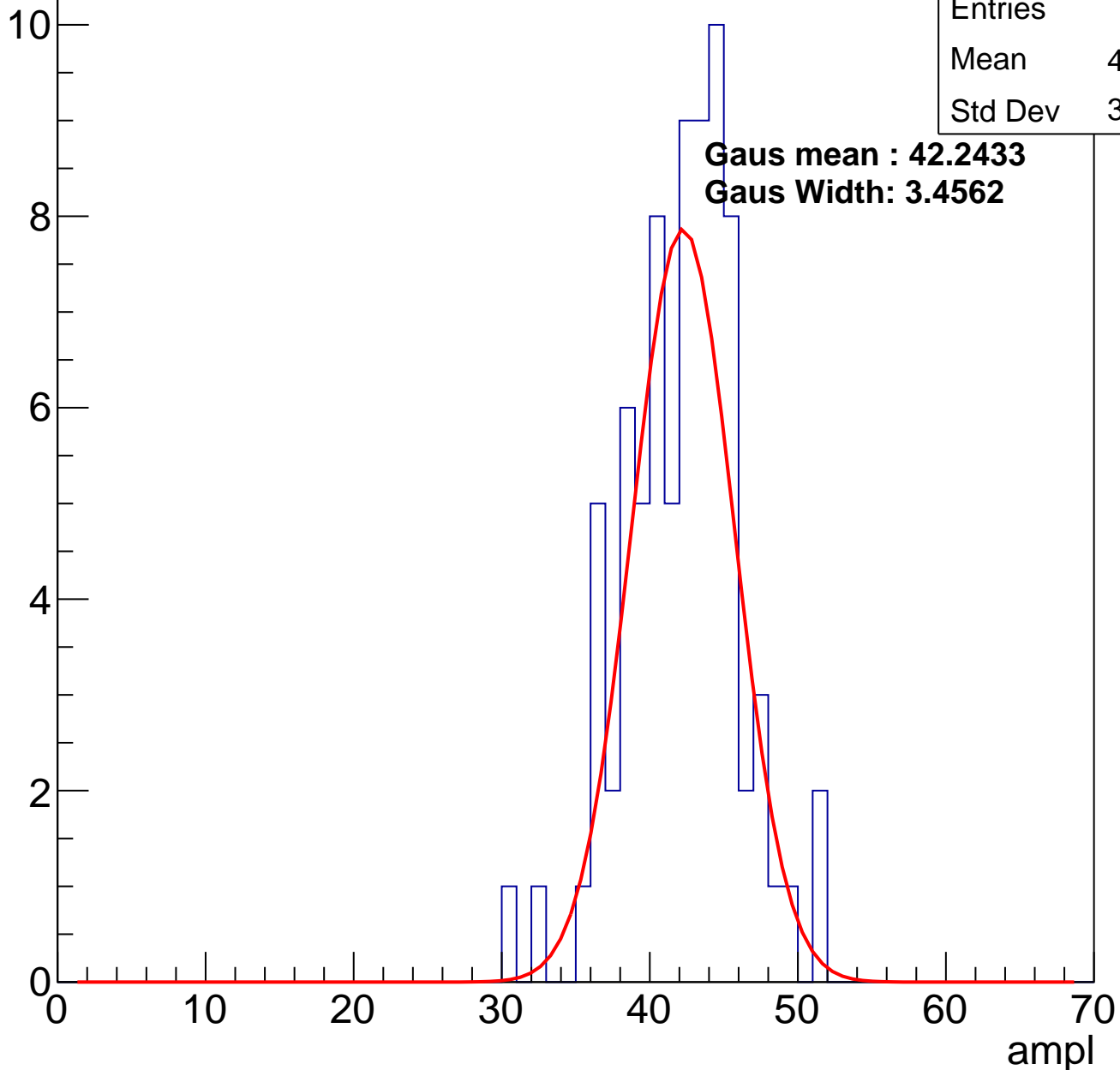
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	41.72
Std Dev	3.848

Gaus mean : 42.2433

Gaus Width: 3.4562

Entry

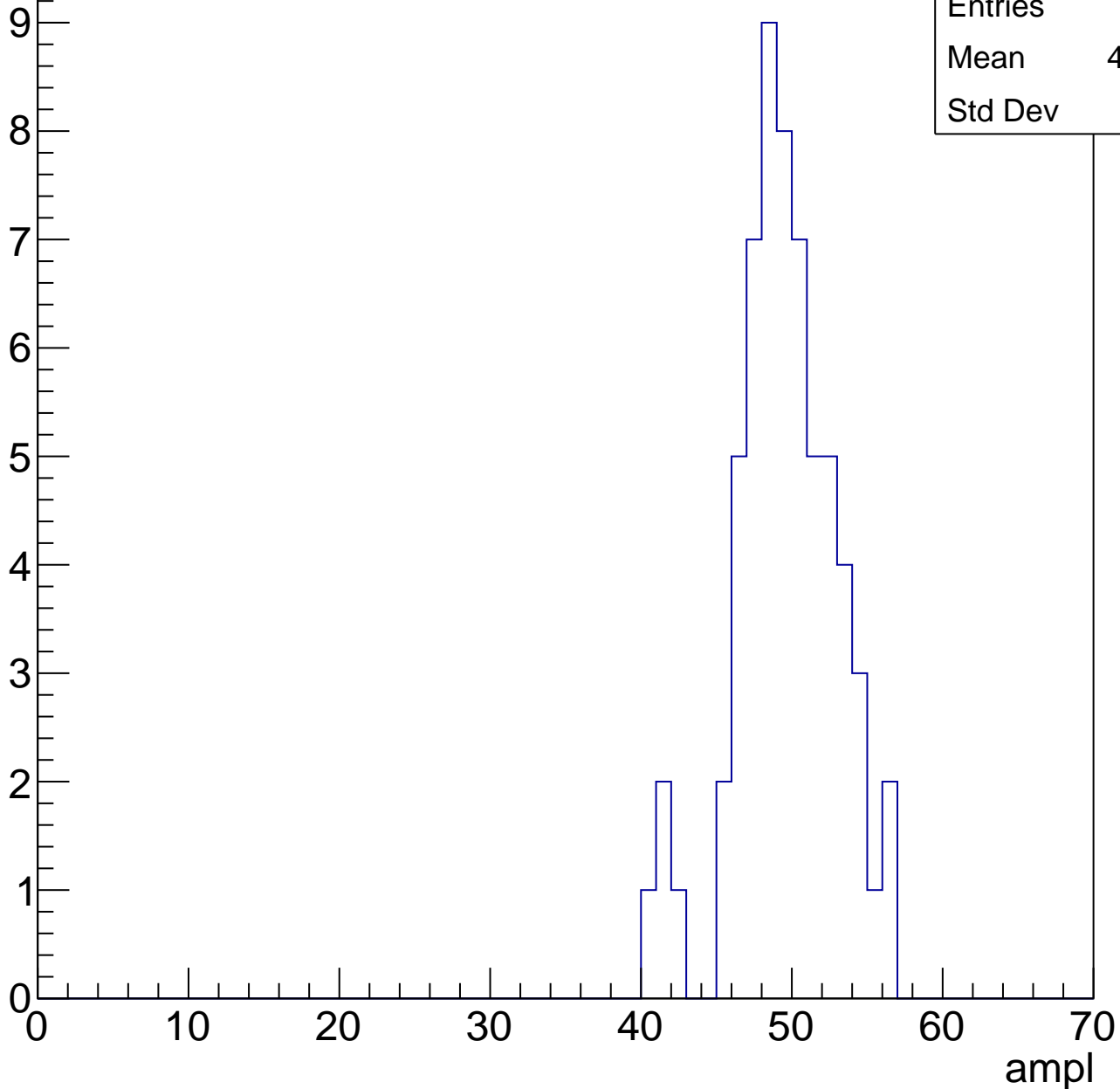


B1L101S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.08
Std Dev	3.4

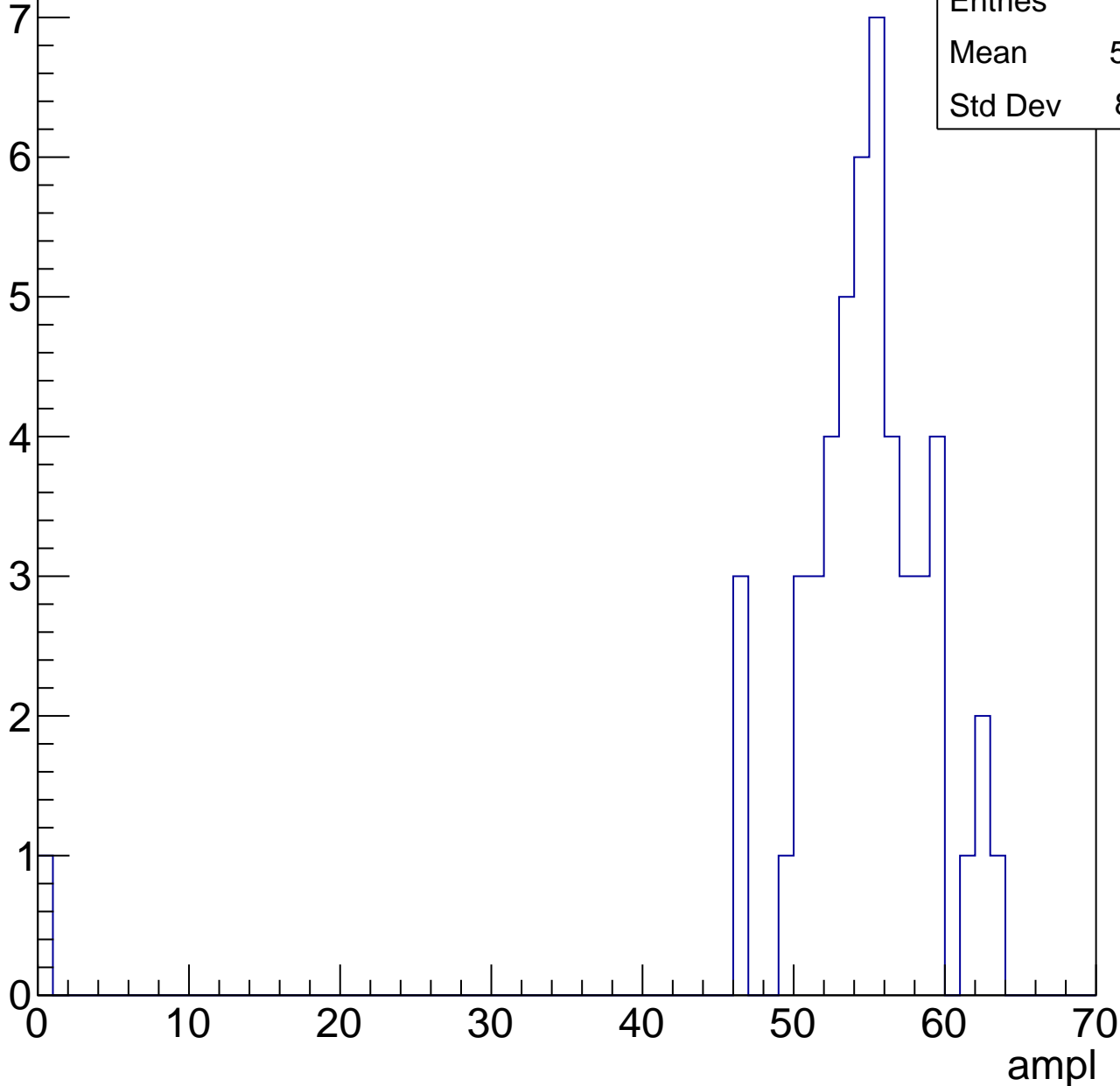


B1L101S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

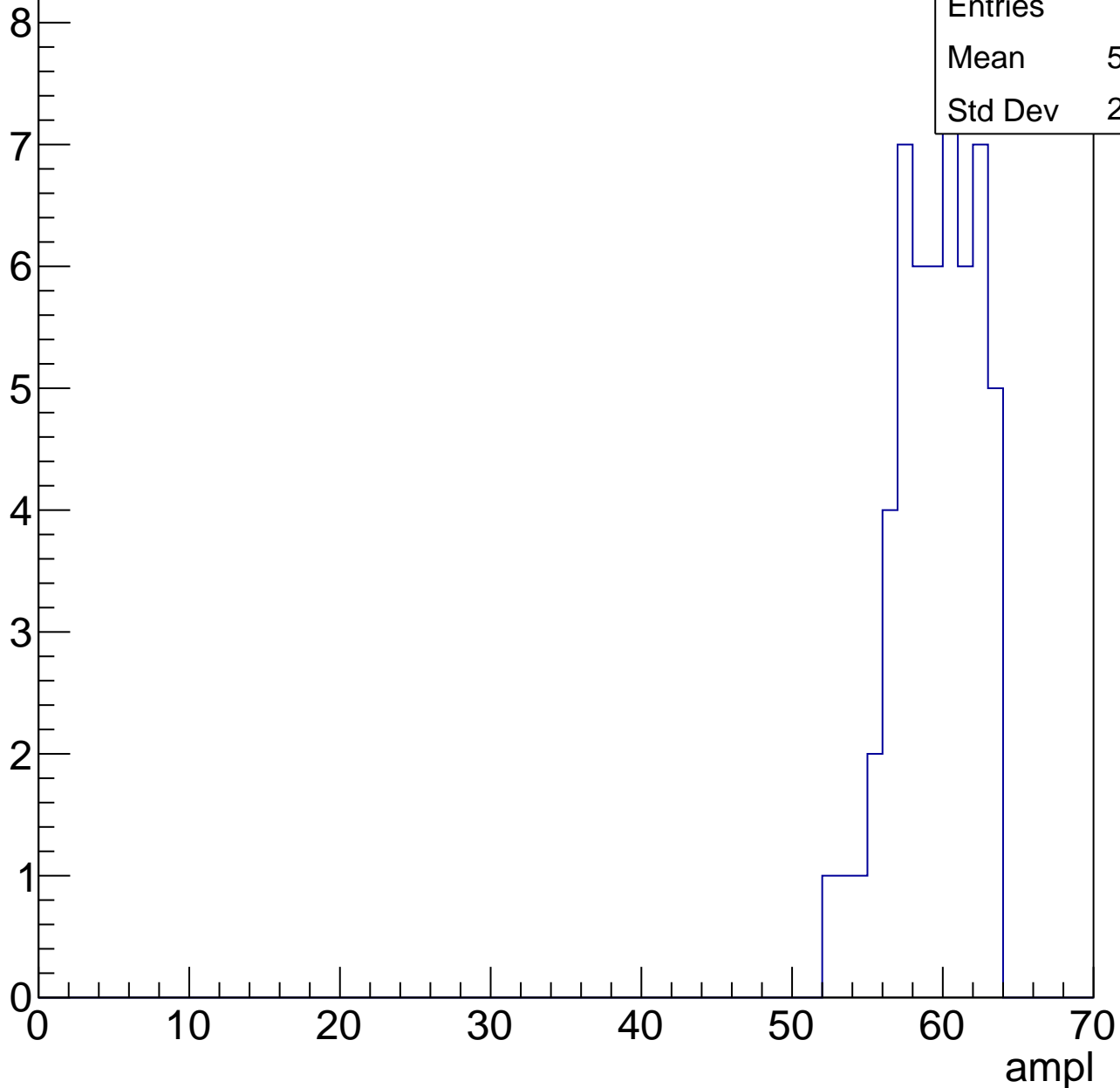
Entries	51
Mean	53.43
Std Dev	8.481



B1L101S, U14-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

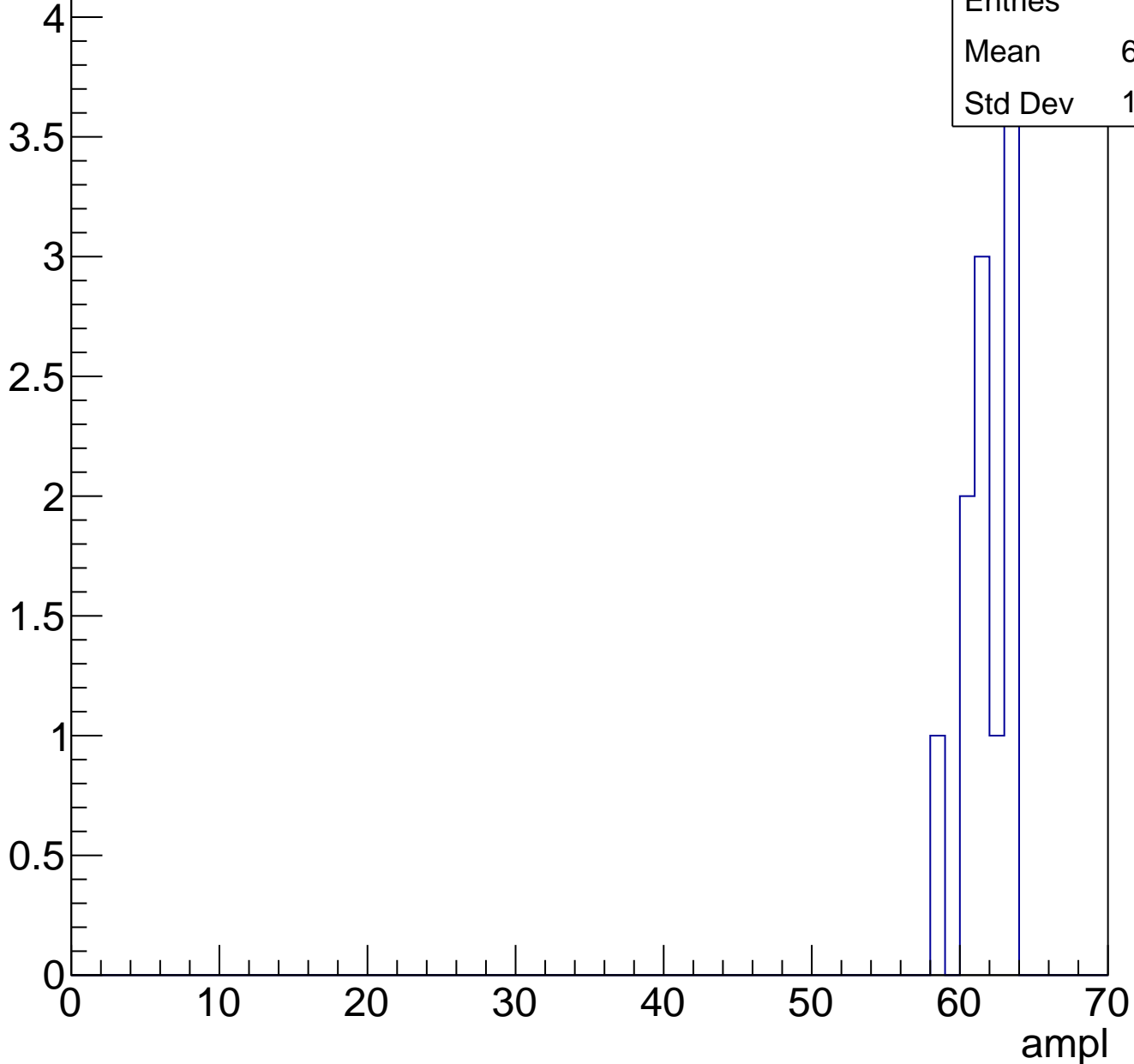


Entries	54
Mean	59.06
Std Dev	2.676

B1L101S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch102, adc0

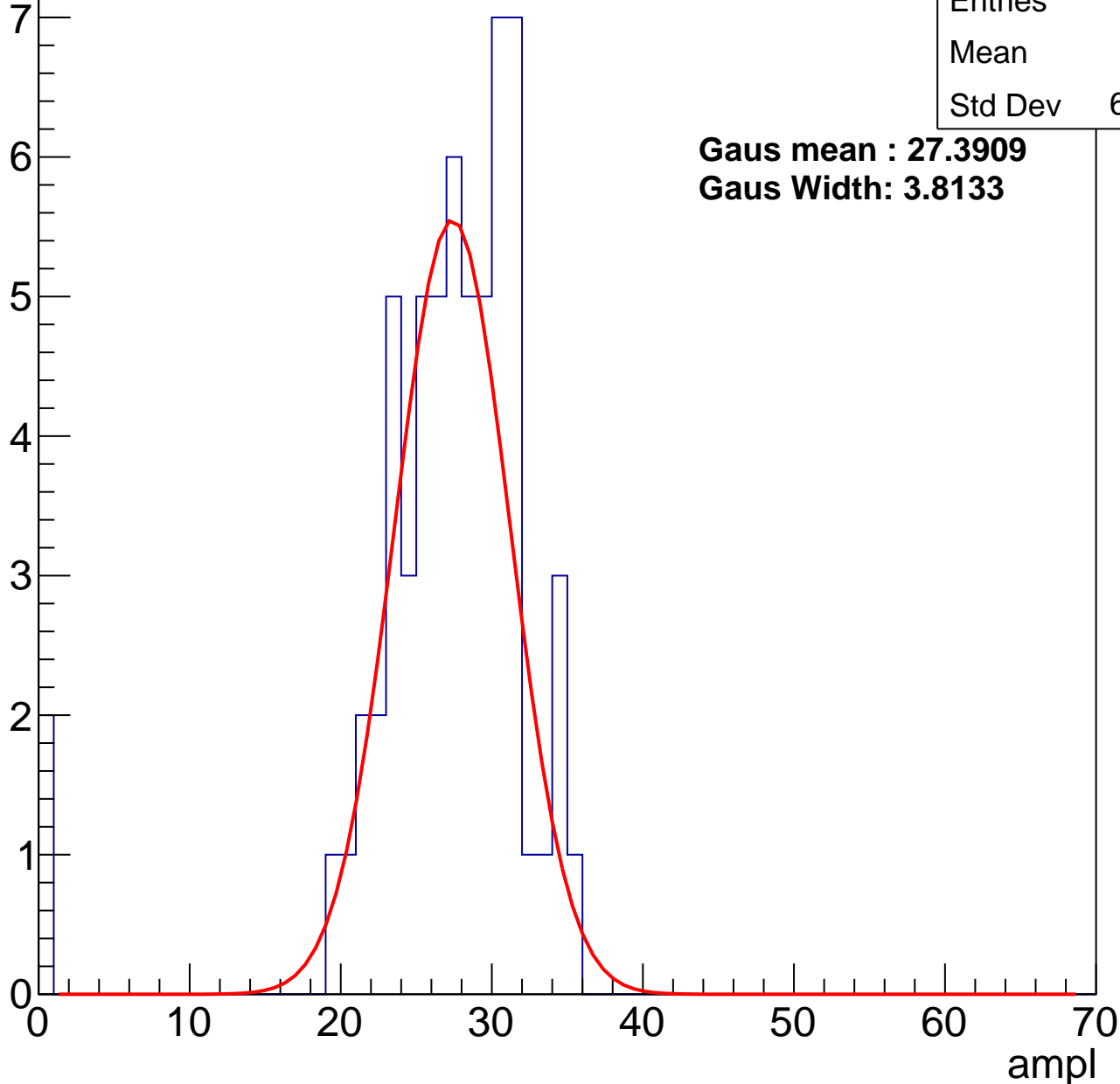
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	26.5
Std Dev	6.066

Gaus mean : 27.3909

Gaus Width: 3.8133



B1L101S, U14-ch102, adc1

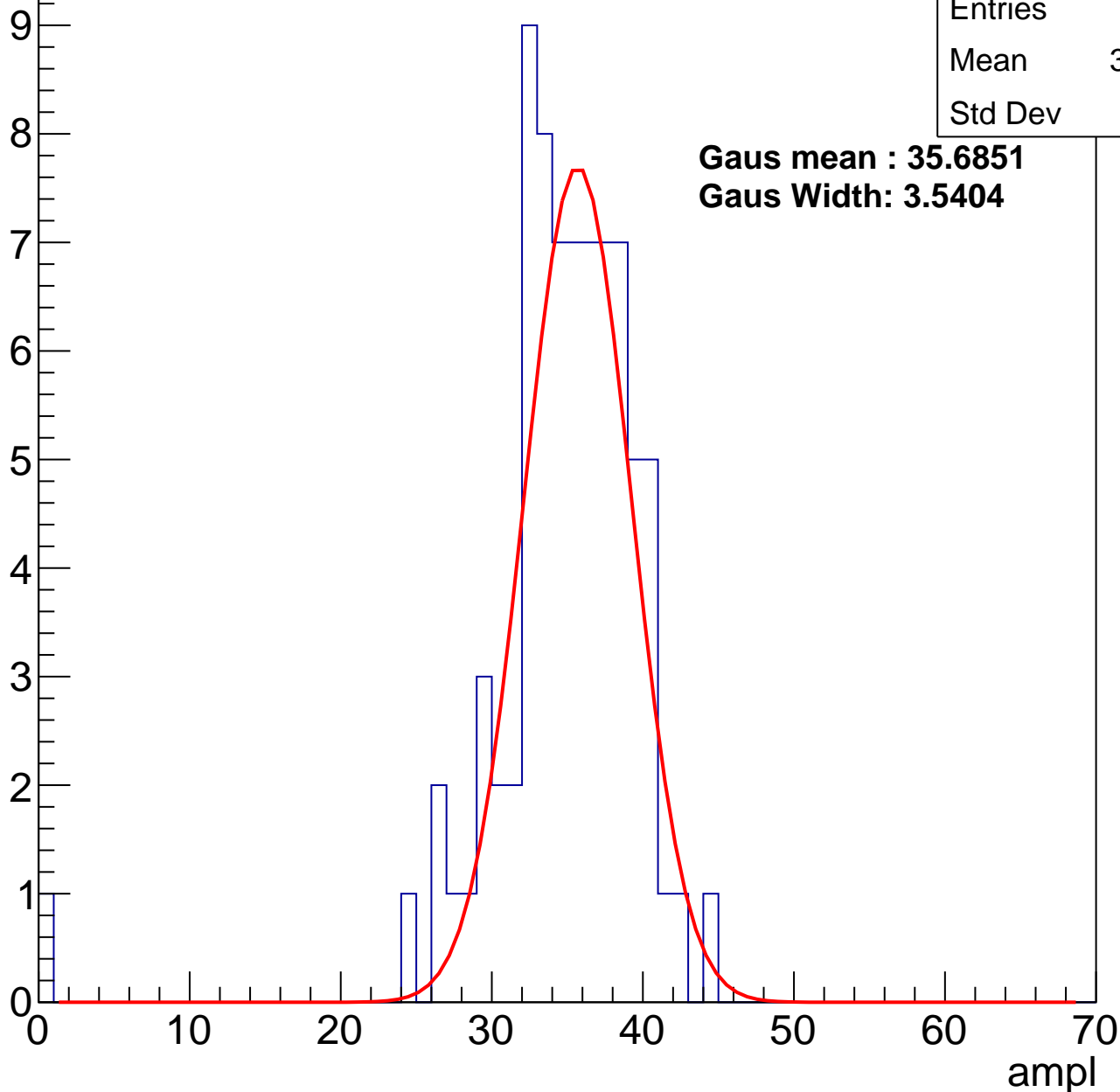
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	34.28
Std Dev	5.5

Gaus mean : 35.6851

Gaus Width: 3.5404



B1L101S, U14-ch102, adc2

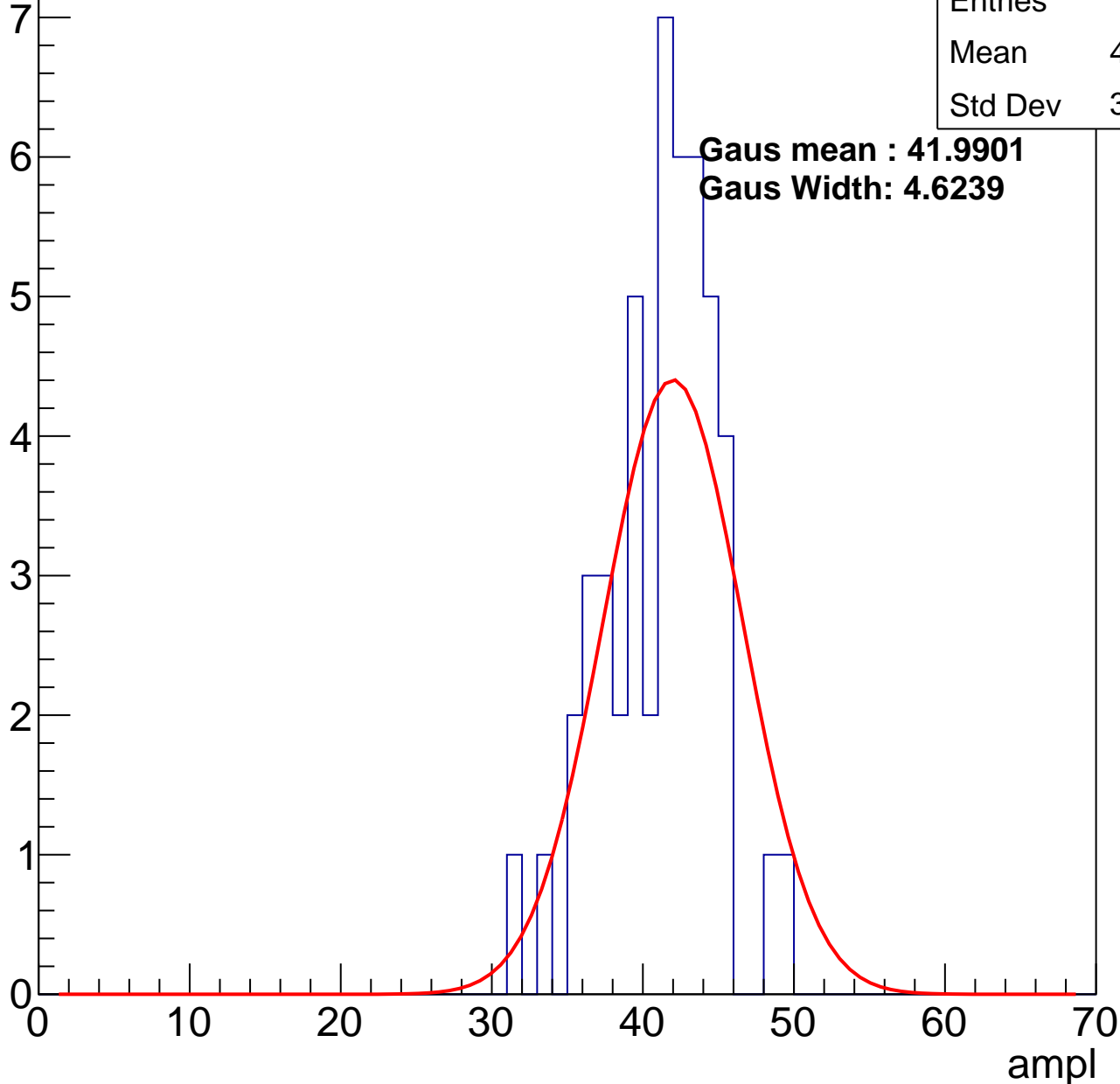
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	40.78
Std Dev	3.638

Gaus mean : 41.9901

Gaus Width: 4.6239

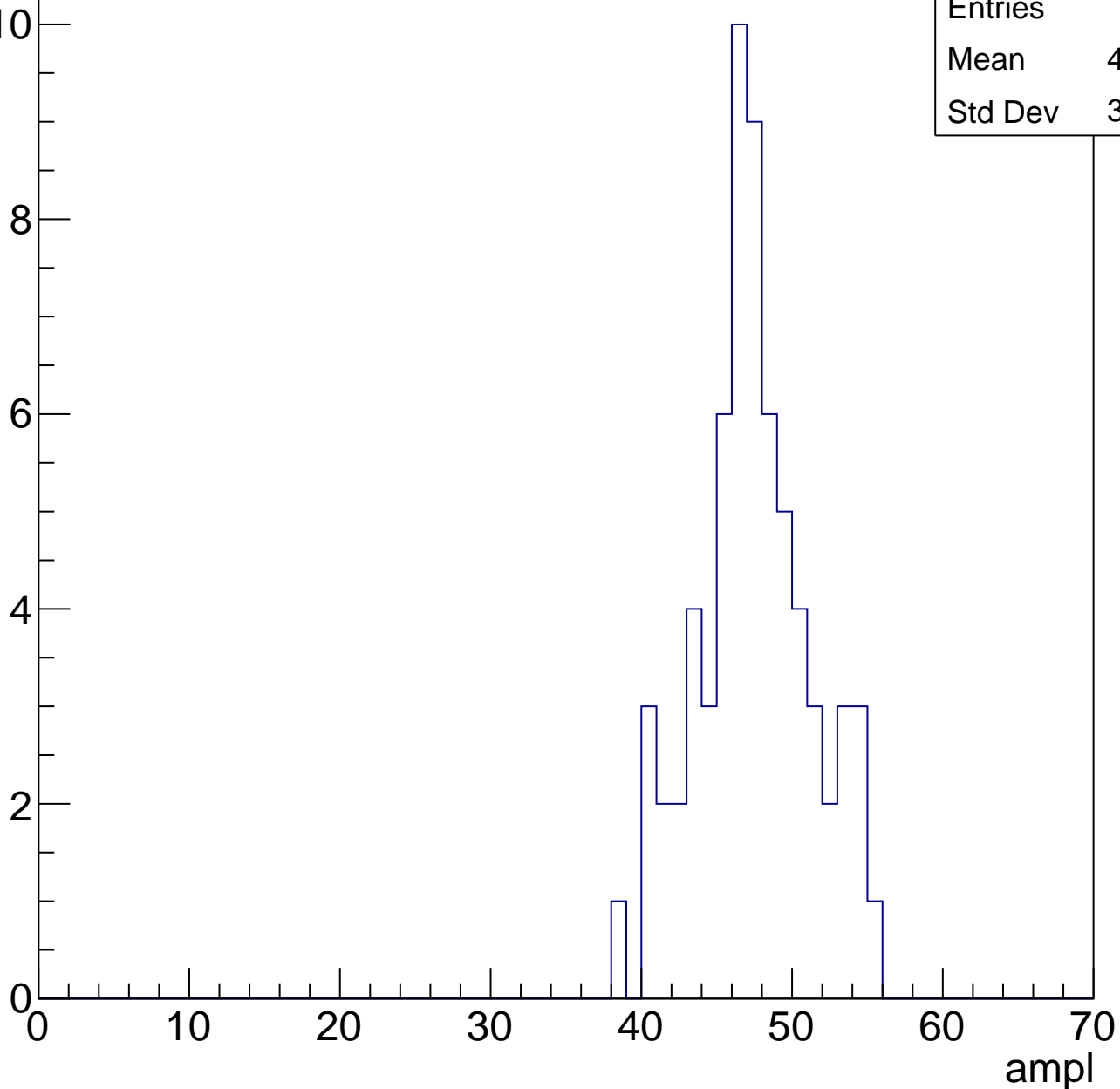


B1L101S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	46.97
Std Dev	3.773

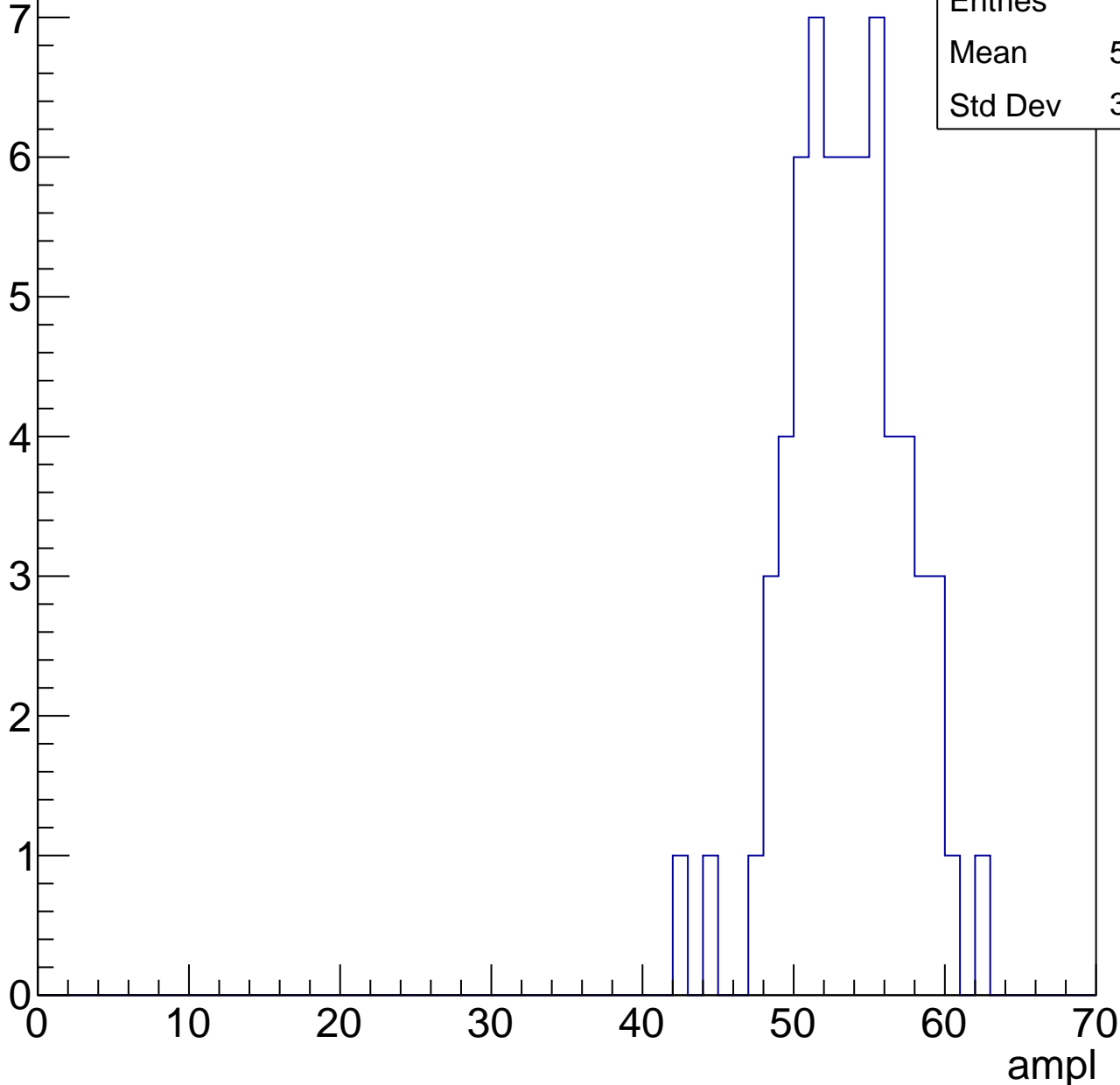


B1L101S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	53.03
Std Dev	3.775

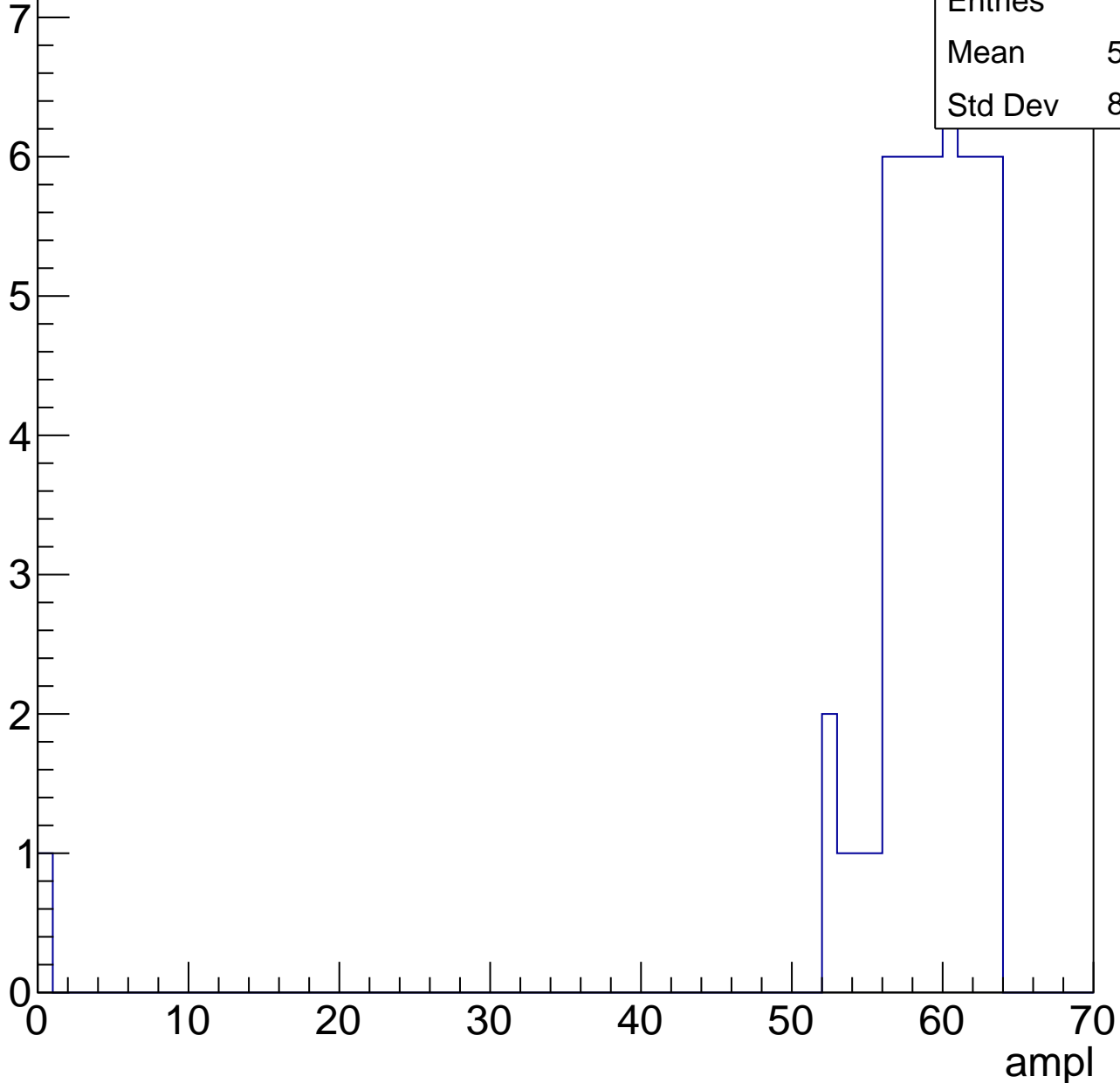


B1L101S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

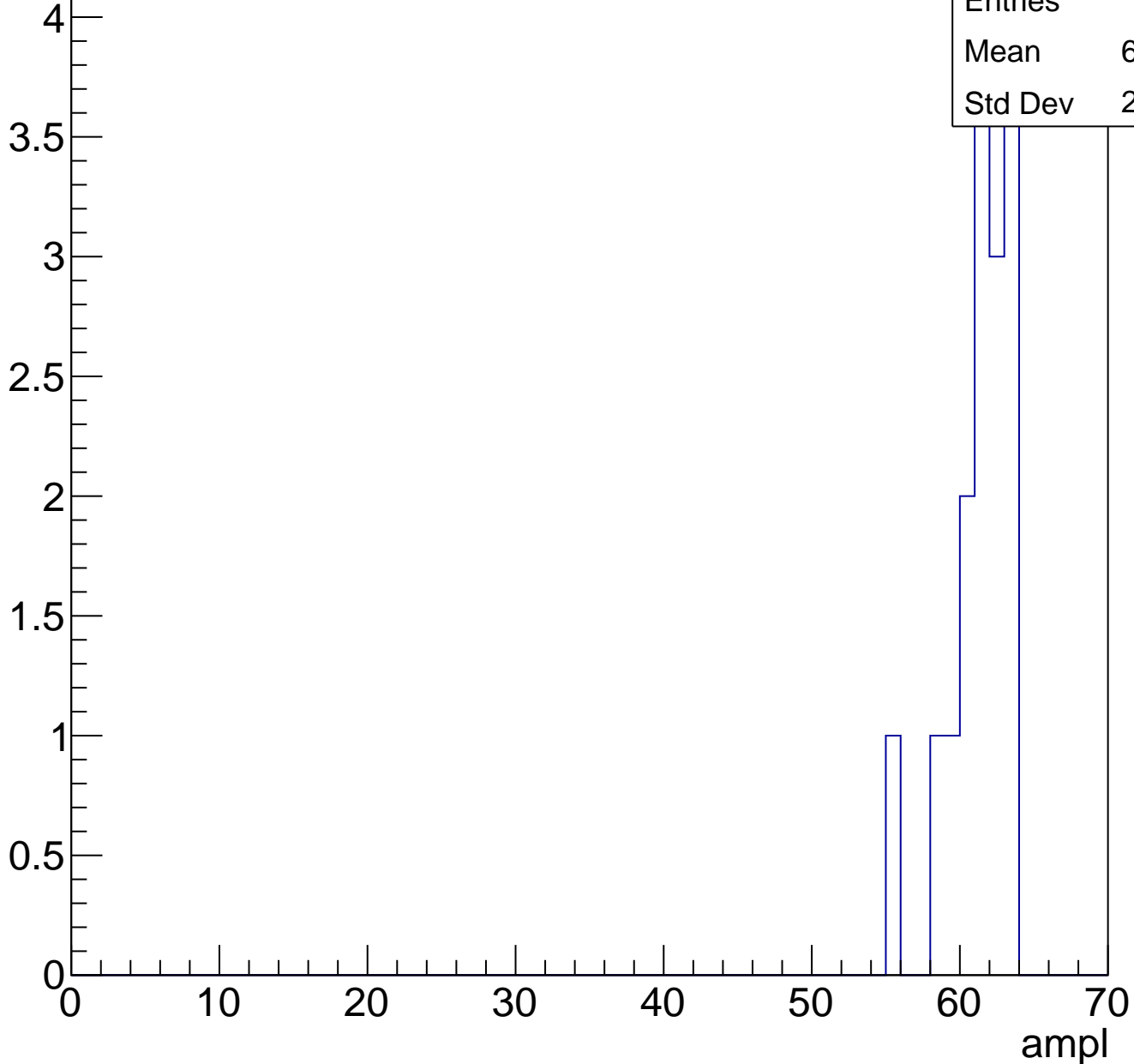
Entries	55
Mean	57.85
Std Dev	8.365



B1L101S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	16
Mean	60.88
Std Dev	2.088

B1L101S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch103, adc0

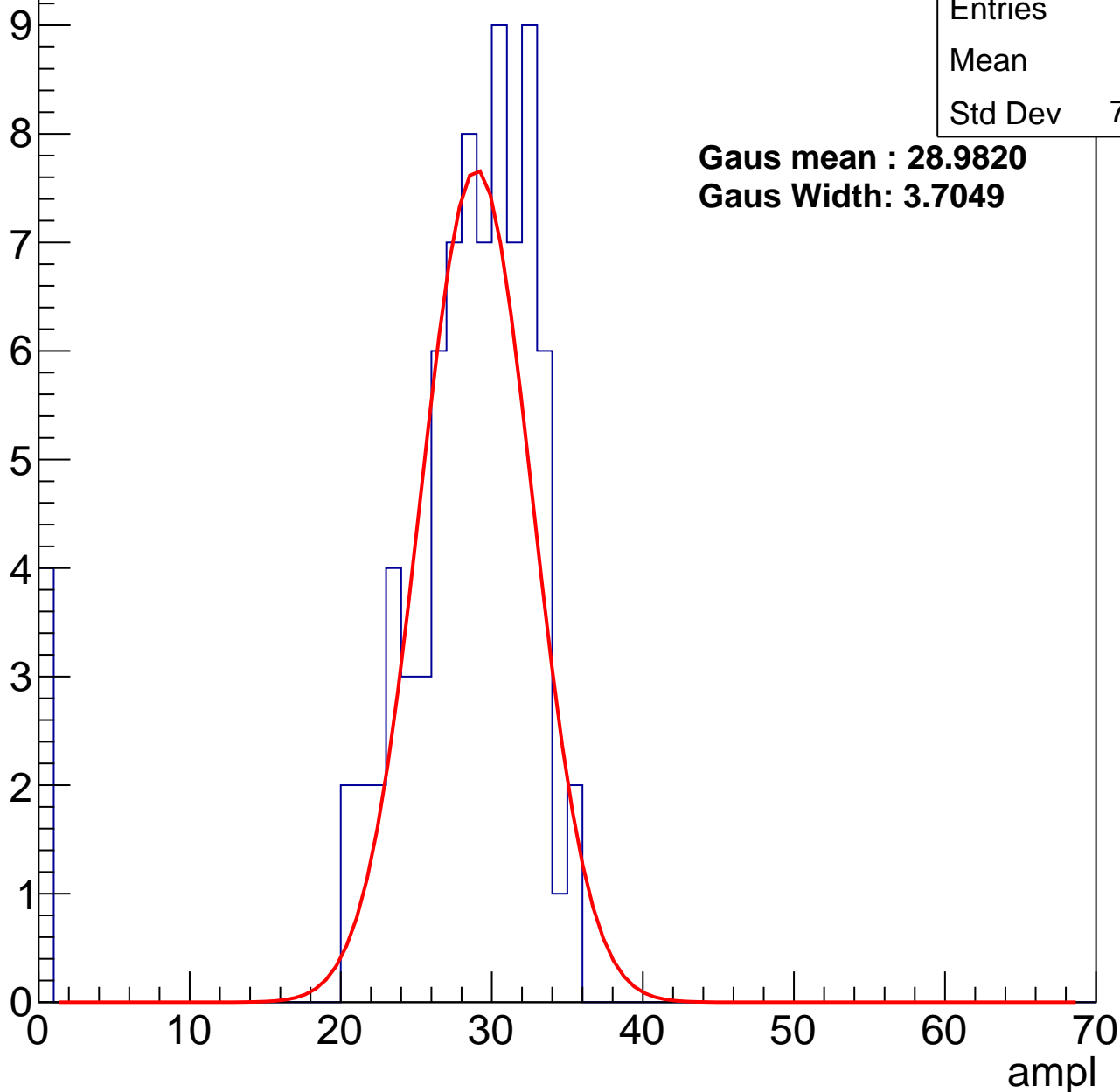
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	27
Std Dev	7.064

Gaus mean : 28.9820

Gaus Width: 3.7049



B1L101S, U14-ch103, adc1

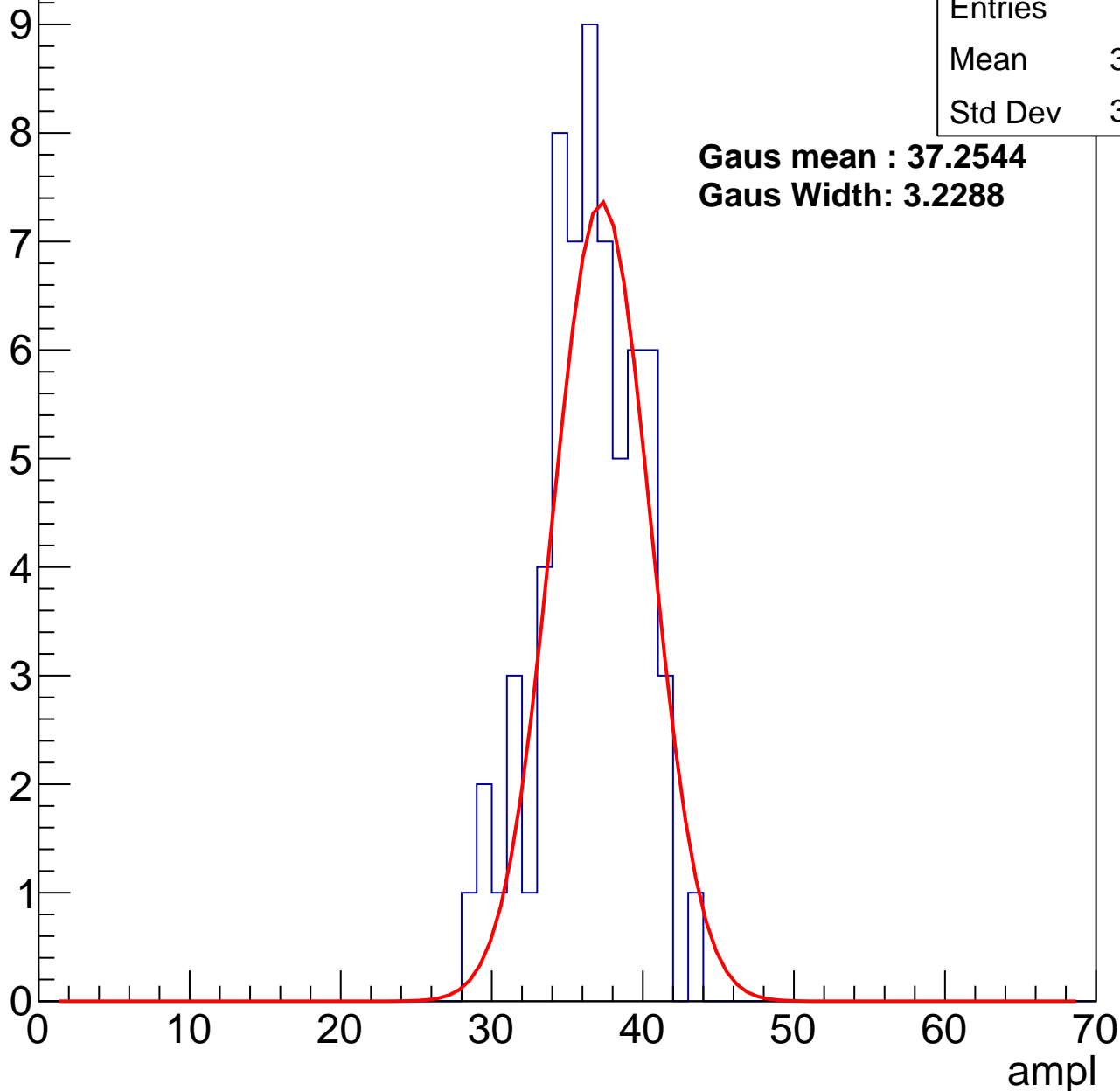
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.98
Std Dev	3.223

Gaus mean : 37.2544

Gaus Width: 3.2288



B1L101S, U14-ch103, adc2

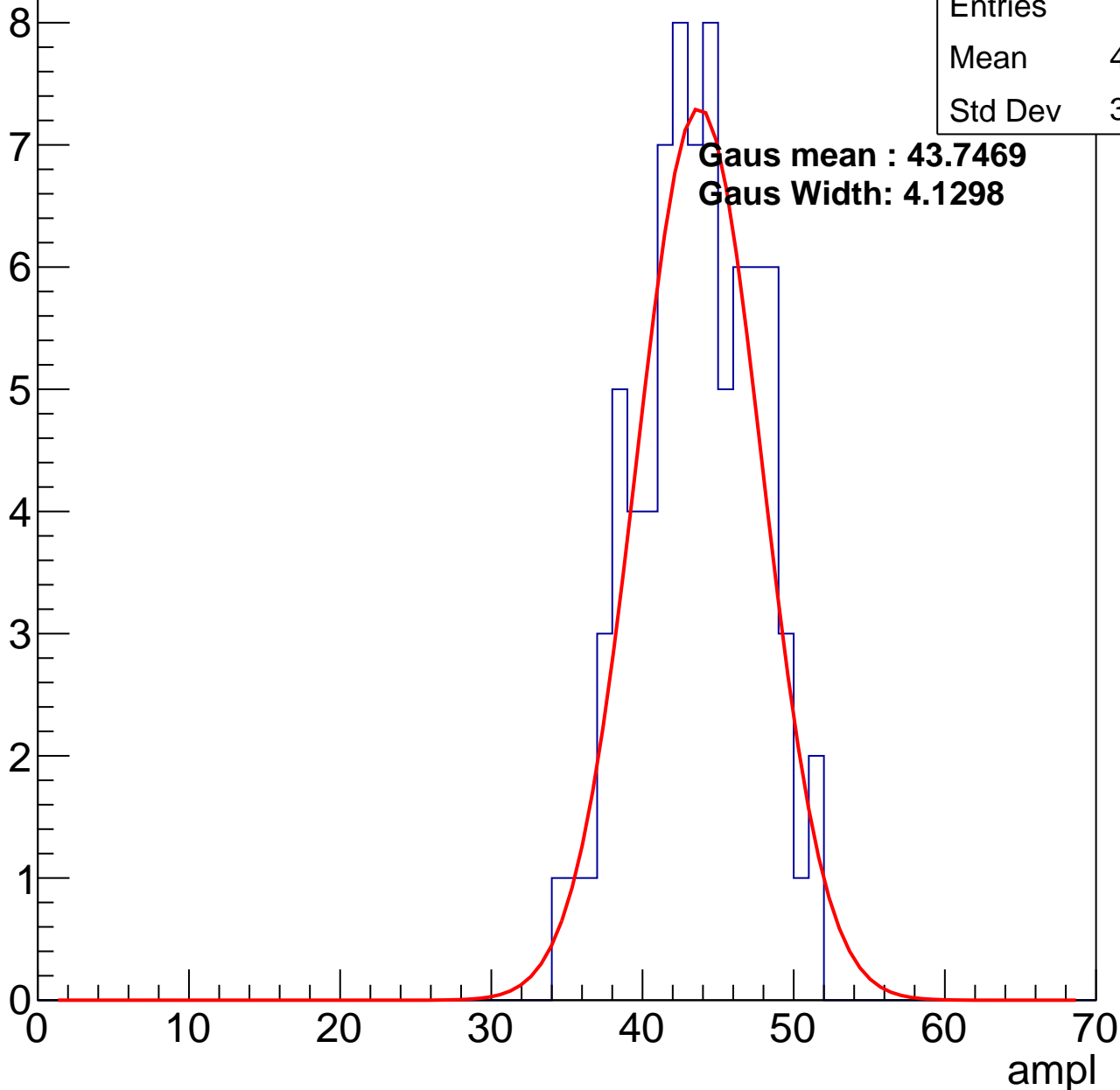
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.18
Std Dev	3.879

Gaus mean : 43.7469

Gaus Width: 4.1298

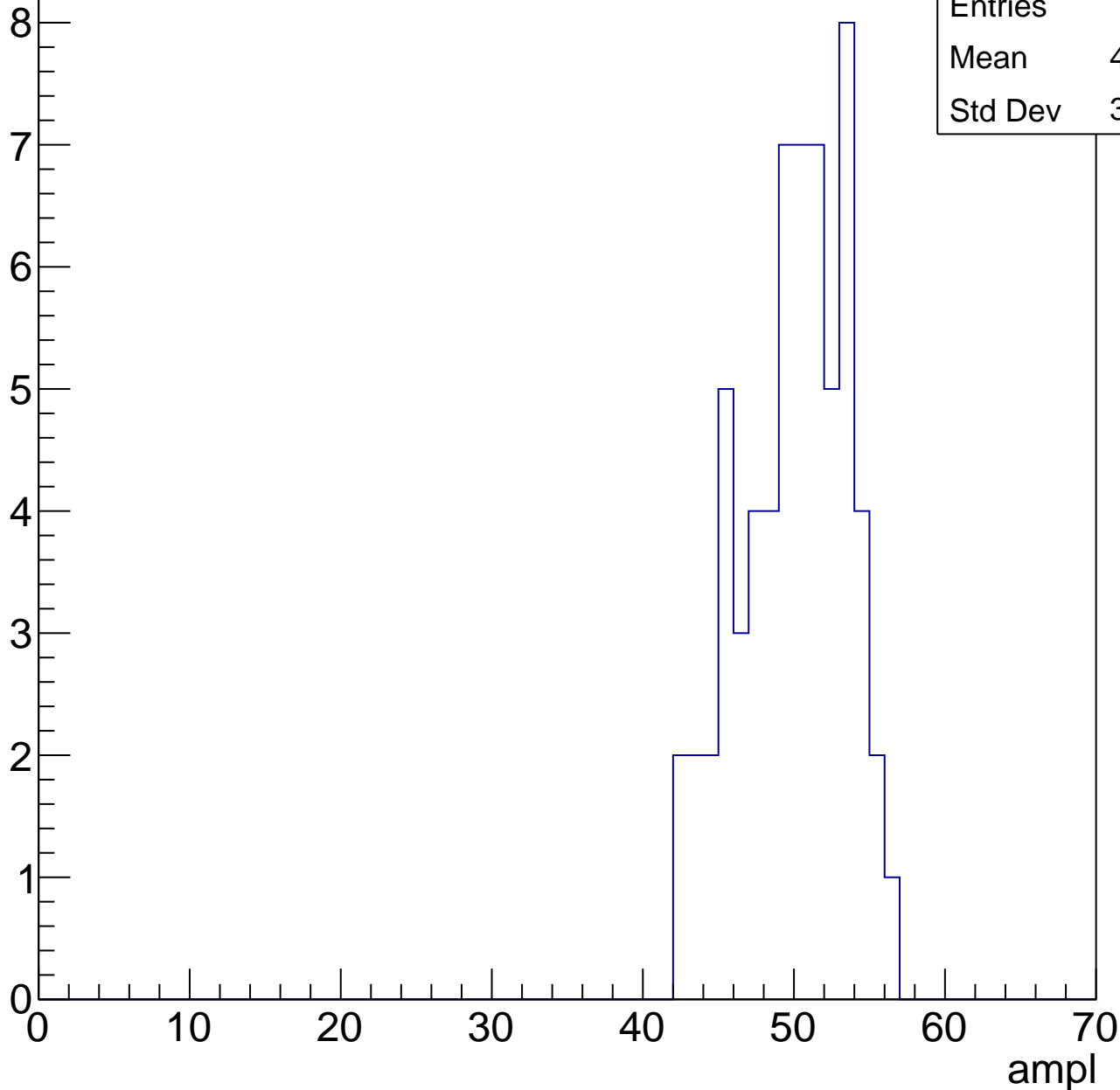


B1L101S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.48
Std Dev	3.477

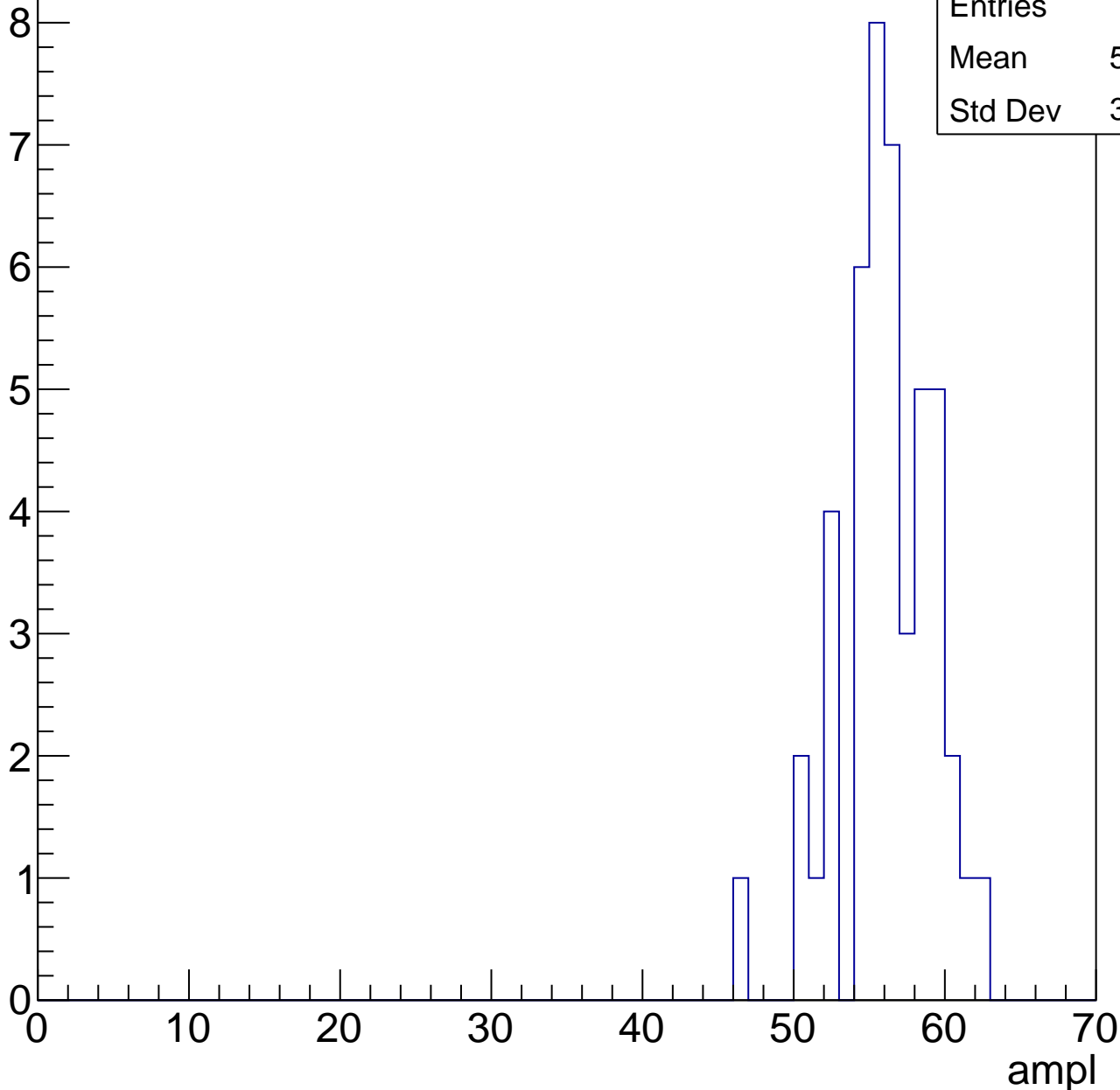


B1L101S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	55.65
Std Dev	3.122

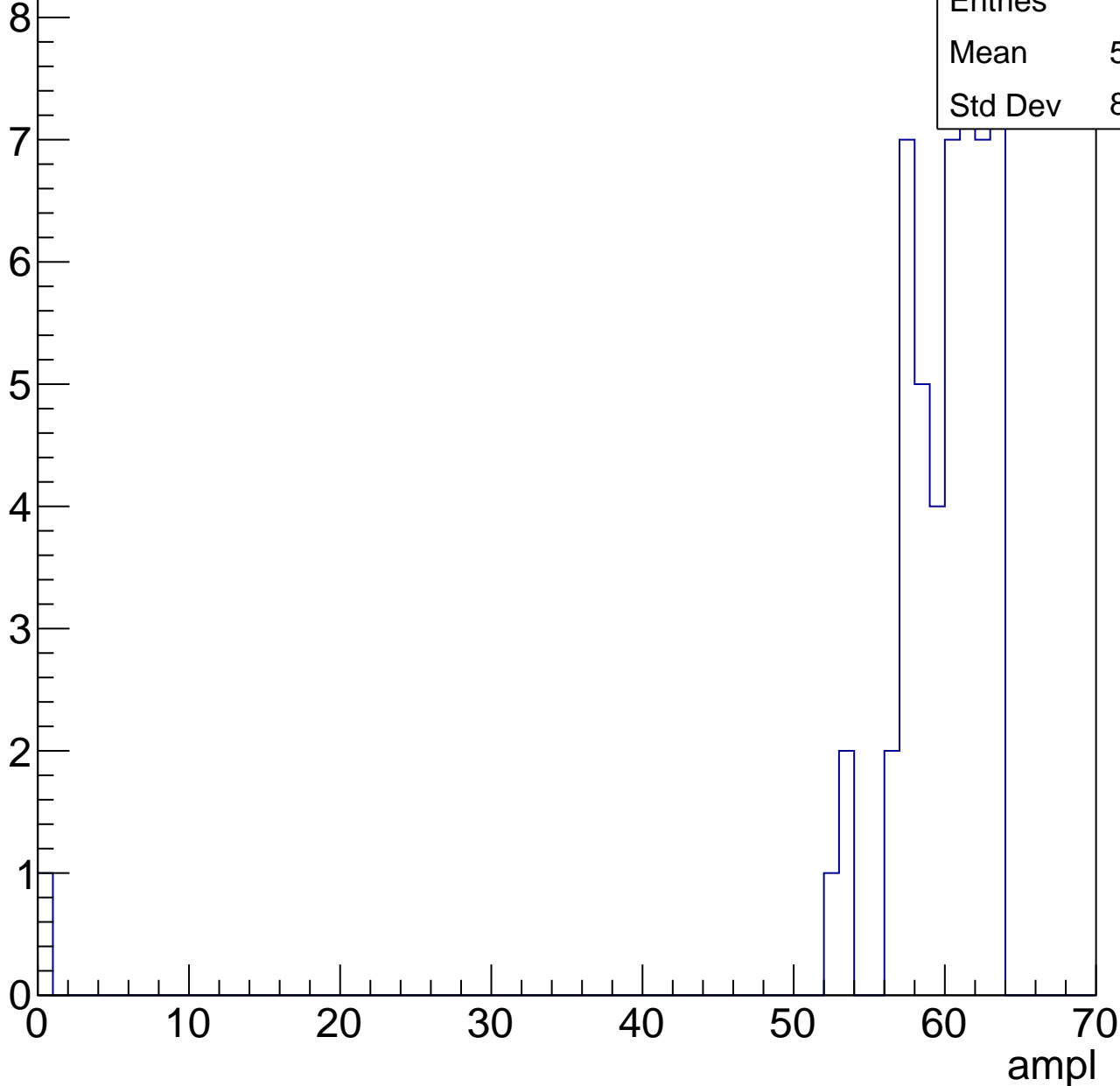


B1L101S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	58.48
Std Dev	8.626



B1L101S, U14-ch103, adc6

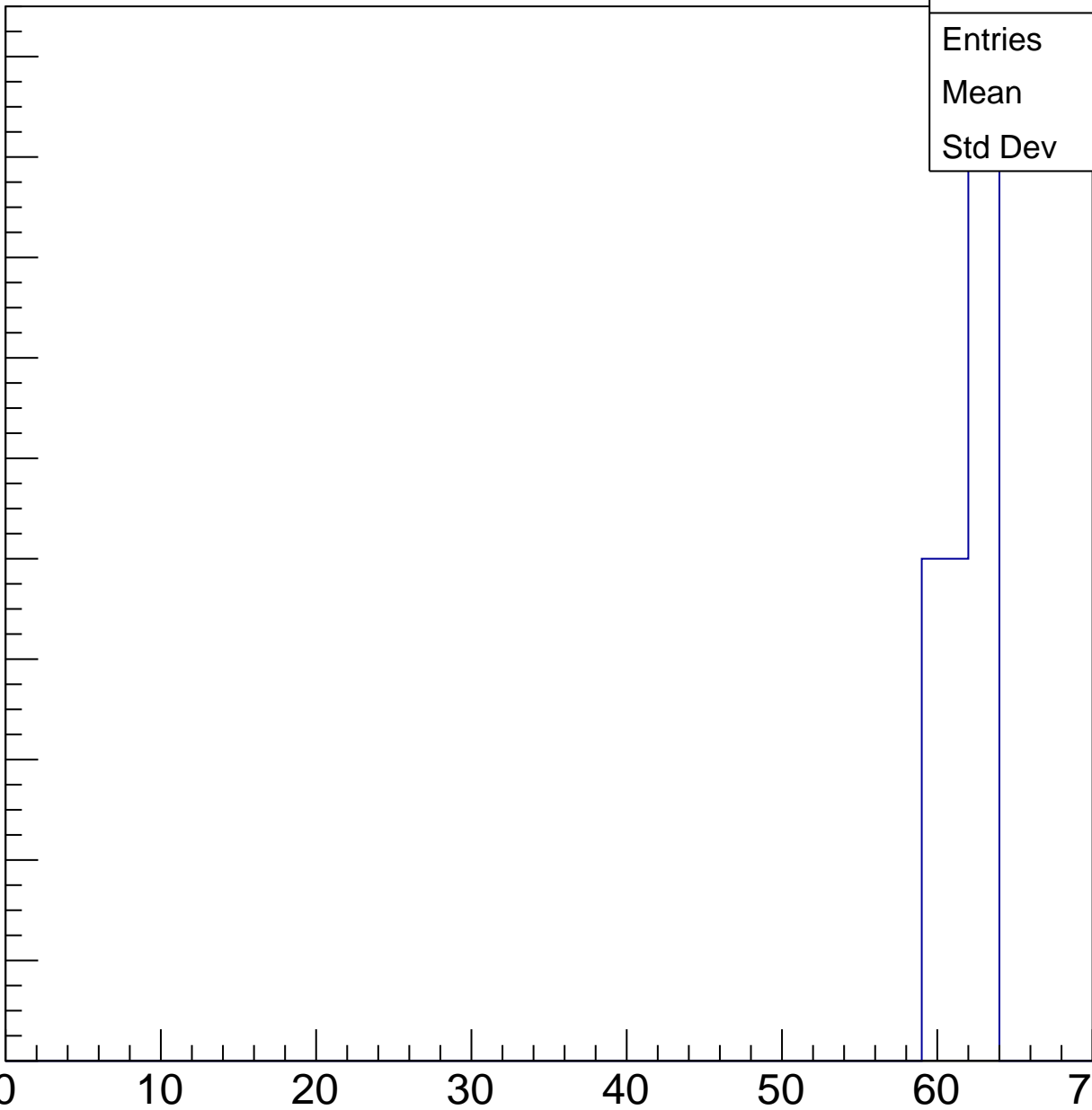
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.43
Std Dev	1.4

ampl



B1L101S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch104, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	27.44
Std Dev	3.869

Gaus mean : 28.2782

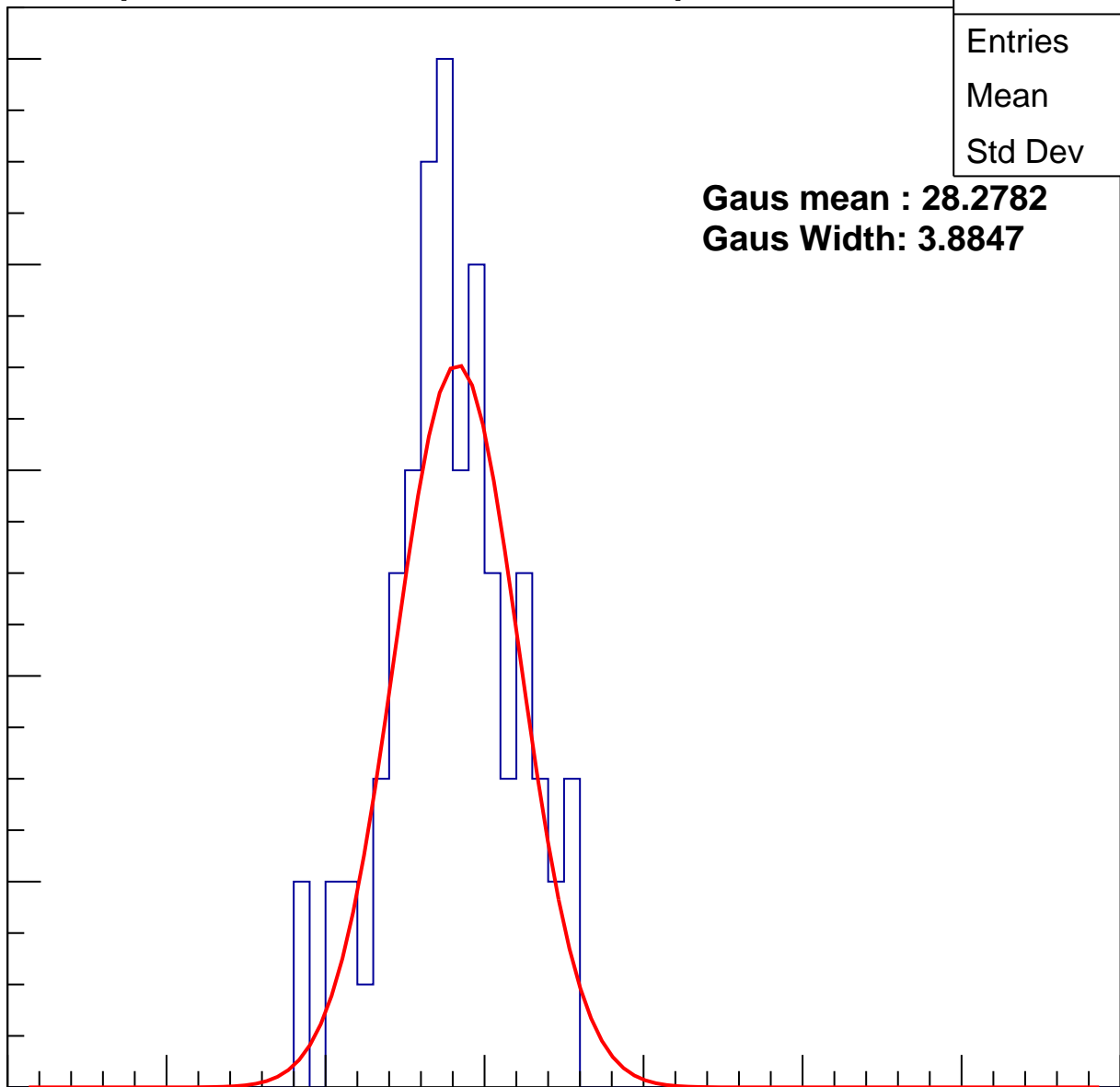
Gaus Width: 3.8847

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch104, adc1

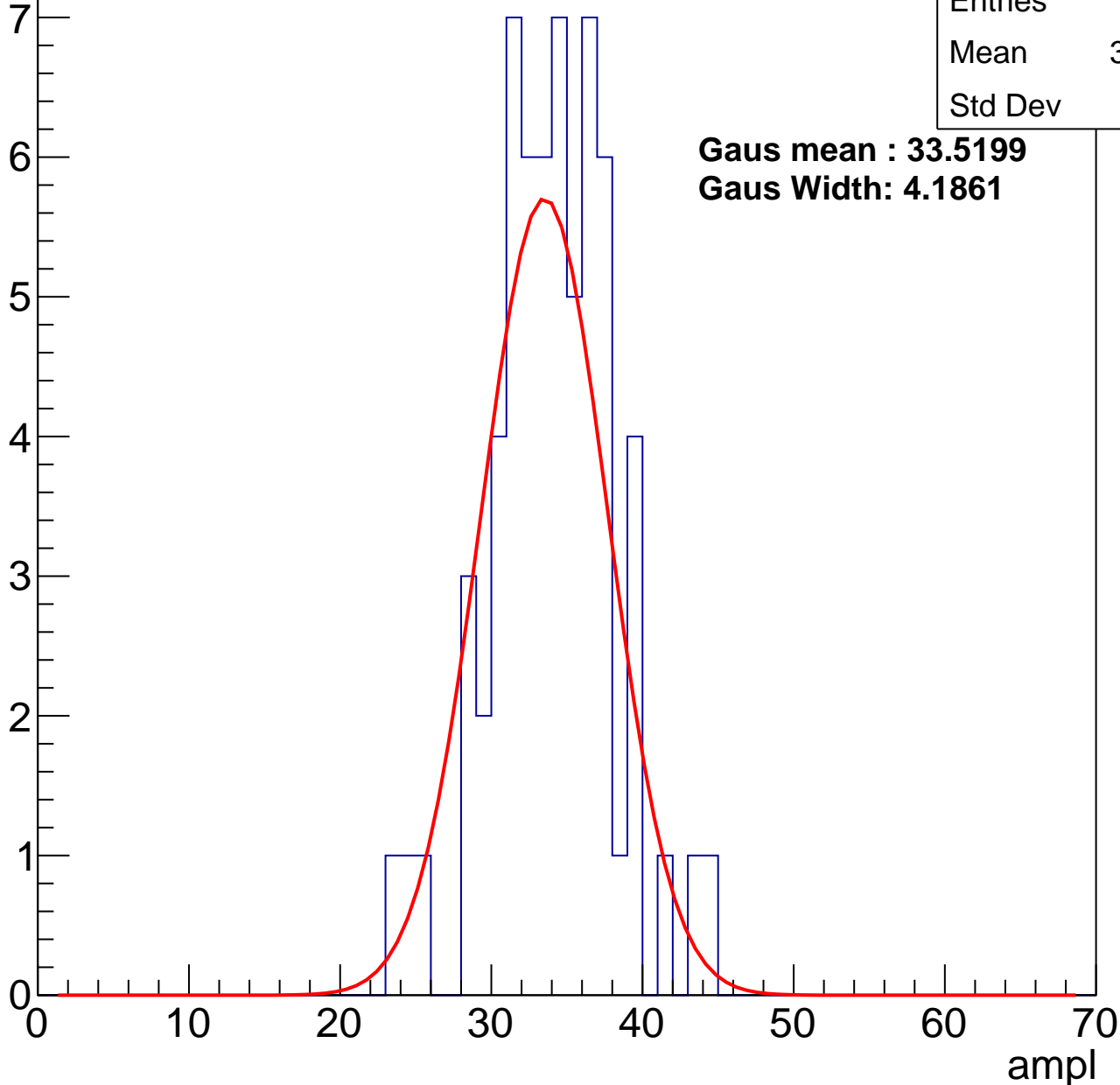
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	33.59
Std Dev	4.03

Gaus mean : 33.5199

Gaus Width: 4.1861



B1L101S, U14-ch104, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	40.99
Std Dev	3.454

Gaus mean : 41.4747

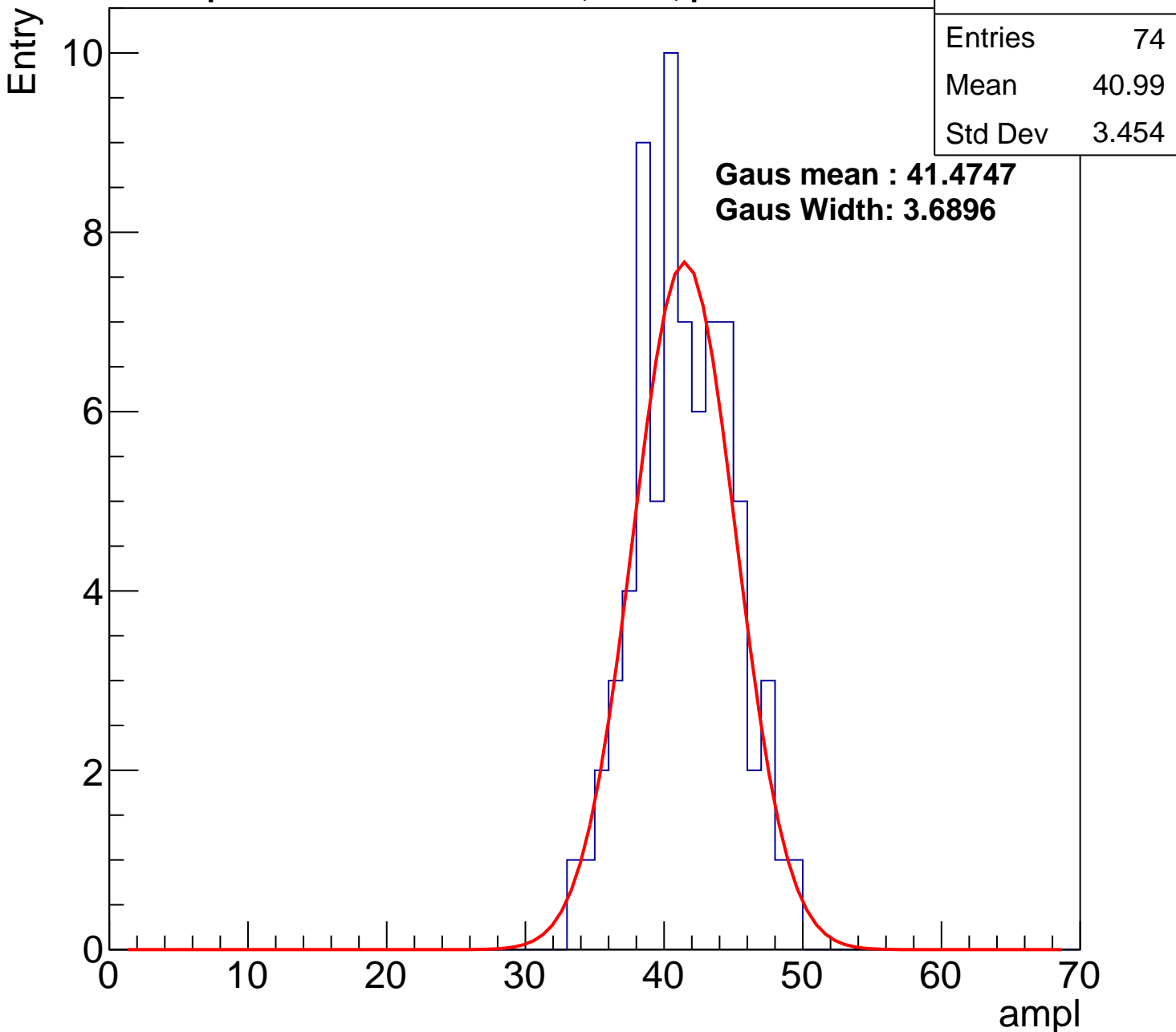
Gaus Width: 3.6896

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

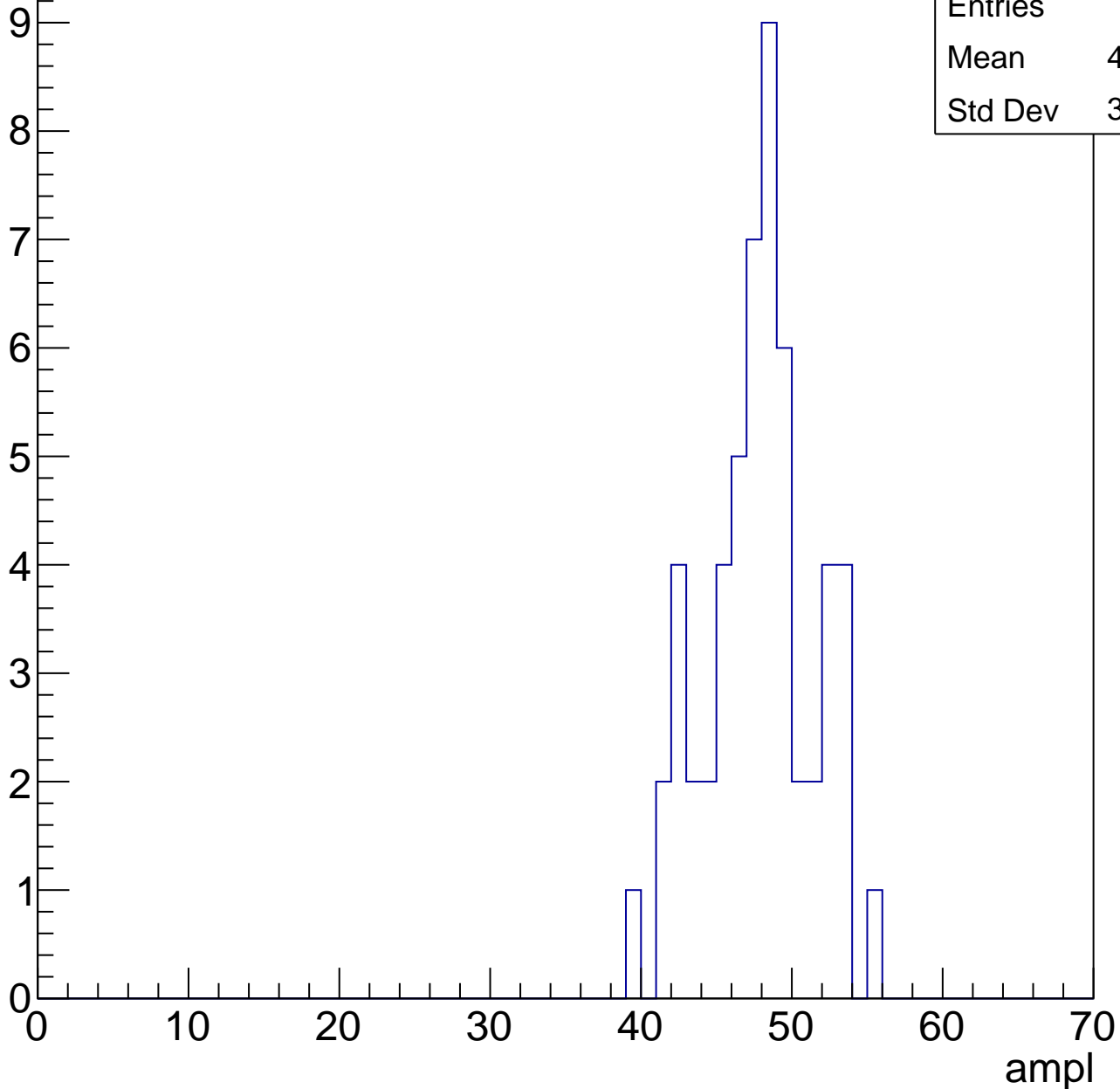


B1L101S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	47.36
Std Dev	3.544

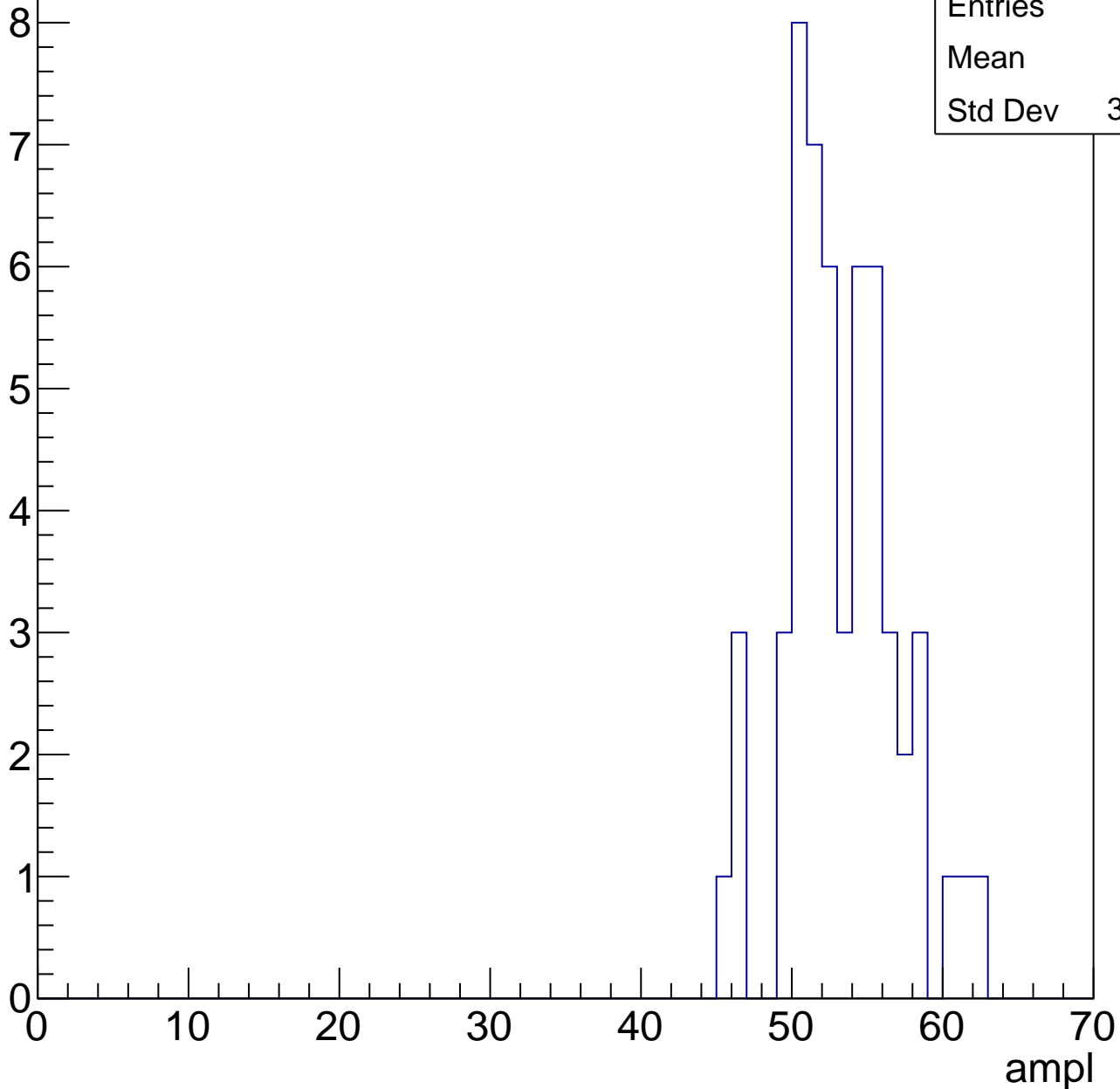


B1L101S, U14-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	52.8
Std Dev	3.653

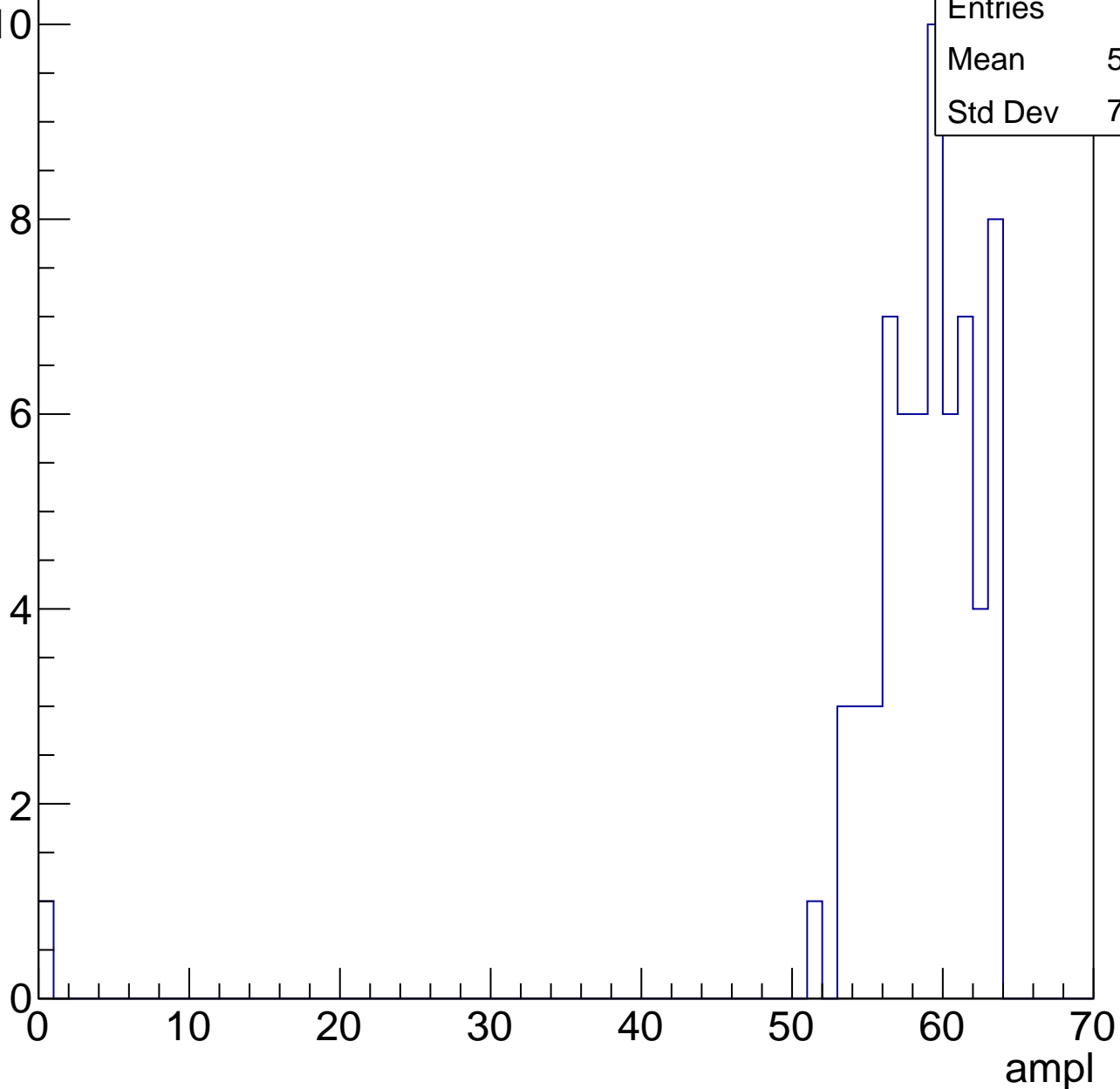


B1L101S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

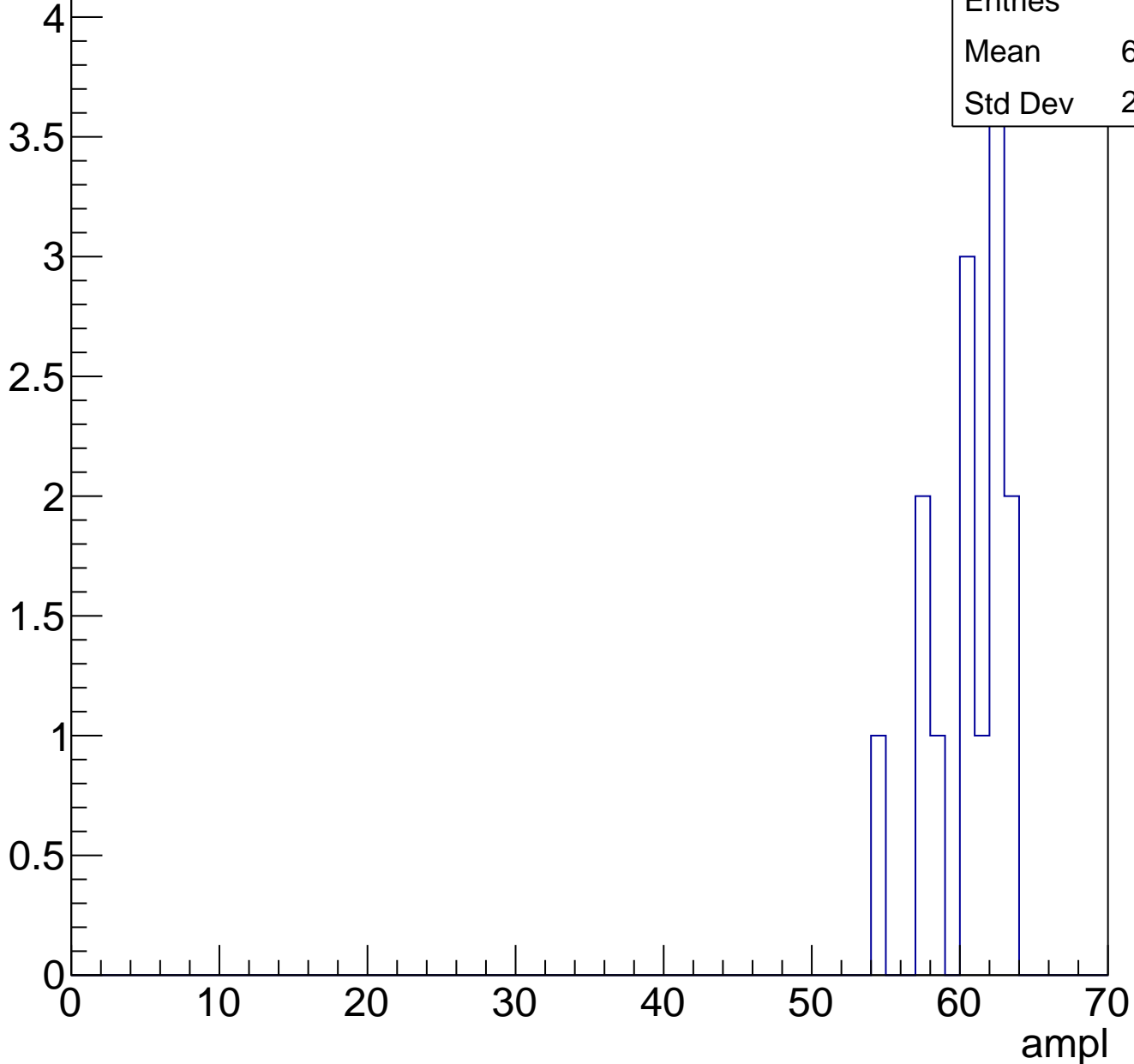
Entries	65
Mean	57.66
Std Dev	7.796



B1L101S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

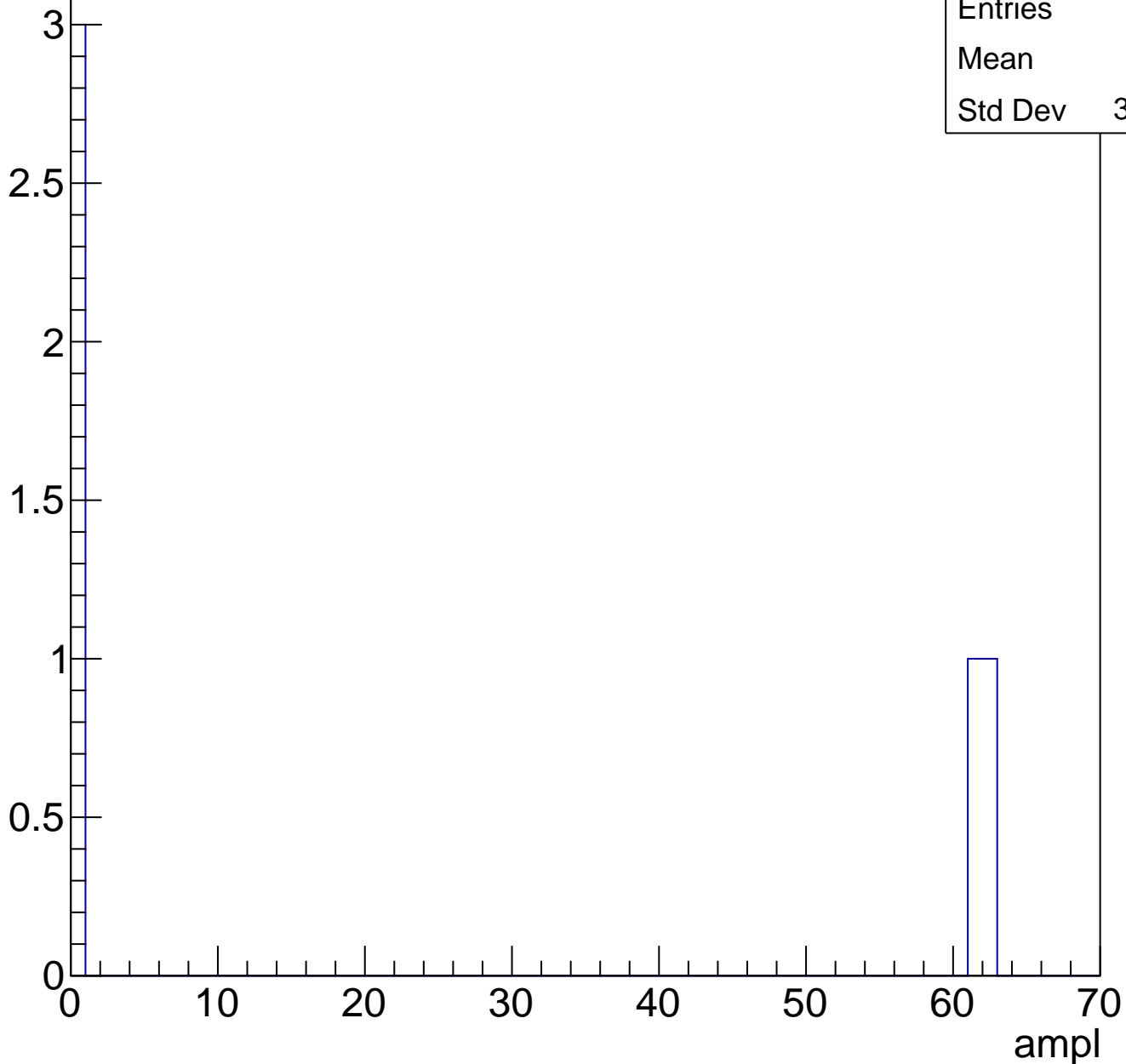
Entry



B1L101S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch105, adc0

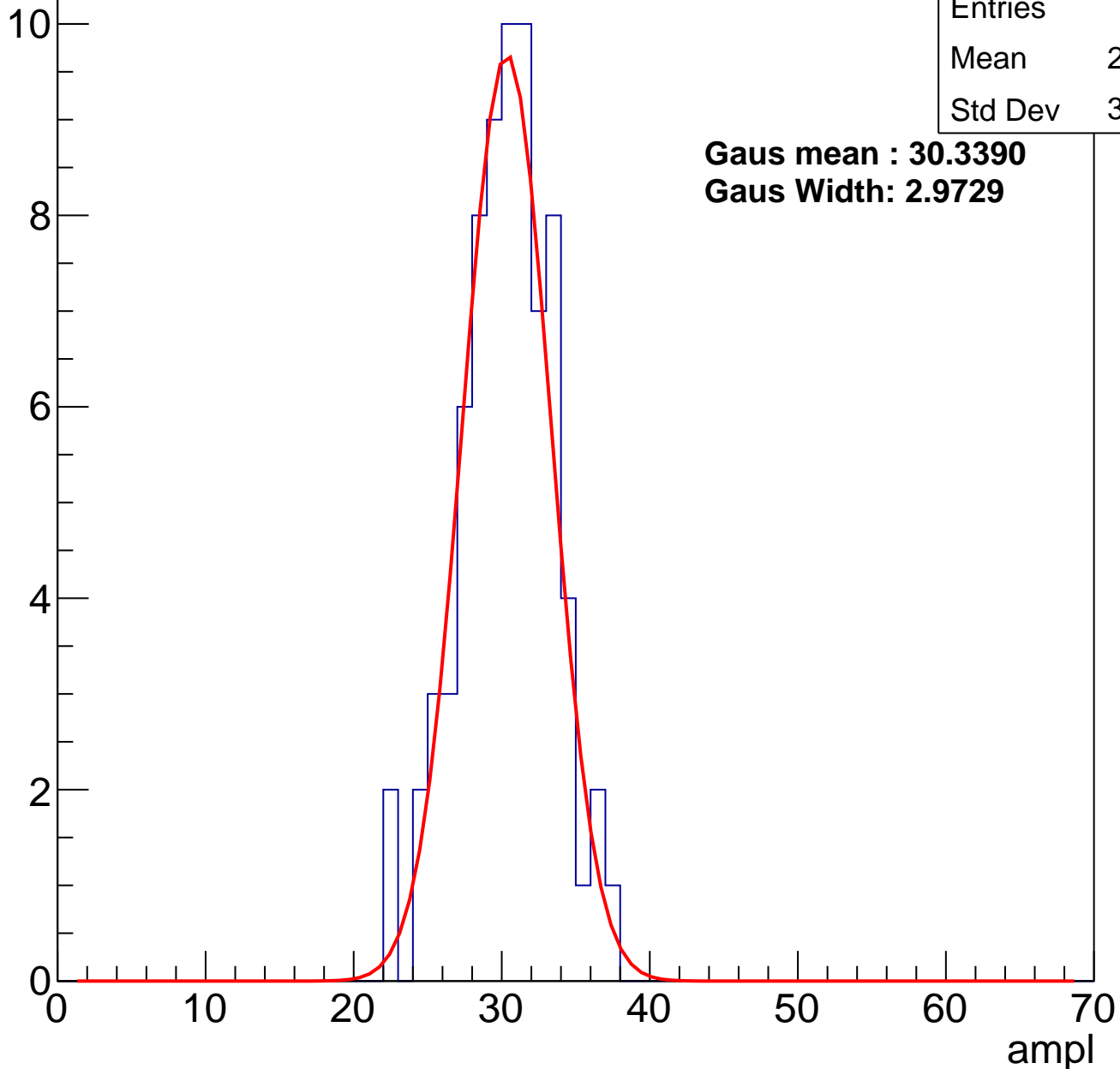
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	29.87
Std Dev	3.113

Gaus mean : 30.3390

Gaus Width: 2.9729

Entry



B1L101S, U14-ch105, adc1

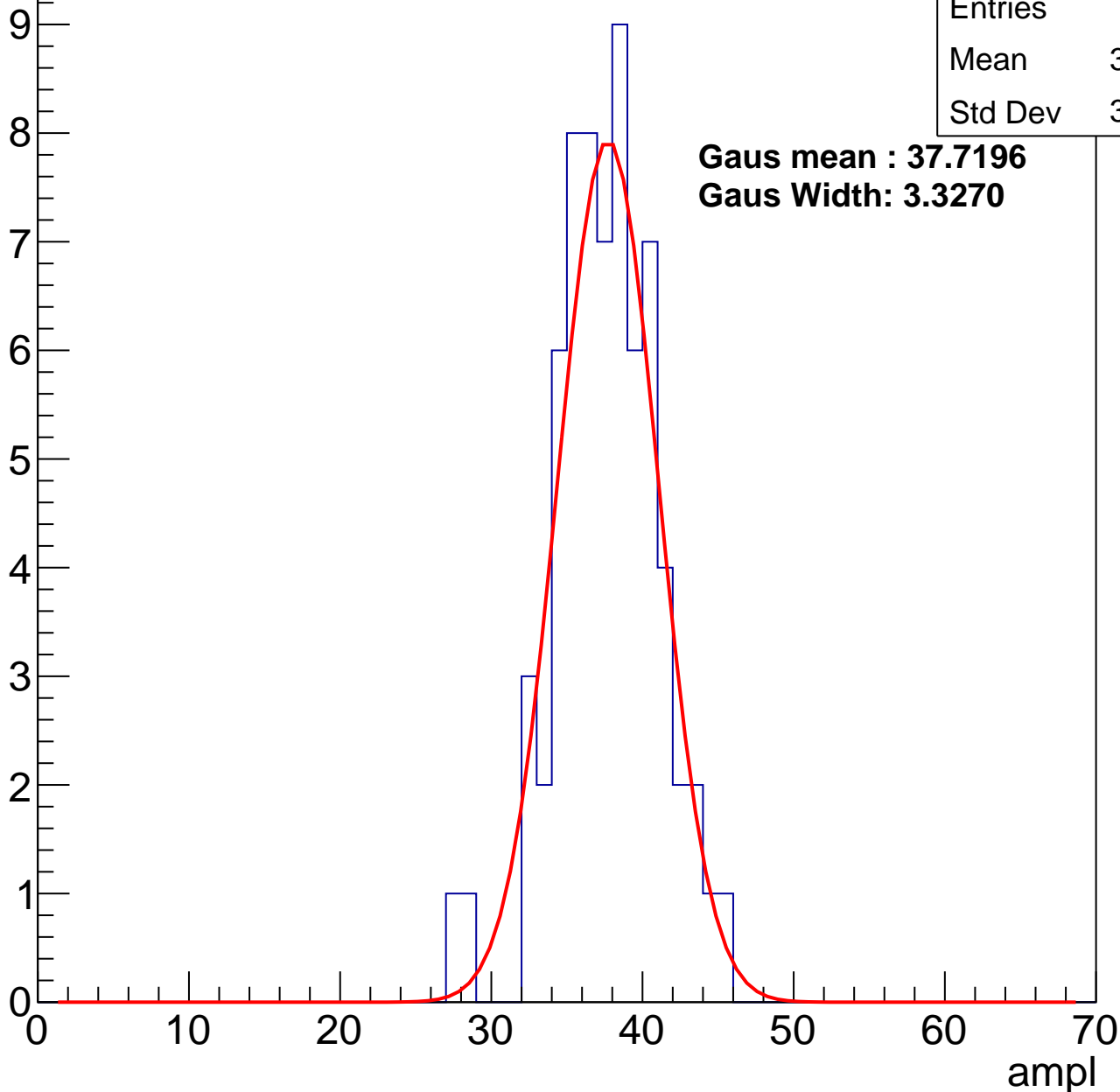
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.16
Std Dev	3.376

Gaus mean : 37.7196

Gaus Width: 3.3270



B1L101S, U14-ch105, adc2

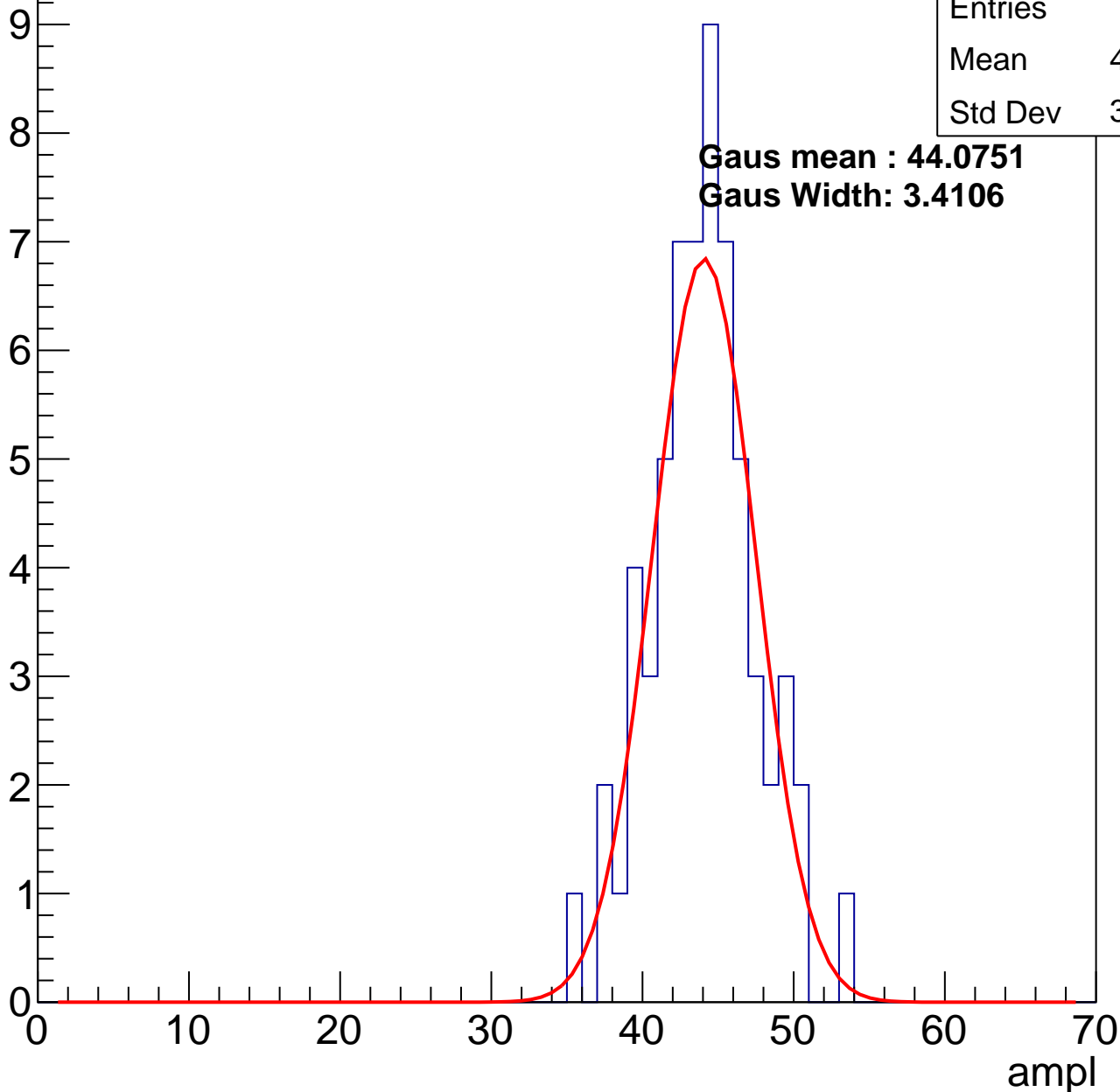
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.56
Std Dev	3.467

Gaus mean : 44.0751

Gaus Width: 3.4106

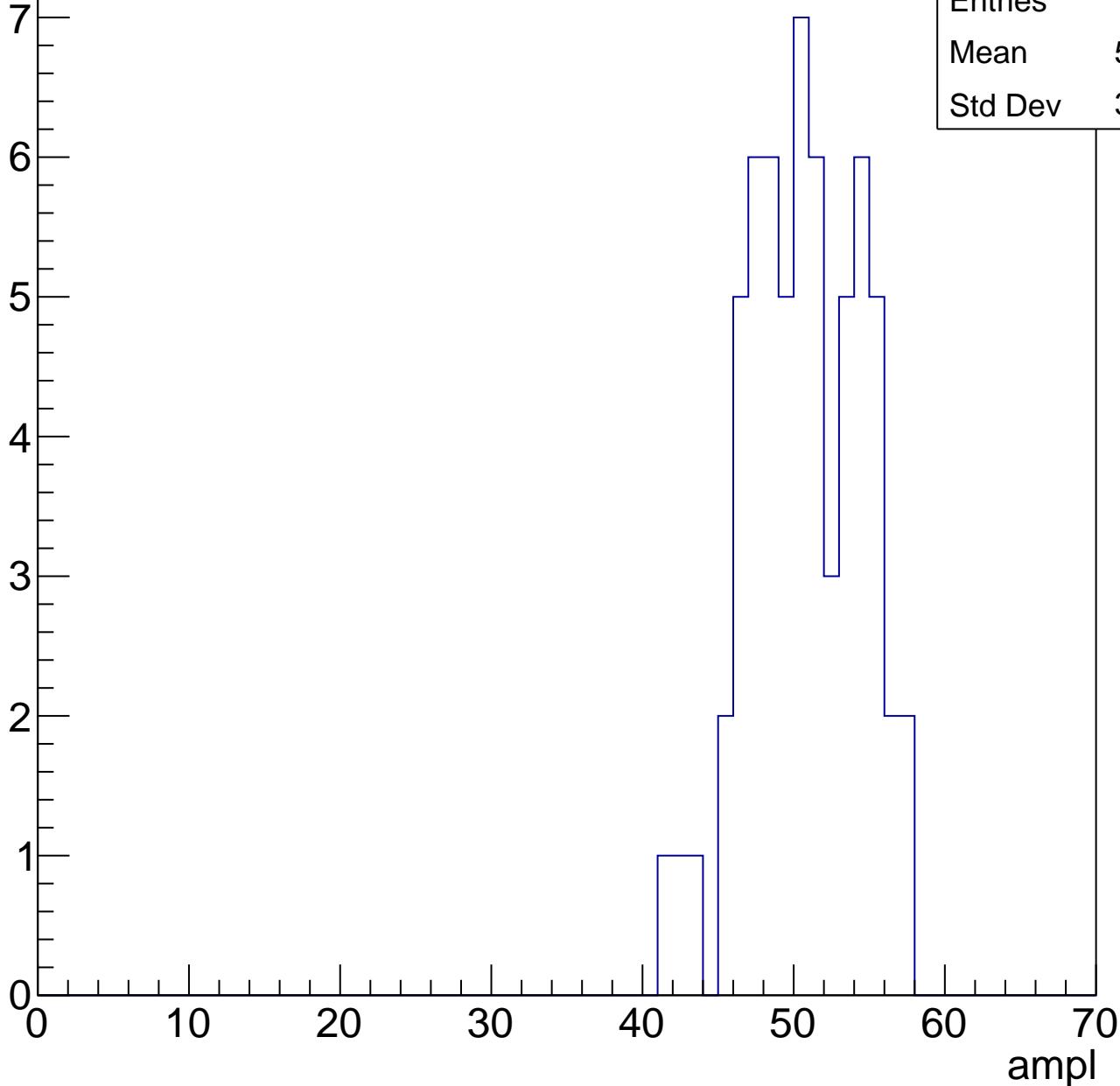


B1L101S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

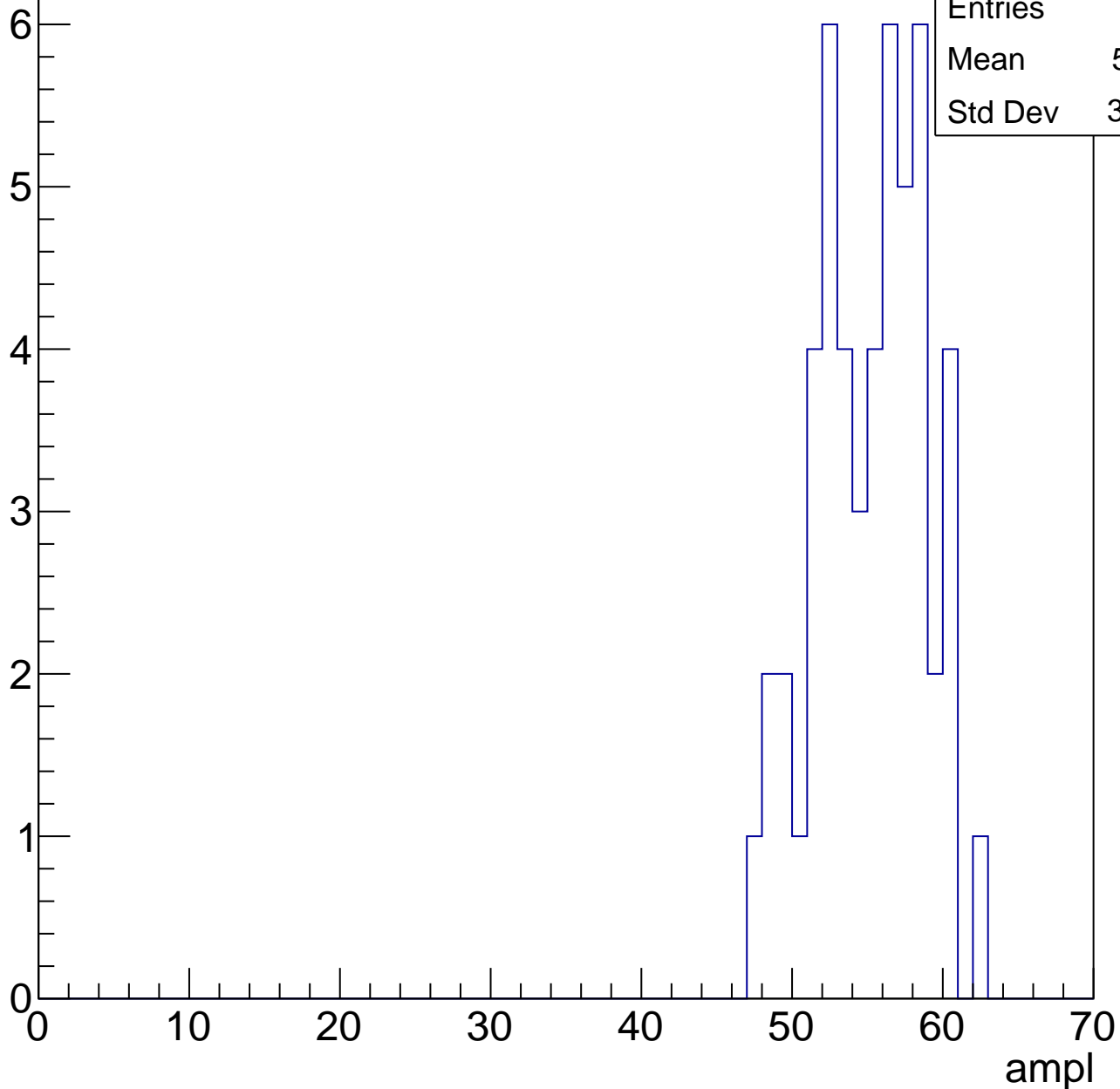
Entries	63
Mean	50.21
Std Dev	3.691



B1L101S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



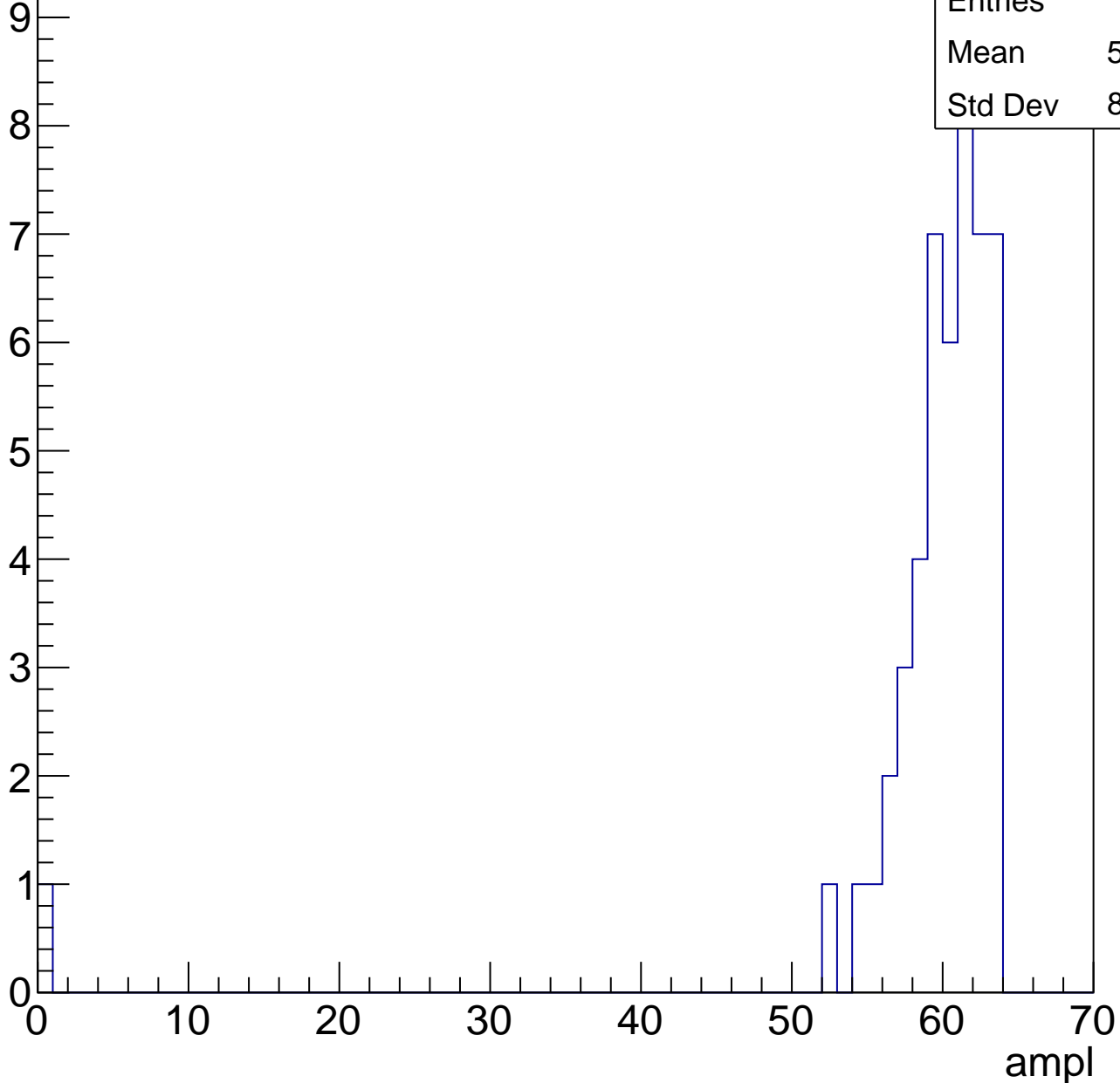
Entries	51
Mean	54.71
Std Dev	3.577

B1L101S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.63
Std Dev	8.824



B1L101S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch106, adc0

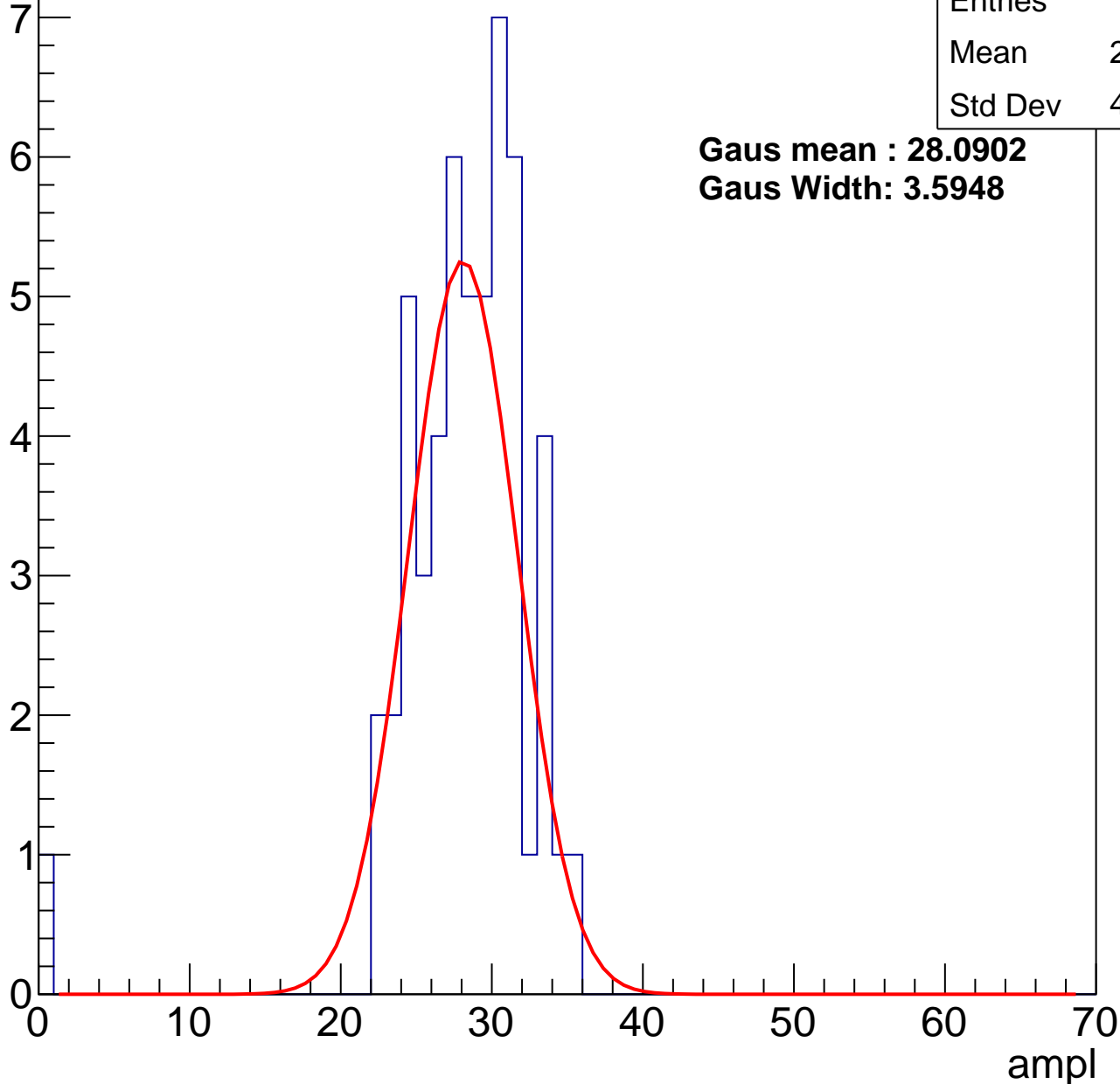
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	27.64
Std Dev	4.983

Gaus mean : 28.0902

Gaus Width: 3.5948



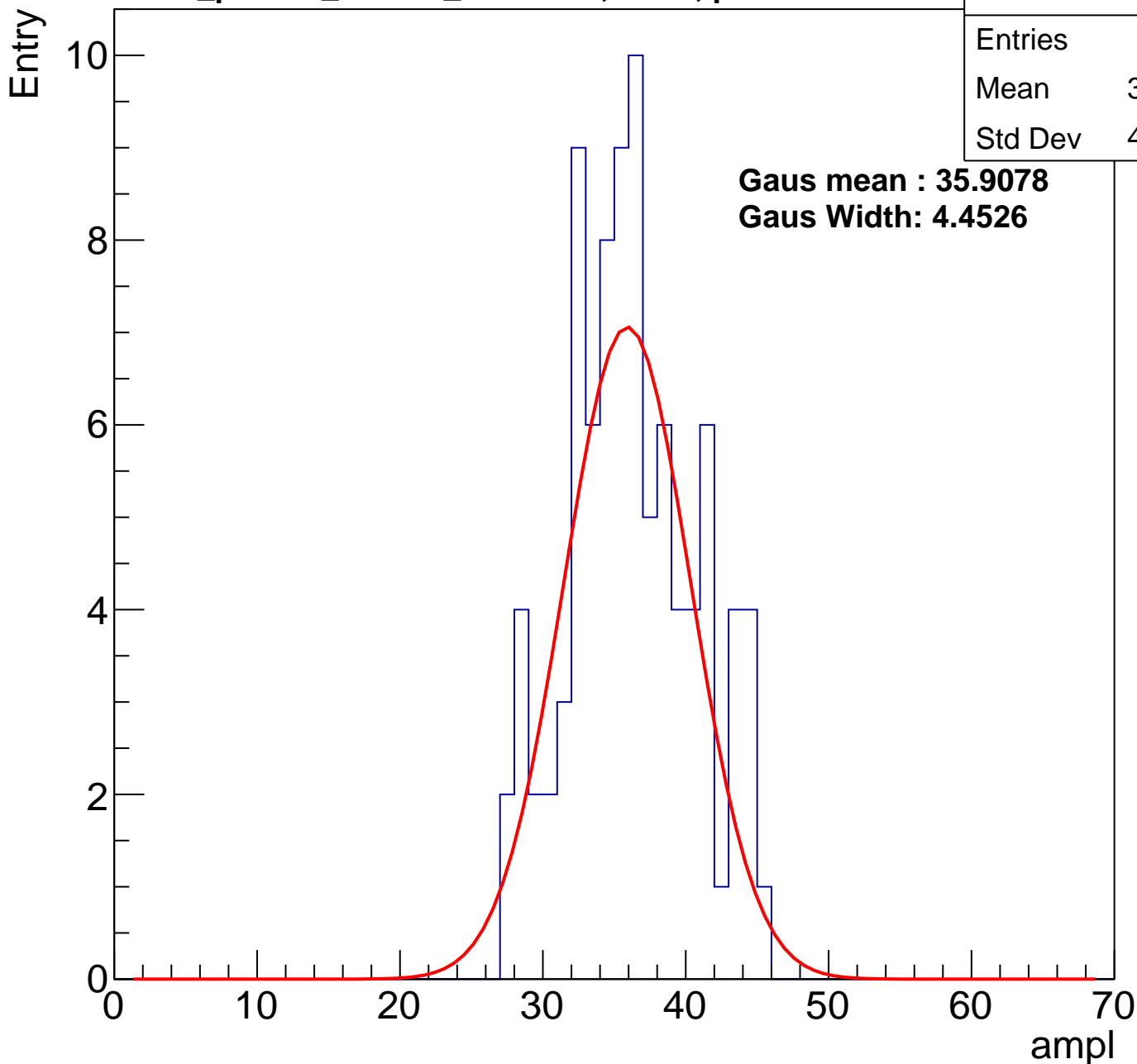
B1L101S, U14-ch106, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	35.78
Std Dev	4.404

Gaus mean : 35.9078

Gaus Width: 4.4526



B1L101S, U14-ch106, adc2

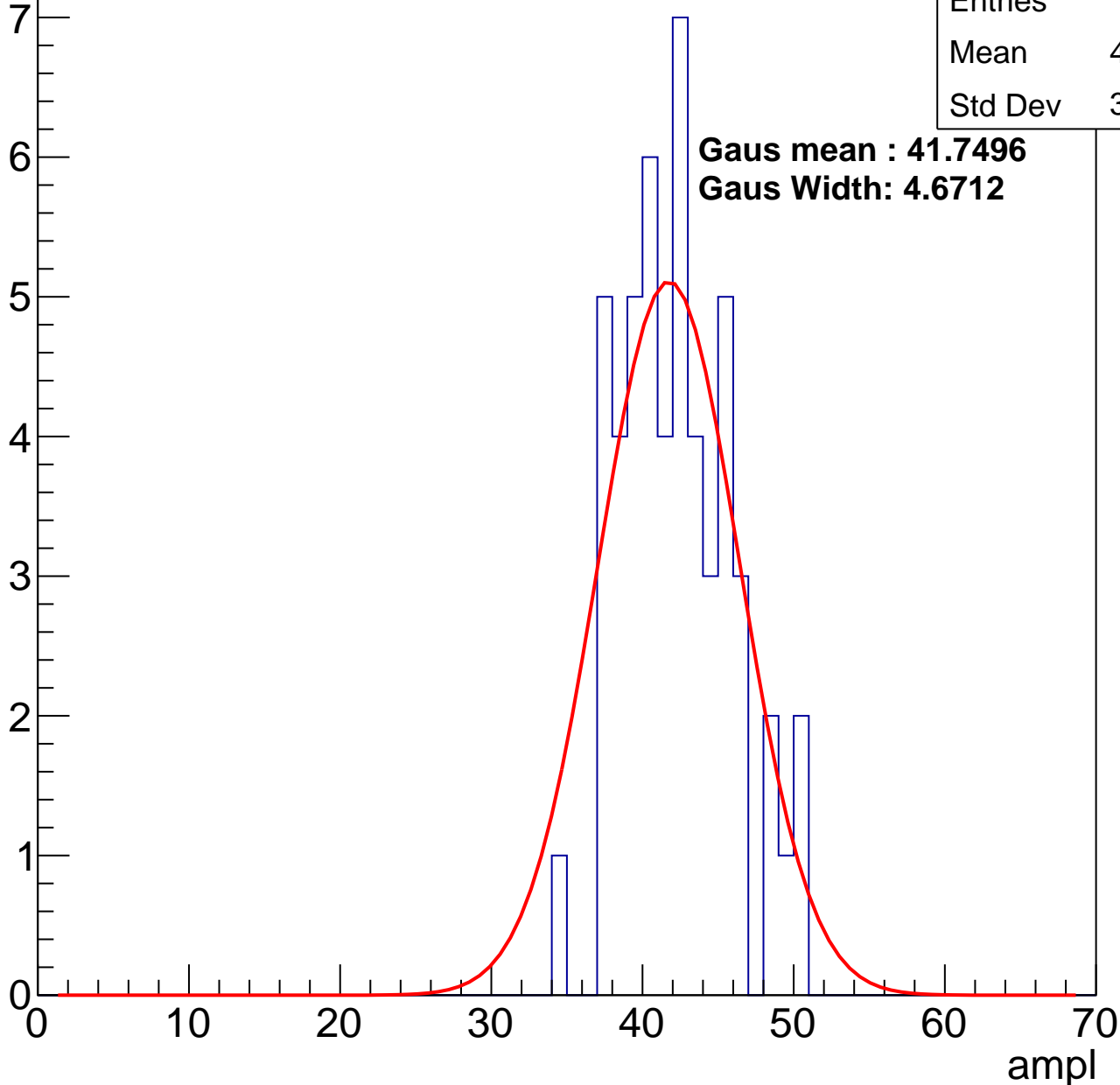
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.85
Std Dev	3.624

Gaus mean : 41.7496

Gaus Width: 4.6712

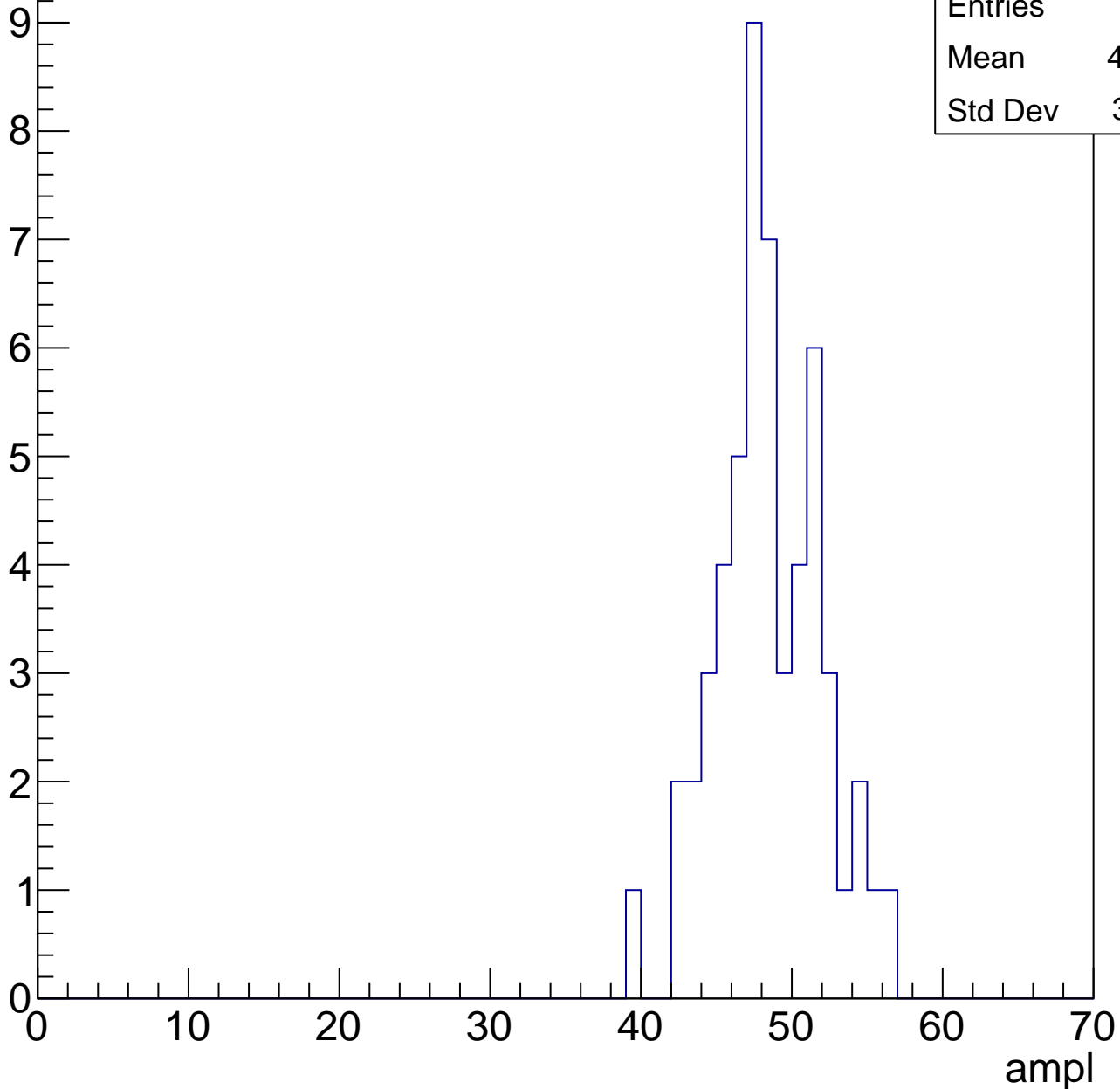


B1L101S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	47.98
Std Dev	3.461

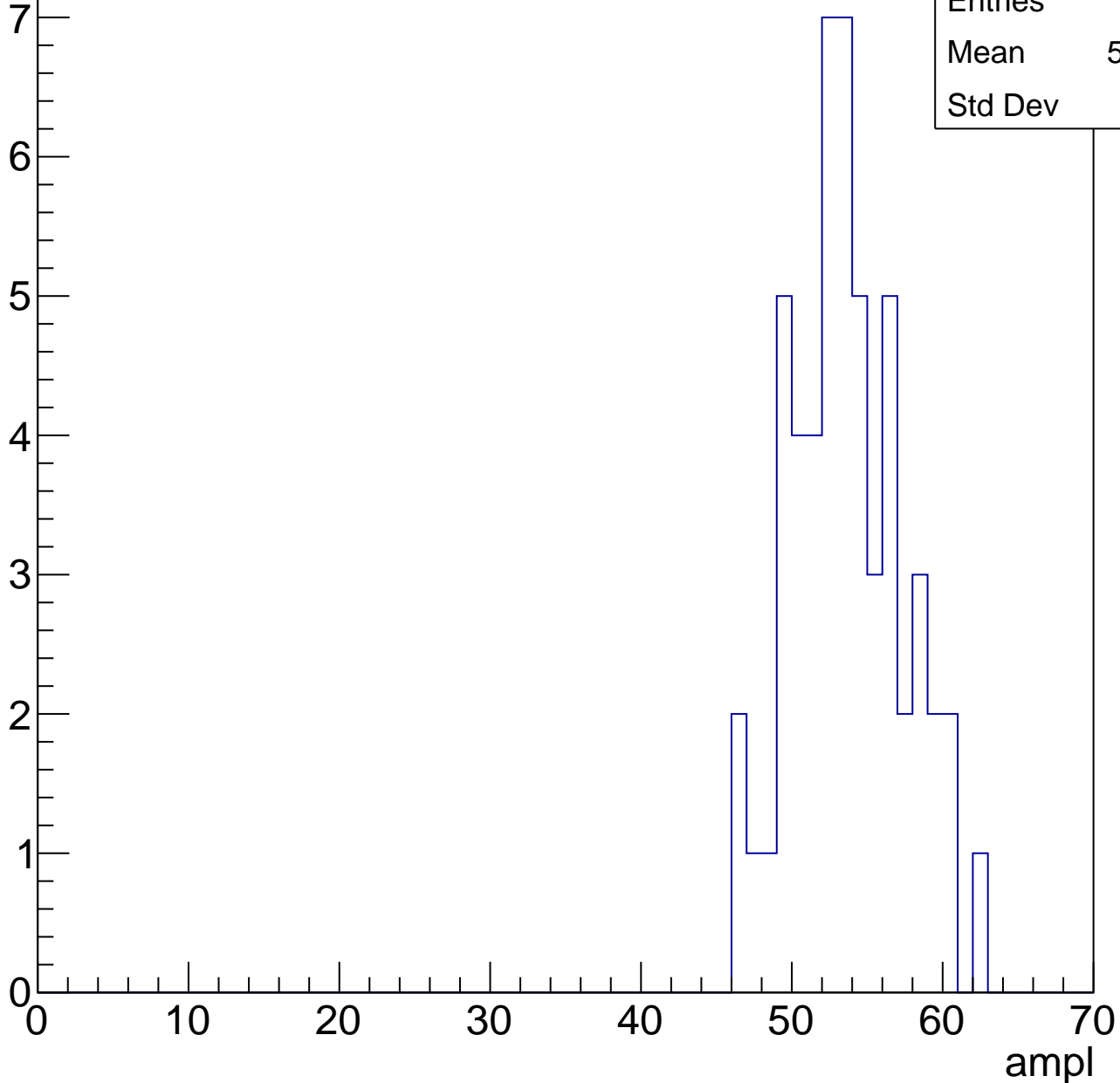


B1L101S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	53.22
Std Dev	3.64

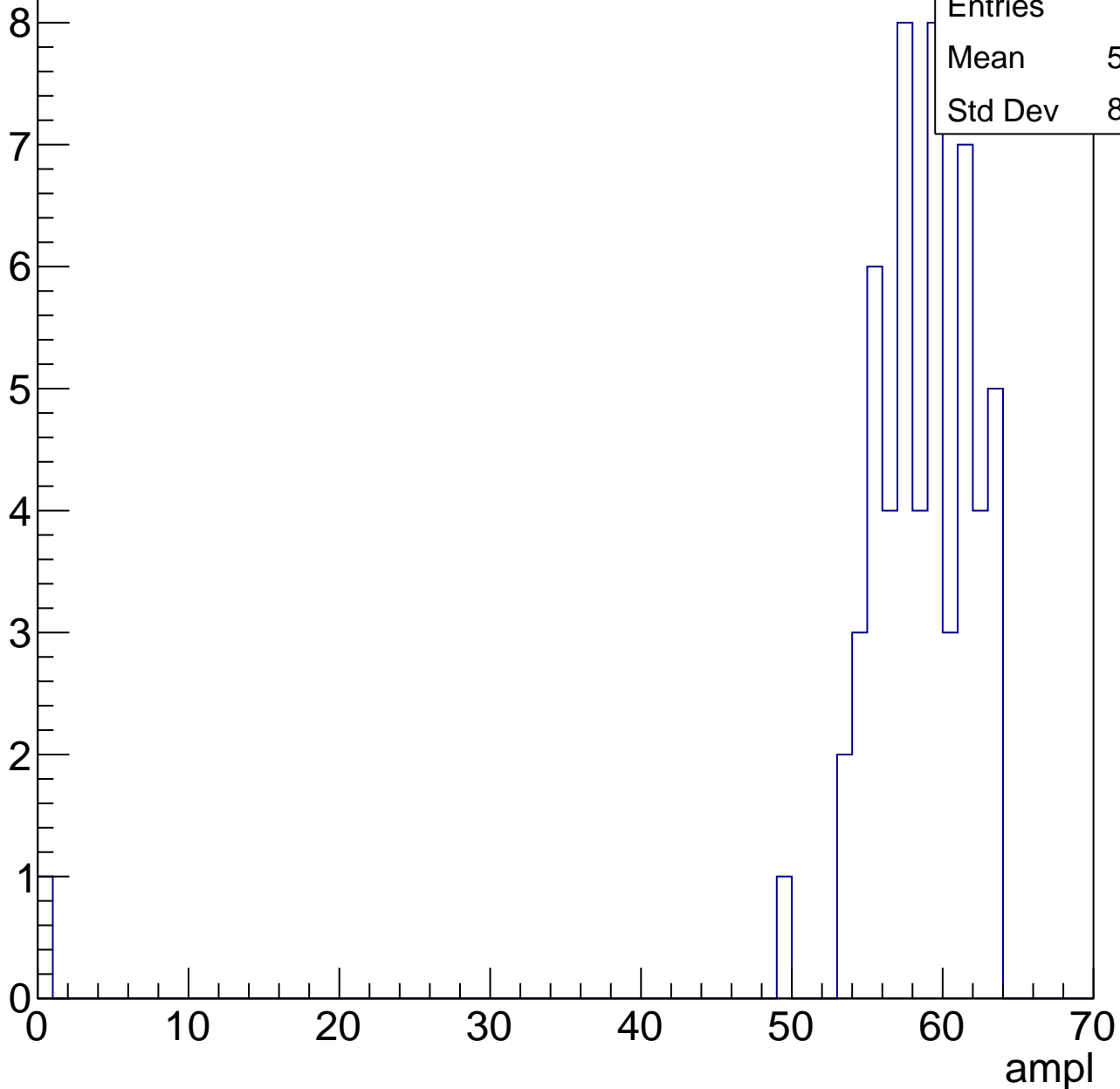


B1L101S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.16
Std Dev	8.295

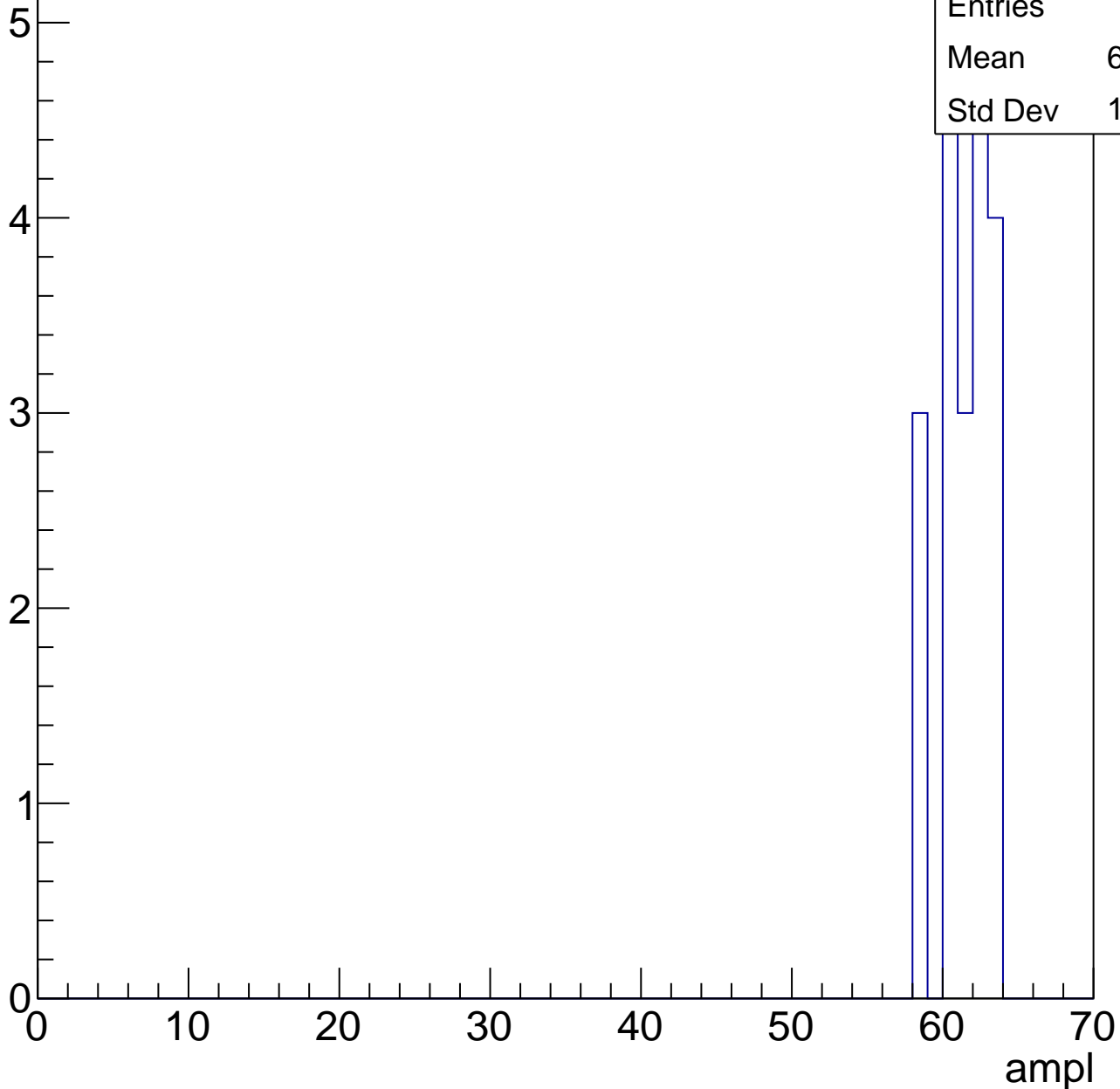


B1L101S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	60.95
Std Dev	1.627



B1L101S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch107, adc0

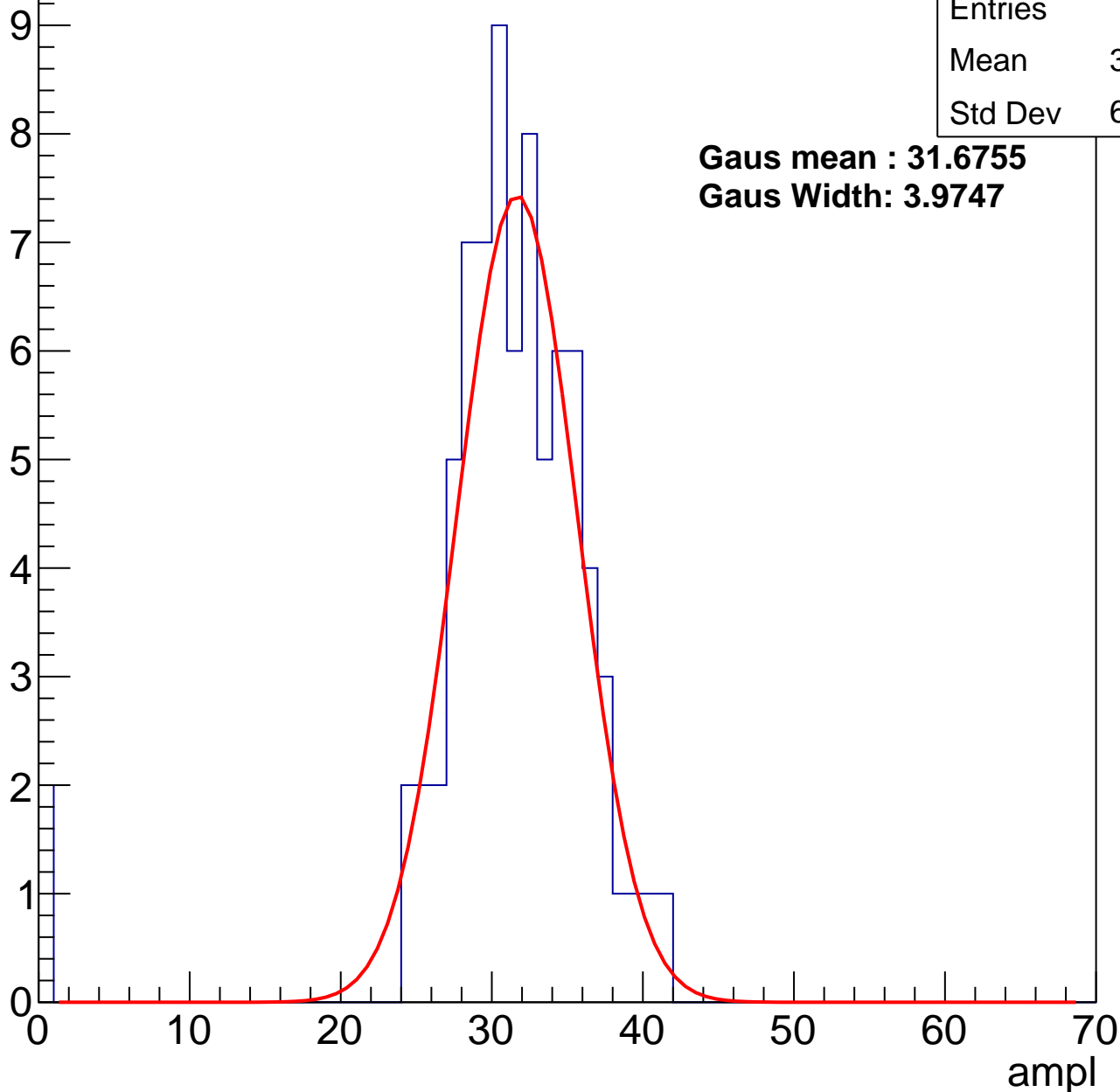
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.62
Std Dev	6.192

Gaus mean : 31.6755

Gaus Width: 3.9747



B1L101S, U14-ch107, adc1

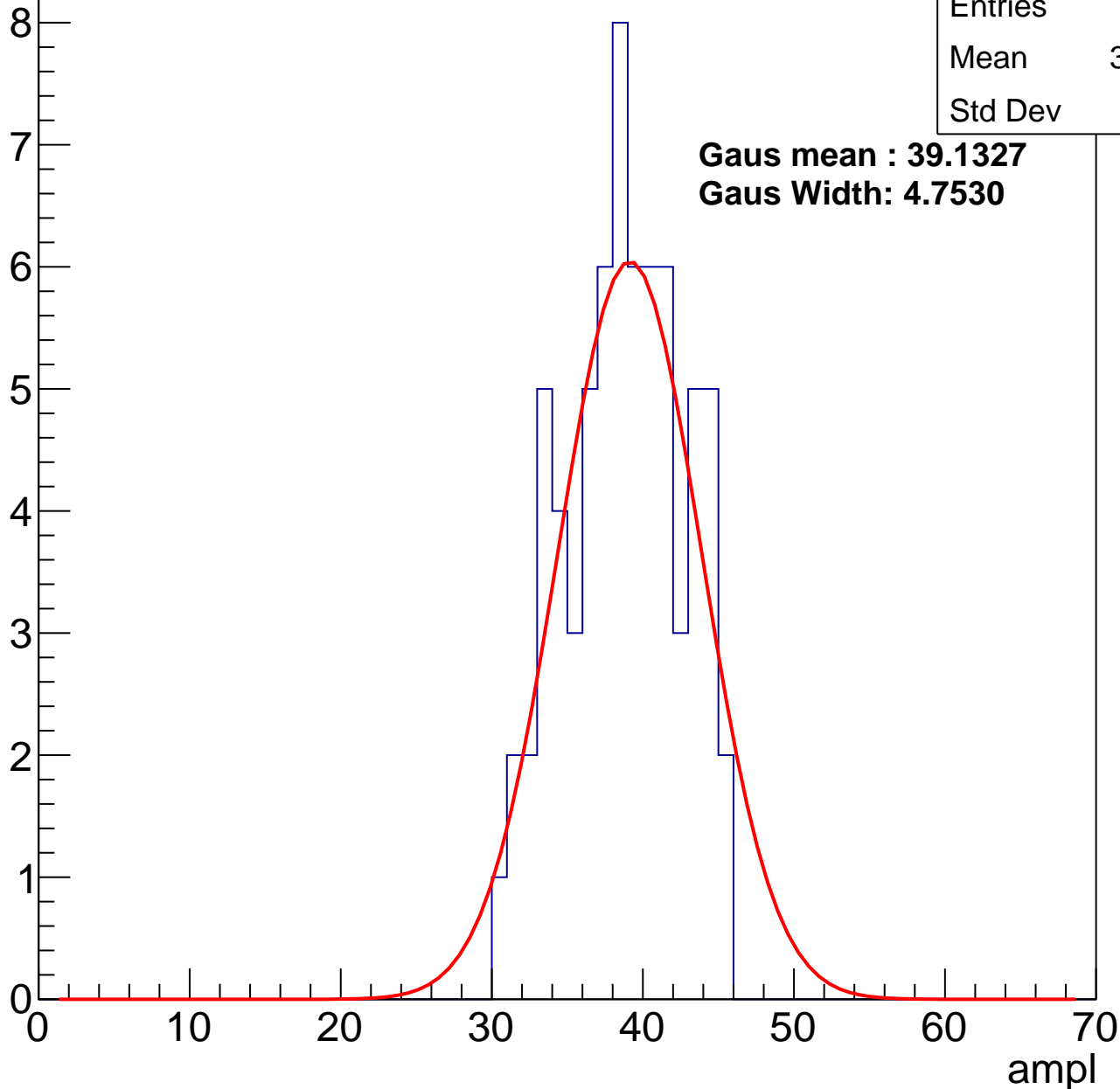
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.25
Std Dev	3.82

Gaus mean : 39.1327

Gaus Width: 4.7530



B1L101S, U14-ch107, adc2

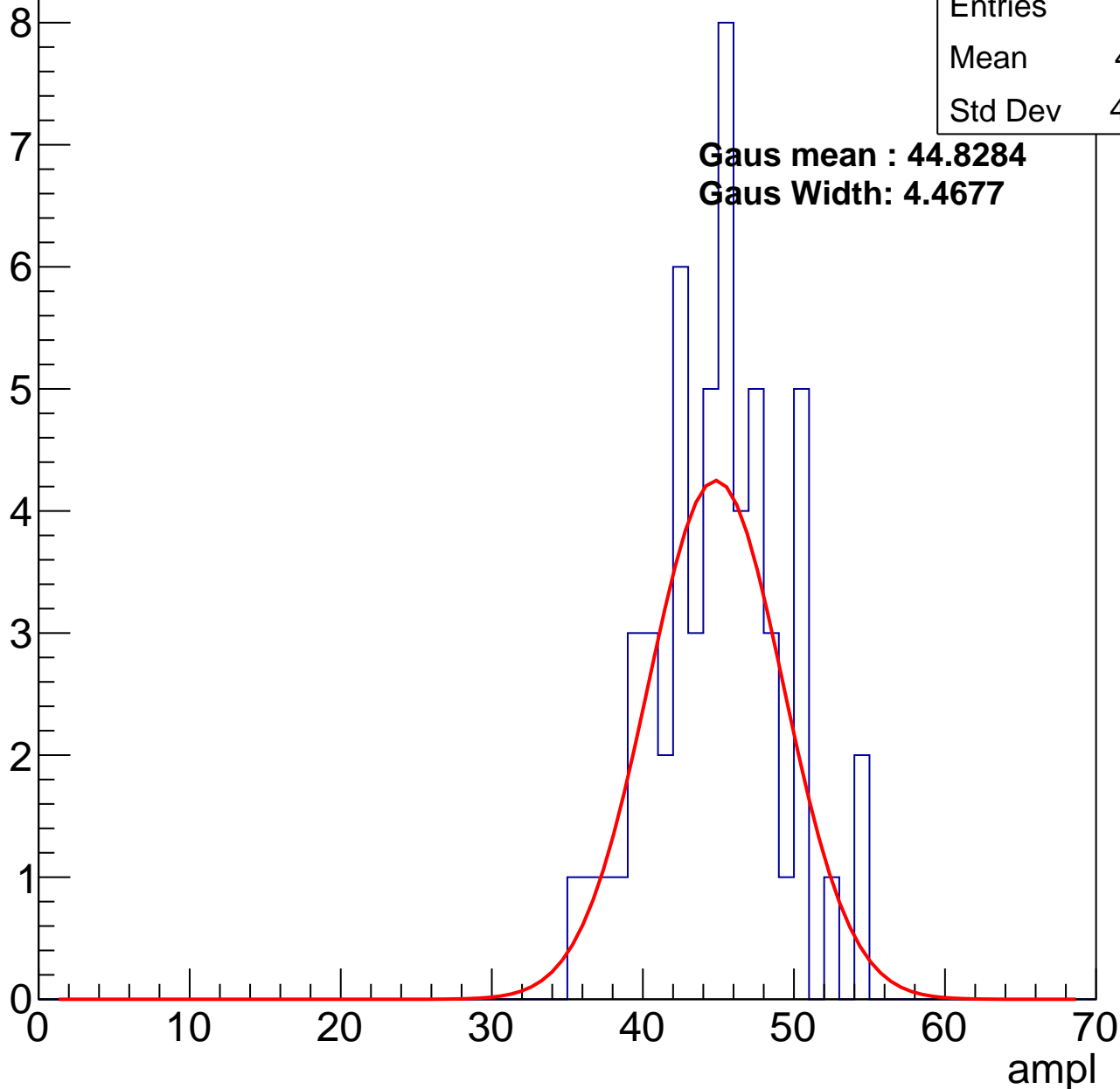
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	44.51
Std Dev	4.199

Gaus mean : 44.8284

Gaus Width: 4.4677



B1L101S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	50.39
Std Dev	3.911

Entry

10

8

6

4

2

0

0

10

20

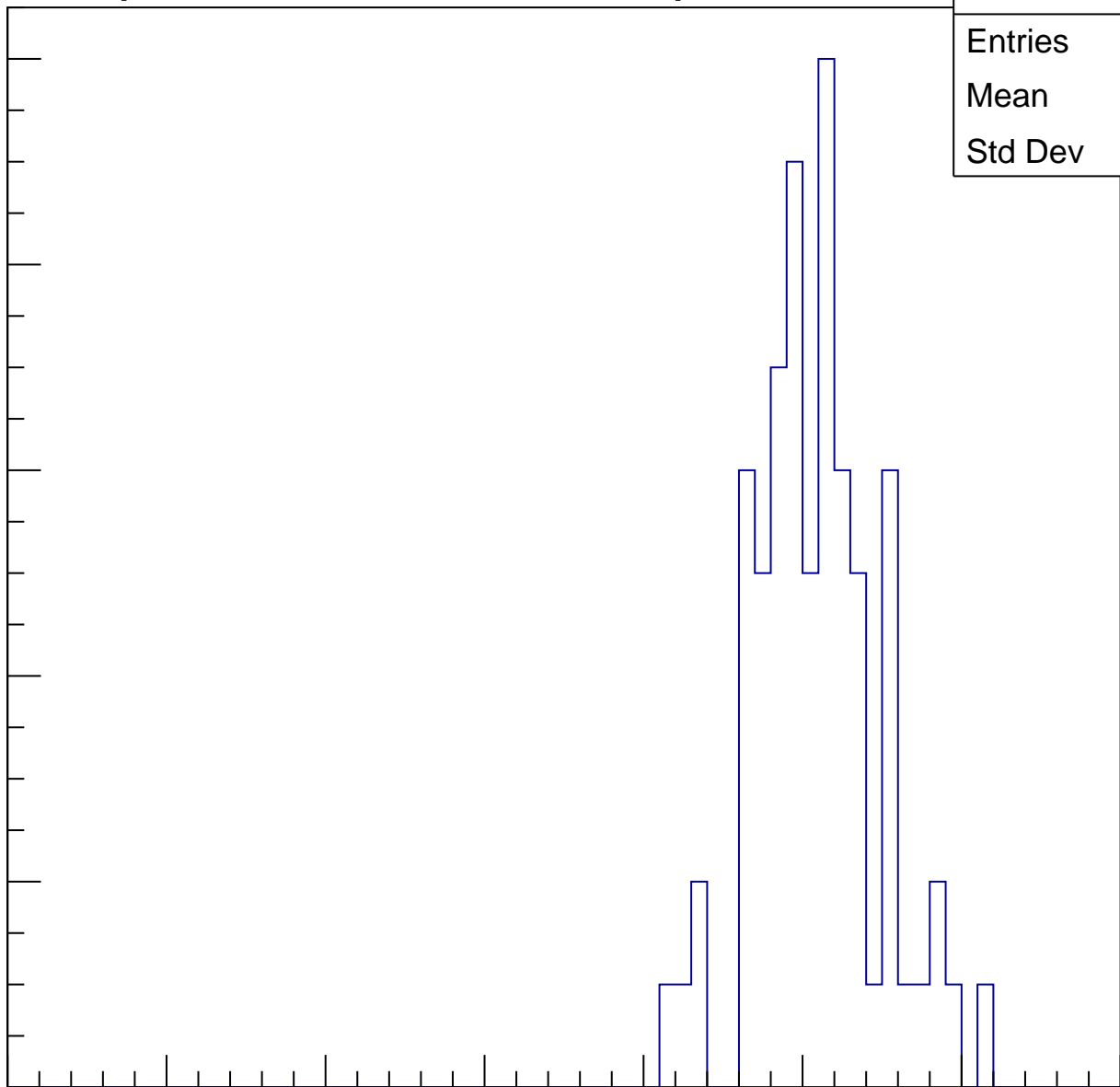
30

40

50

60

ampl

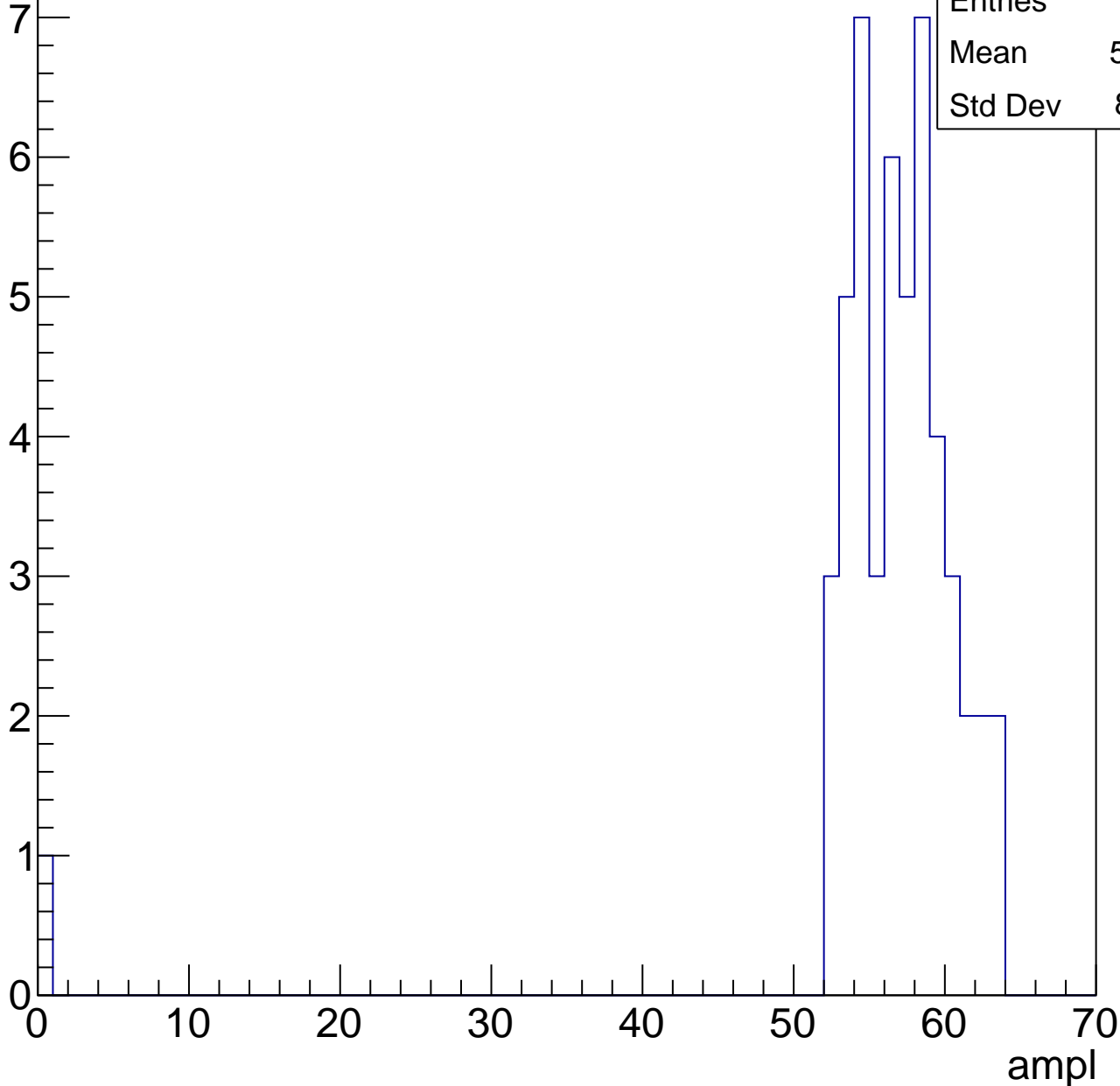


B1L101S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.58
Std Dev	8.471

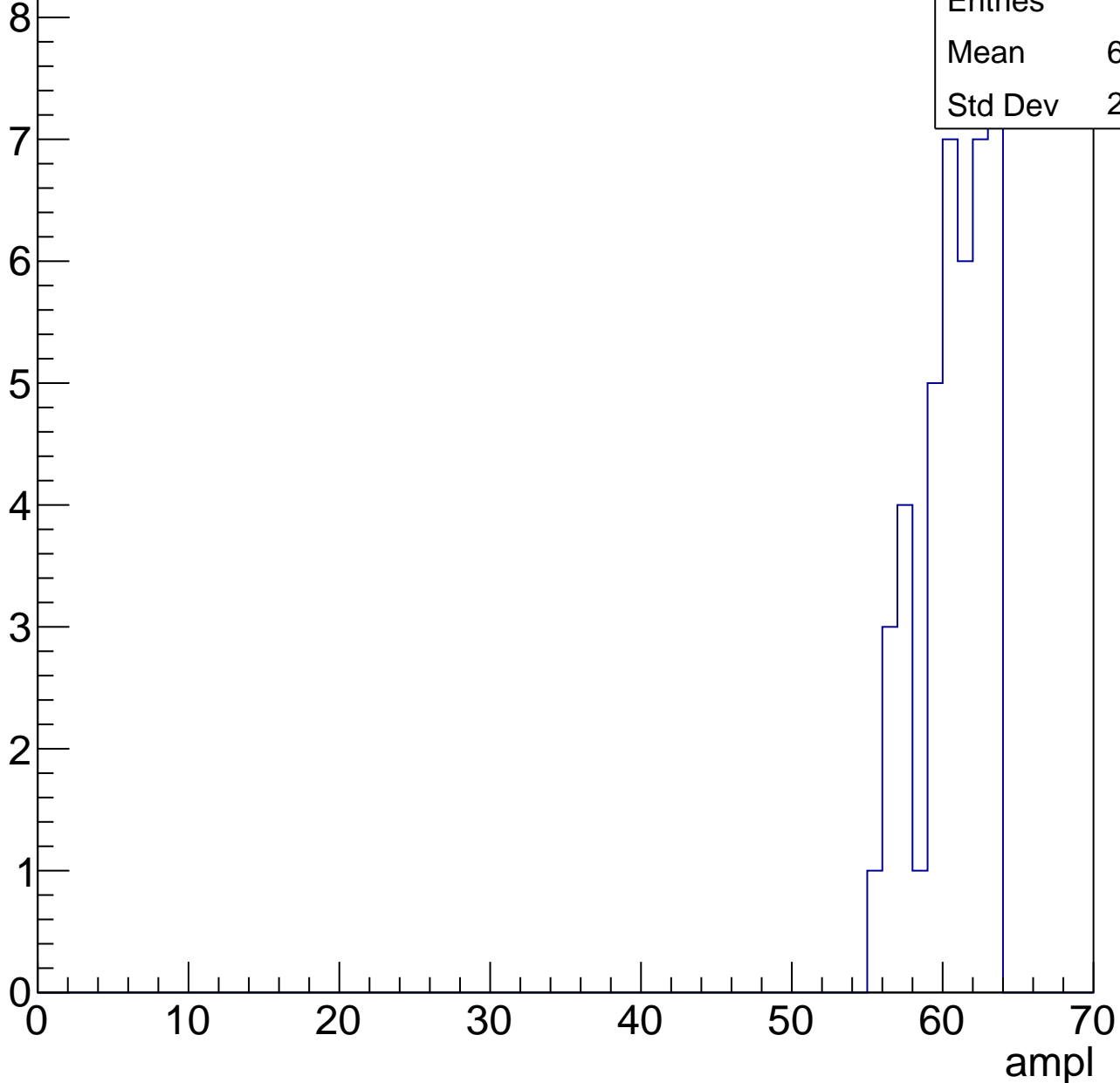


B1L101S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	60.19
Std Dev	2.302



B1L101S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch108, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	28.26
Std Dev	3.876

Gaus mean : 28.7553

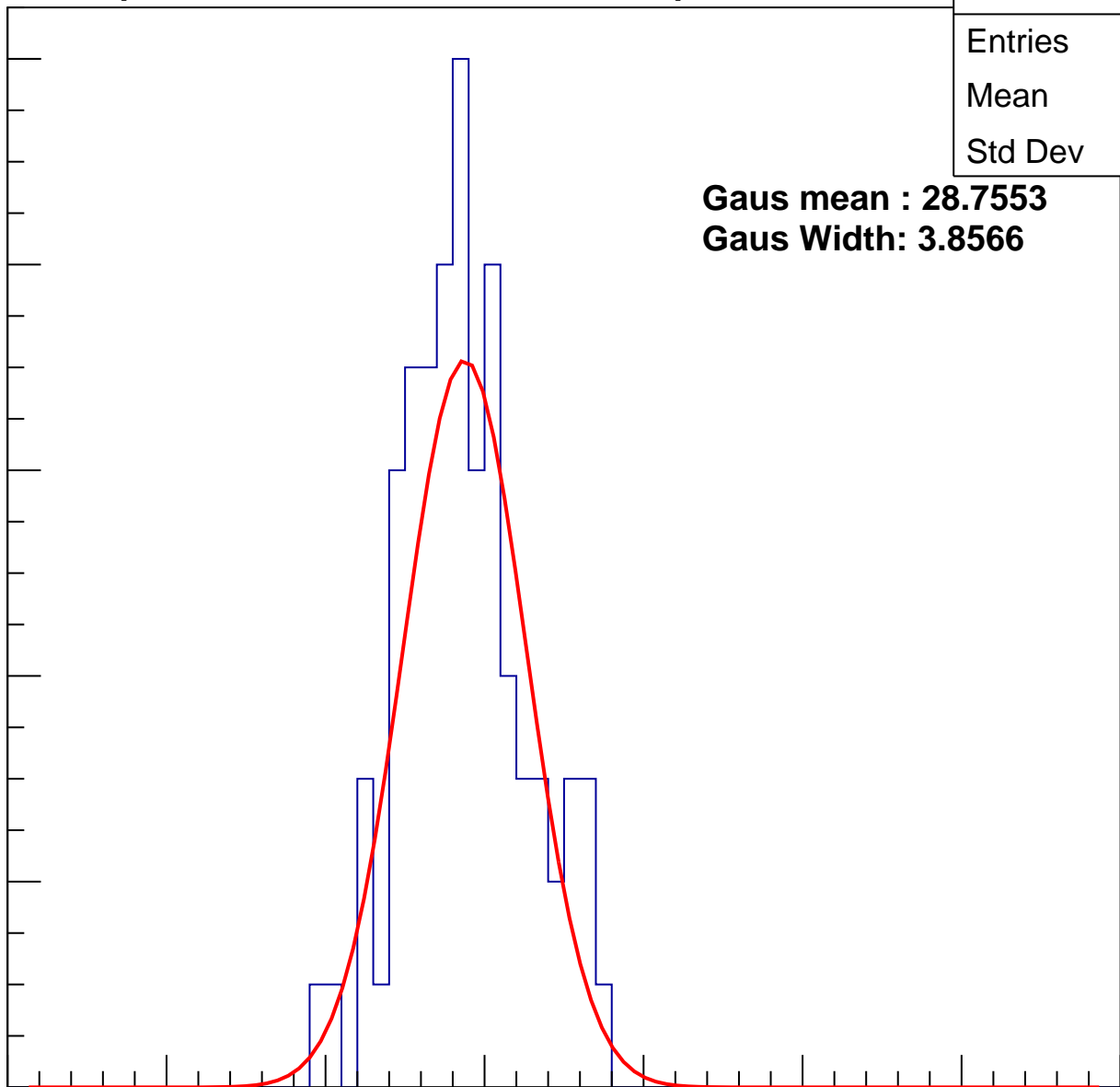
Gaus Width: 3.8566

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch108, adc1

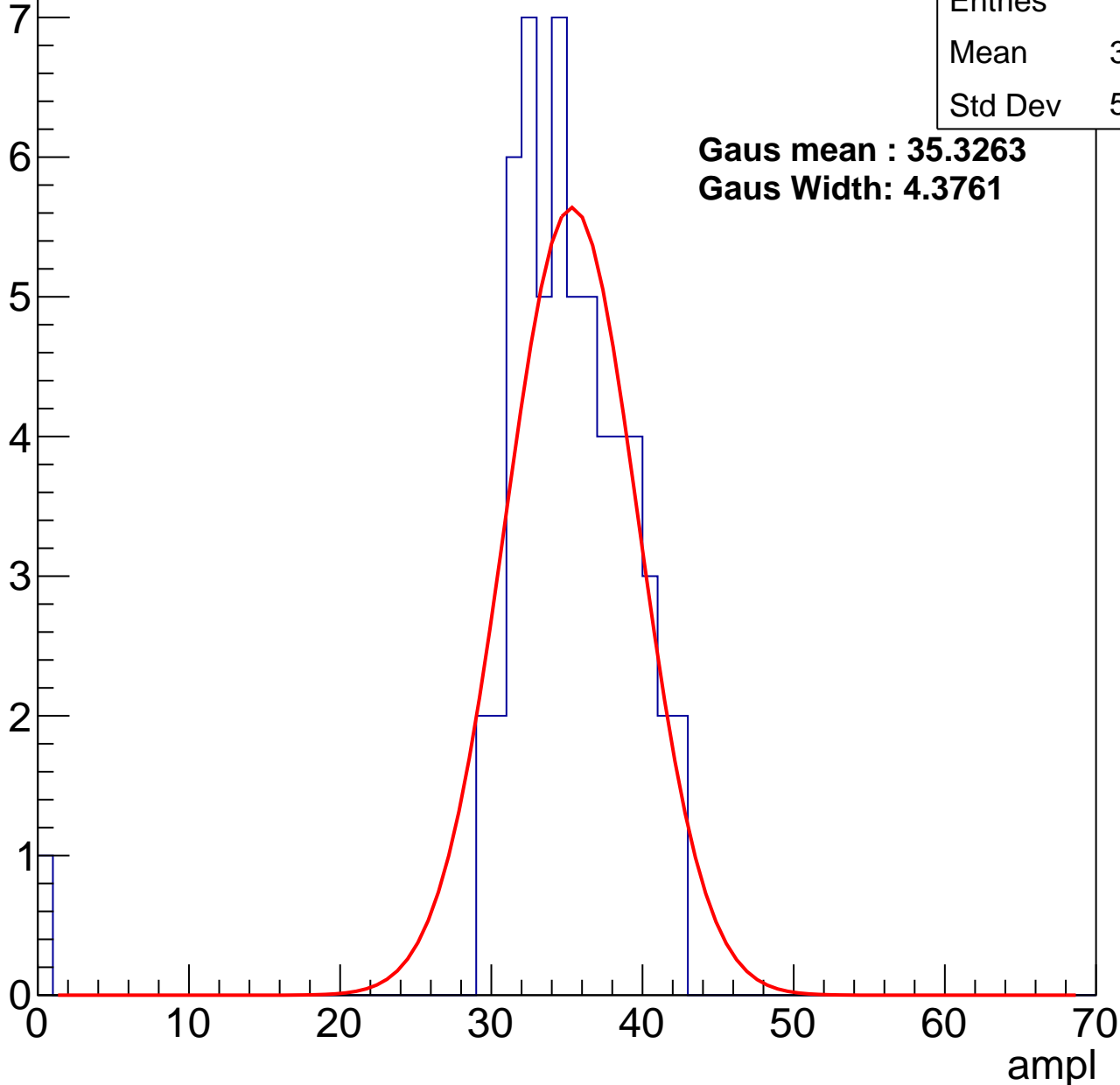
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.37
Std Dev	5.639

Gaus mean : 35.3263

Gaus Width: 4.3761



B1L101S, U14-ch108, adc2

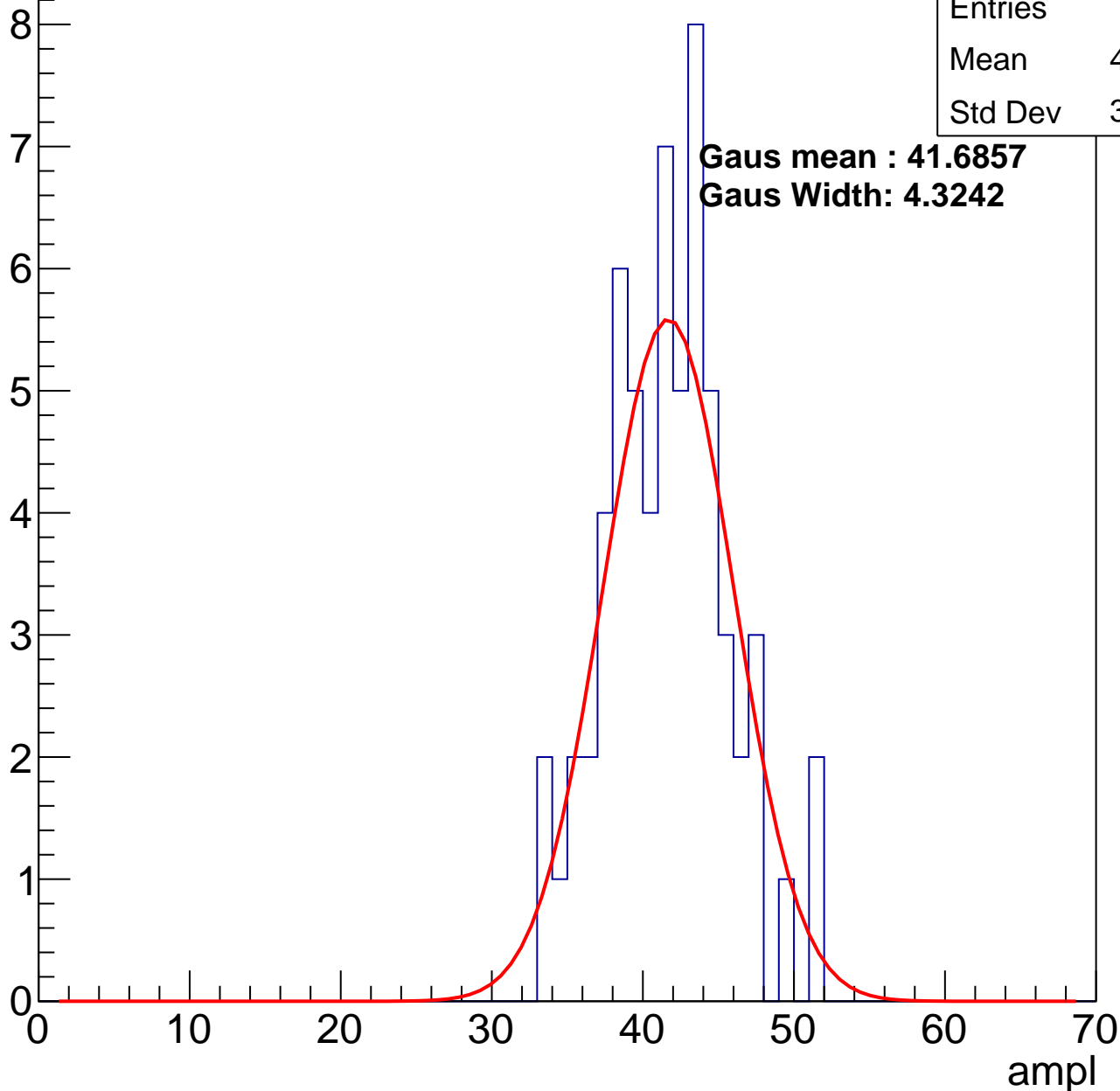
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.18
Std Dev	3.998

Gaus mean : 41.6857

Gaus Width: 4.3242

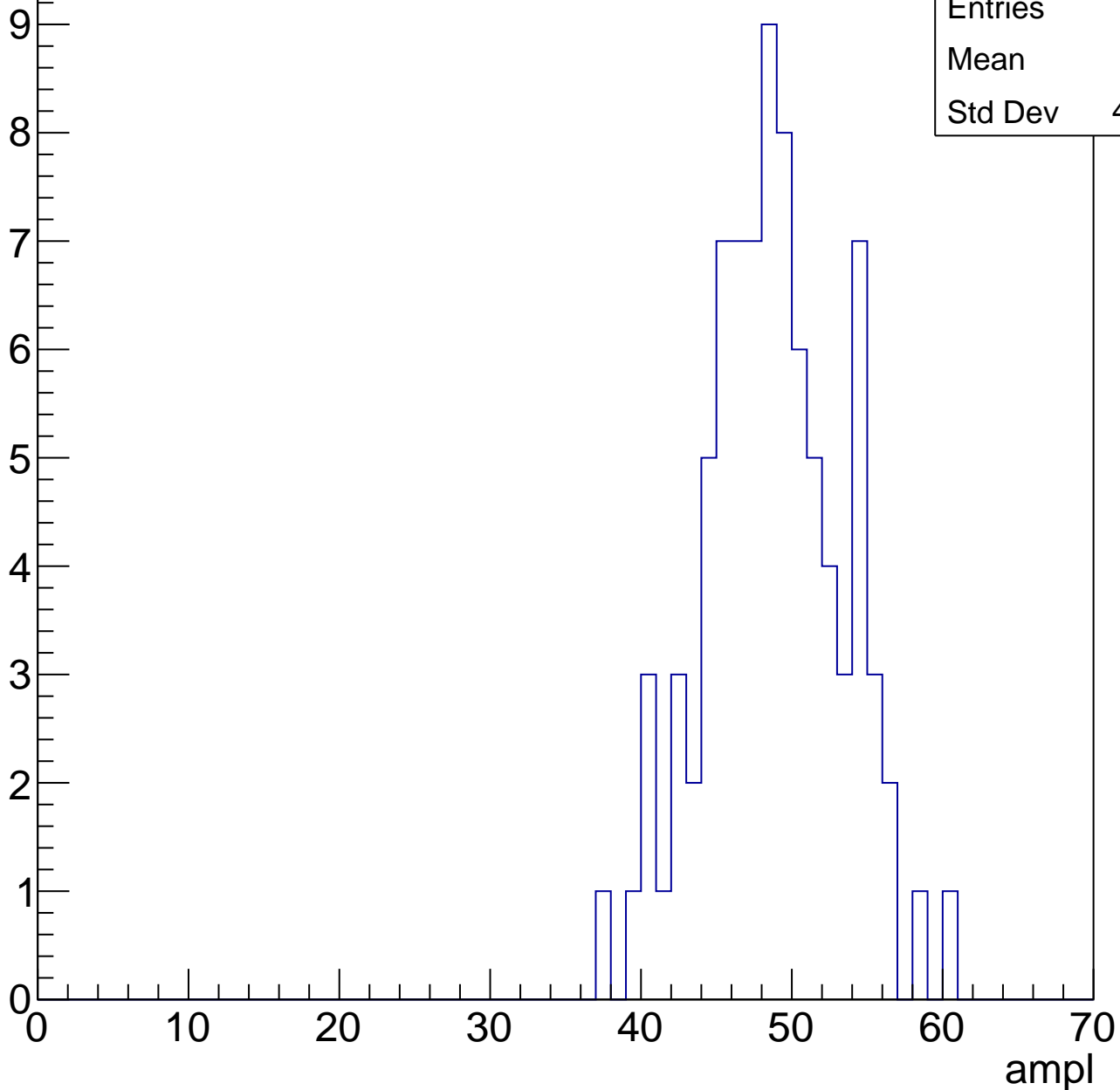


B1L101S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	48.3
Std Dev	4.521

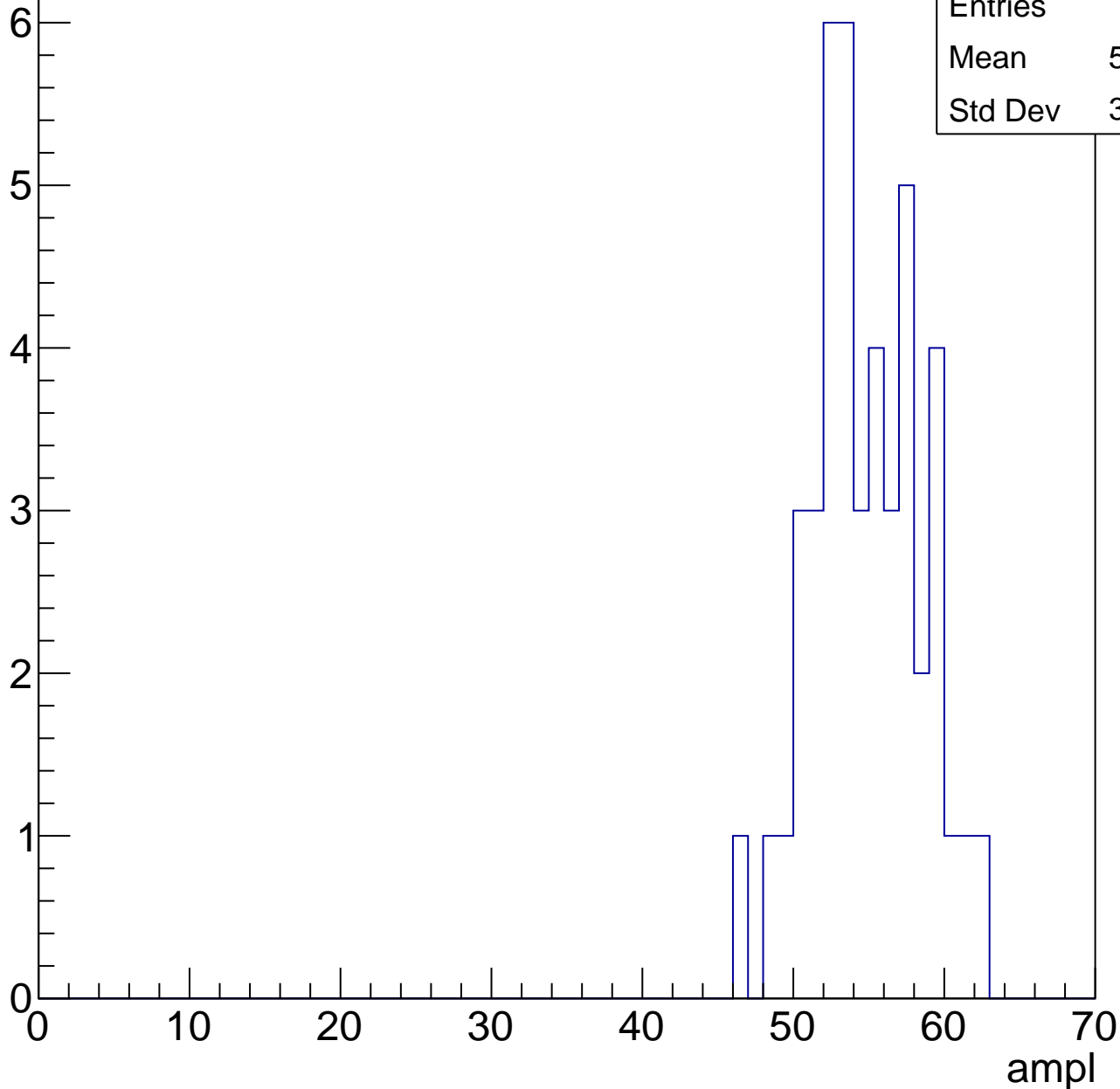


B1L101S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	54.36
Std Dev	3.547

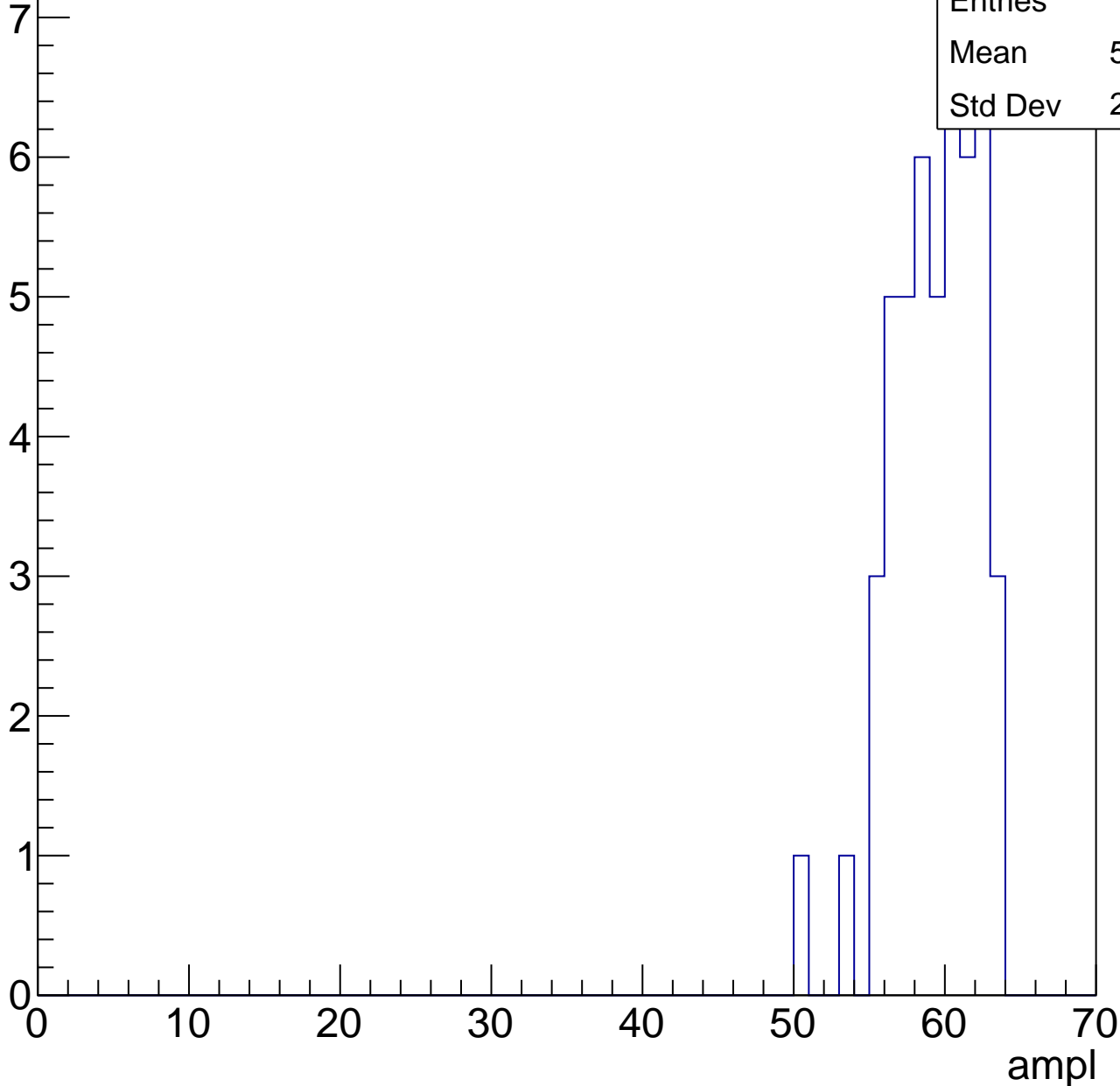


B1L101S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.88
Std Dev	2.775

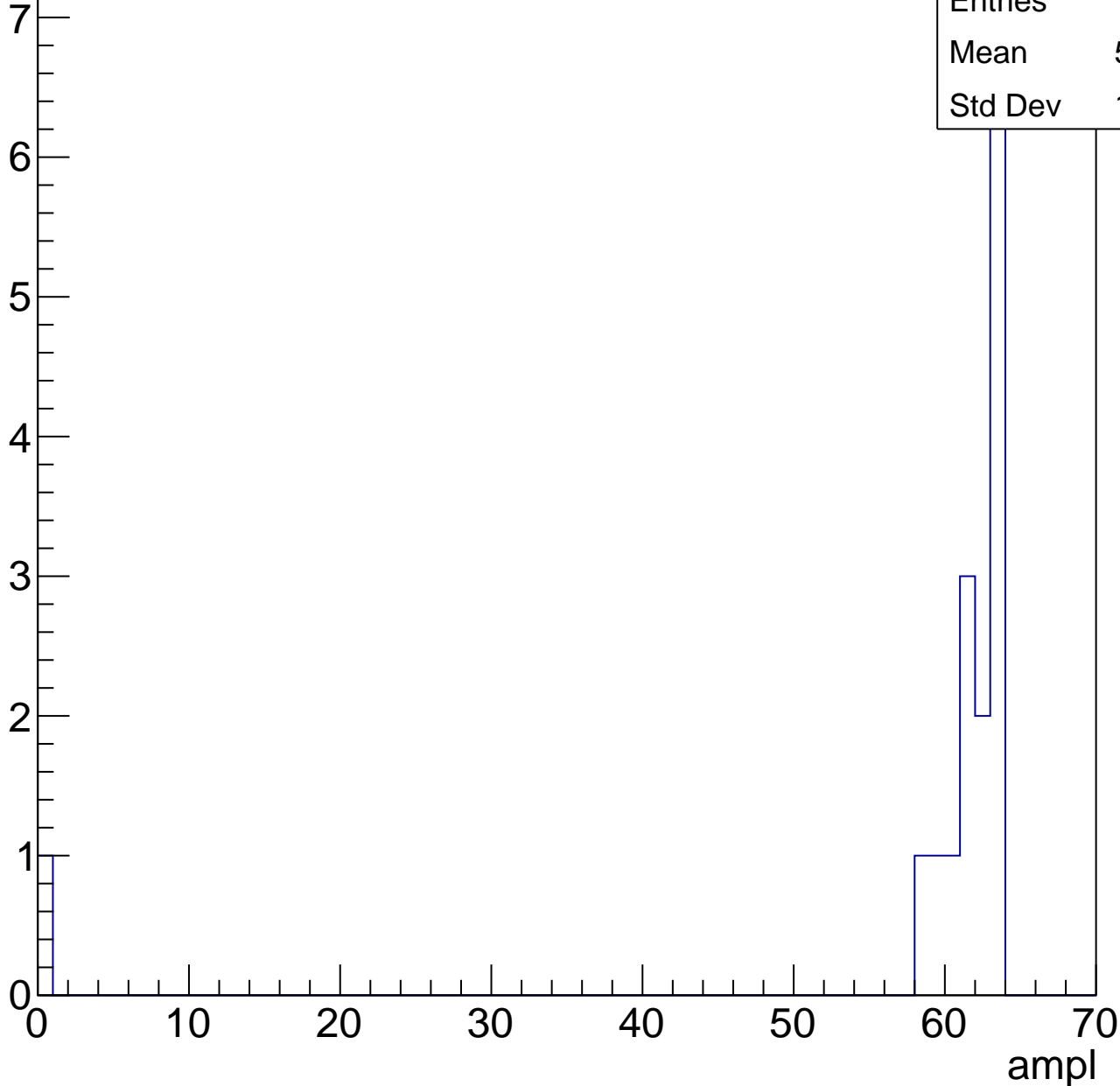


B1L101S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.81
Std Dev	15.01



B1L101S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch109, adc0

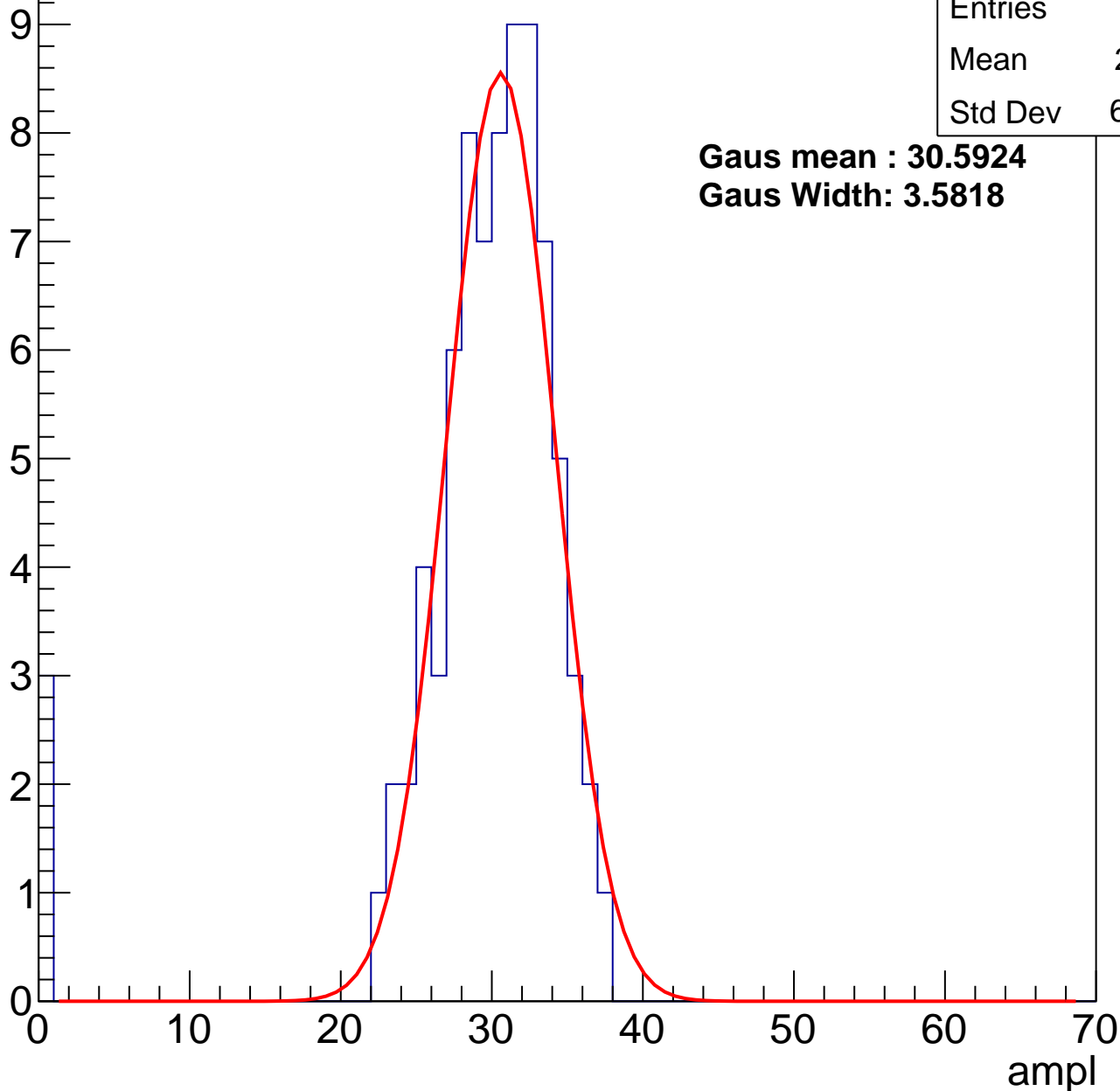
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.81
Std Dev	6.563

Gaus mean : 30.5924

Gaus Width: 3.5818



B1L101S, U14-ch109, adc1

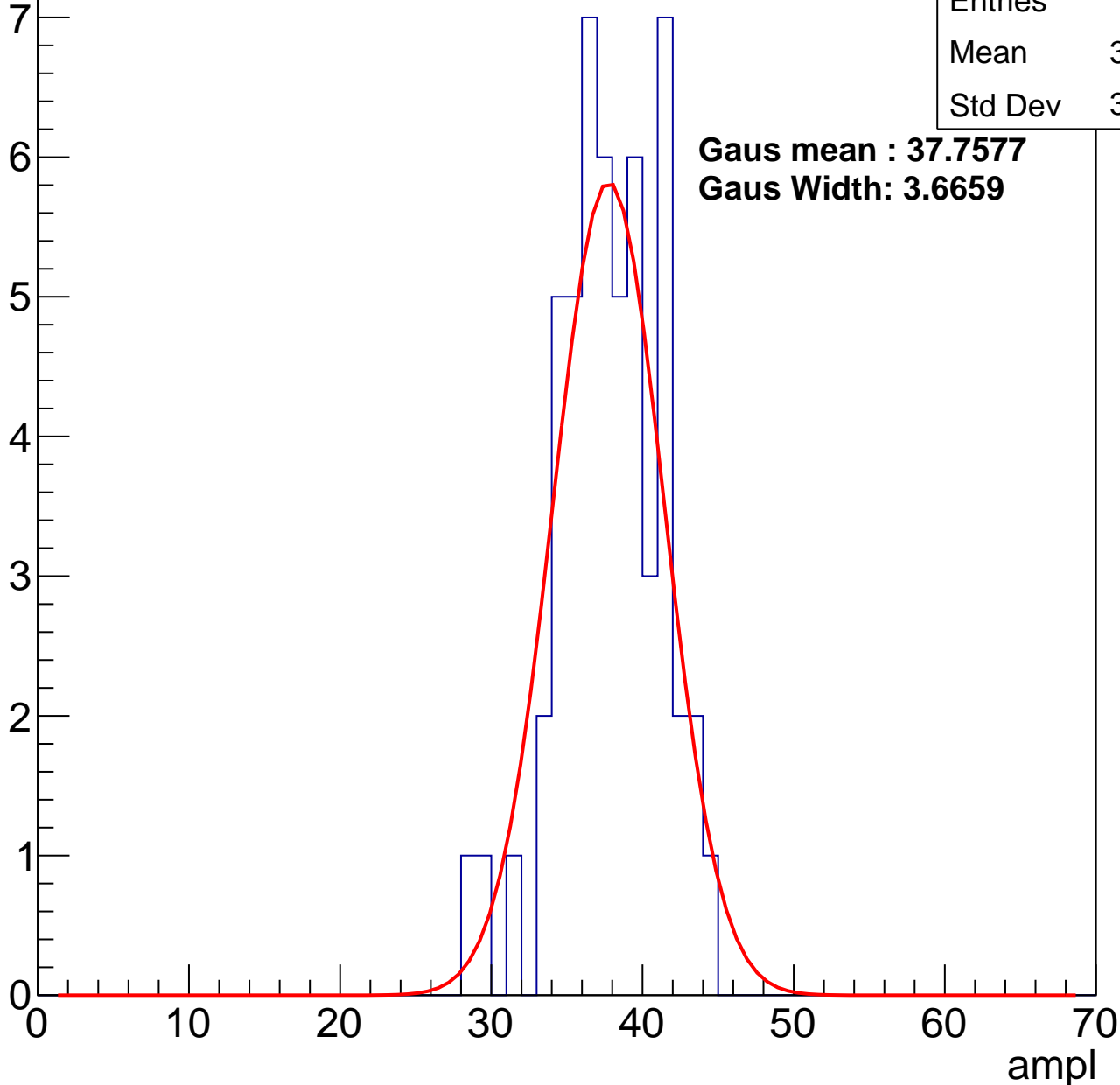
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	37.37
Std Dev	3.379

Gaus mean : 37.7577

Gaus Width: 3.6659



B1L101S, U14-ch109, adc2

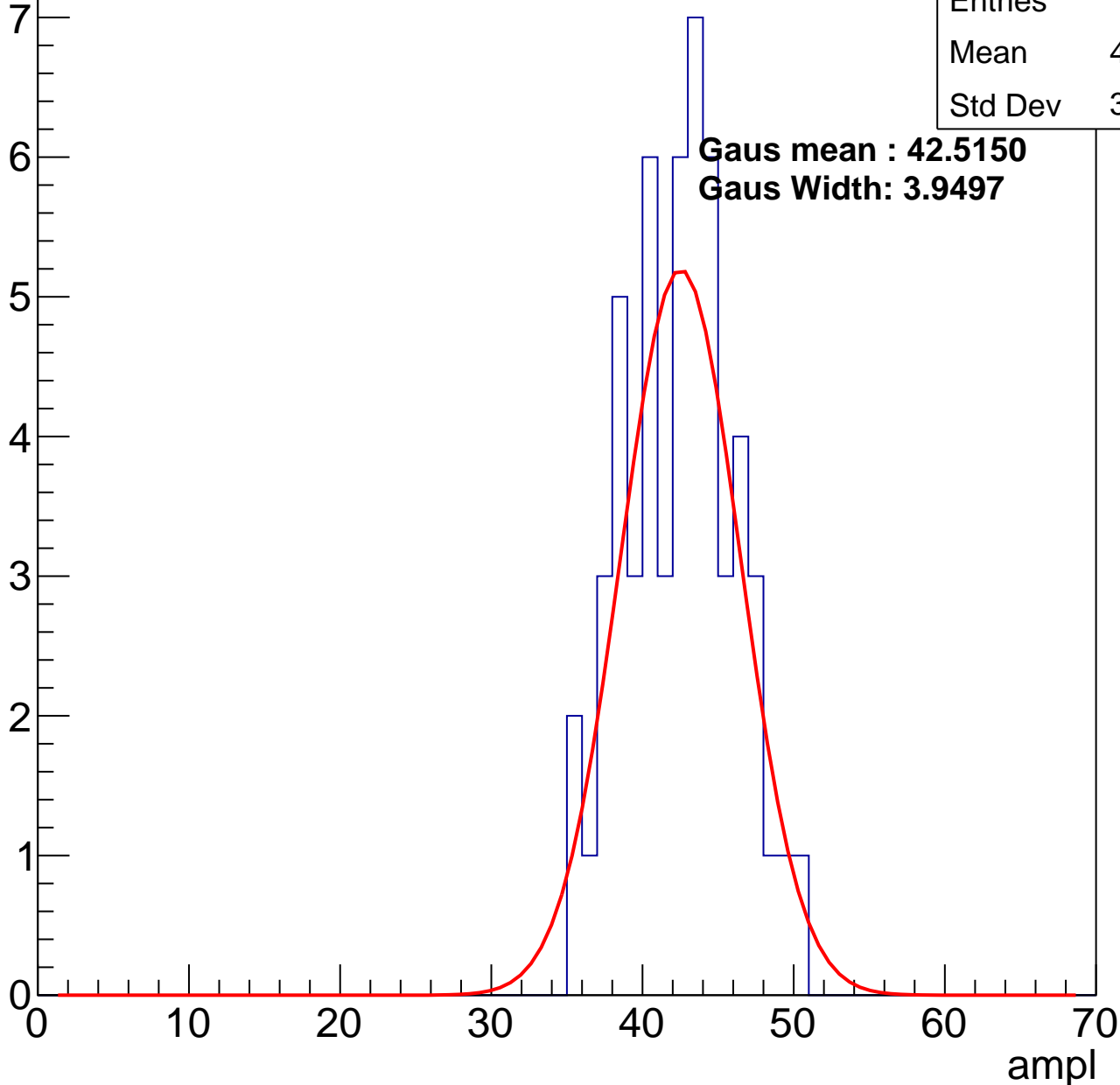
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.02
Std Dev	3.539

Gaus mean : 42.5150

Gaus Width: 3.9497

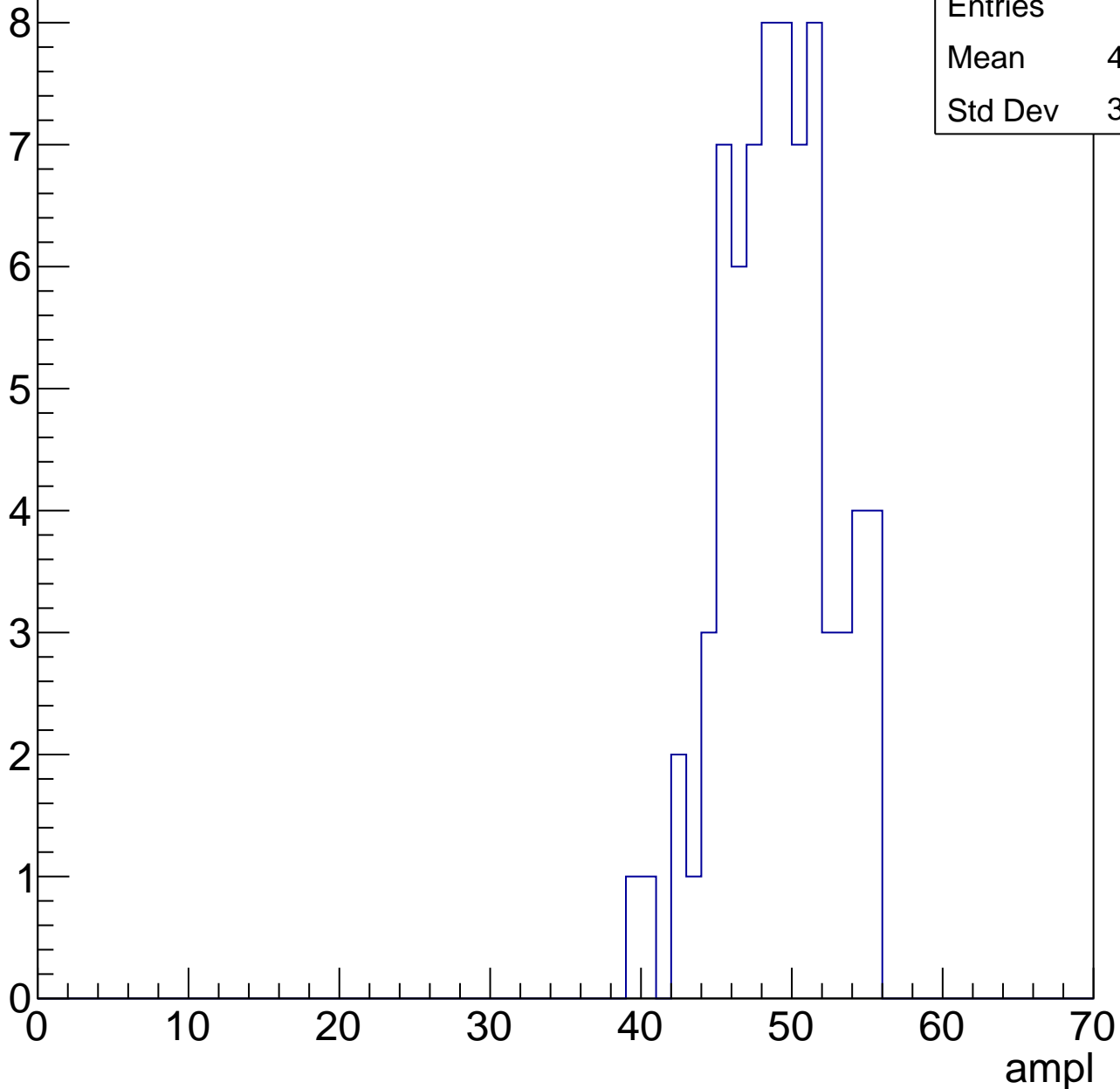


B1L101S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	48.53
Std Dev	3.577

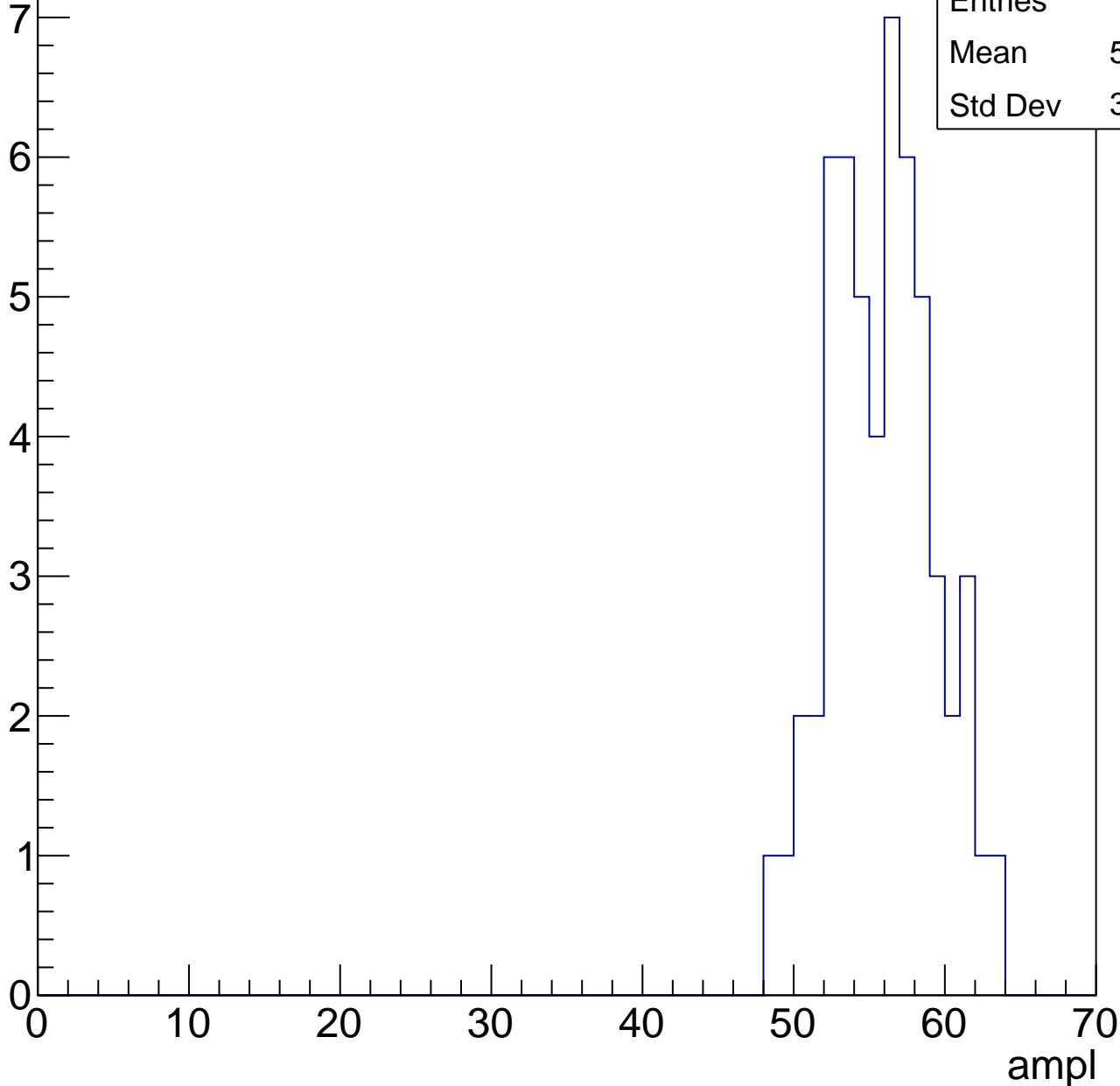


B1L101S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.42
Std Dev	3.399



B1L101S, U14-ch109, adc5

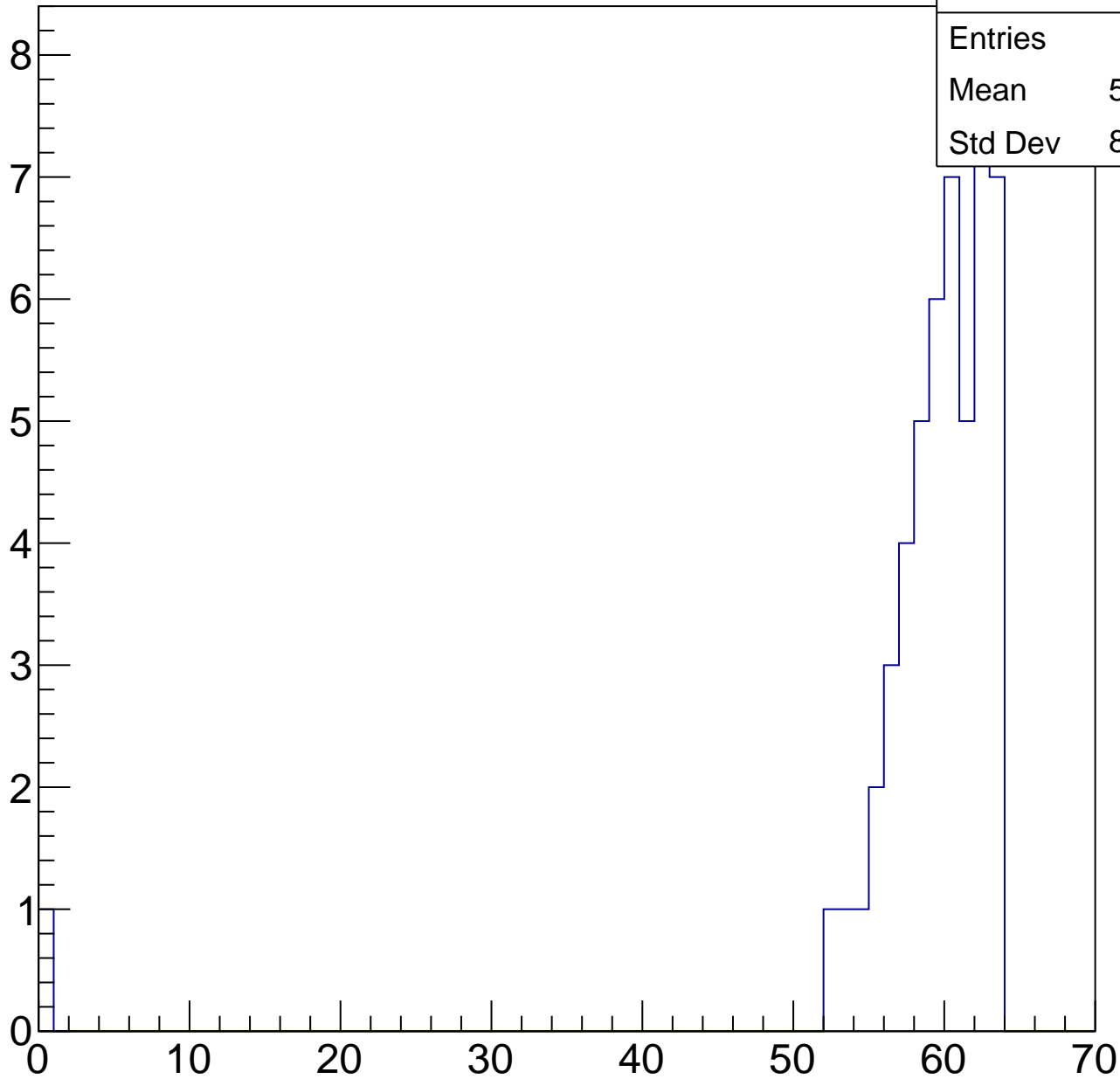
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.25
Std Dev	8.693

ampl



B1L101S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

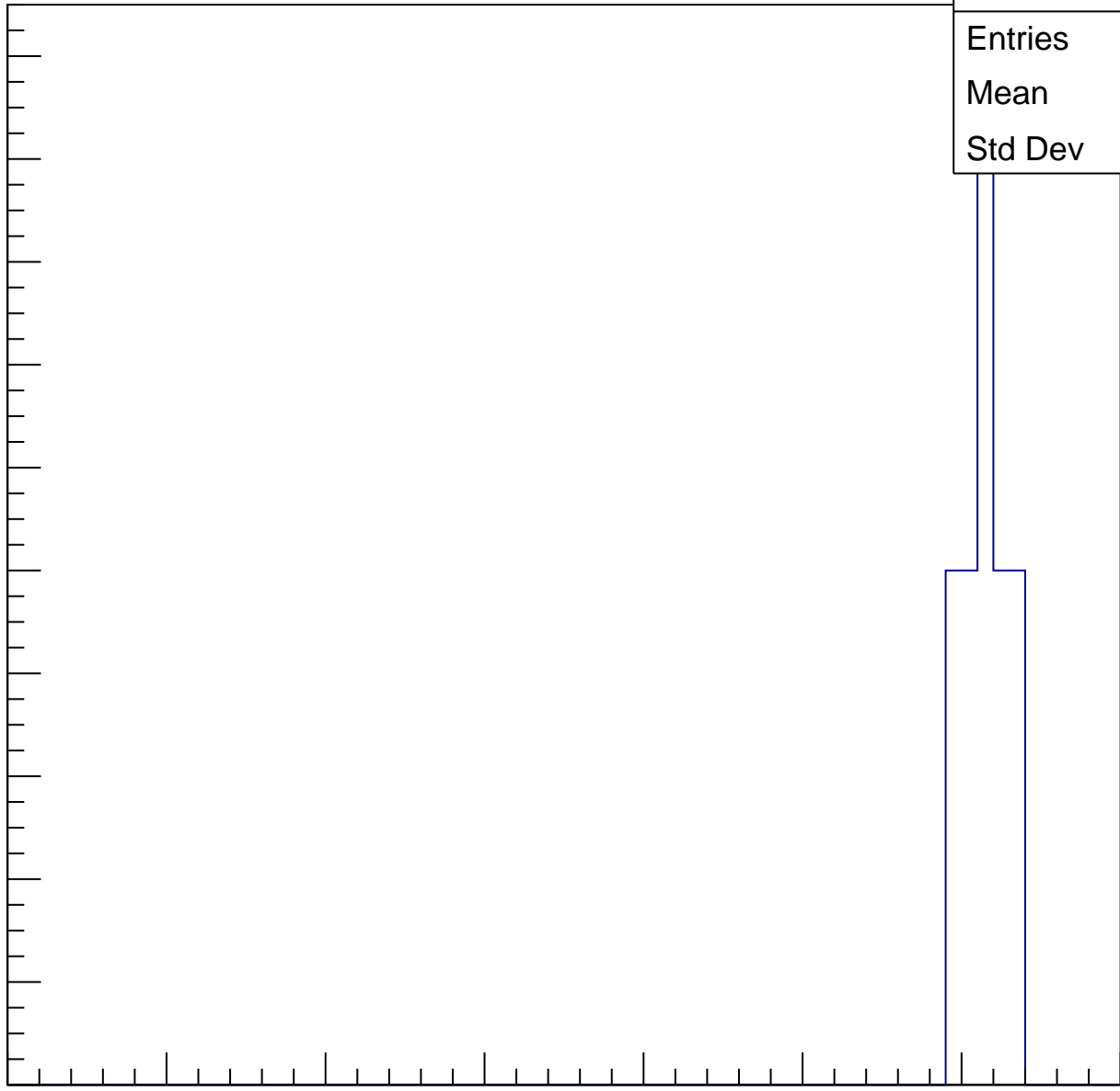
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61
Std Dev	1.291

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch110, adc0

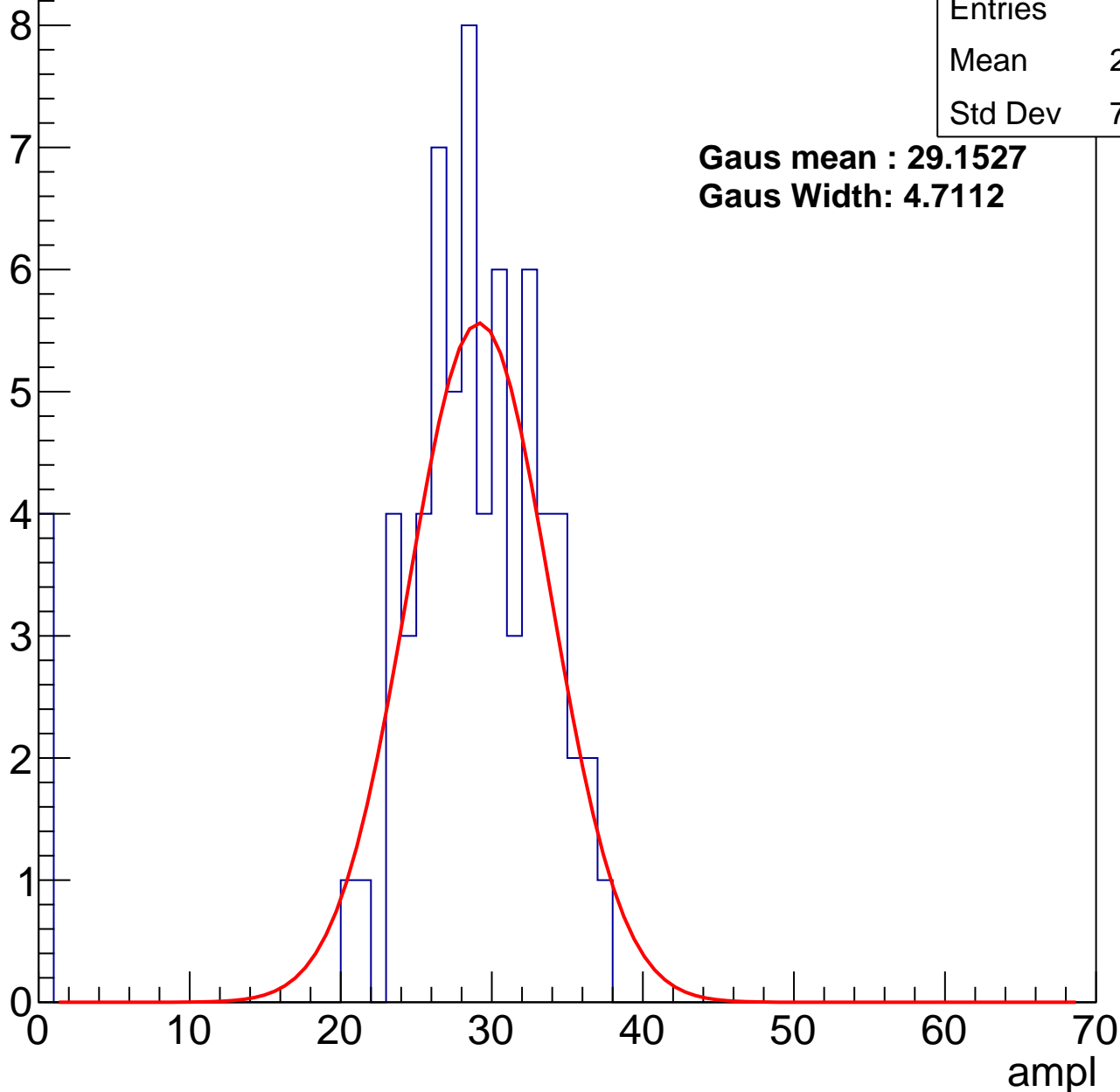
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	27.16
Std Dev	7.727

Gaus mean : 29.1527

Gaus Width: 4.7112



B1L101S, U14-ch110, adc1

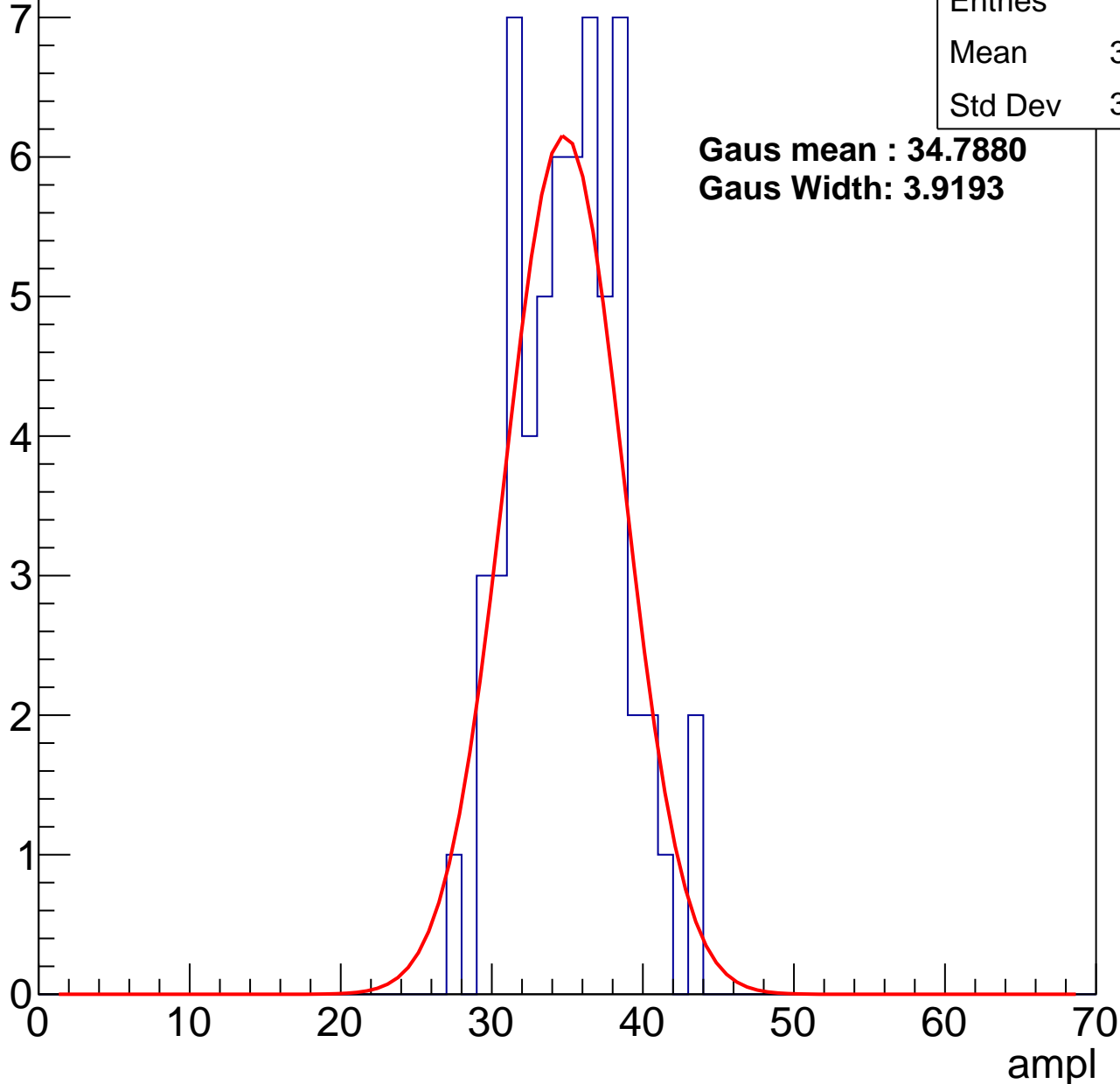
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	34.69
Std Dev	3.509

Gaus mean : 34.7880

Gaus Width: 3.9193



B1L101S, U14-ch110, adc2

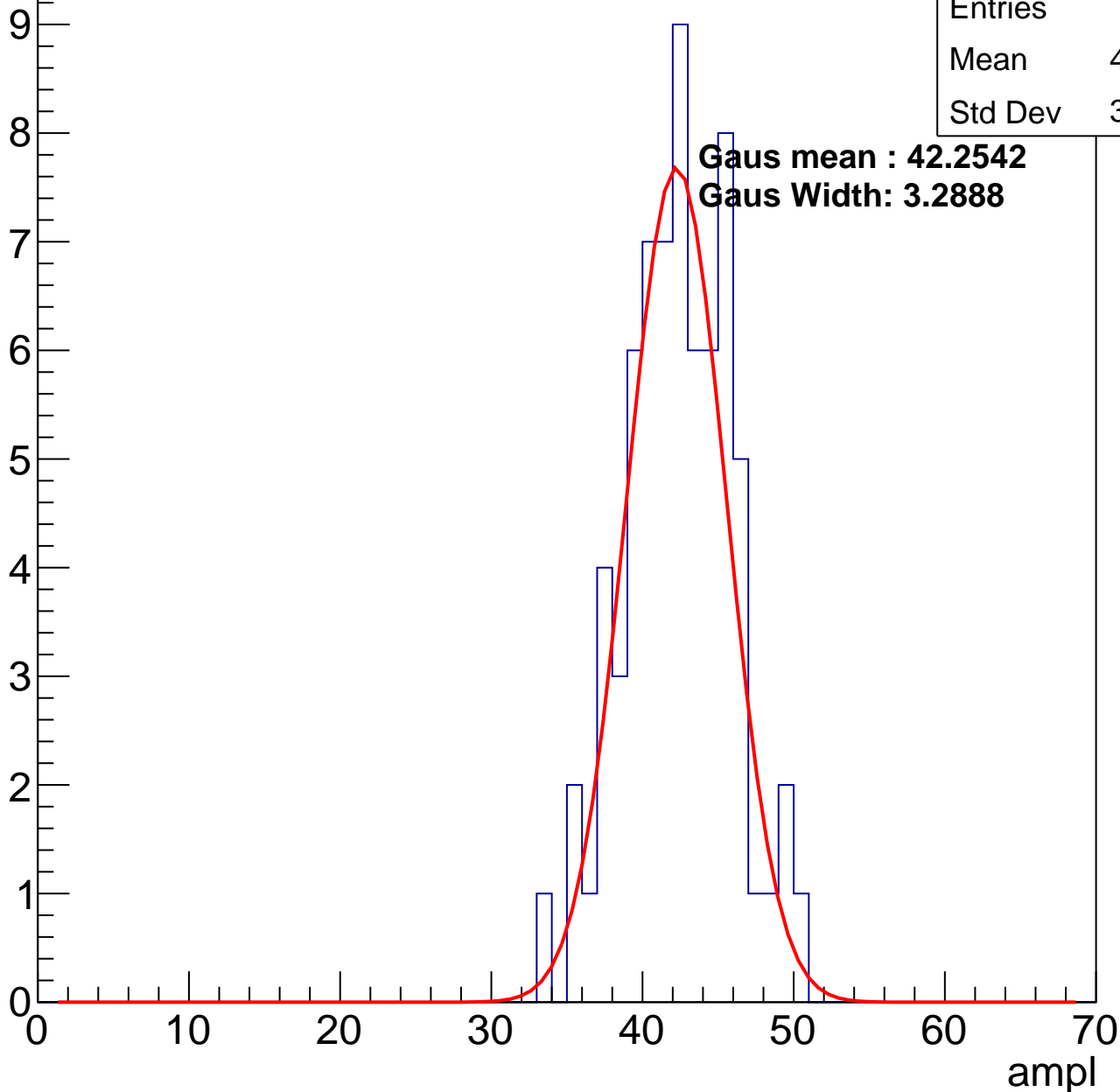
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	41.93
Std Dev	3.498

Gaus mean : 42.2542

Gaus Width: 3.2888

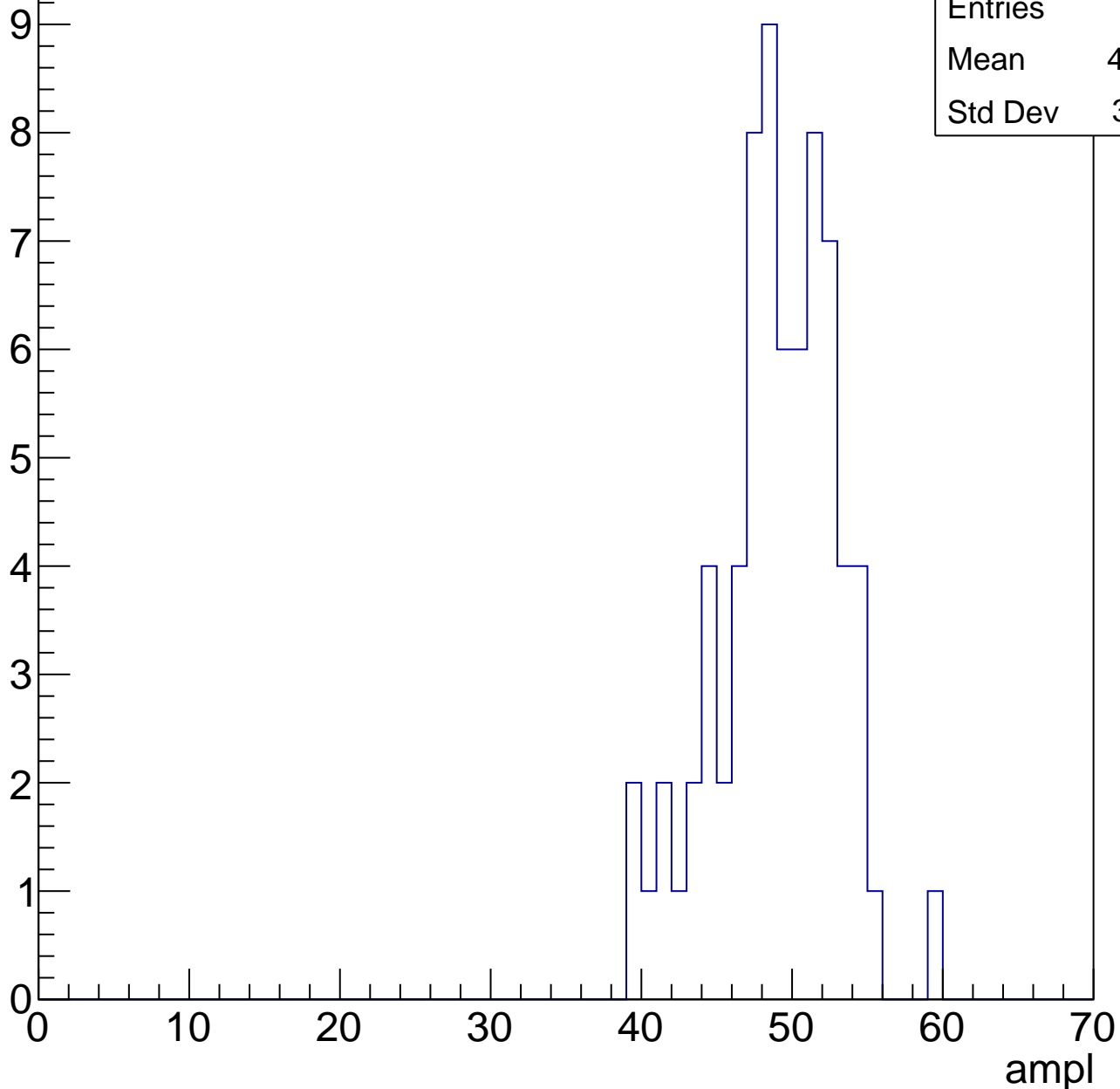


B1L101S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	48.53
Std Dev	3.951

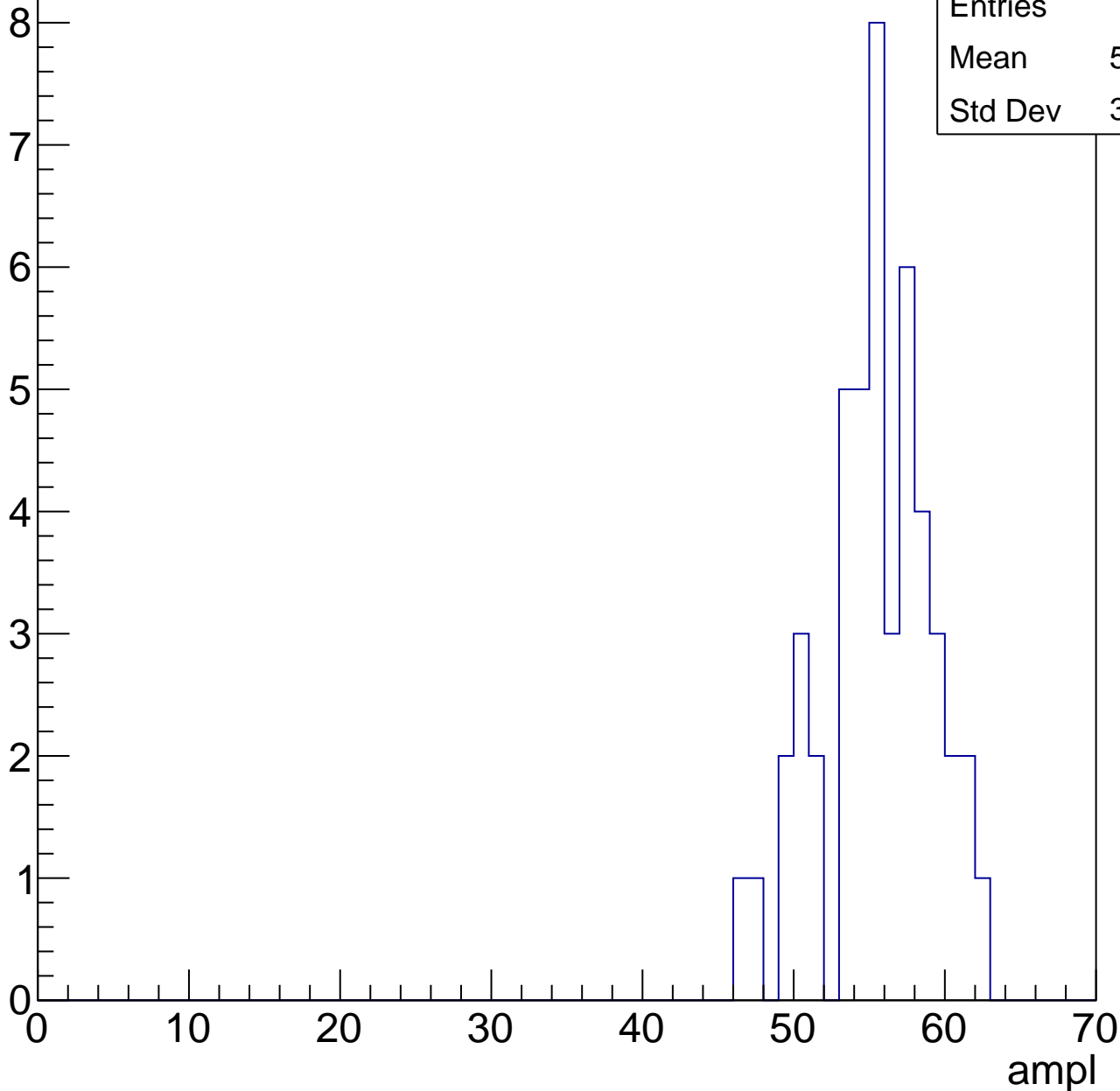


B1L101S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	55.02
Std Dev	3.626

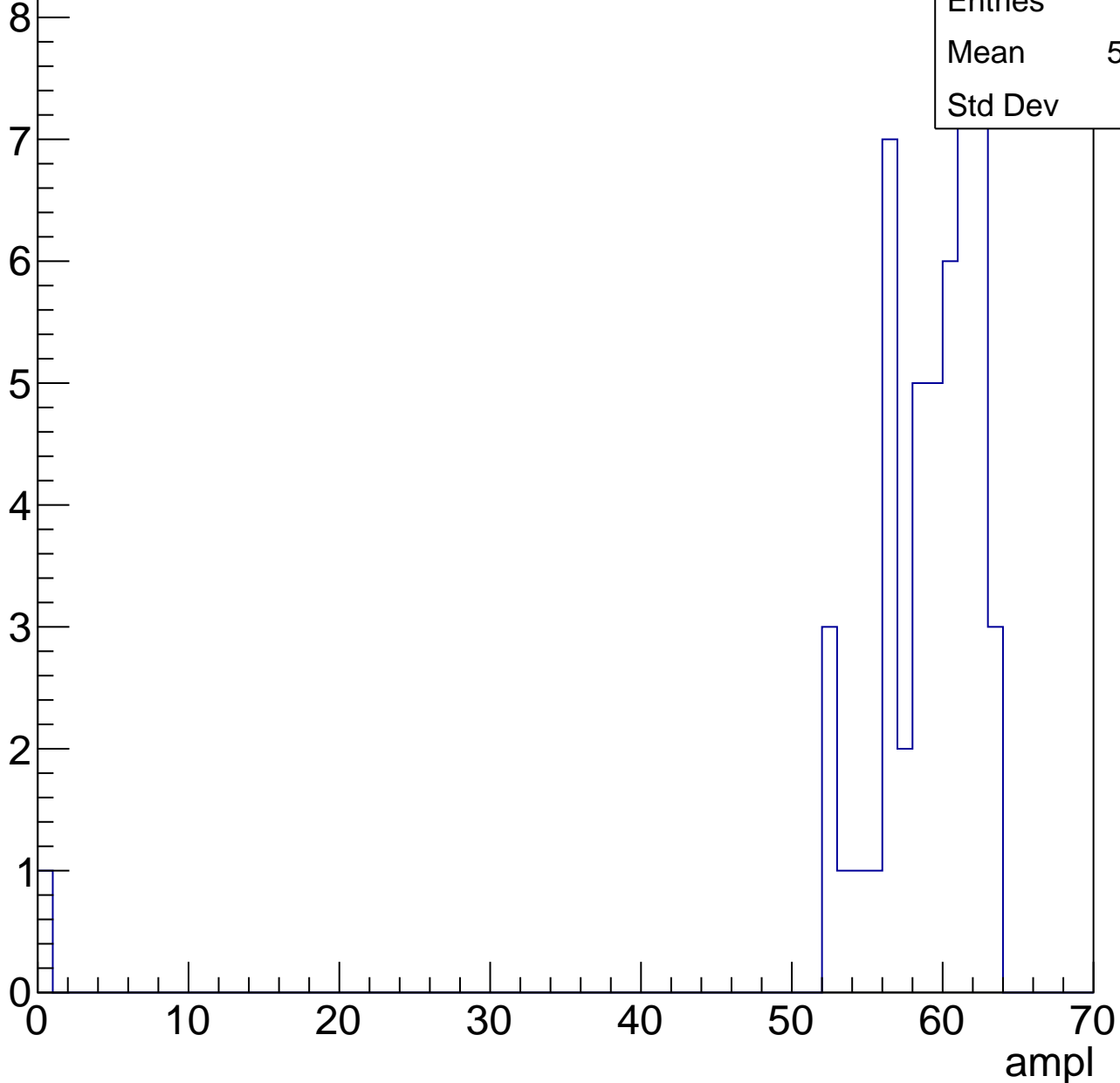


B1L101S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.69
Std Dev	8.69

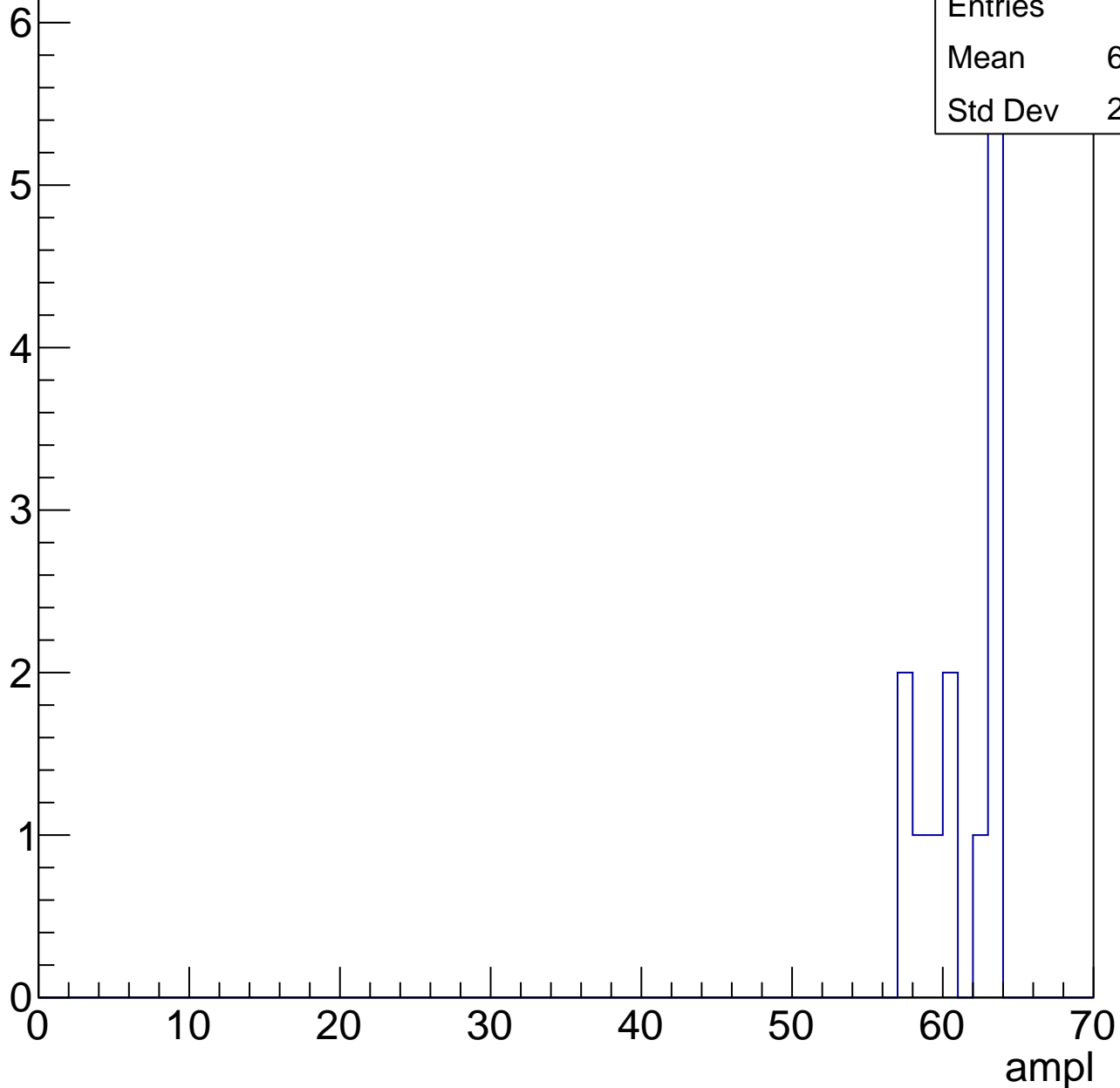


B1L101S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

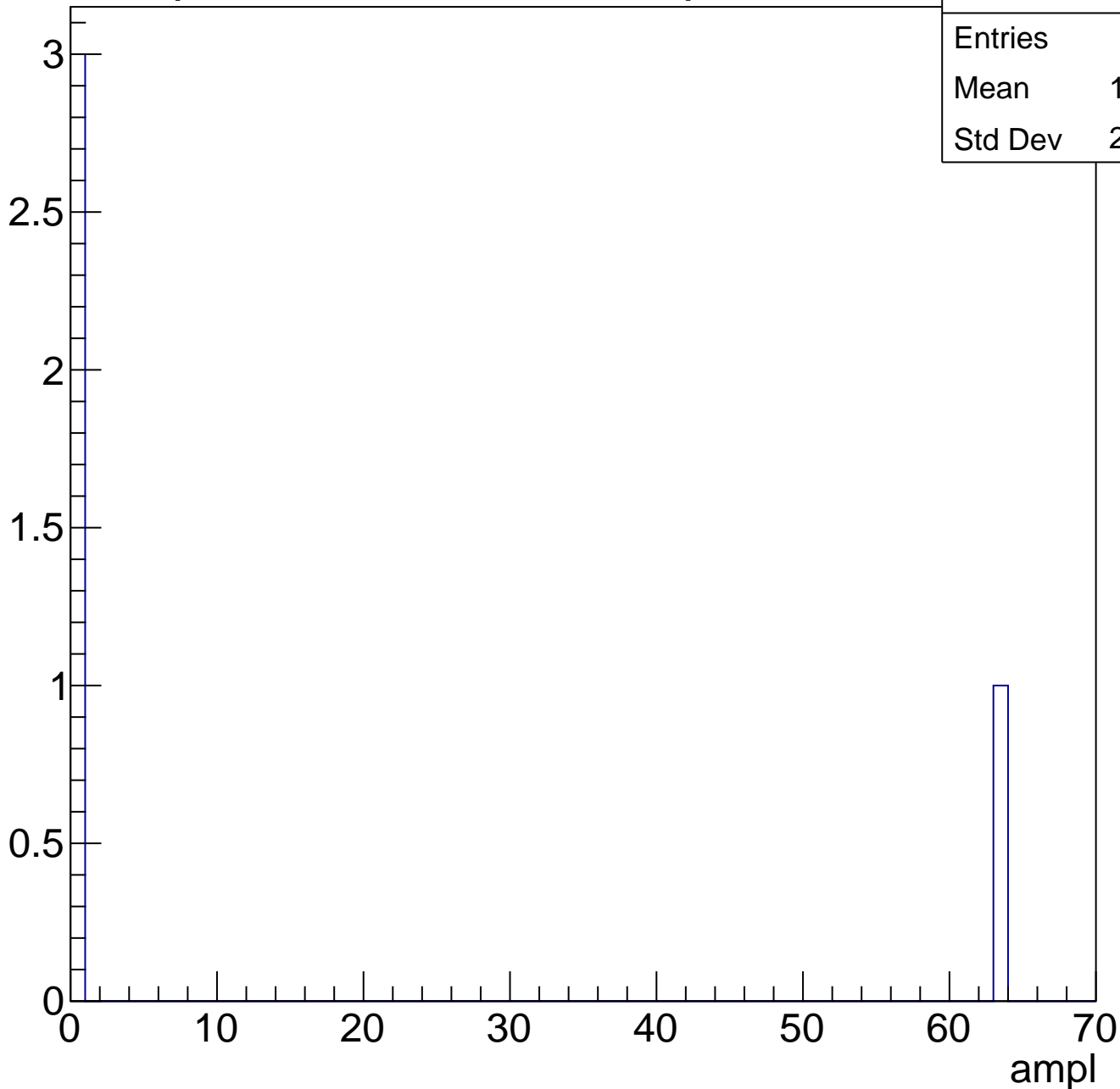
Entries	13
Mean	60.85
Std Dev	2.348



B1L101S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch111, adc0

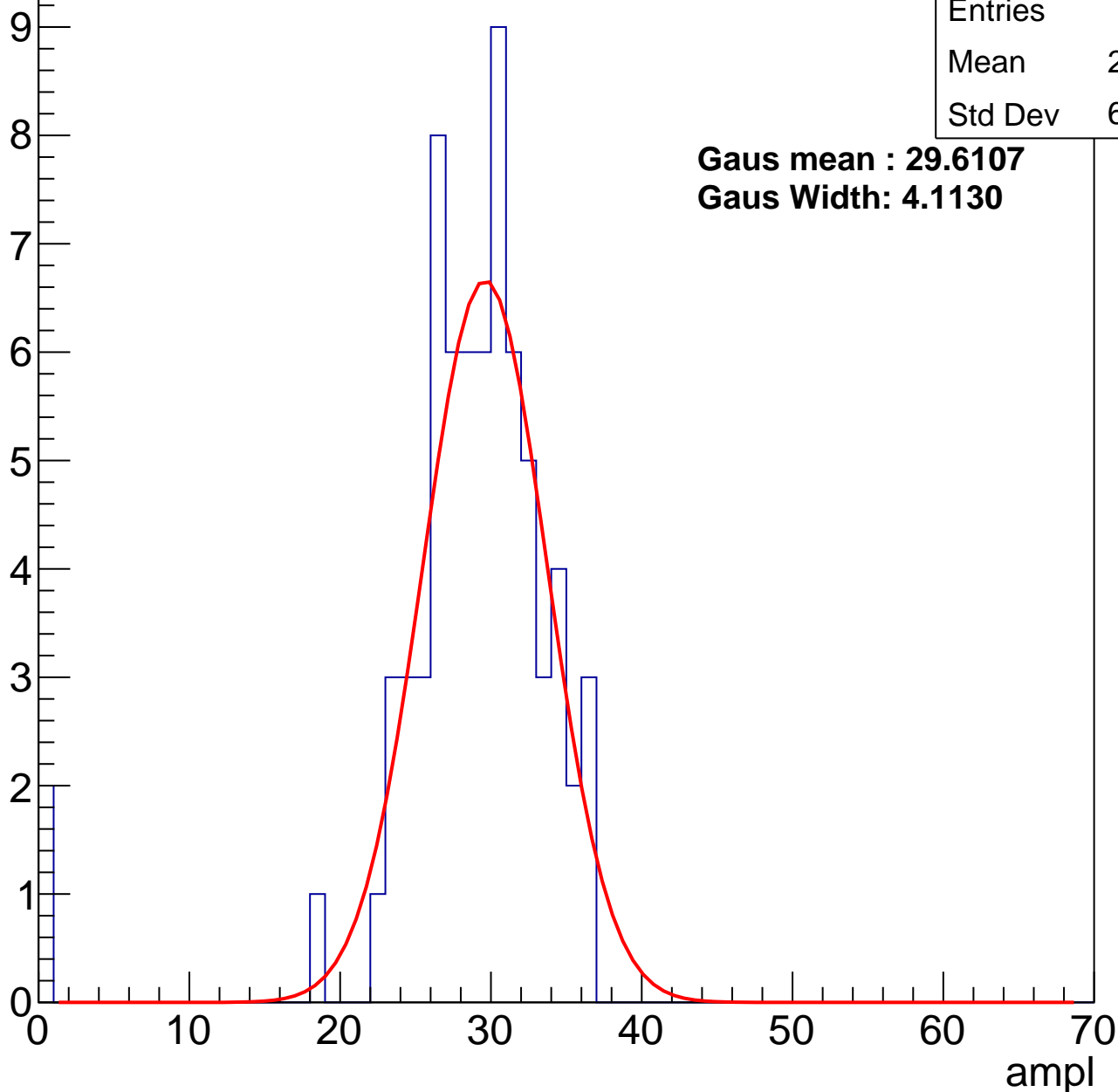
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.13
Std Dev	6.023

Gaus mean : 29.6107

Gaus Width: 4.1130



B1L101S, U14-ch111, adc1

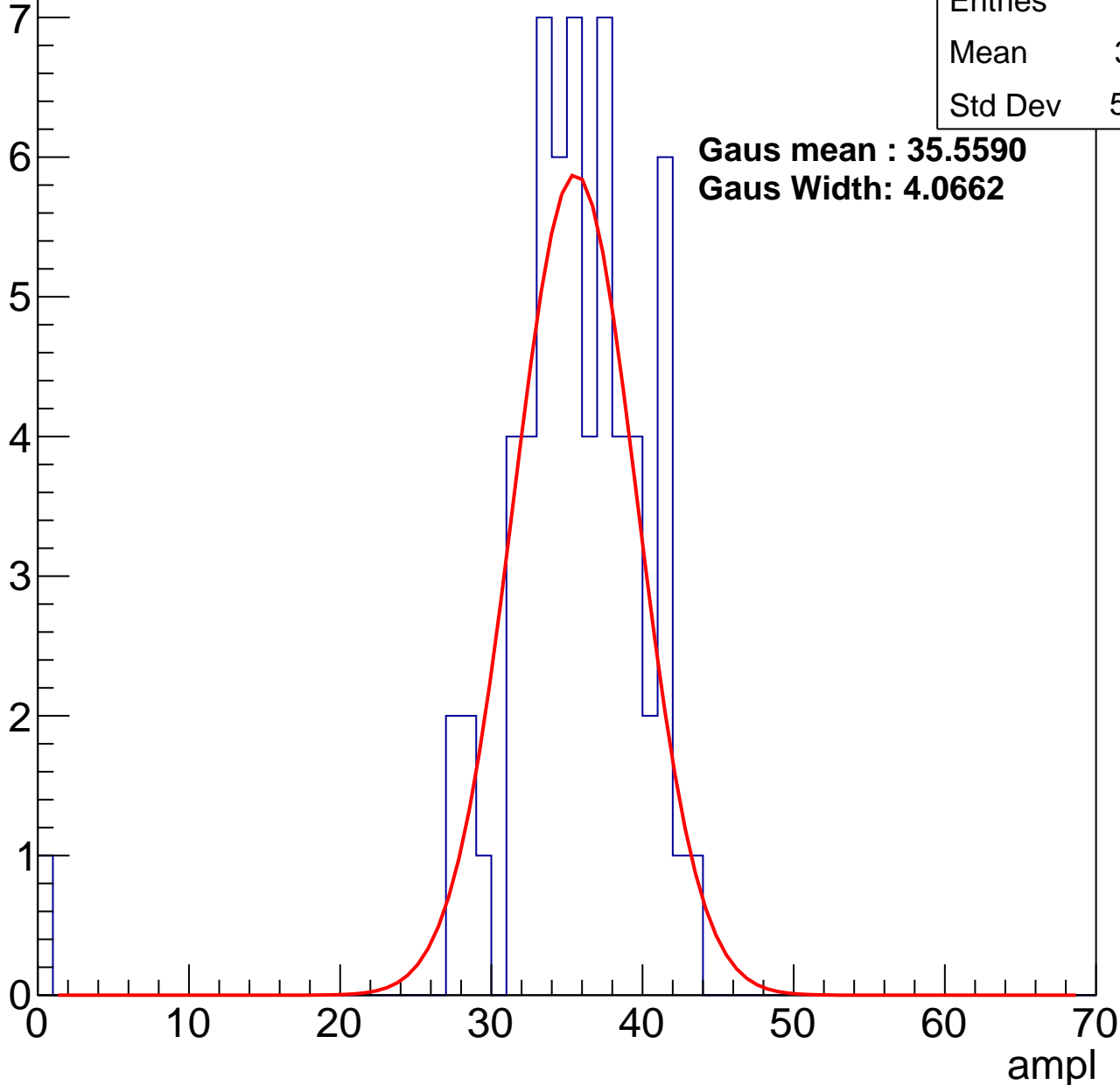
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	34.81
Std Dev	5.814

Gaus mean : 35.5590

Gaus Width: 4.0662



B1L101S, U14-ch111, adc2

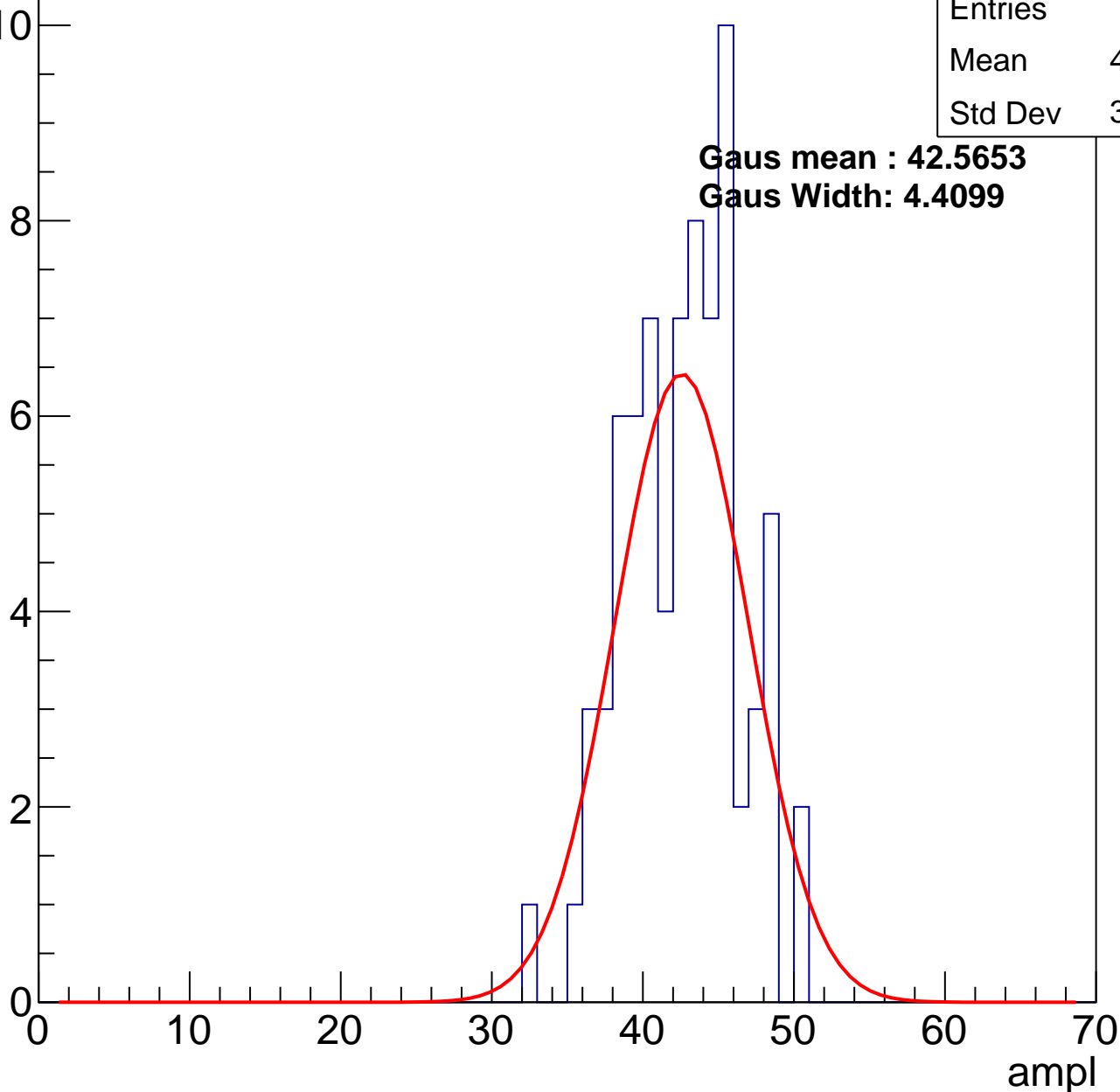
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	42.15
Std Dev	3.748

Gaus mean : 42.5653

Gaus Width: 4.4099

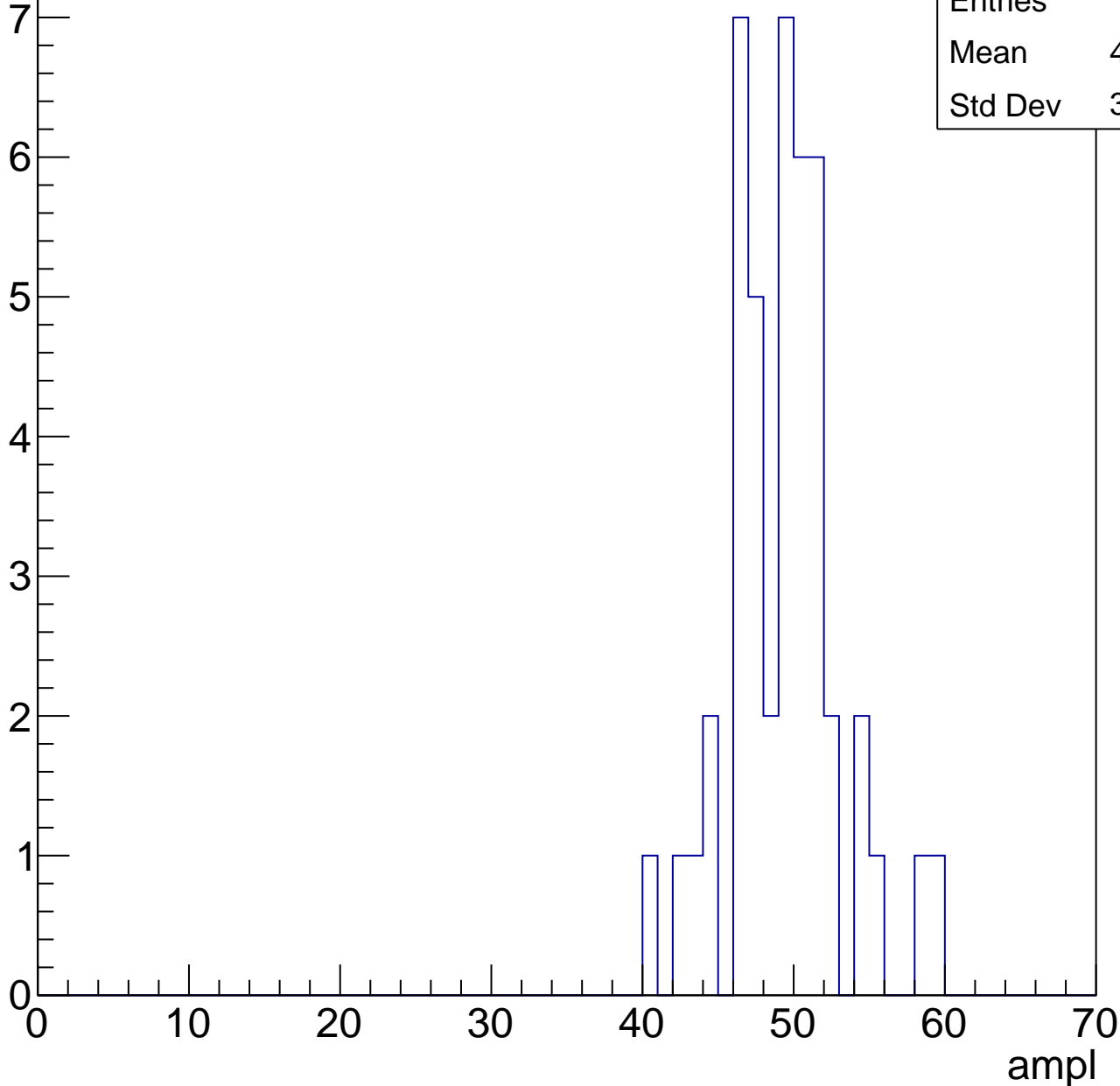


B1L101S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	48.87
Std Dev	3.697

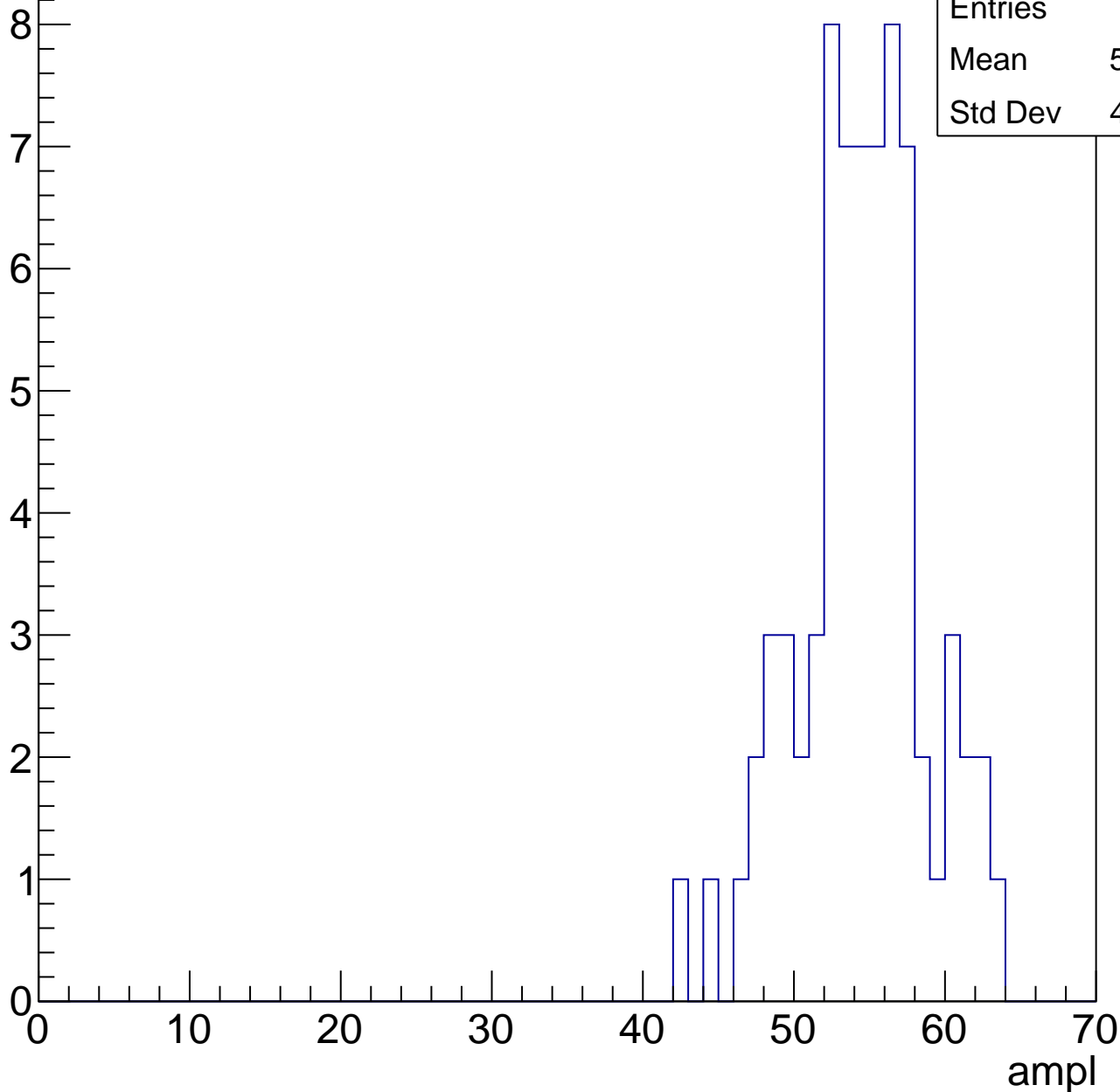


B1L101S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	53.96
Std Dev	4.214

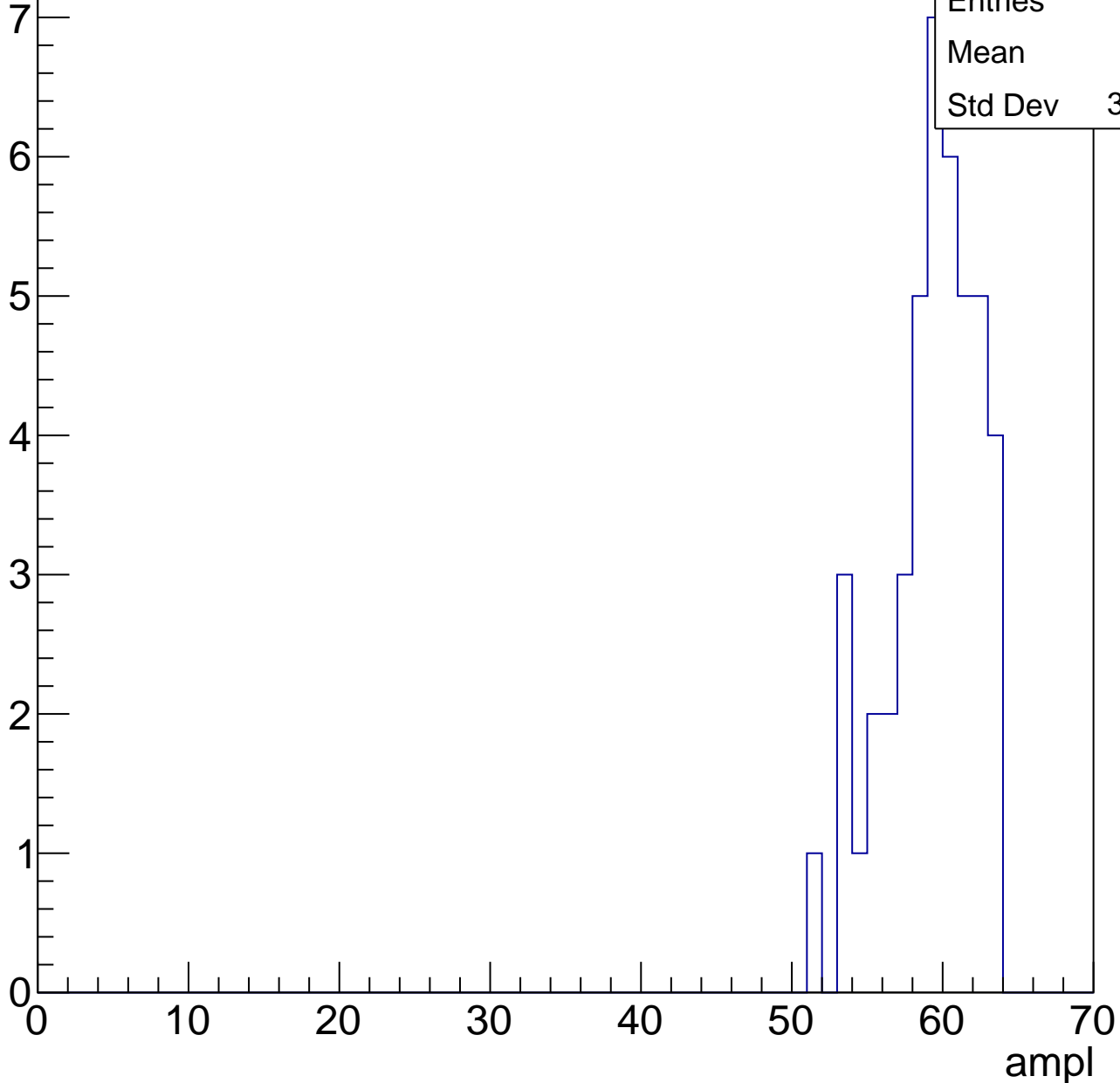


B1L101S, U14-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.8
Std Dev	3.004

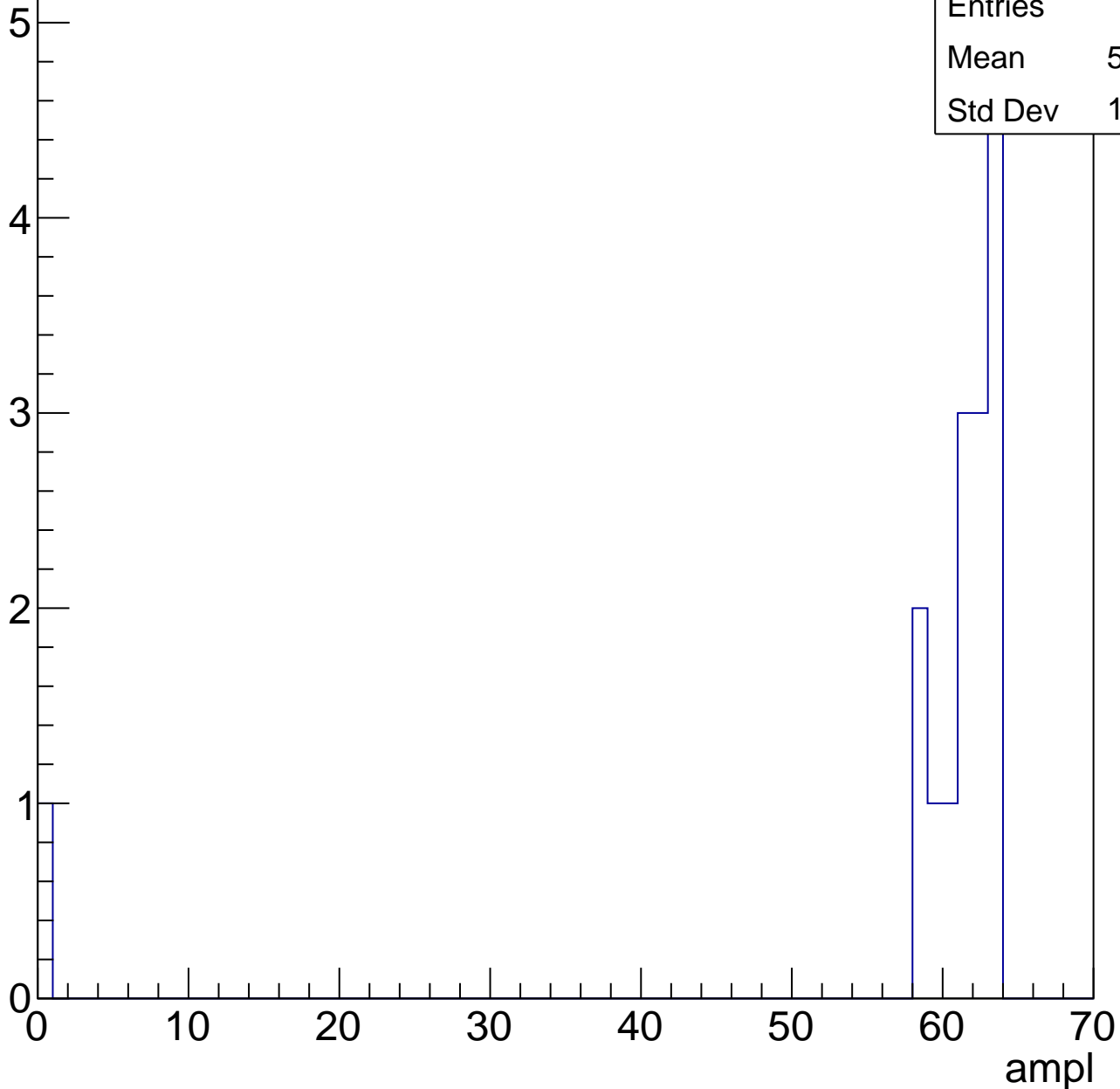


B1L101S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.44
Std Dev	14.92



B1L101S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch112, adc0

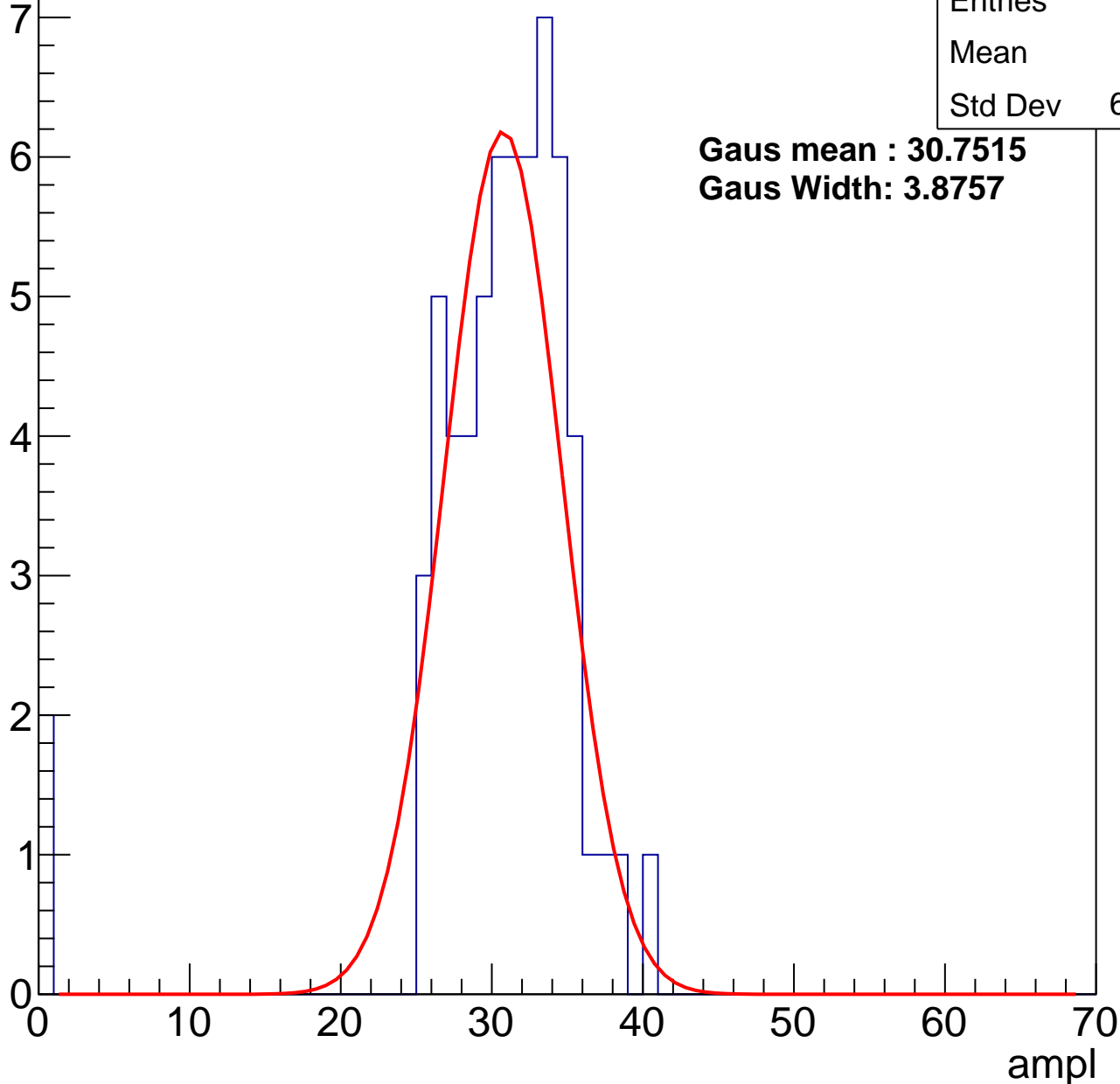
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.9
Std Dev	6.412

Gaus mean : 30.7515

Gaus Width: 3.8757



B1L101S, U14-ch112, adc1

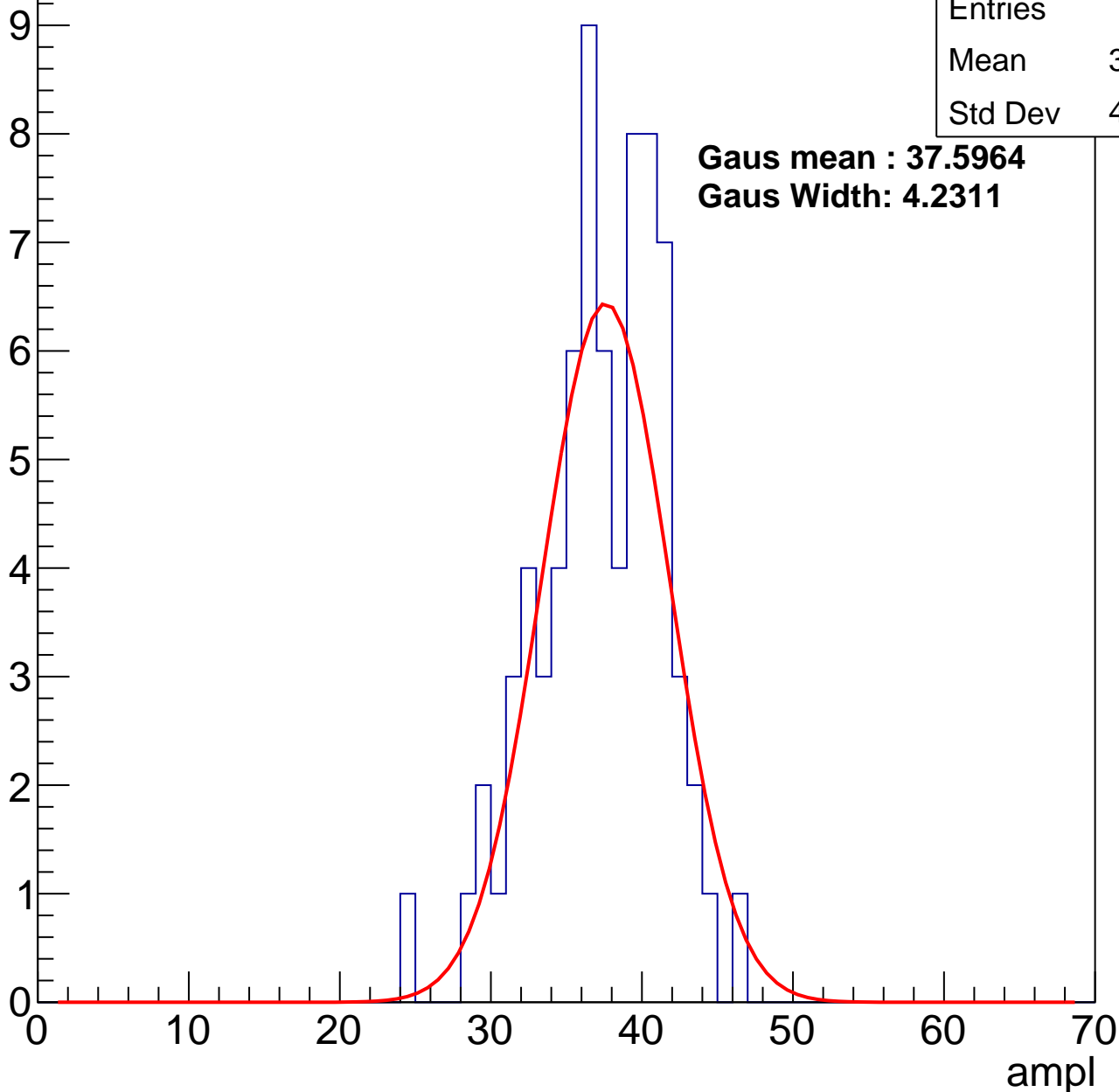
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.82
Std Dev	4.105

Gaus mean : 37.5964

Gaus Width: 4.2311



B1L101S, U14-ch112, adc2

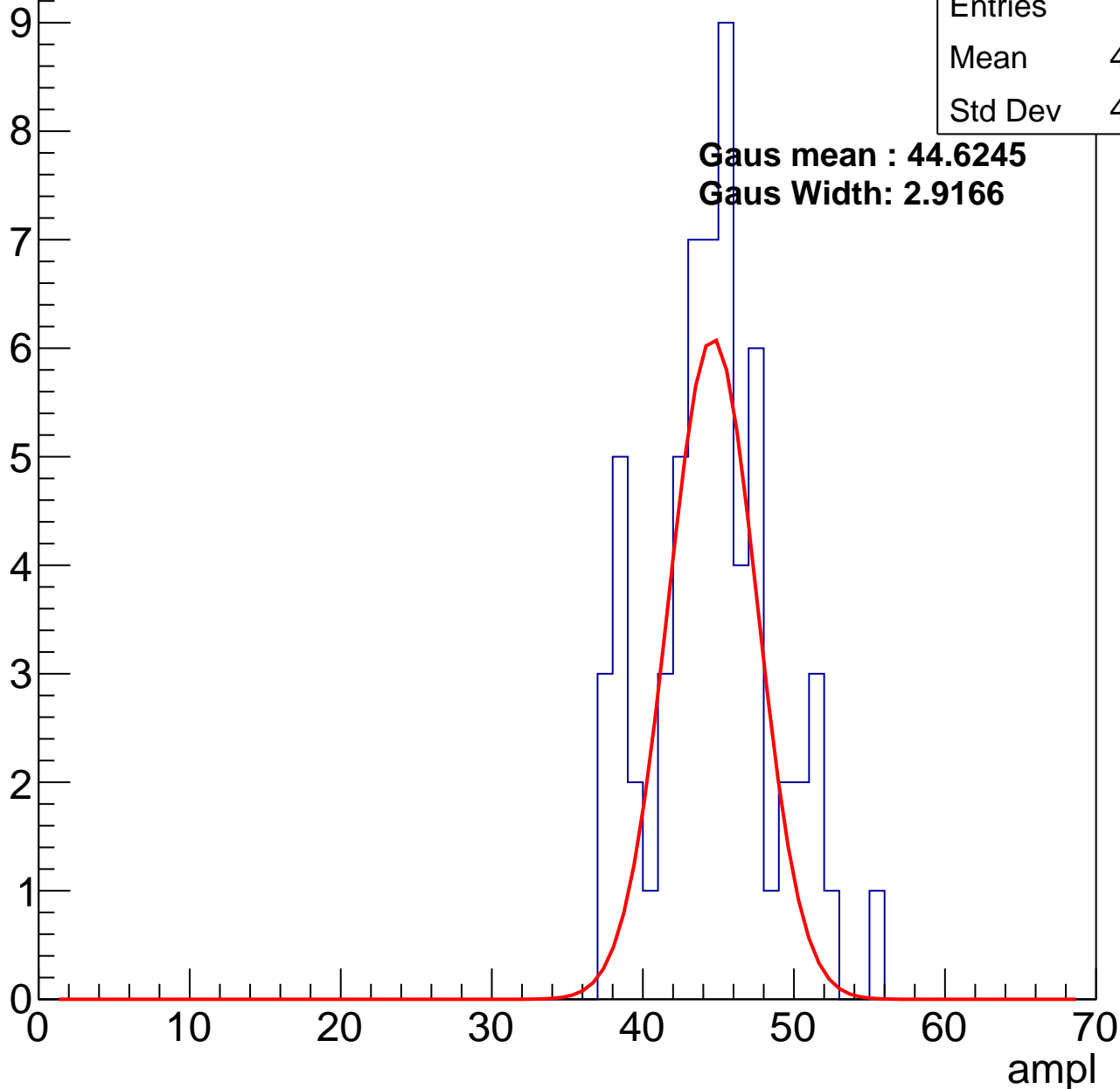
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.16
Std Dev	4.005

Gaus mean : 44.6245

Gaus Width: 2.9166

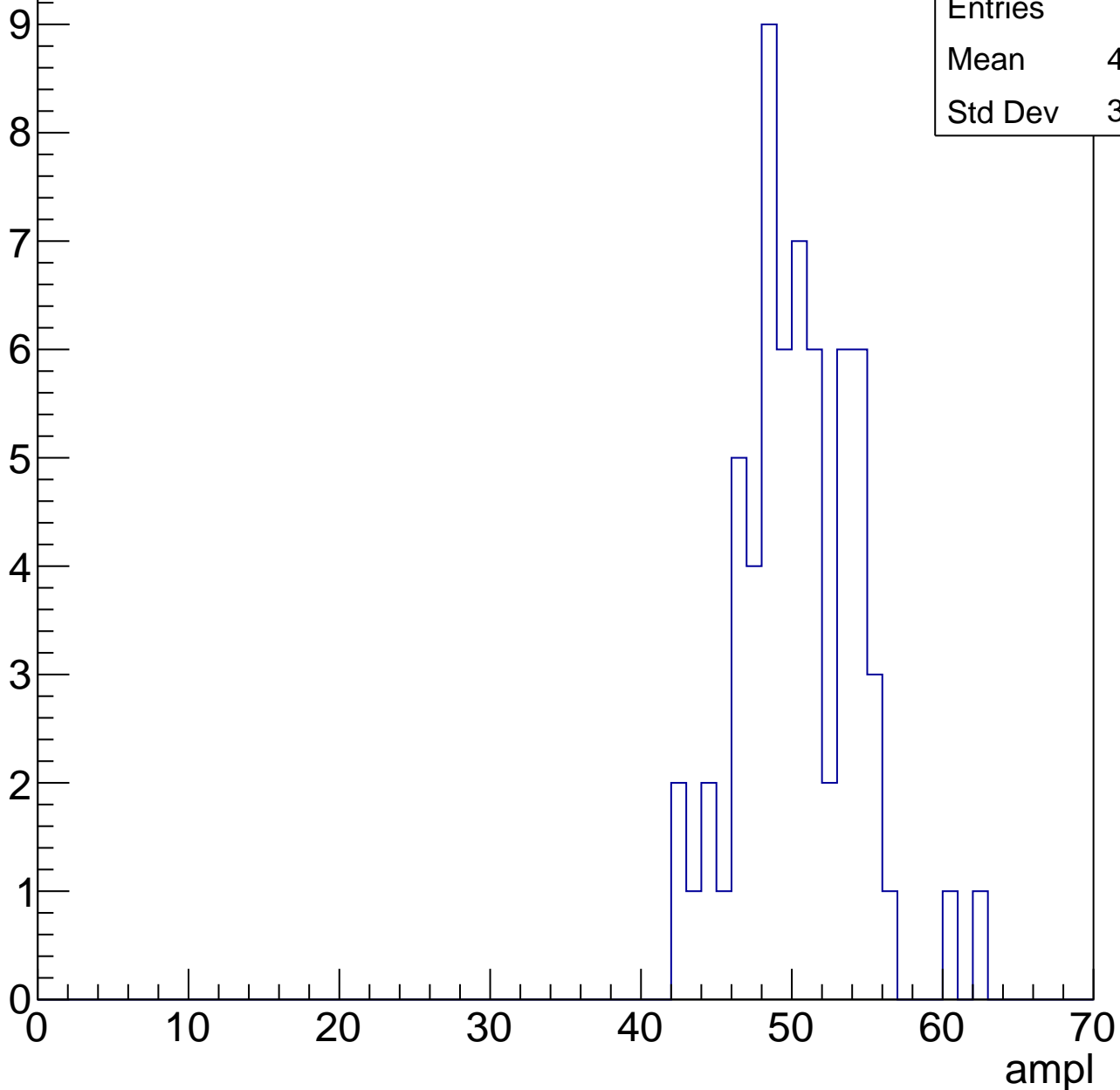


B1L101S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.98
Std Dev	3.885

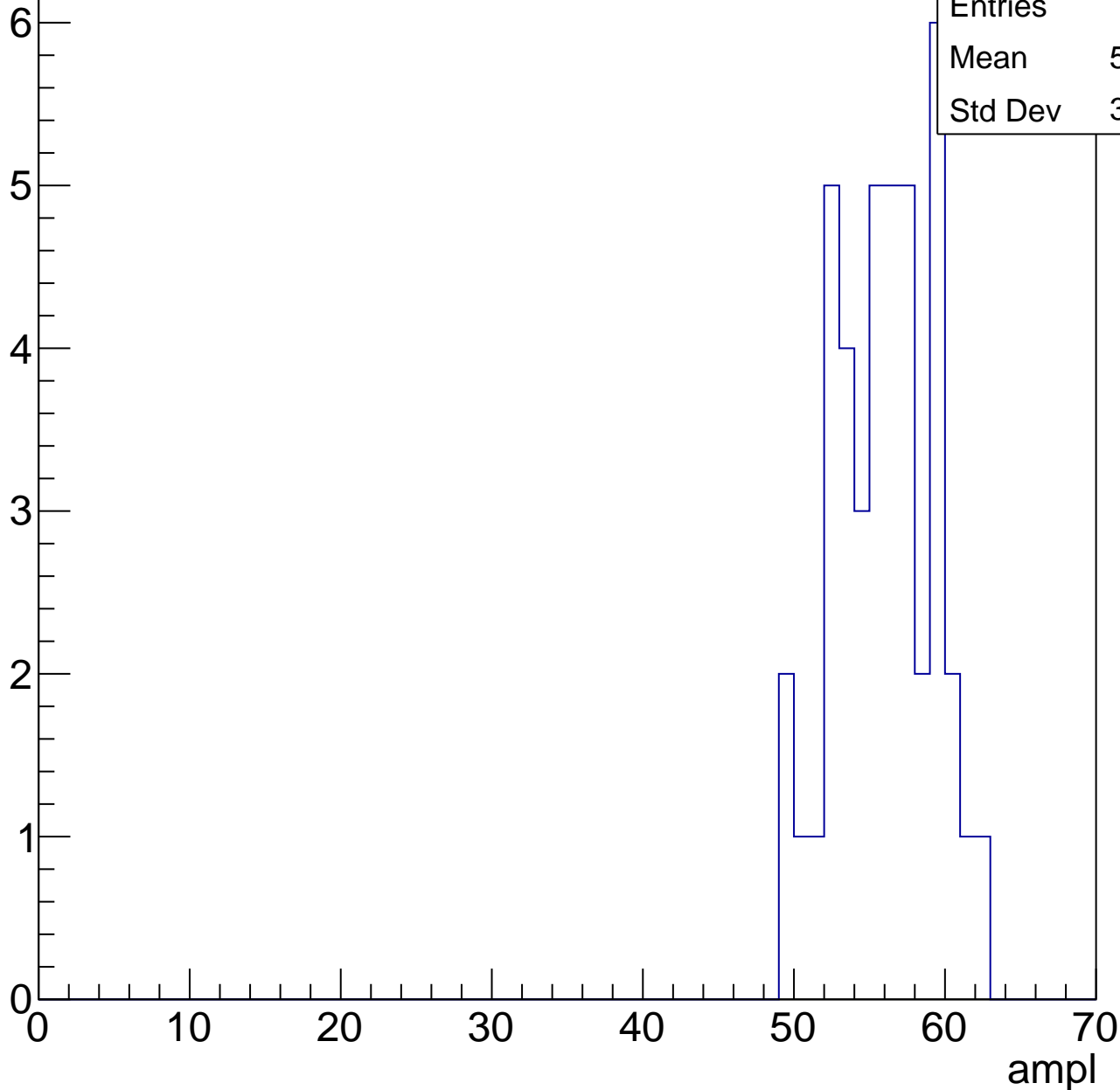


B1L101S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

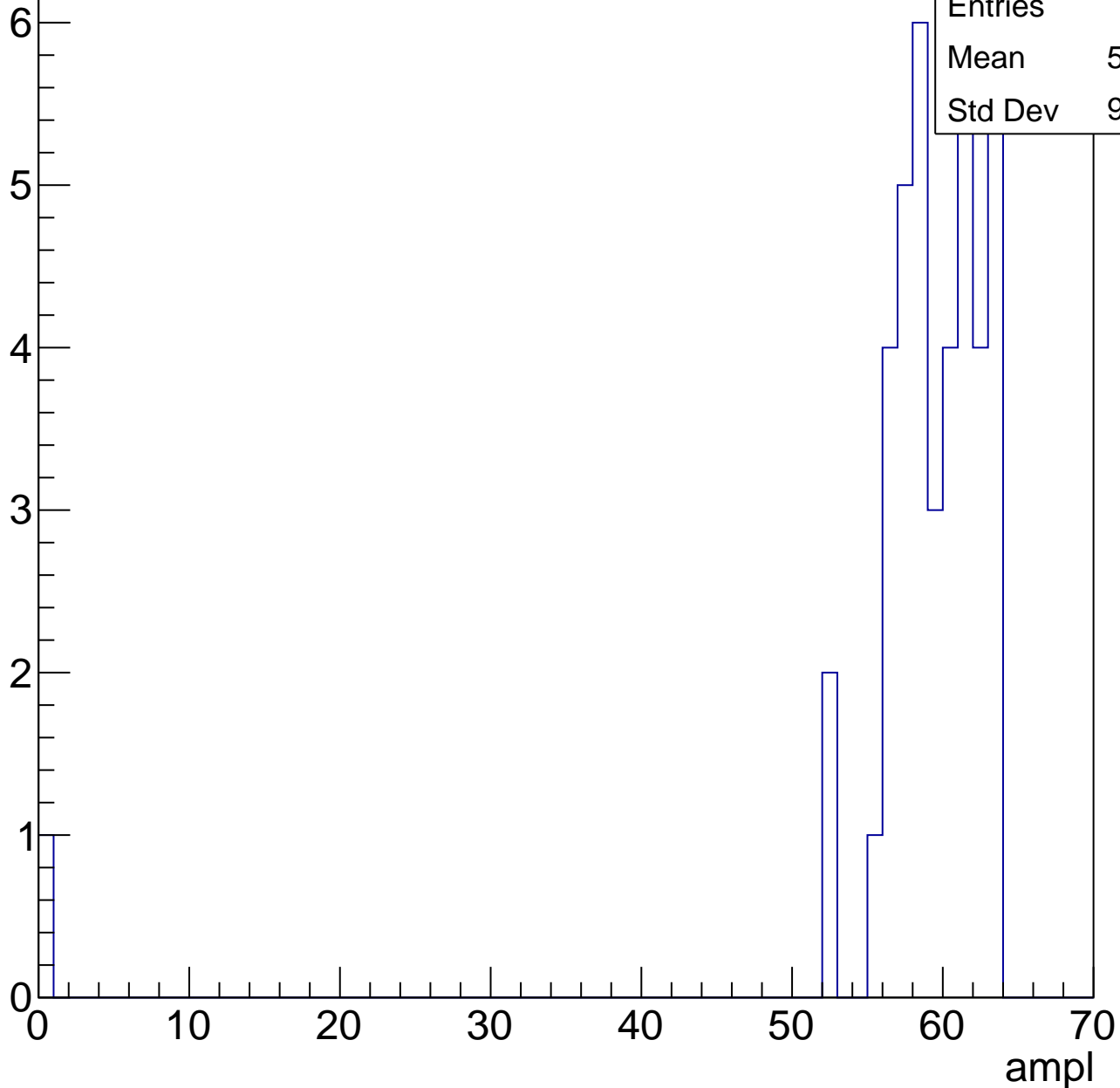
Entries	43
Mean	55.49
Std Dev	3.202



B1L101S, U14-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

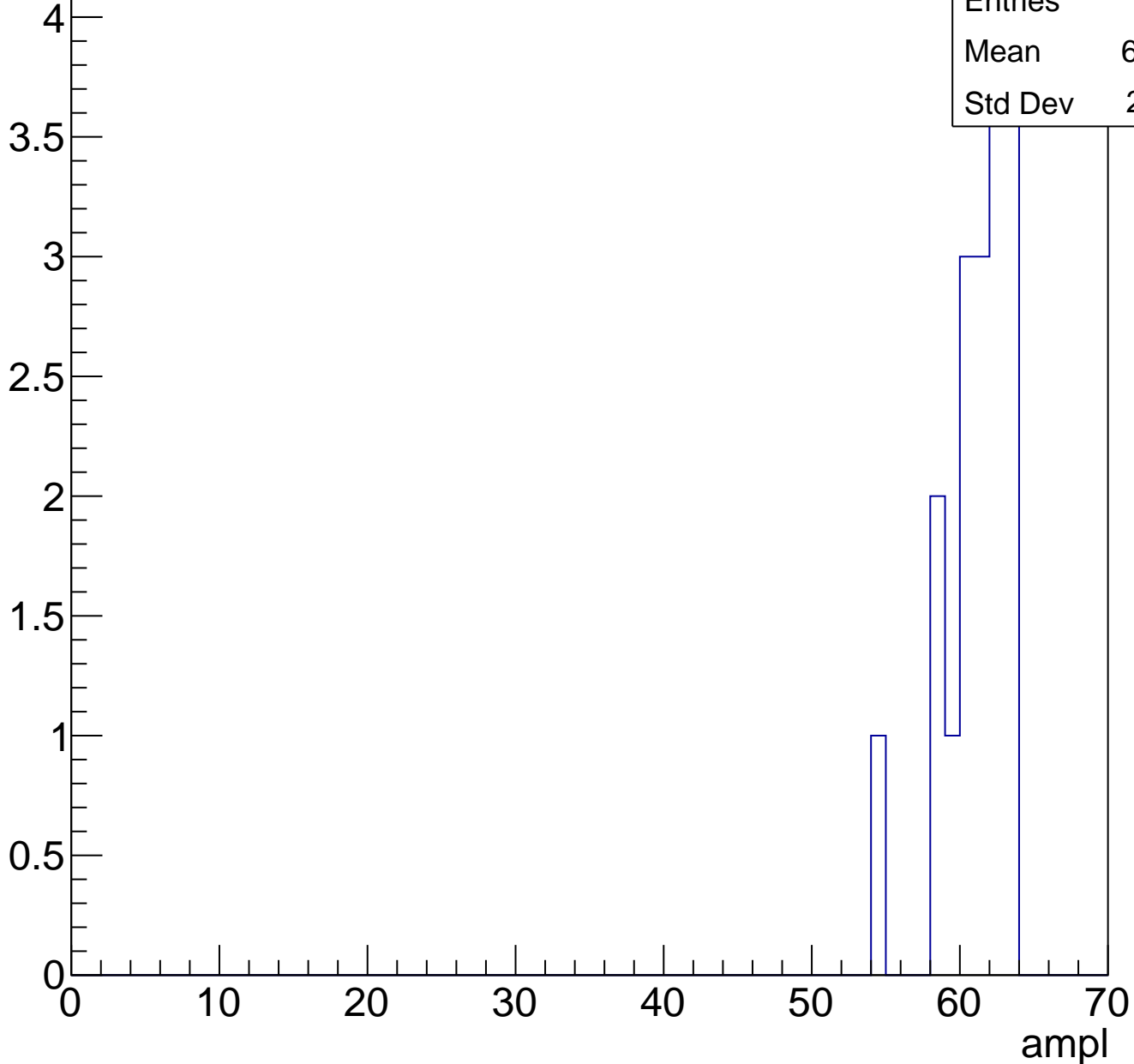
Entry



B1L101S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch113, adc0

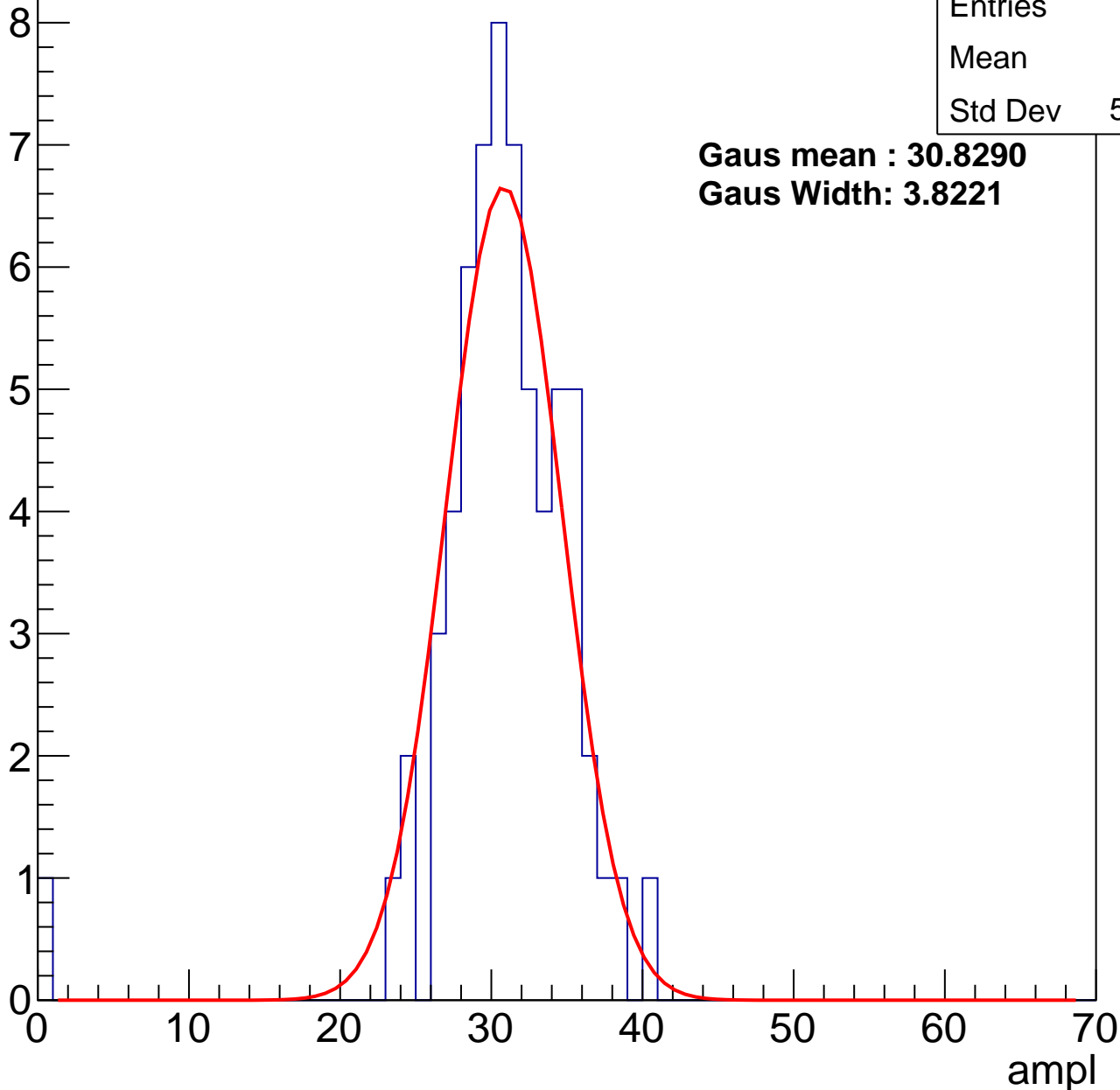
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.3
Std Dev	5.169

Gaus mean : 30.8290

Gaus Width: 3.8221



B1L101S, U14-ch113, adc1

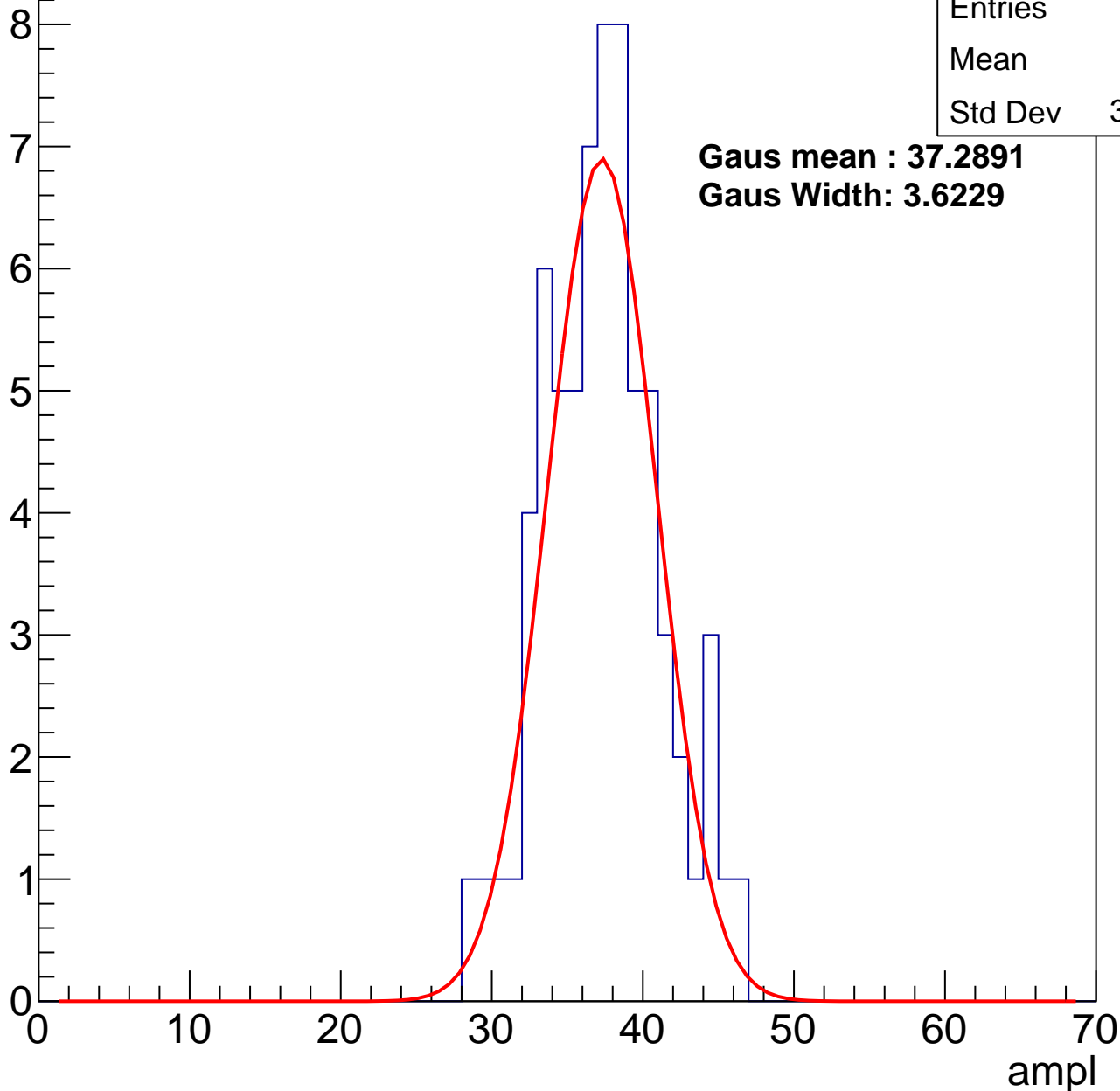
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.9
Std Dev	3.828

Gaus mean : 37.2891

Gaus Width: 3.6229



B1L101S, U14-ch113, adc2

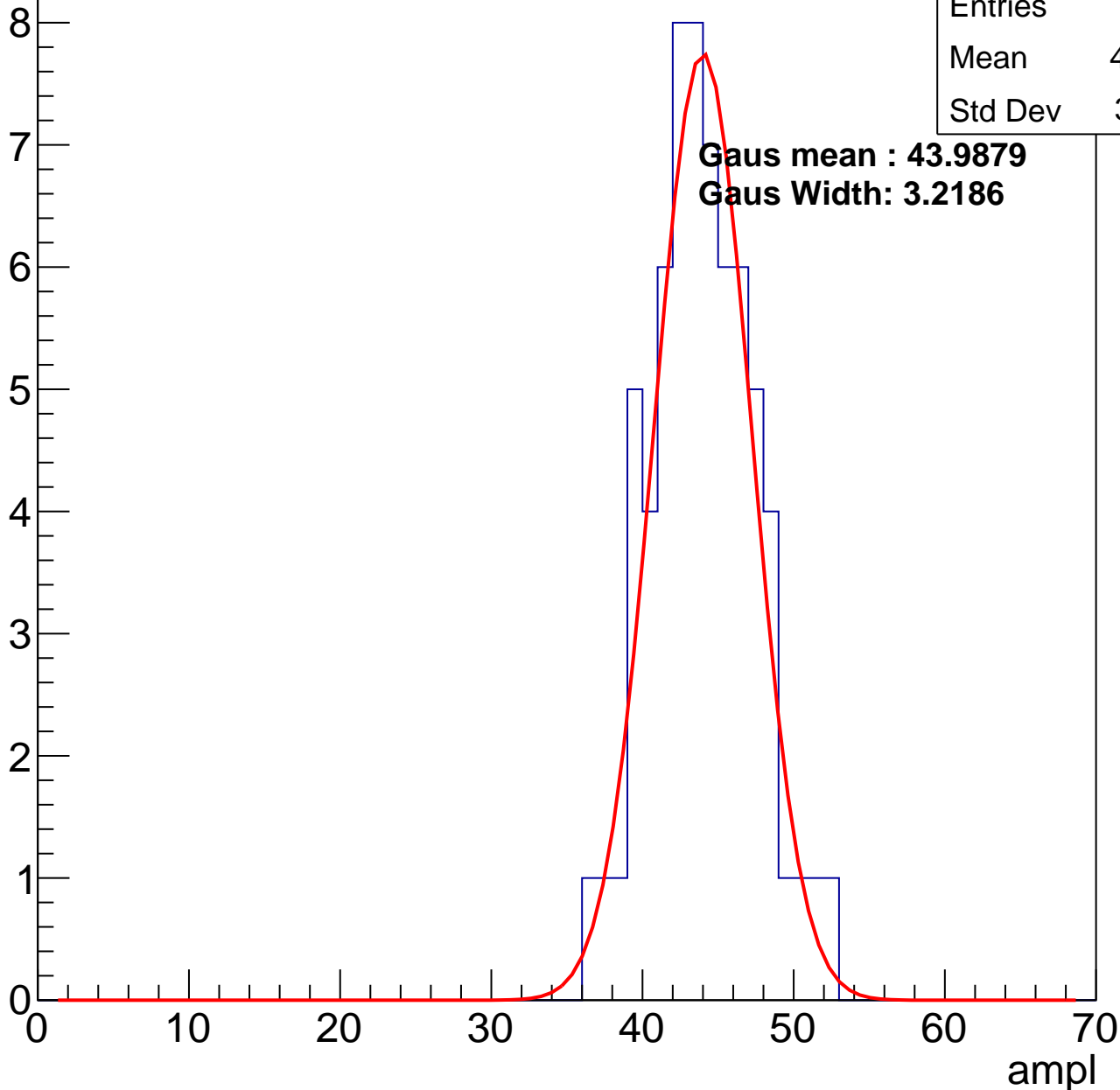
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.56
Std Dev	3.331

Gaus mean : 43.9879

Gaus Width: 3.2186

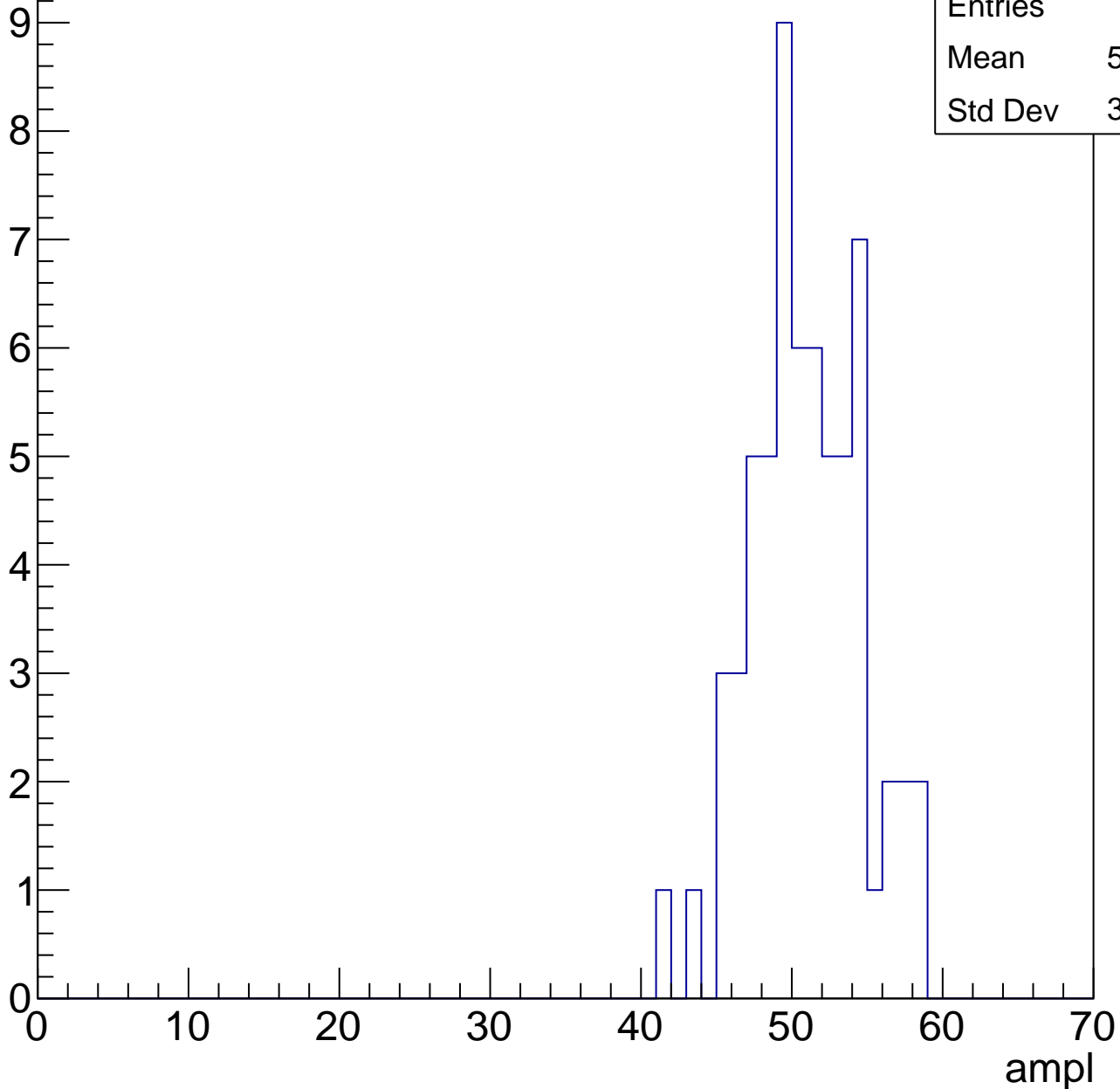


B1L101S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

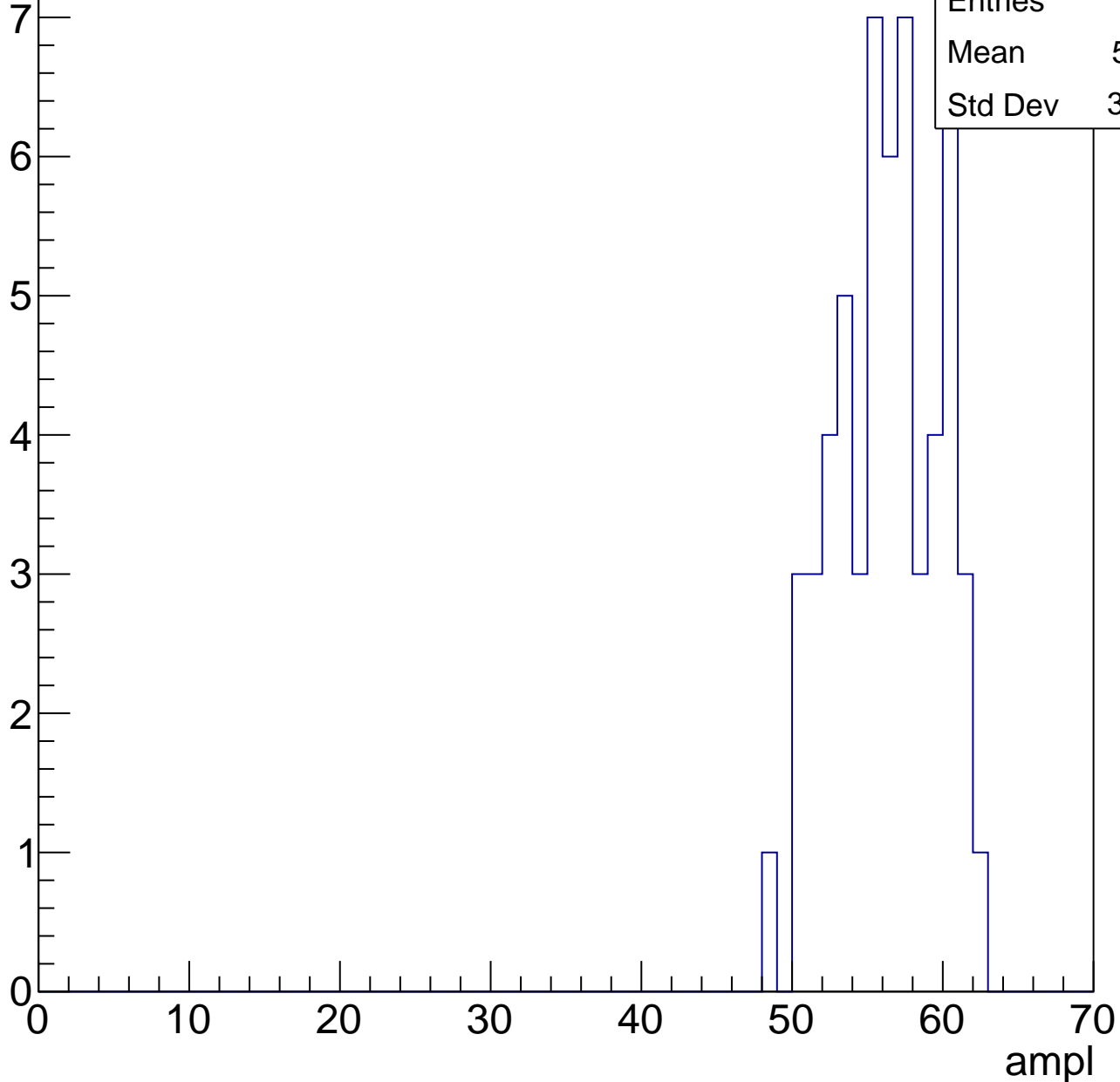
Entries	63
Mean	50.46
Std Dev	3.607



B1L101S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

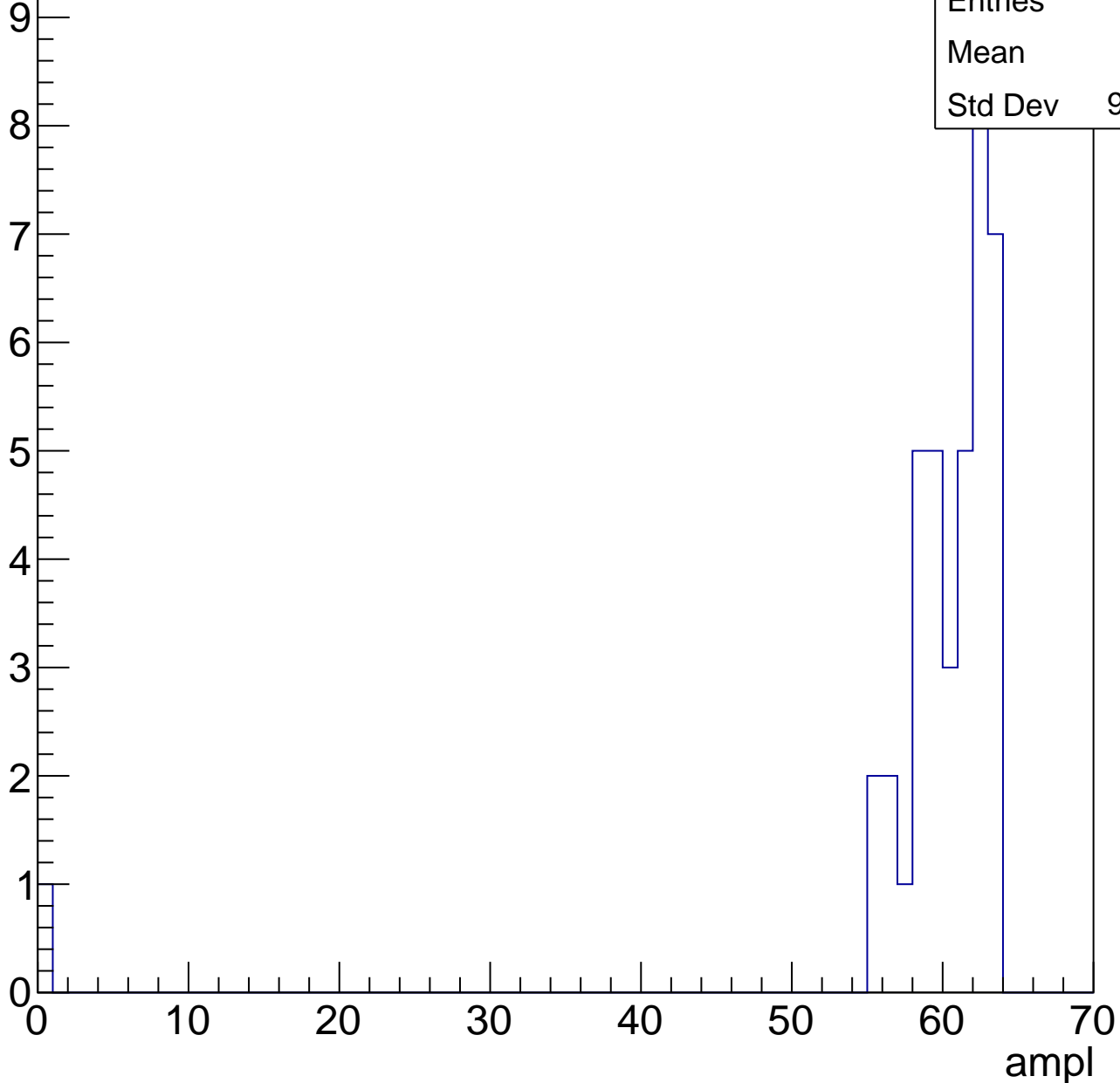


B1L101S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

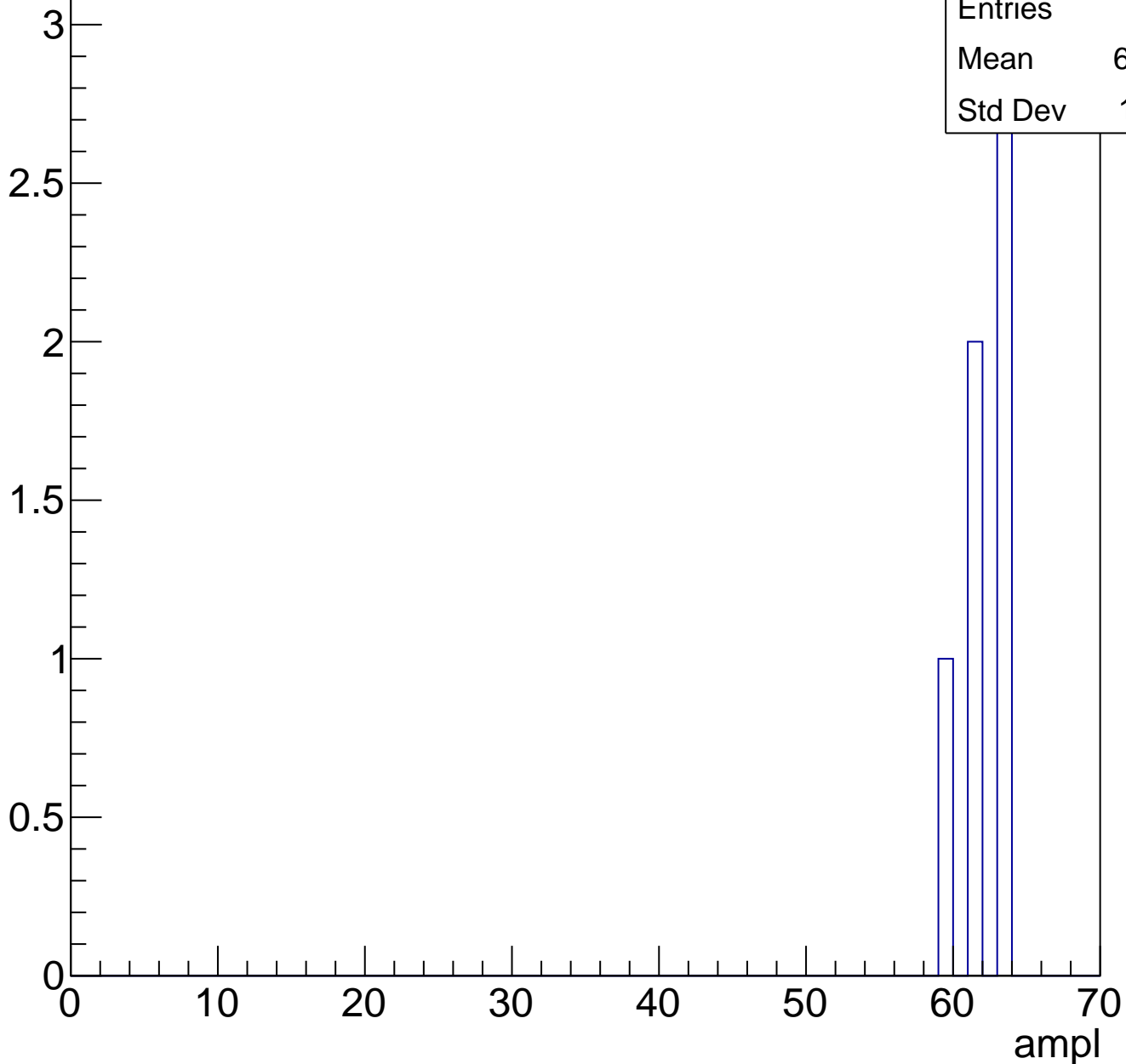
Entries	40
Mean	58.7
Std Dev	9.686



B1L101S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch114, adc0

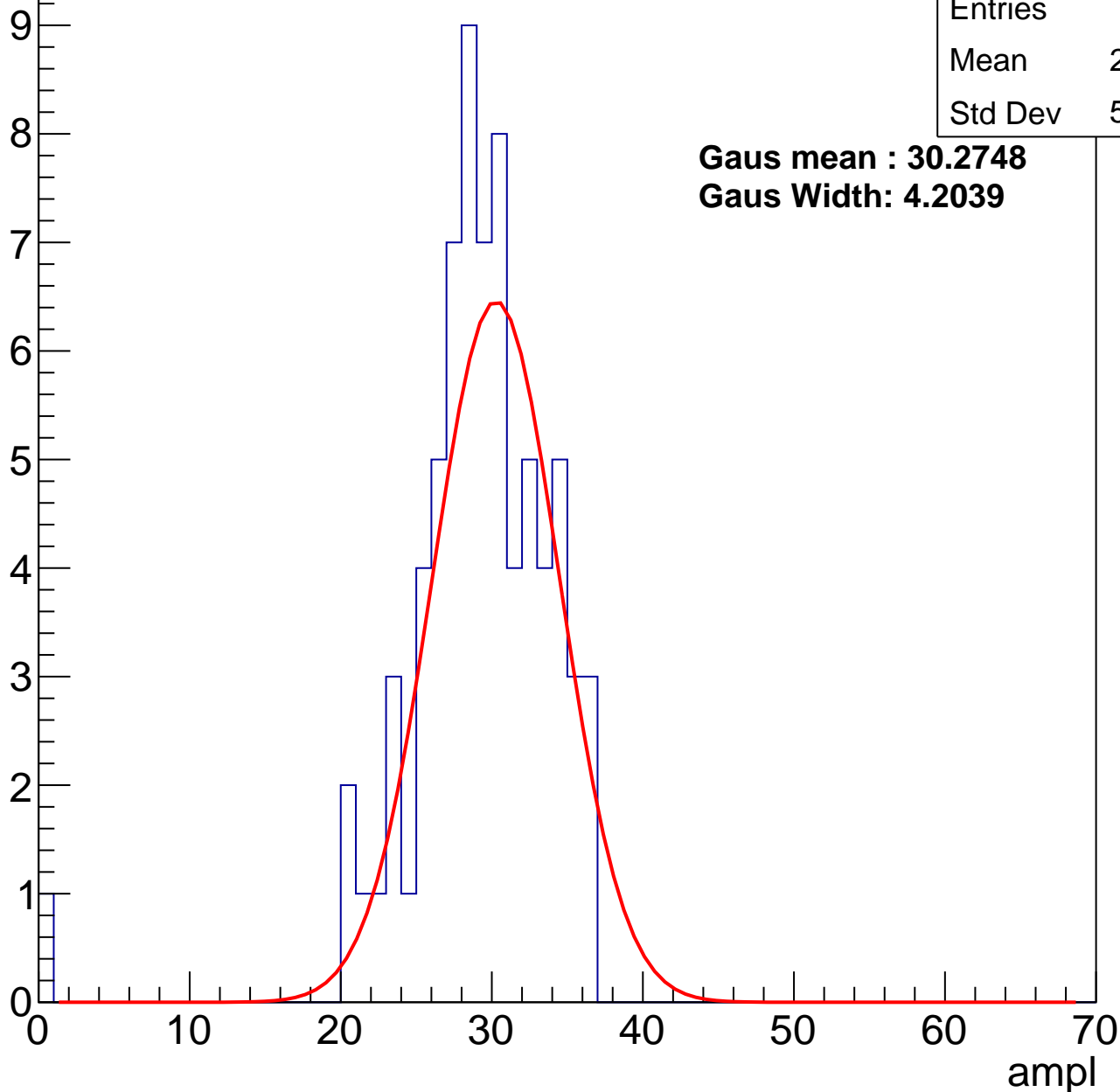
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.62
Std Dev	5.098

Gaus mean : 30.2748

Gaus Width: 4.2039



B1L101S, U14-ch114, adc1

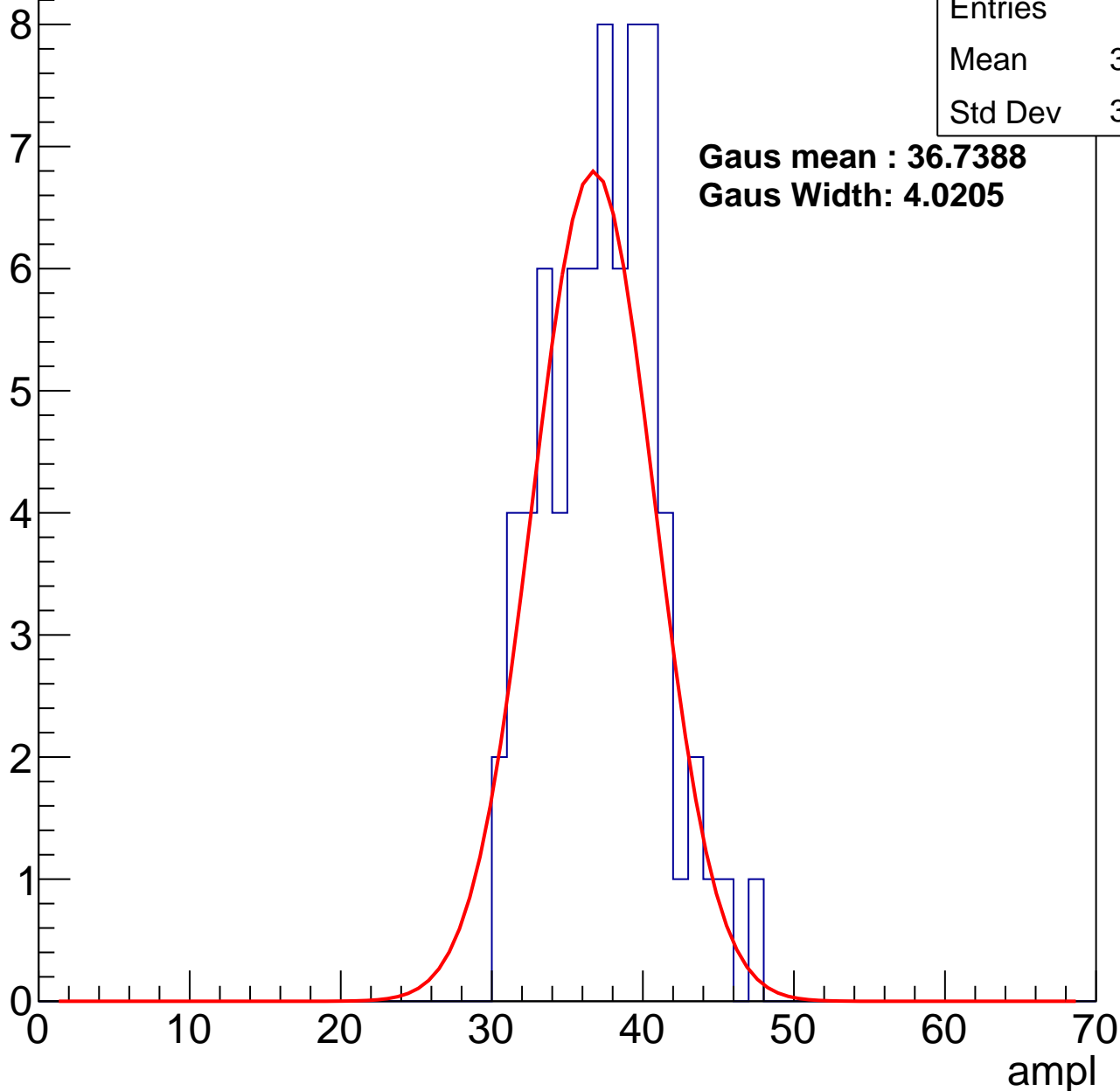
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.89
Std Dev	3.703

Gaus mean : 36.7388

Gaus Width: 4.0205



B1L101S, U14-ch114, adc2

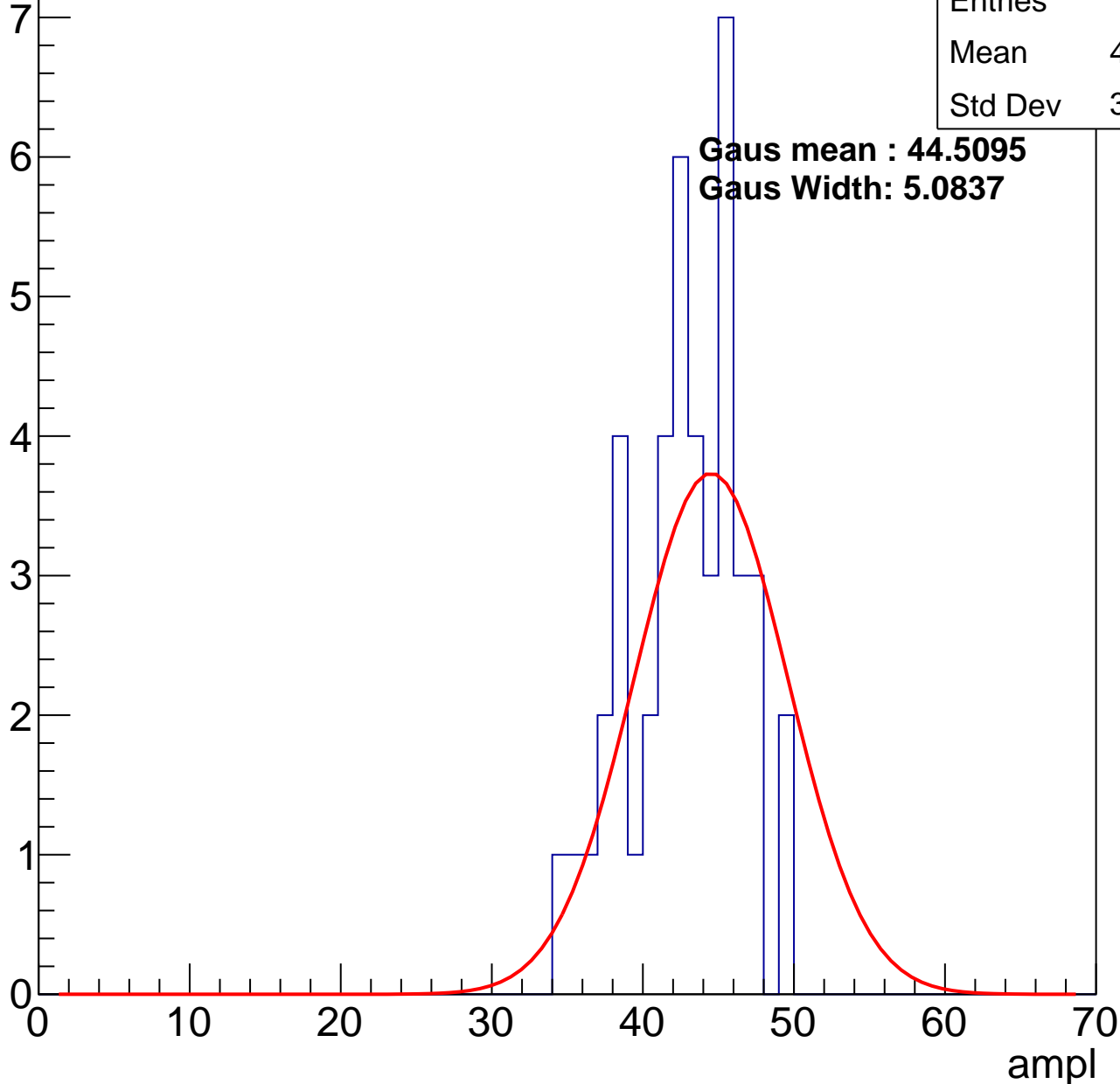
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	42.32
Std Dev	3.629

Gaus mean : 44.5095

Gaus Width: 5.0837

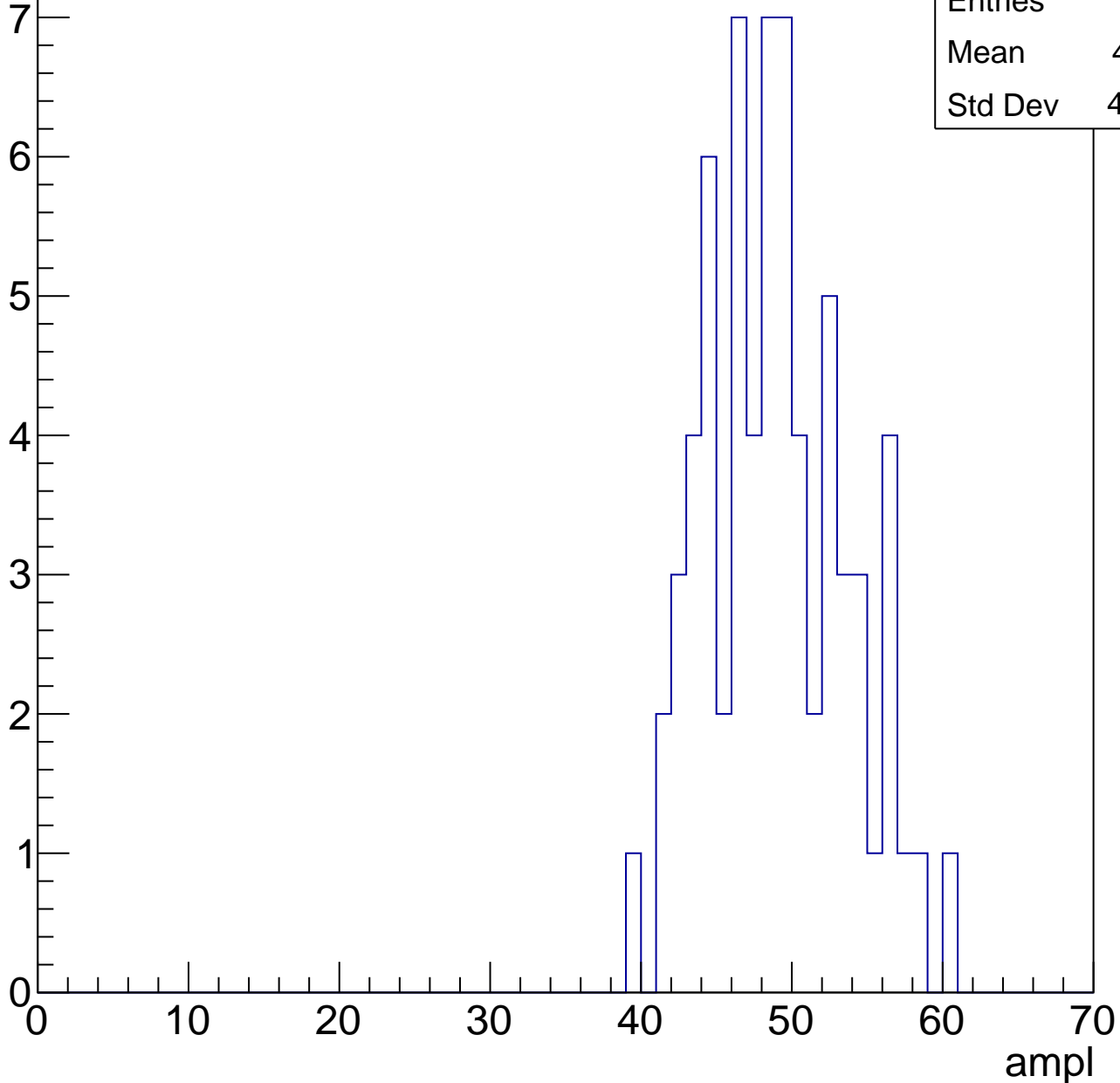


B1L101S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.51
Std Dev	4.616

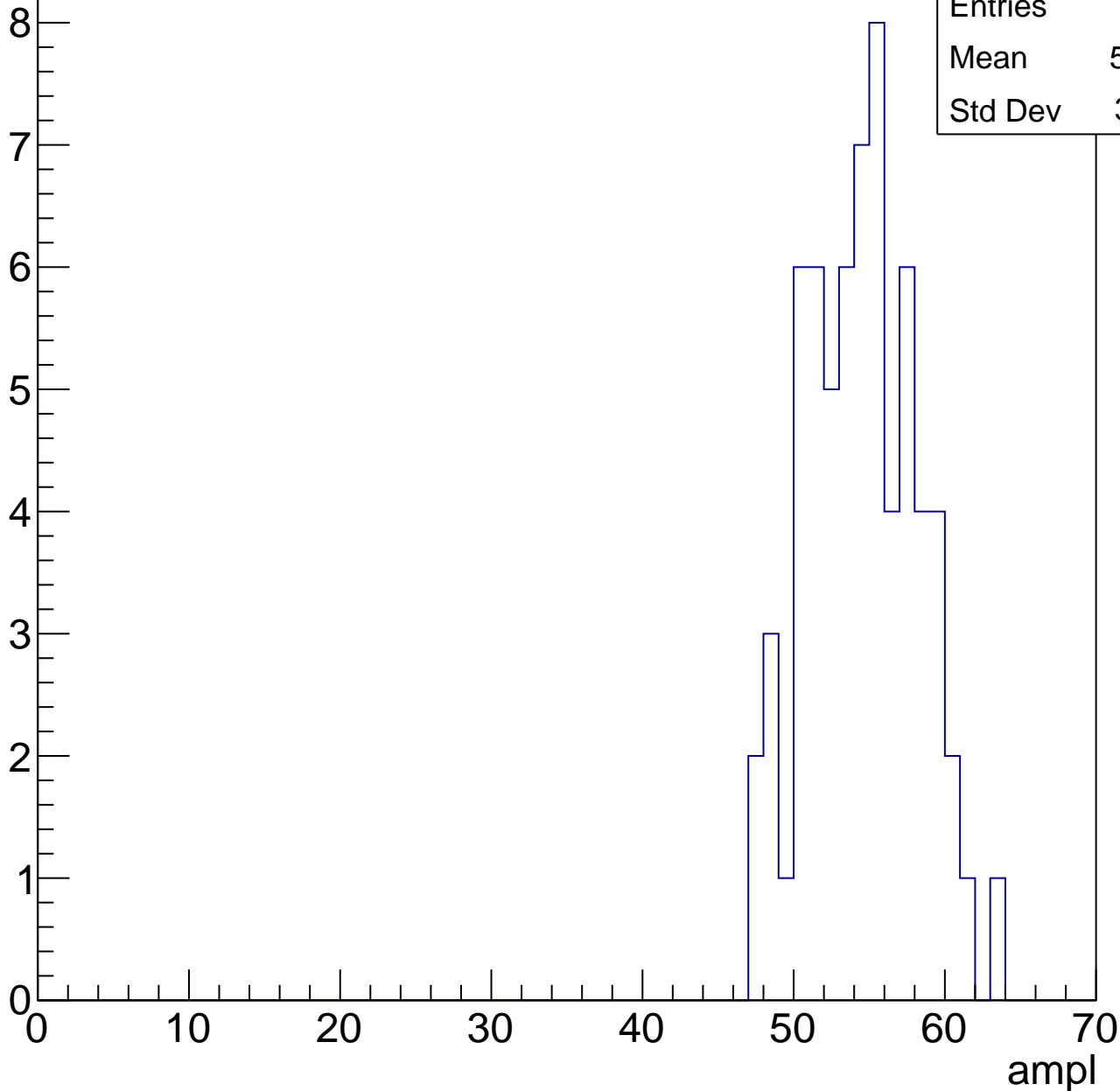


B1L101S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	54.05
Std Dev	3.591

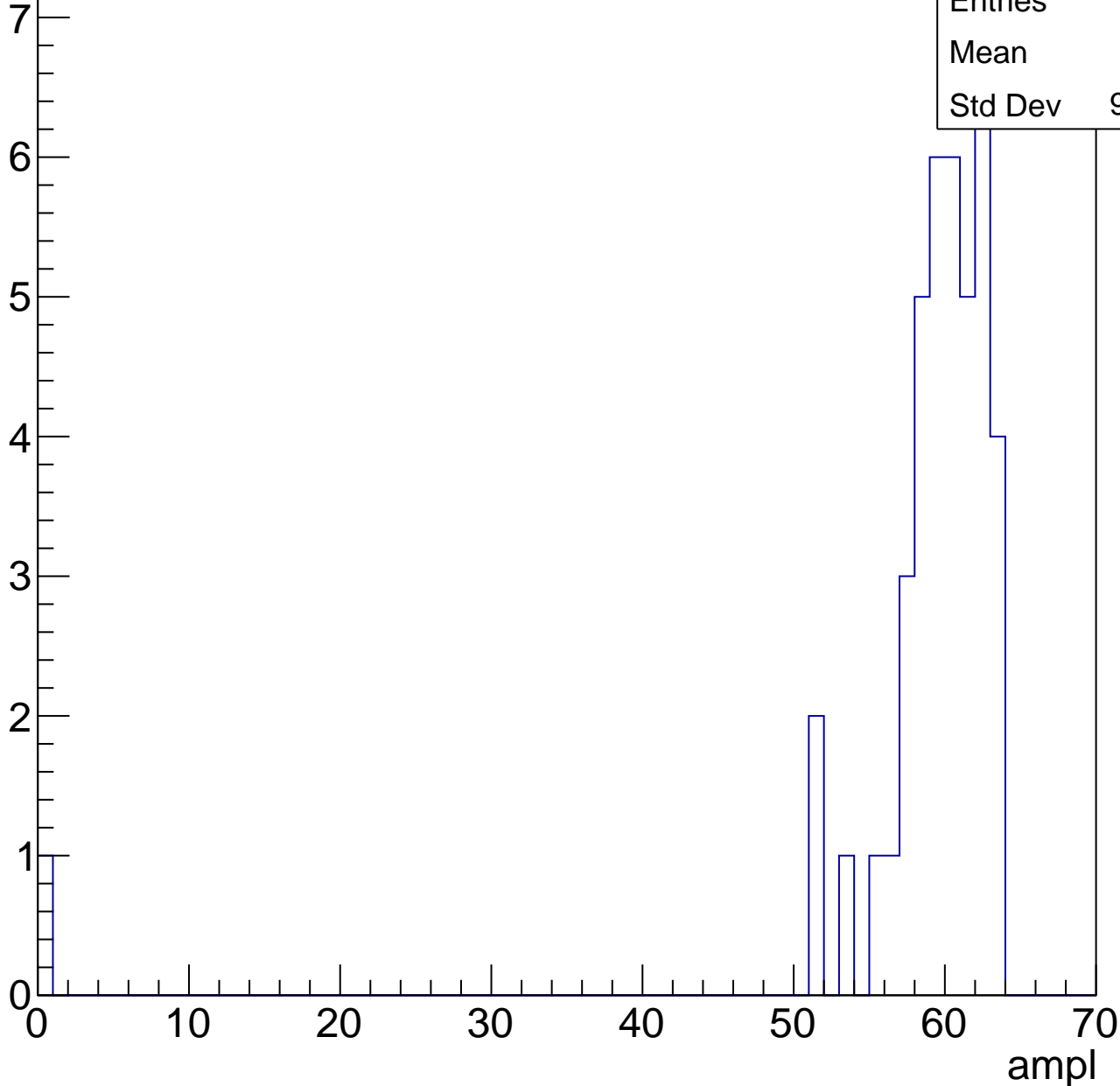


B1L101S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	57.9
Std Dev	9.499

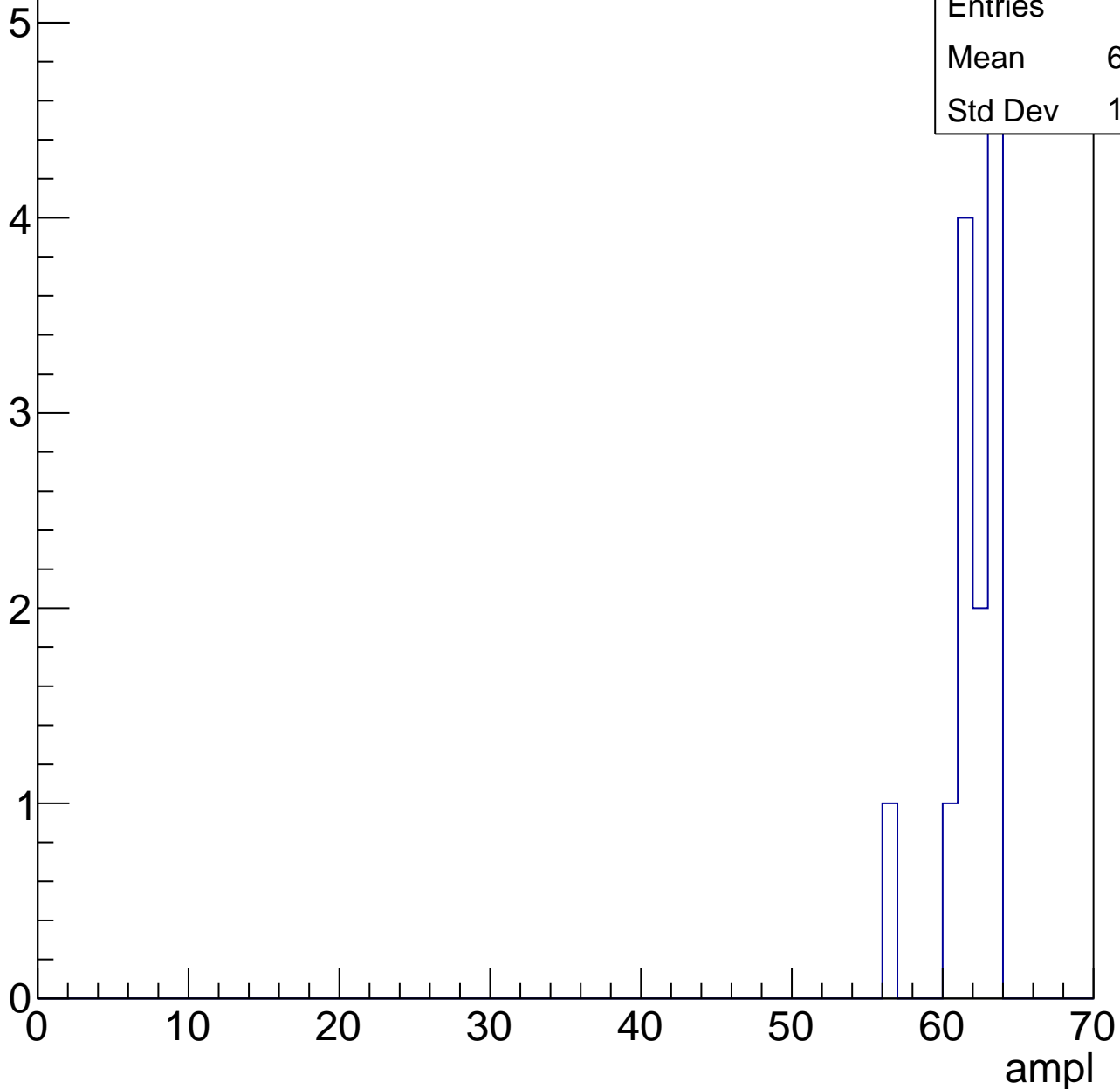


B1L101S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.46
Std Dev	1.865



B1L101S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch115, adc0

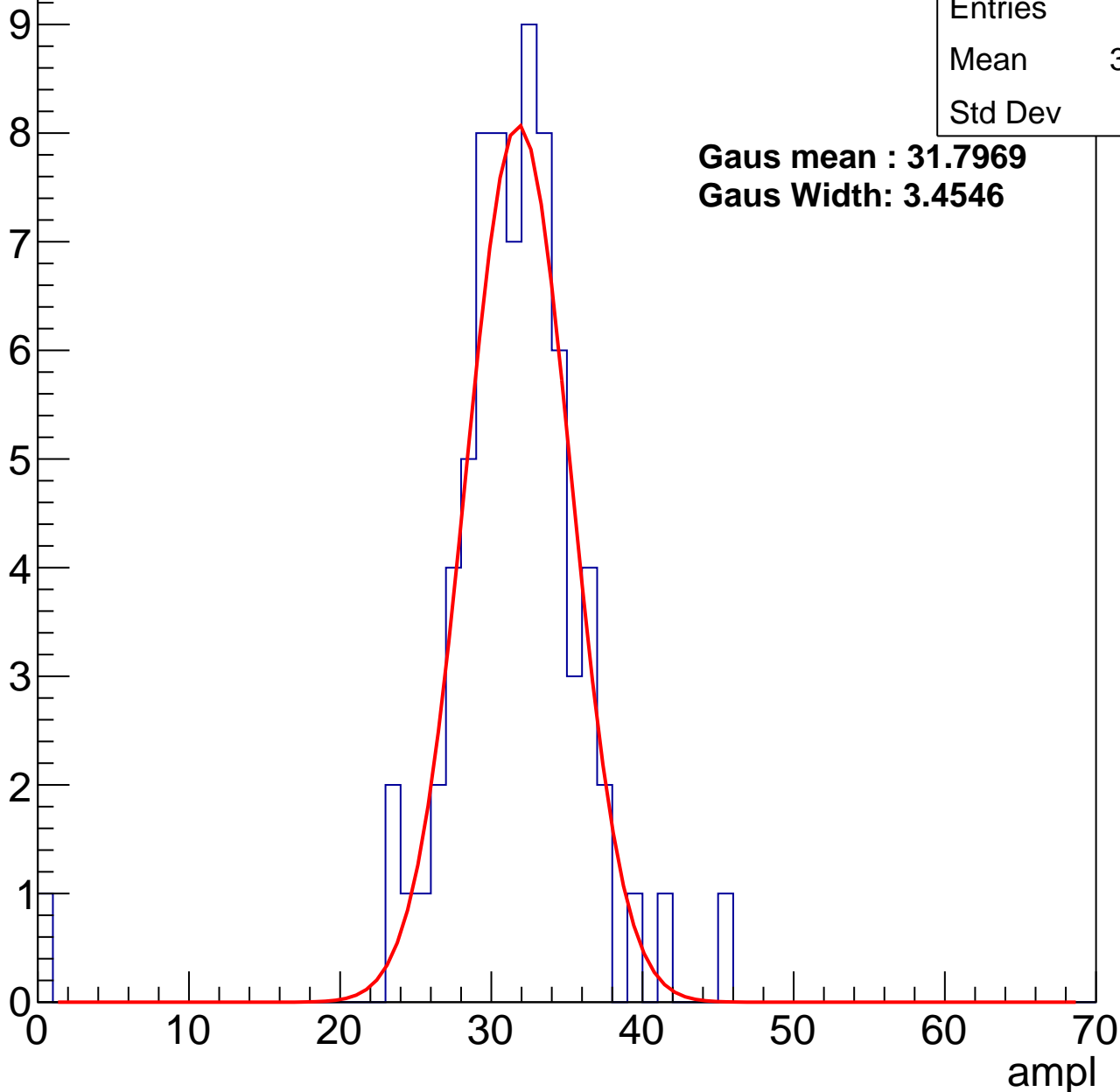
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.92
Std Dev	5.26

Gaus mean : 31.7969

Gaus Width: 3.4546



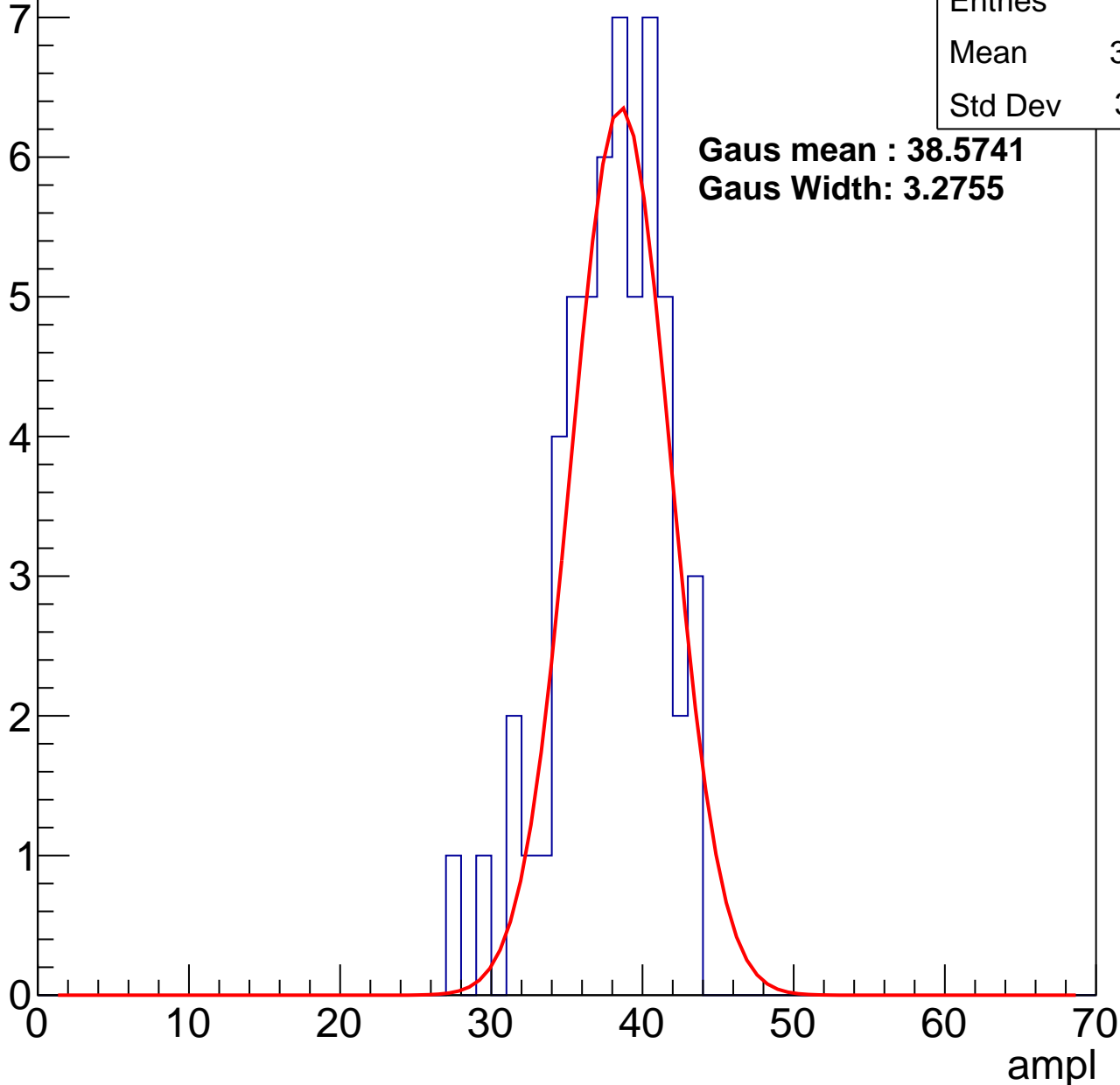
B1L101S, U14-ch115, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.36
Std Dev	3.461

Gaus mean : 38.5741
Gaus Width: 3.2755



B1L101S, U14-ch115, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	44.22
Std Dev	4.116

Gaus mean : 45.1116

Gaus Width: 4.6963

Entry

10

8

6

4

2

0

0

10

20

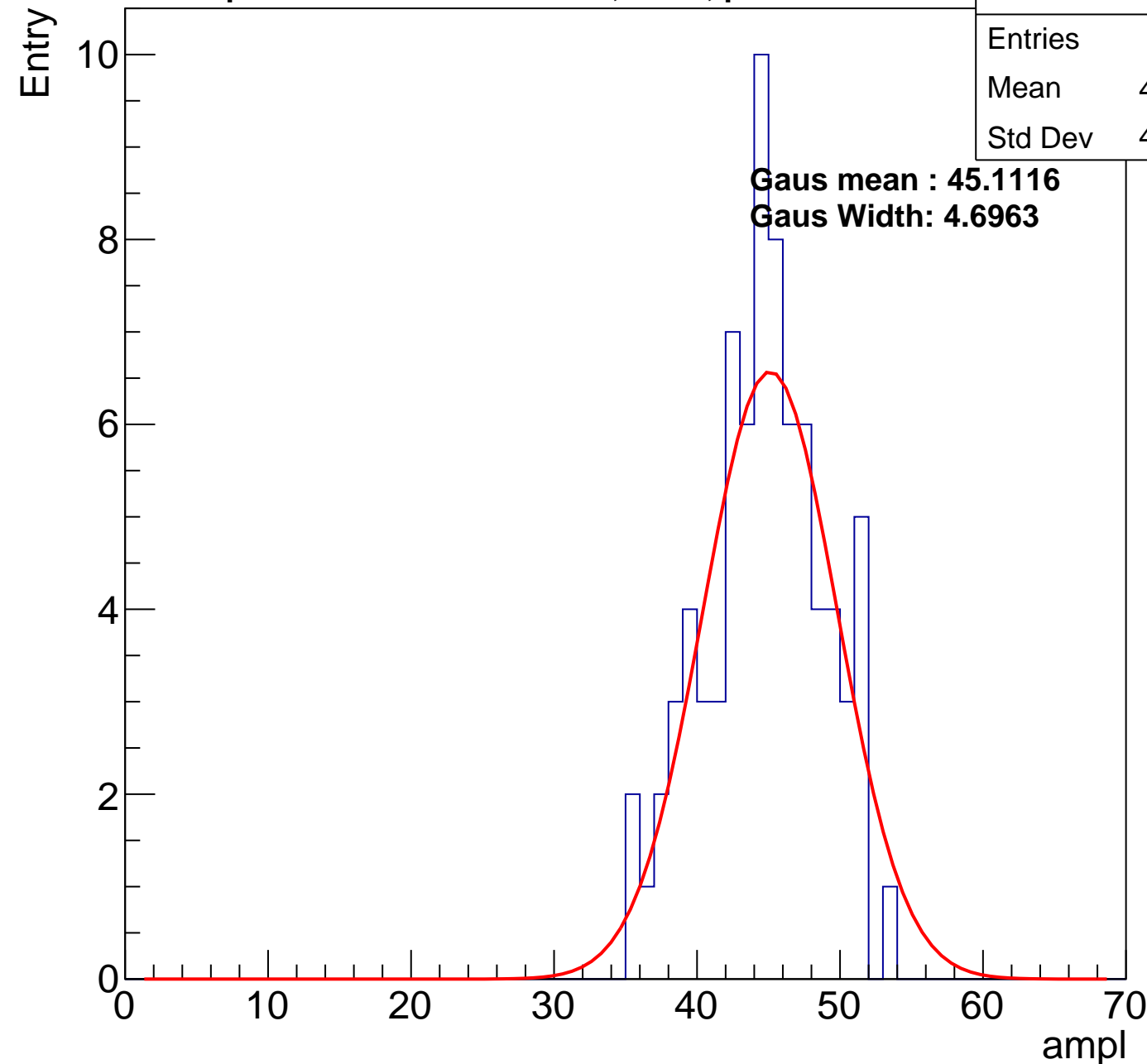
30

40

50

60

ampl

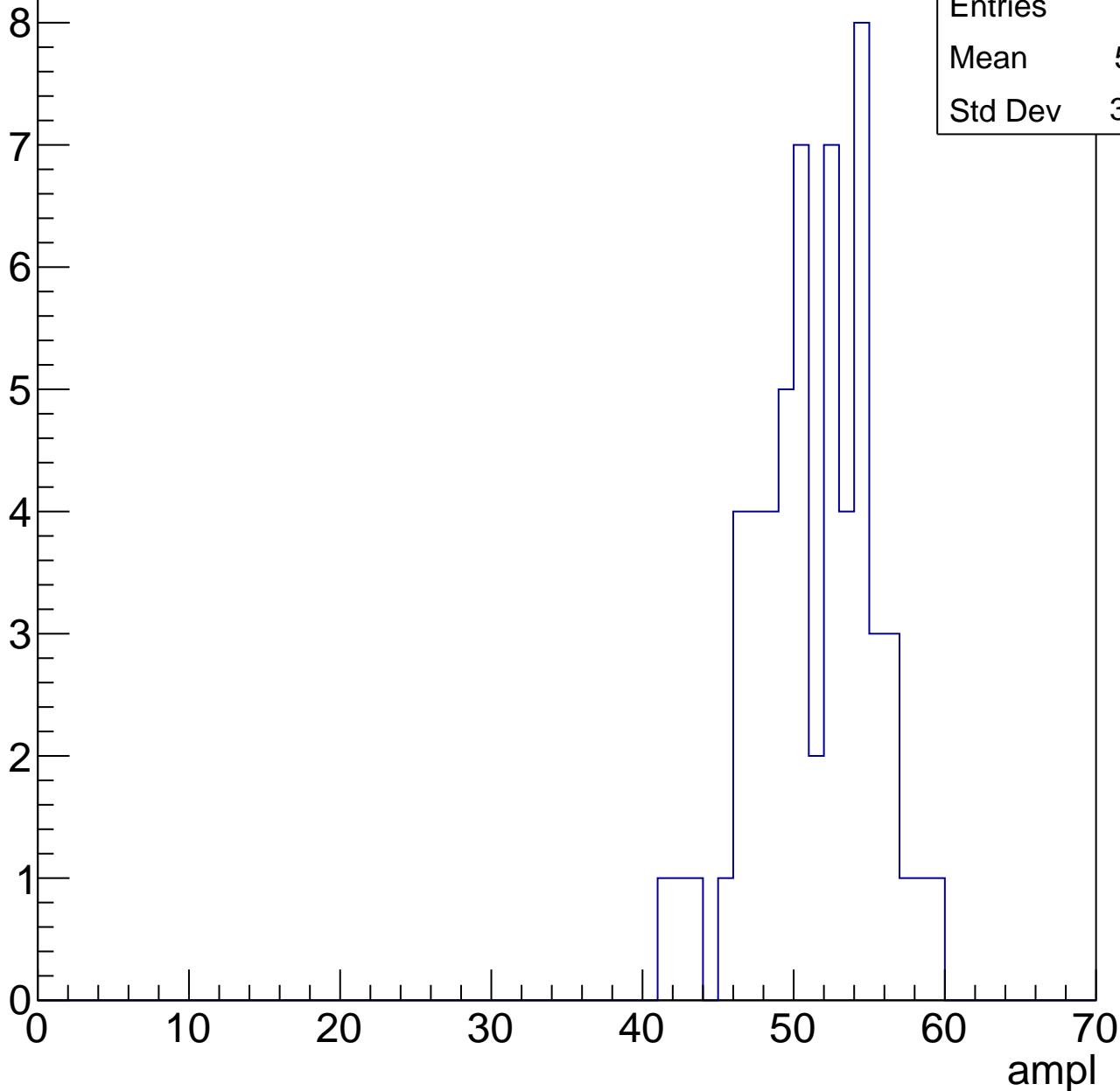


B1L101S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.81
Std Dev	3.879

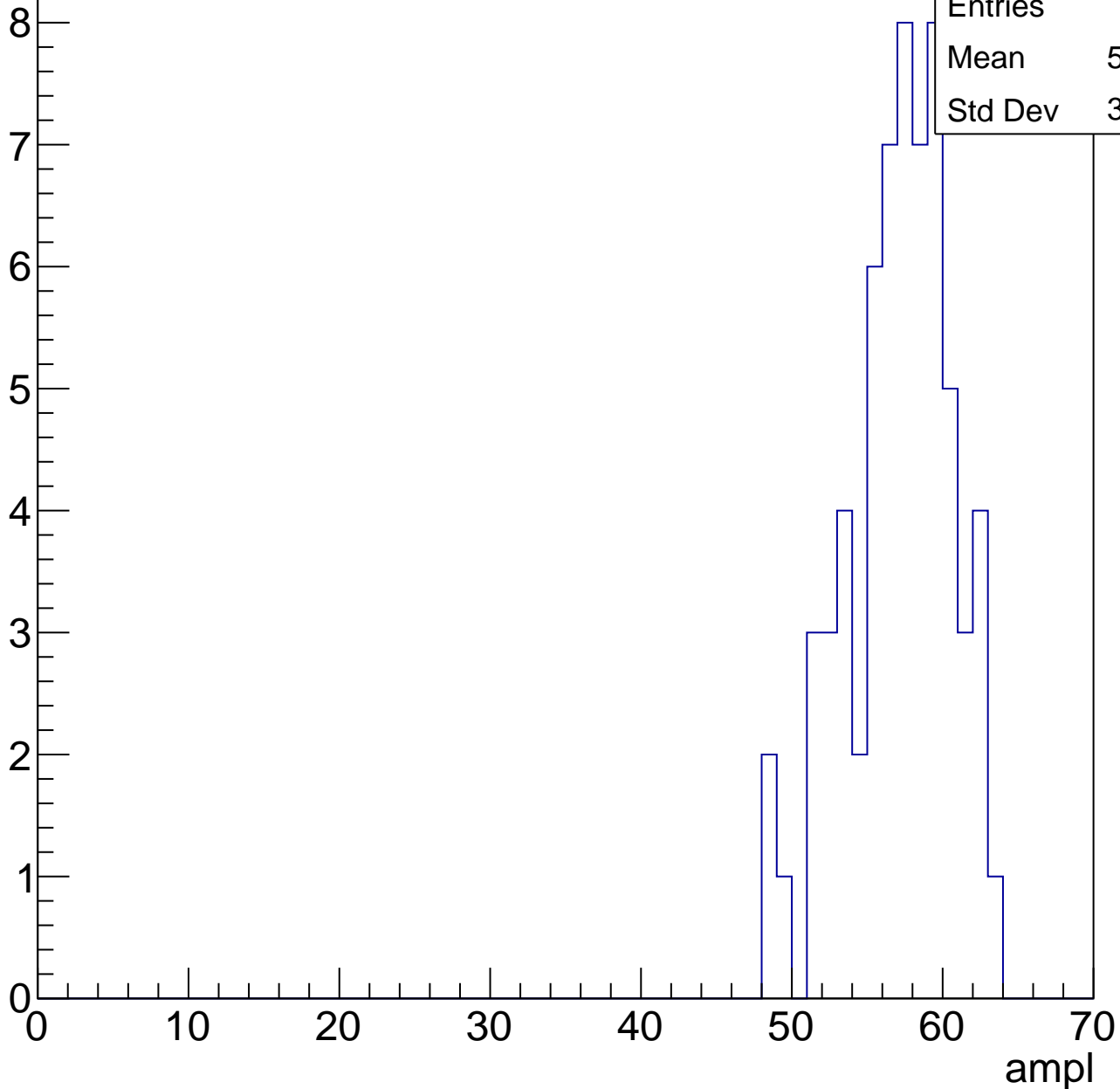


B1L101S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	56.62
Std Dev	3.493

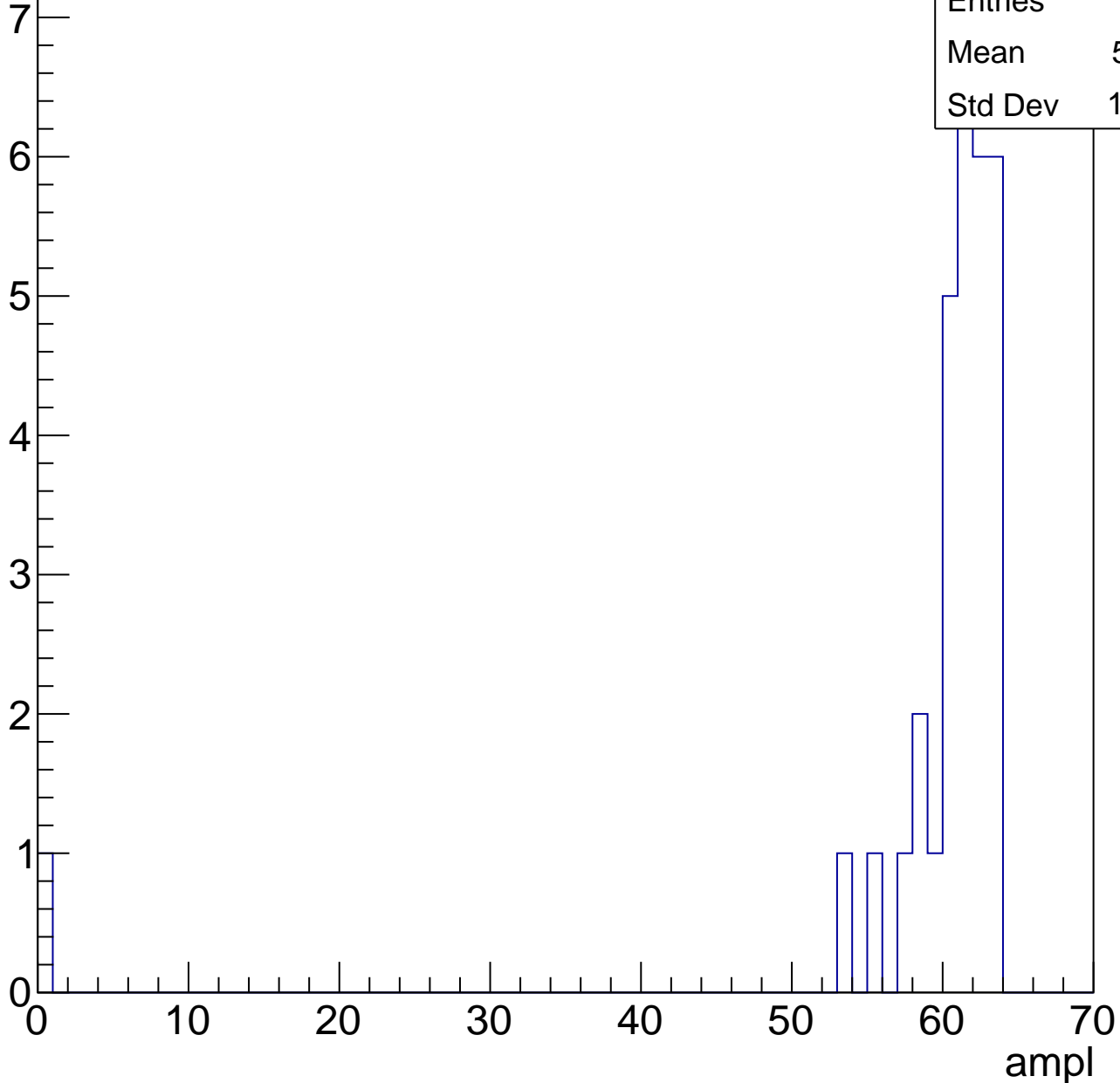


B1L101S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.61
Std Dev	10.95



B1L101S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U14-ch116, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	29.52
Std Dev	3.423

Gaus mean : 30.4003

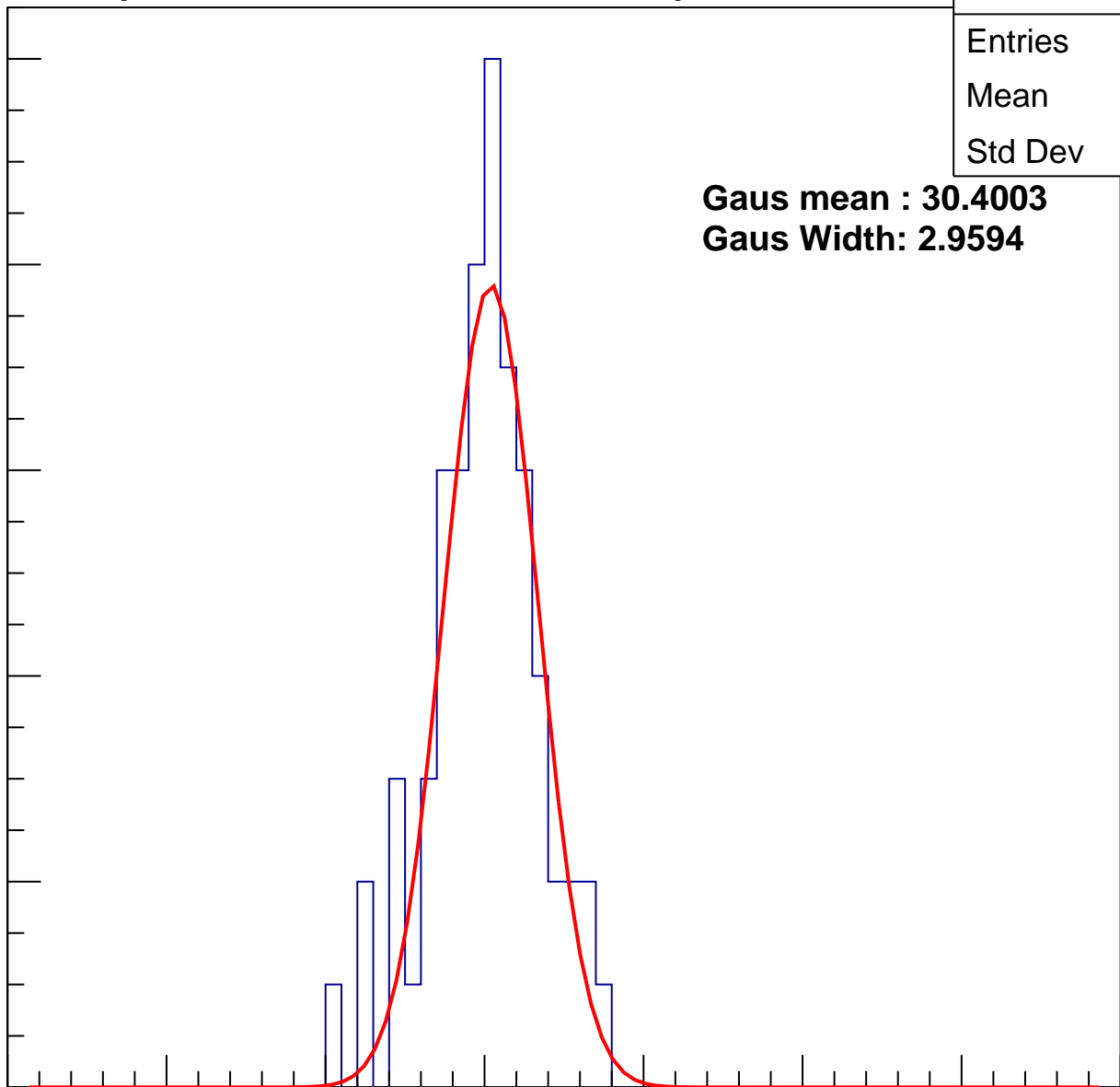
Gaus Width: 2.9594

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U14-ch116, adc1

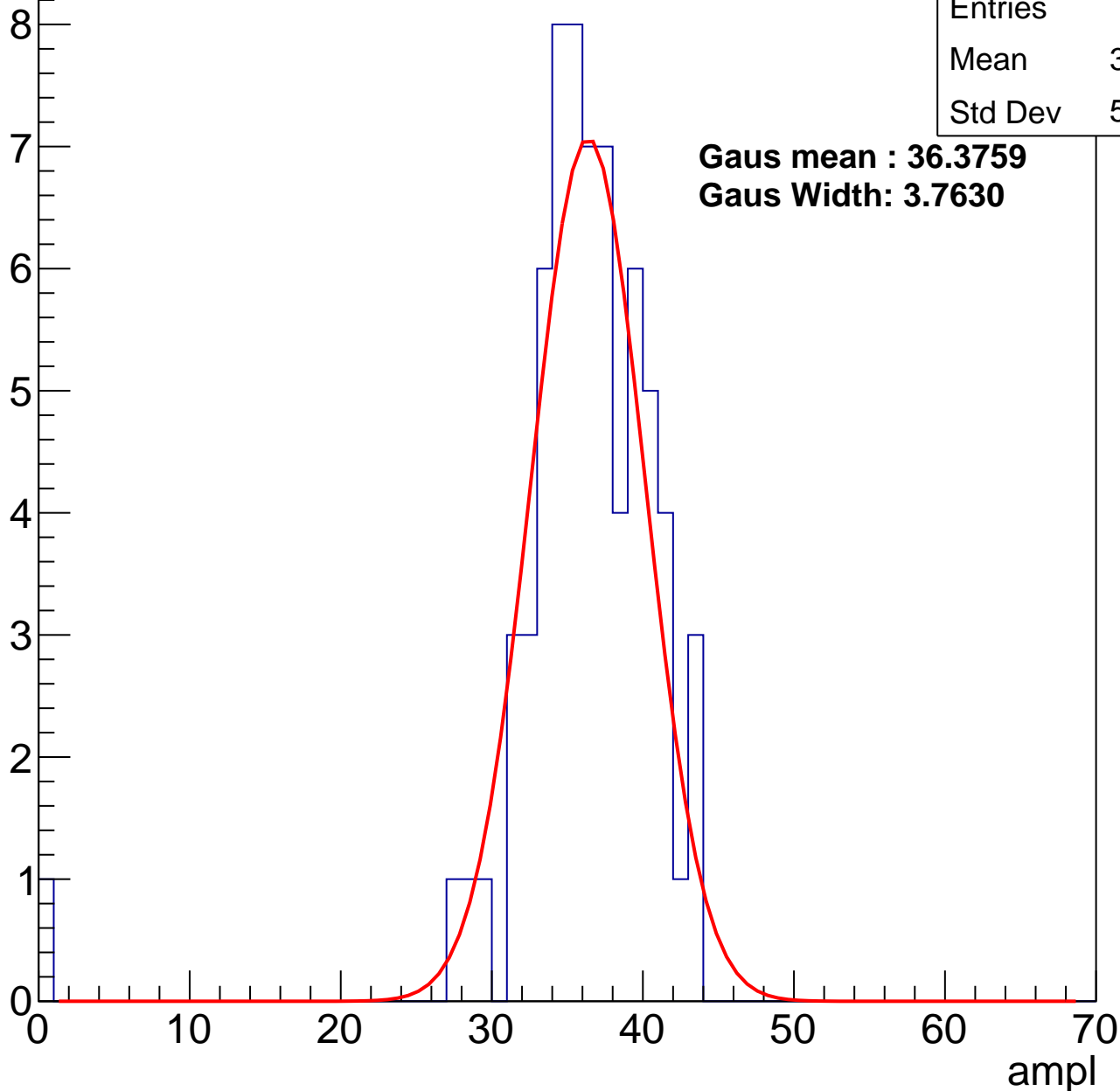
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.58
Std Dev	5.562

Gaus mean : 36.3759

Gaus Width: 3.7630



B1L101S, U14-ch116, adc2

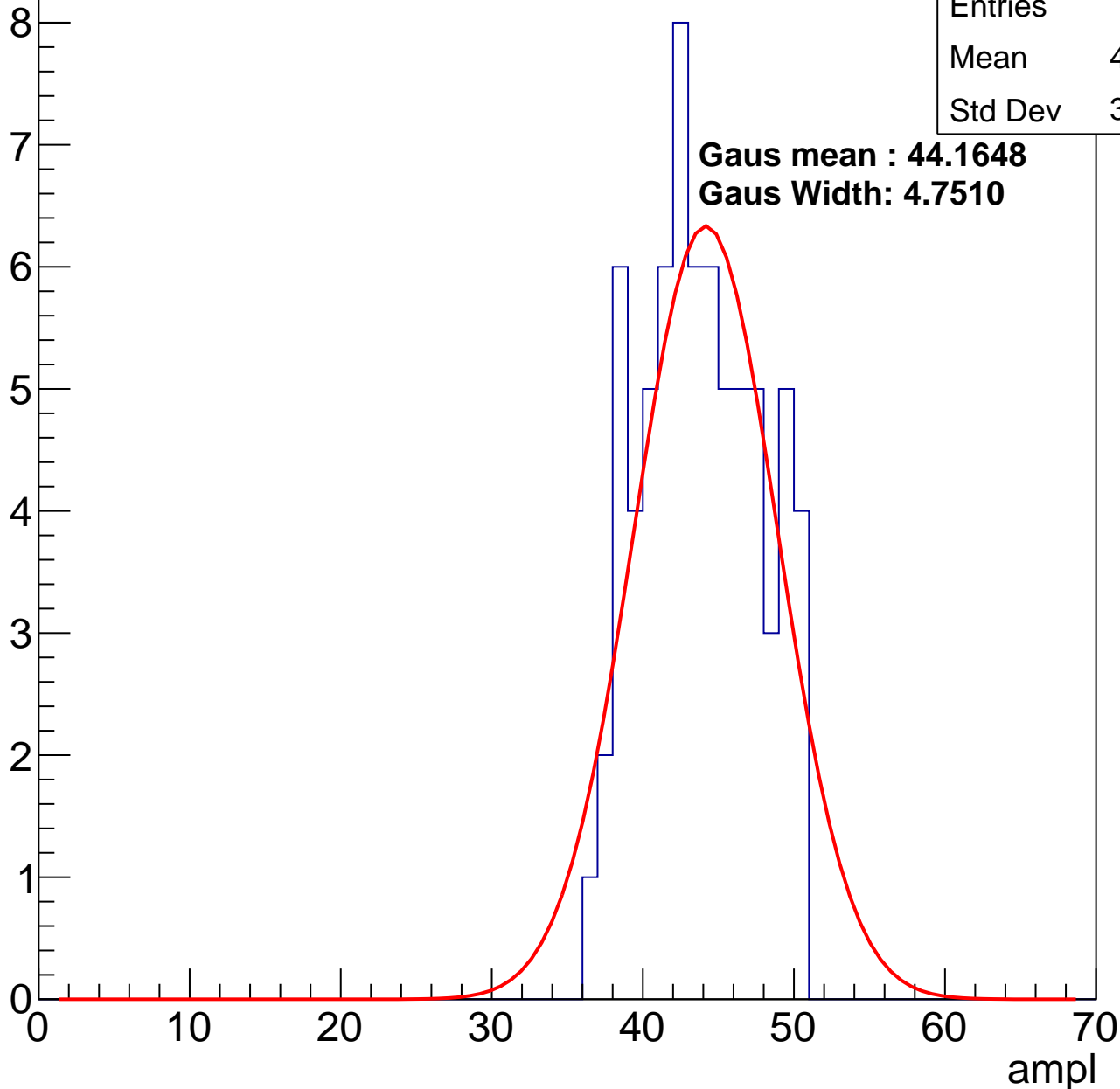
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.34
Std Dev	3.764

Gaus mean : 44.1648

Gaus Width: 4.7510

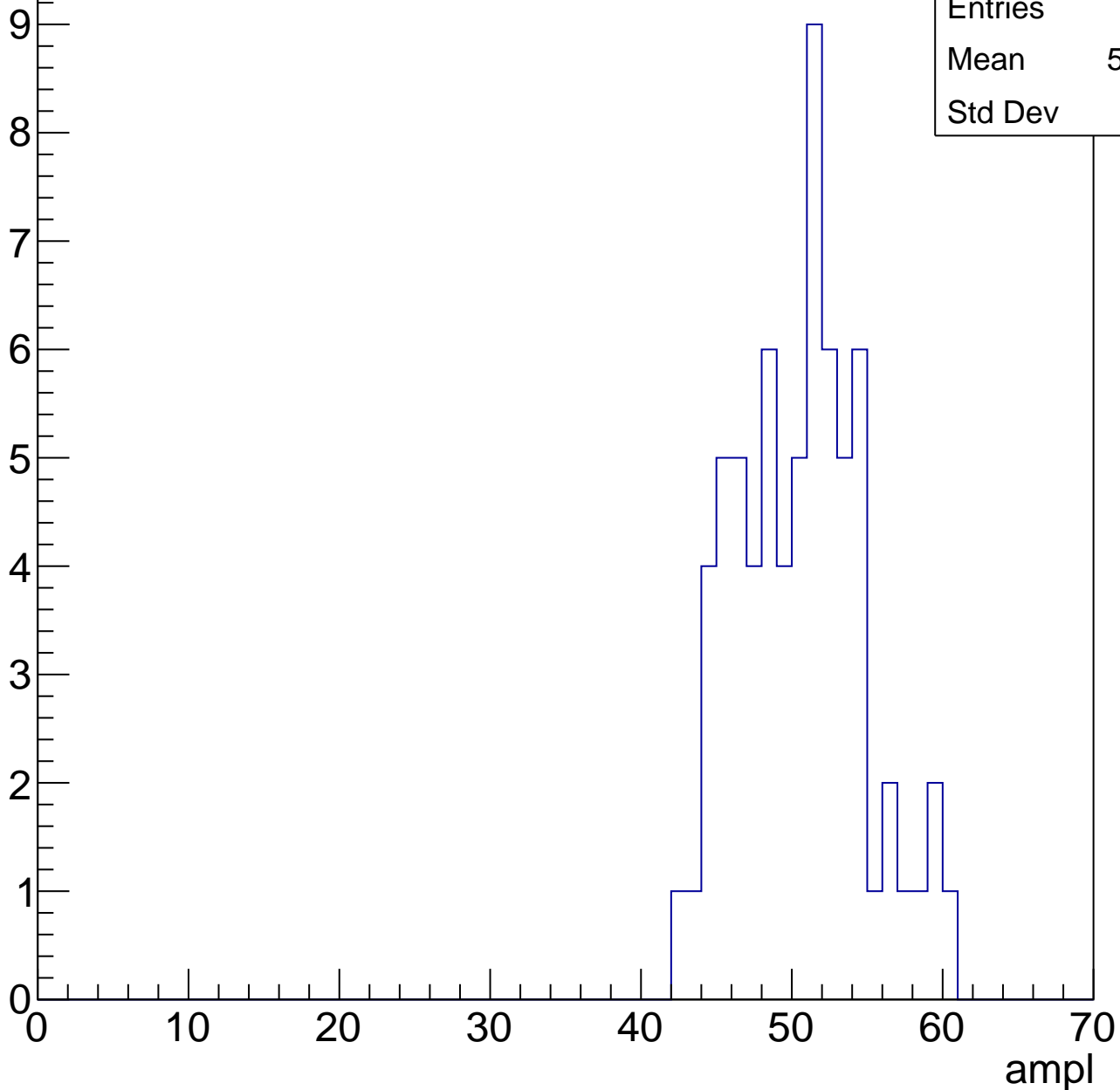


B1L101S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.12
Std Dev	4.12

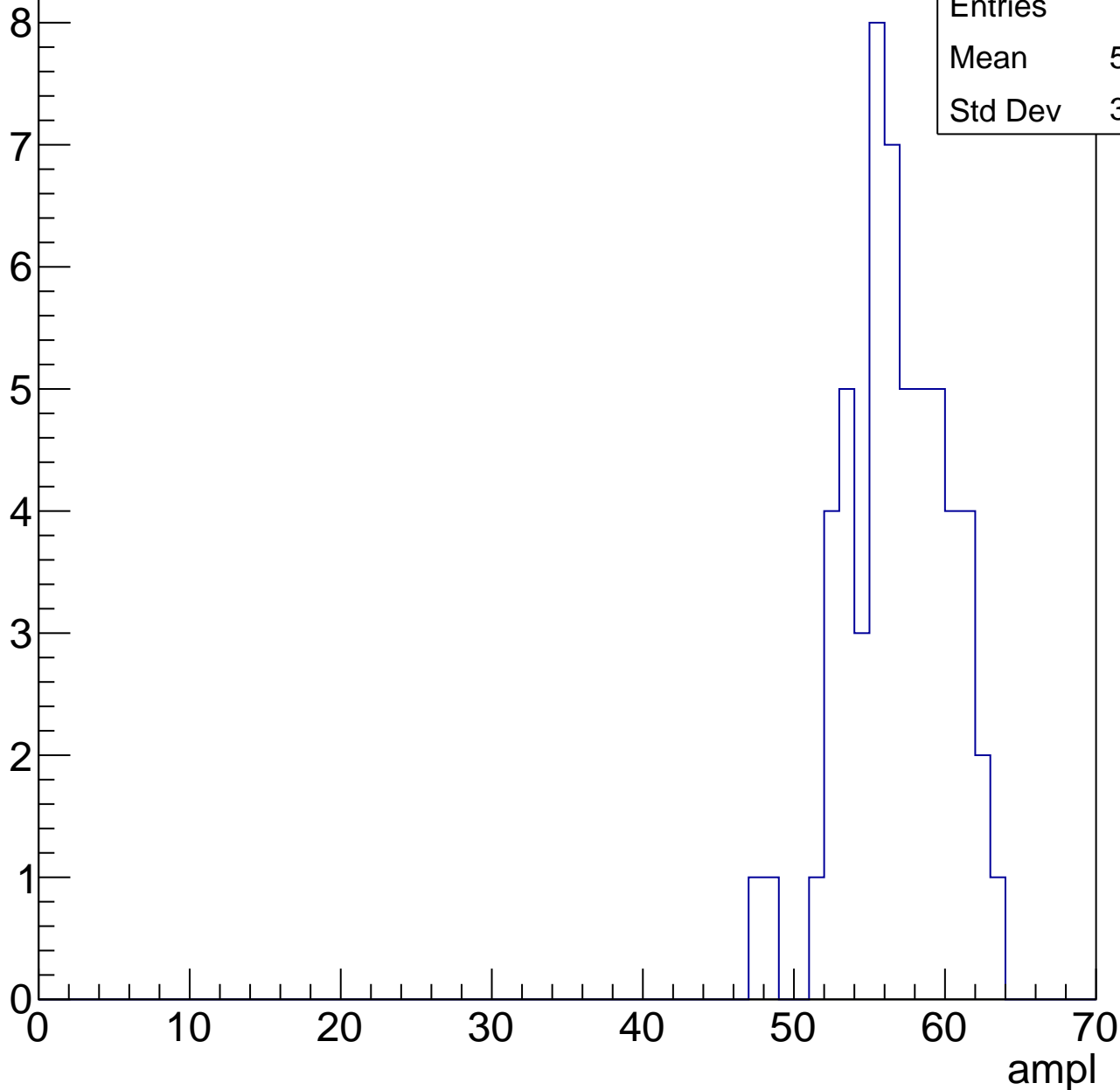


B1L101S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.32
Std Dev	3.402

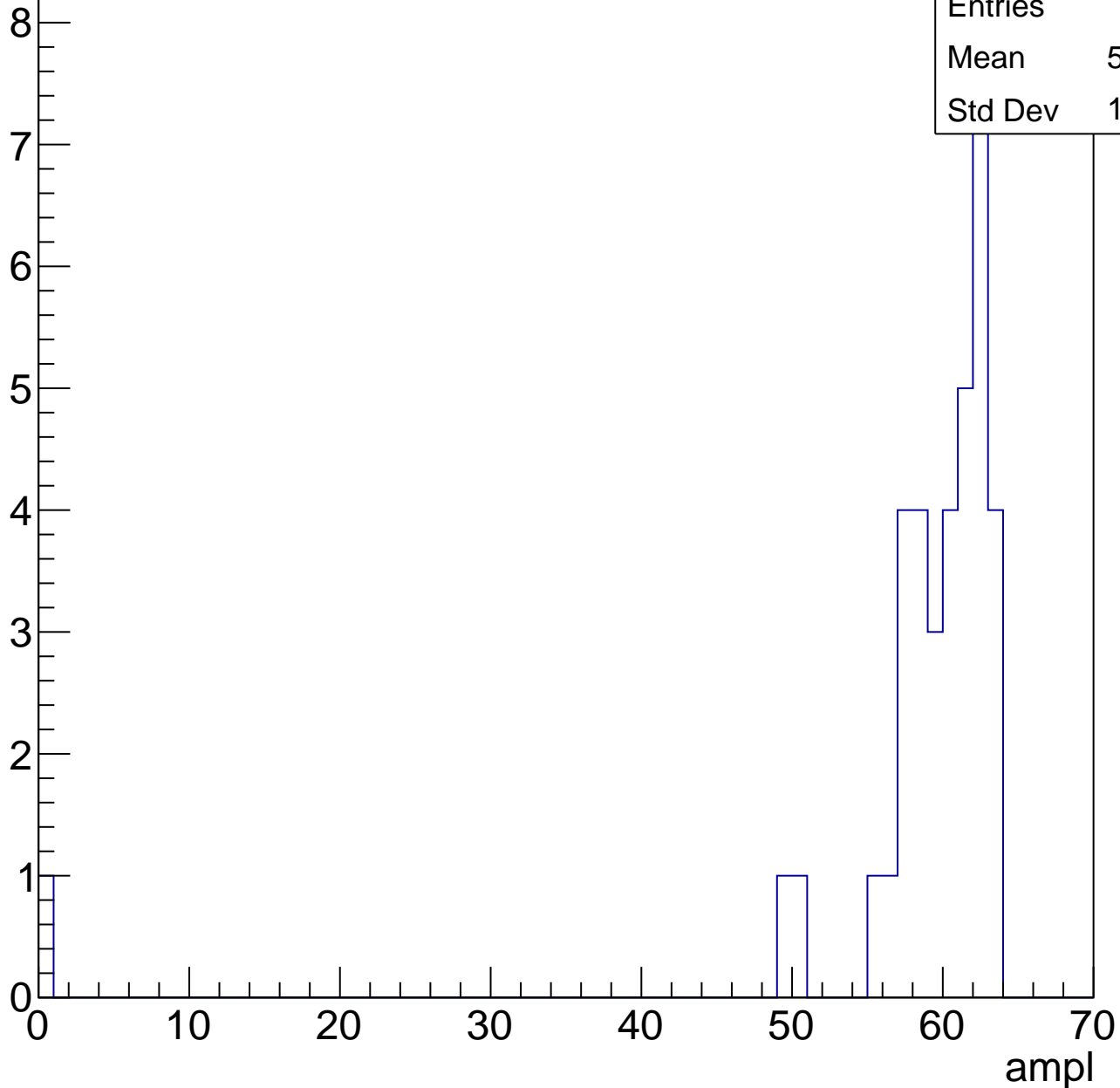


B1L101S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	57.84
Std Dev	10.16

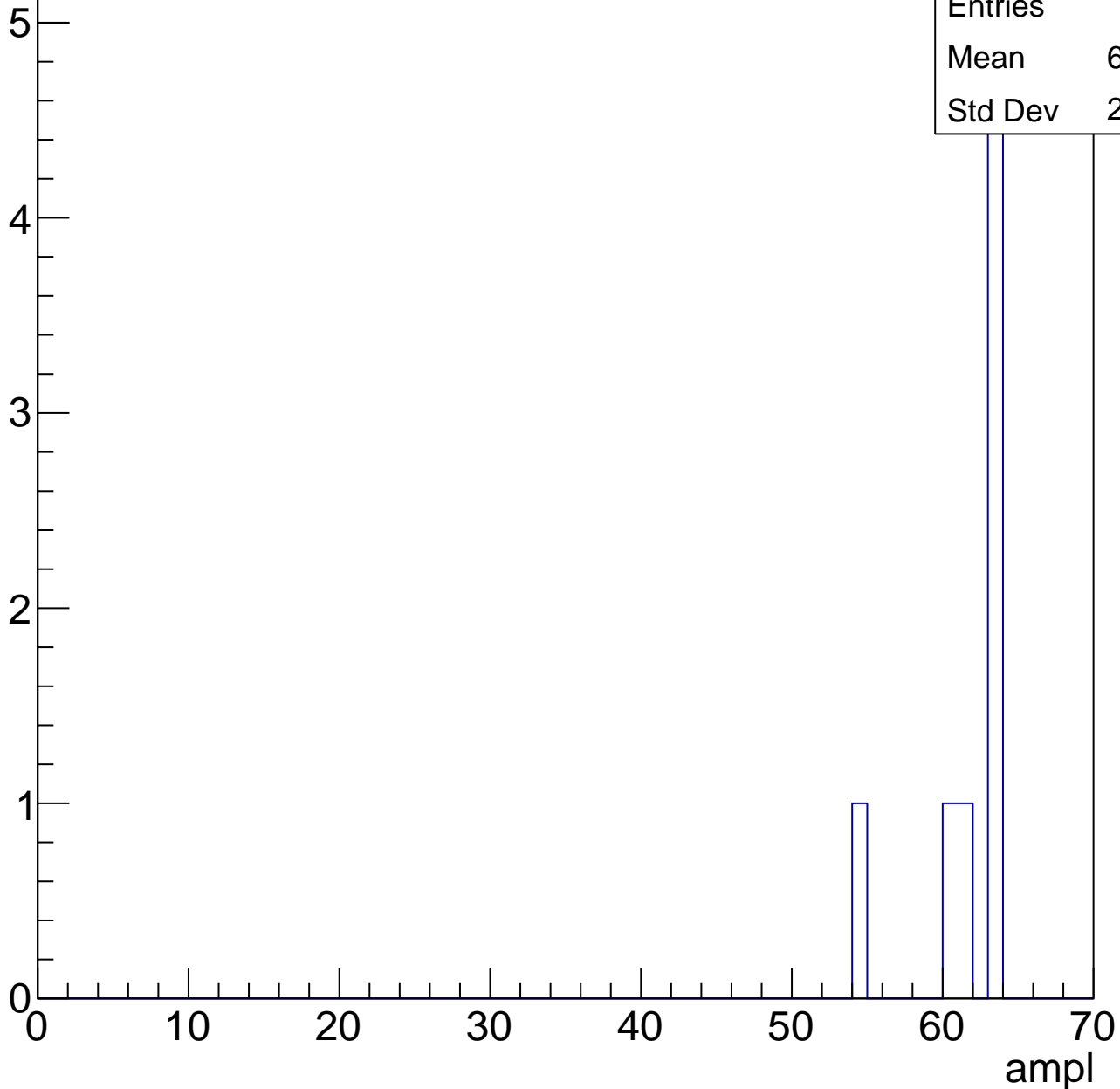


B1L101S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	61.25
Std Dev	2.947



B1L101S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U14-ch117, adc0

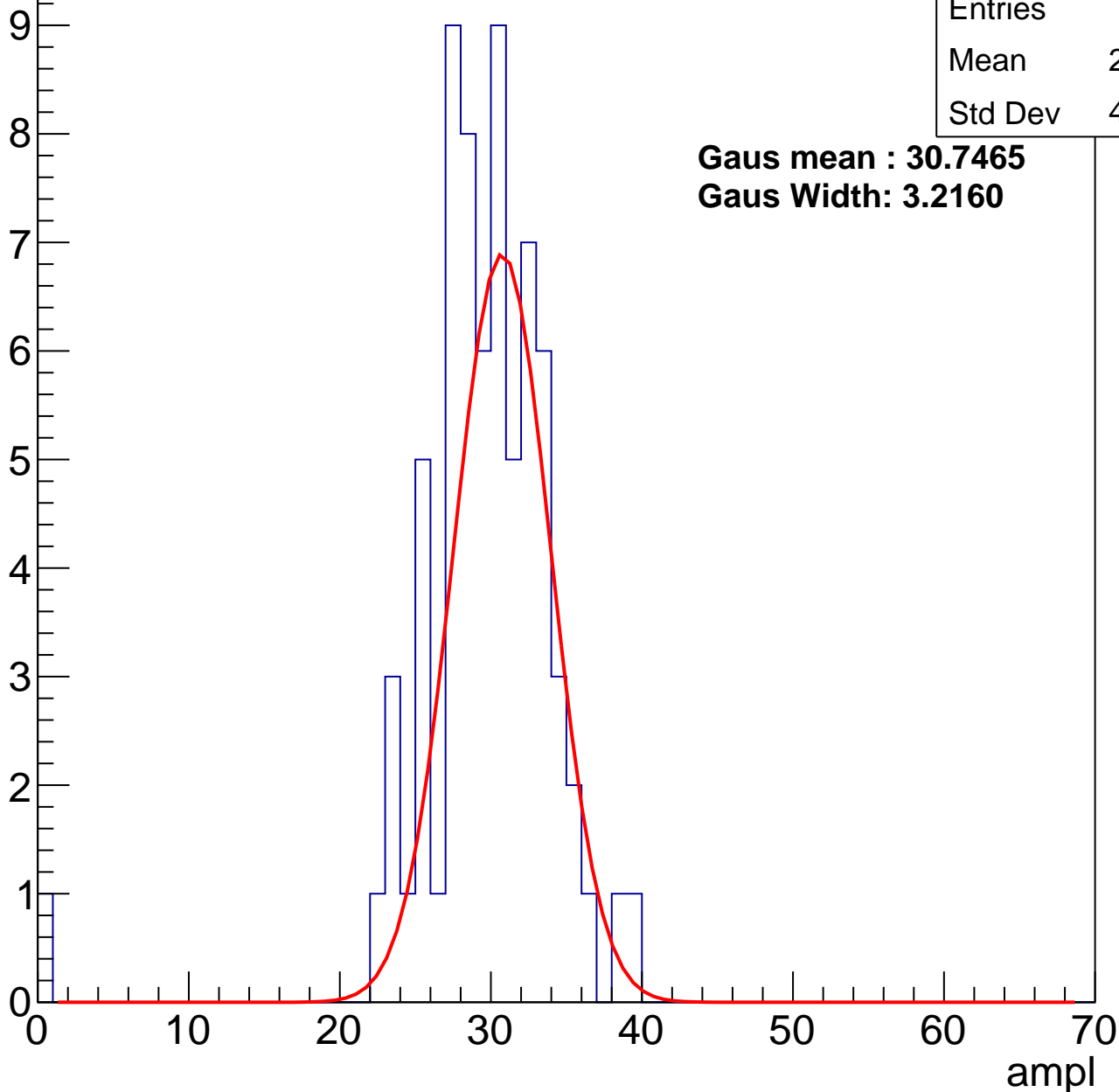
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.13
Std Dev	4.957

Gaus mean : 30.7465

Gaus Width: 3.2160



B1L101S, U14-ch117, adc1

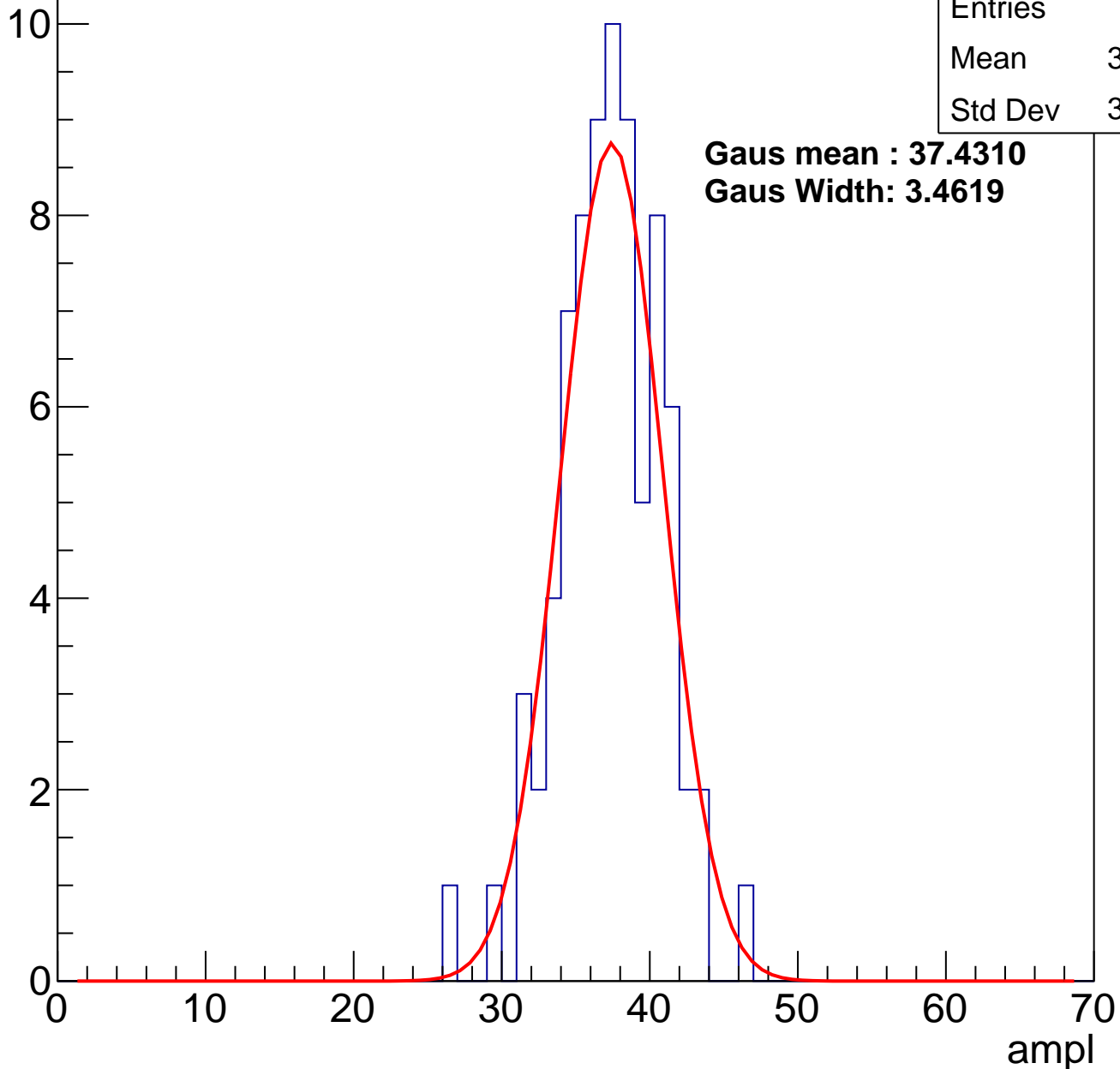
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	36.86
Std Dev	3.415

Gaus mean : 37.4310

Gaus Width: 3.4619

Entry



B1L101S, U14-ch117, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	45.39
Std Dev	3.389

Gaus mean : 45.6892

Gaus Width: 3.5138

Entry

10

8

6

4

2

0

0

10

20

30

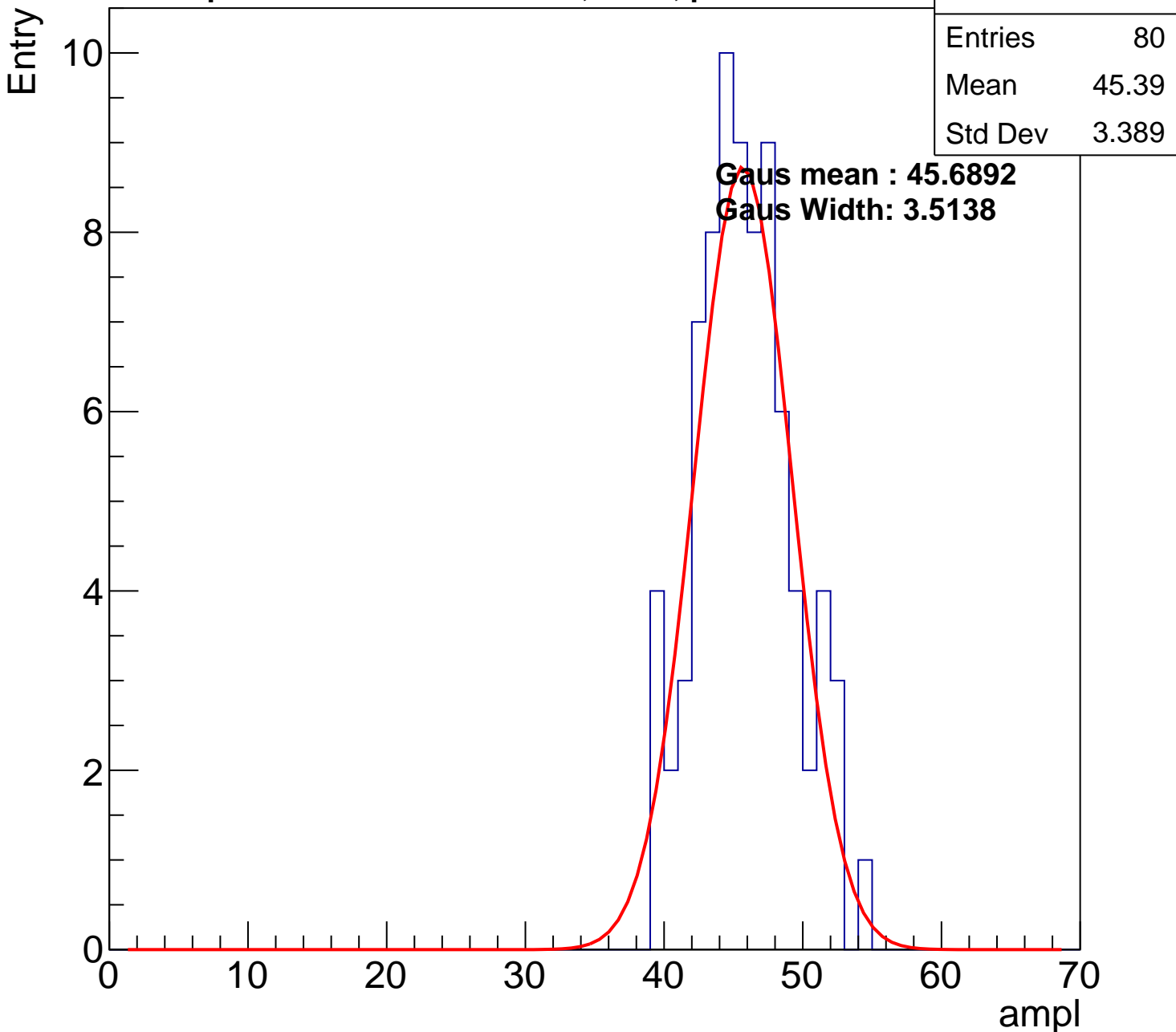
40

50

60

70

ampl

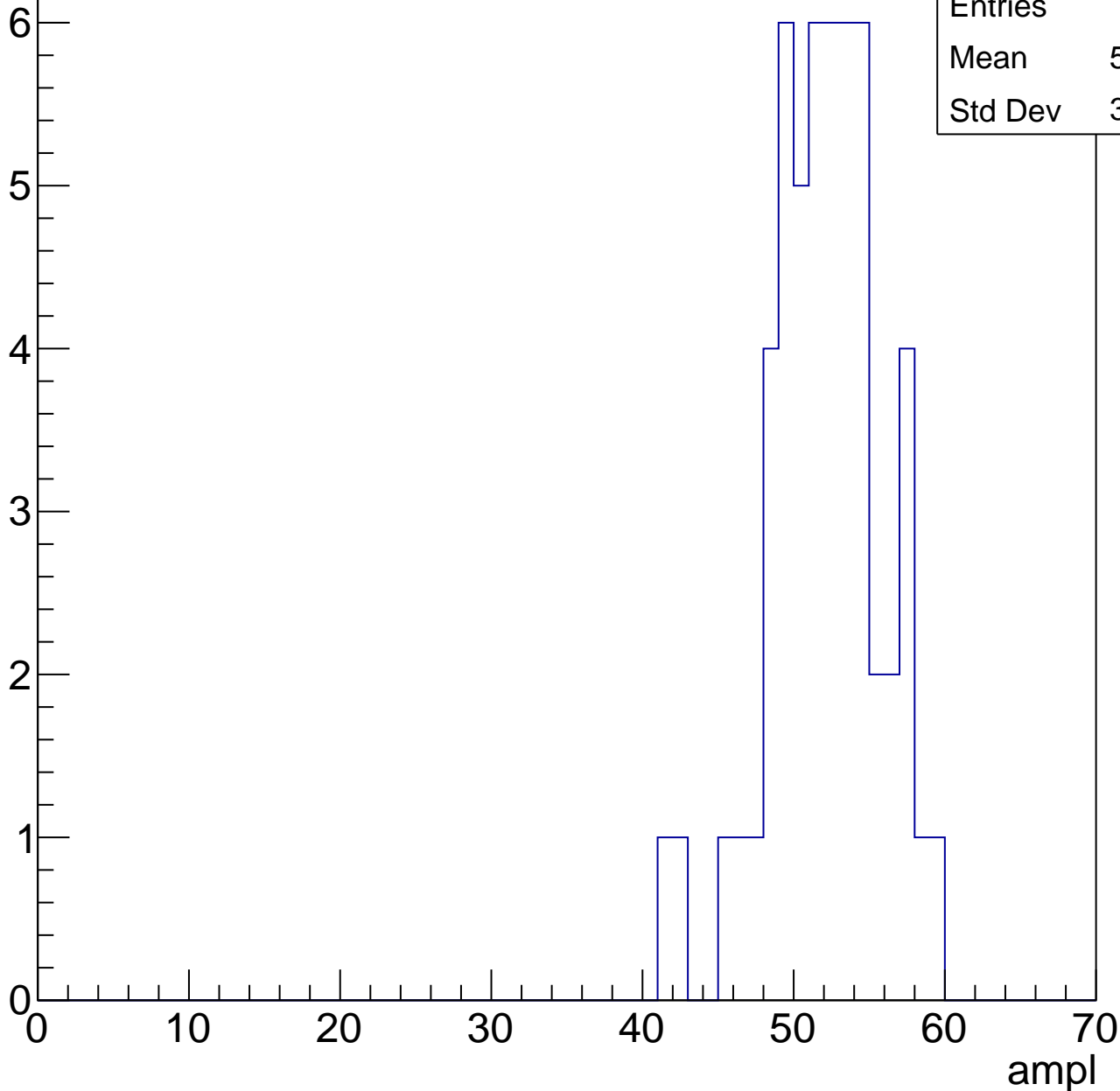


B1L101S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	51.56
Std Dev	3.675

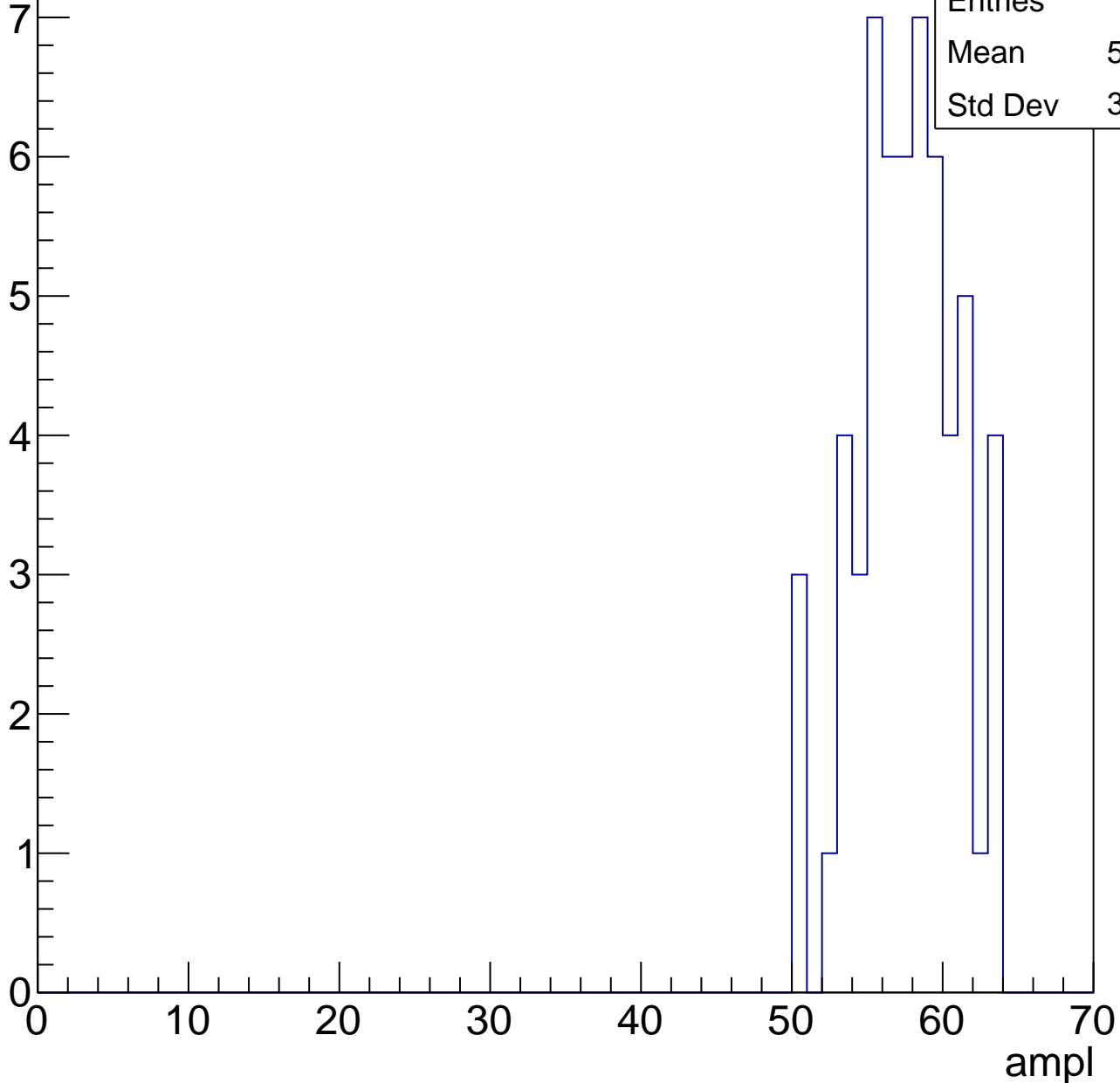


B1L101S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	57.16
Std Dev	3.276

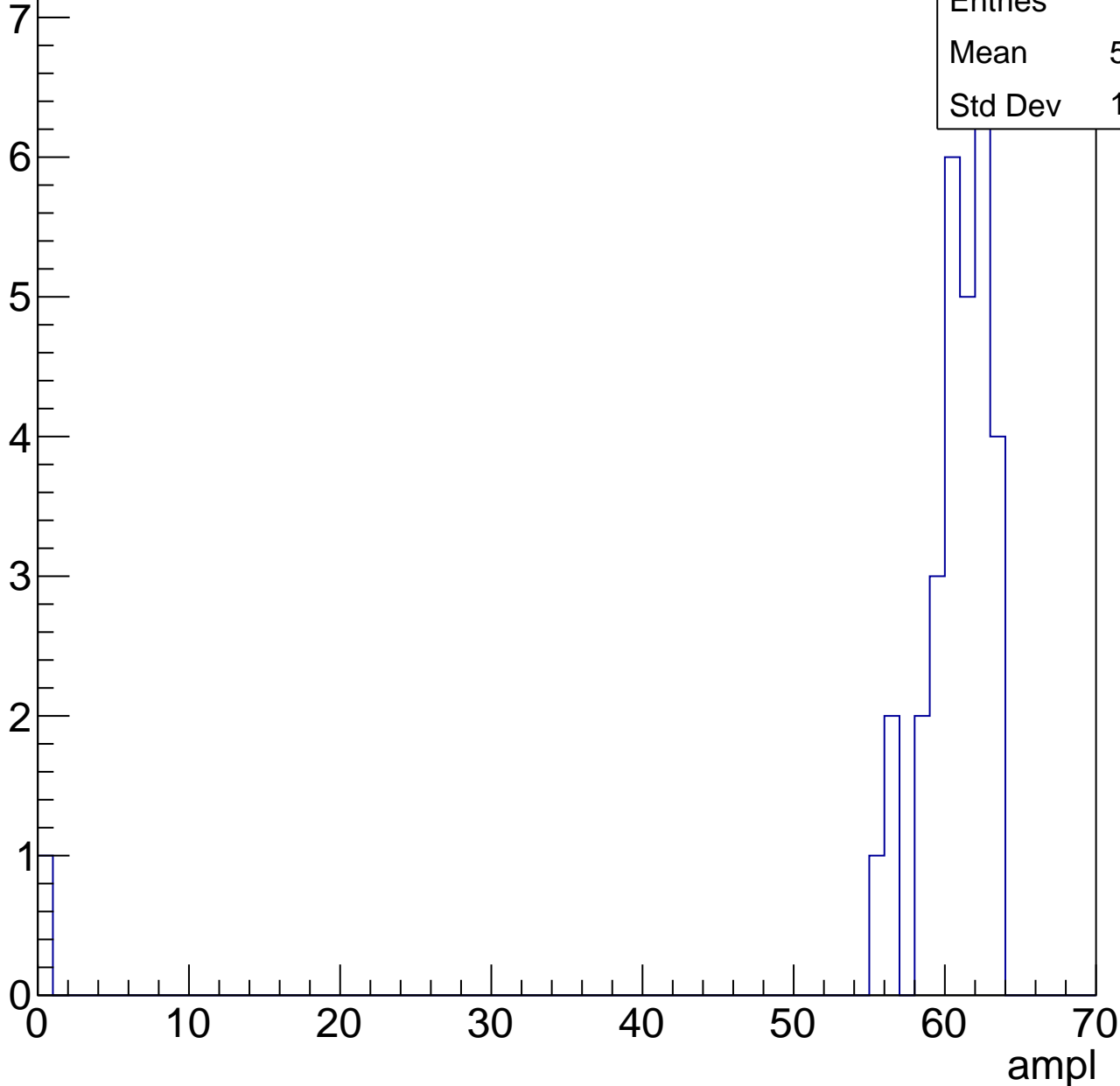


B1L101S, U14-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.42
Std Dev	10.87



B1L101S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L101S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	15
Std Dev	0

ampl

B1L101S, U14-ch118, adc0

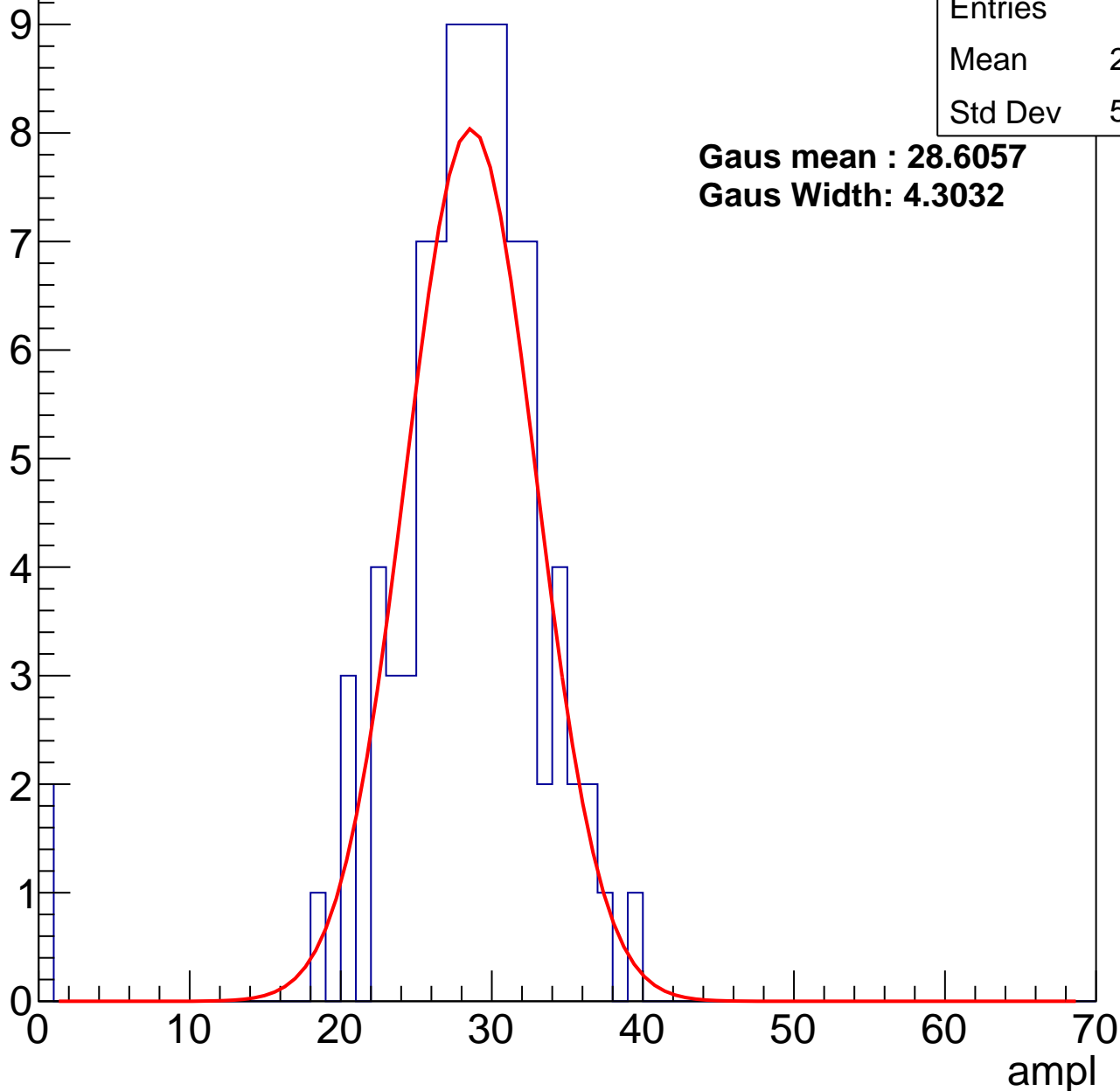
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	92
Mean	27.73
Std Dev	5.769

Gaus mean : 28.6057

Gaus Width: 4.3032



B1L101S, U14-ch118, adc1

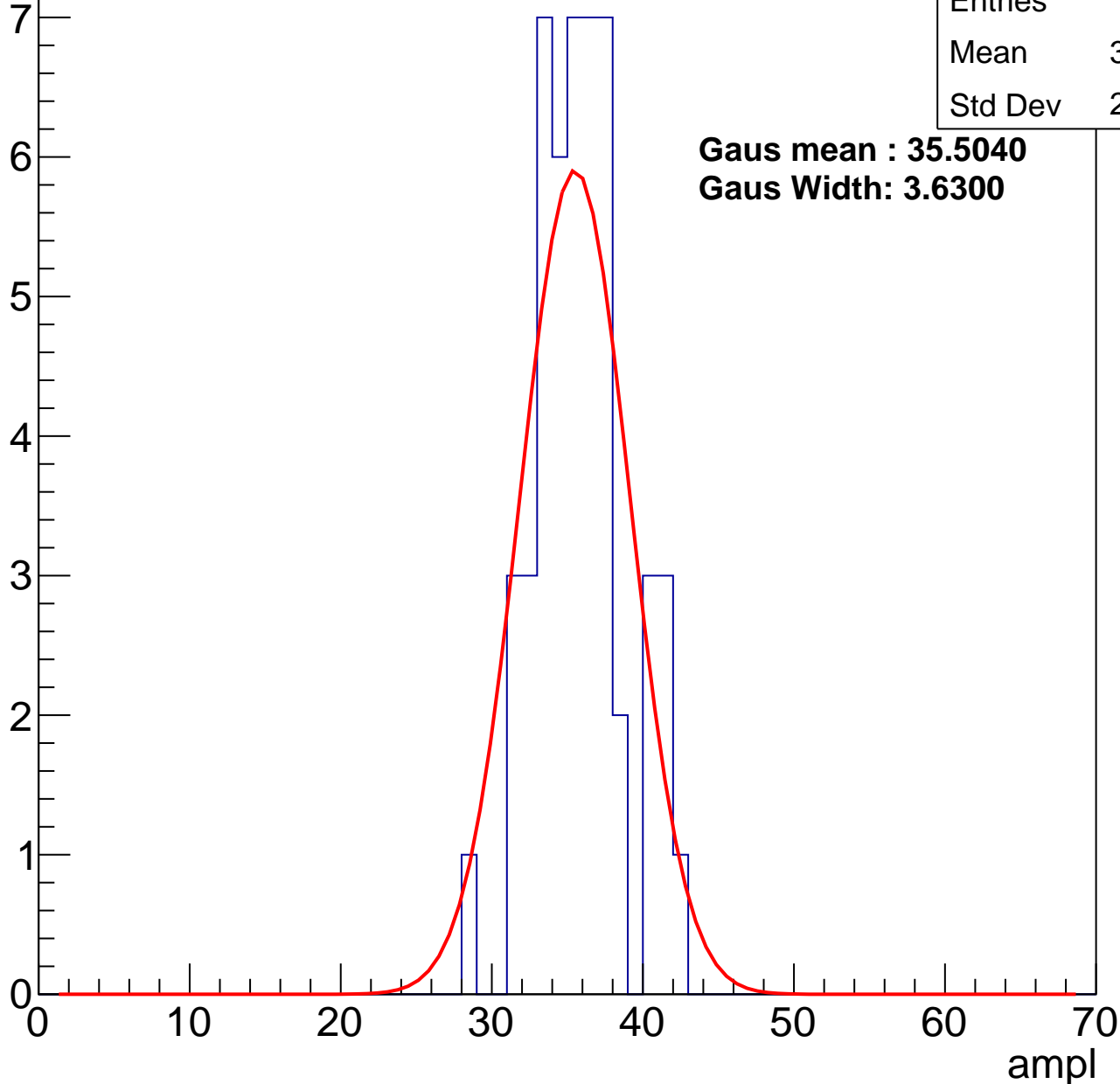
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	35.38
Std Dev	2.952

Gaus mean : 35.5040

Gaus Width: 3.6300



B1L101S, U14-ch118, adc2

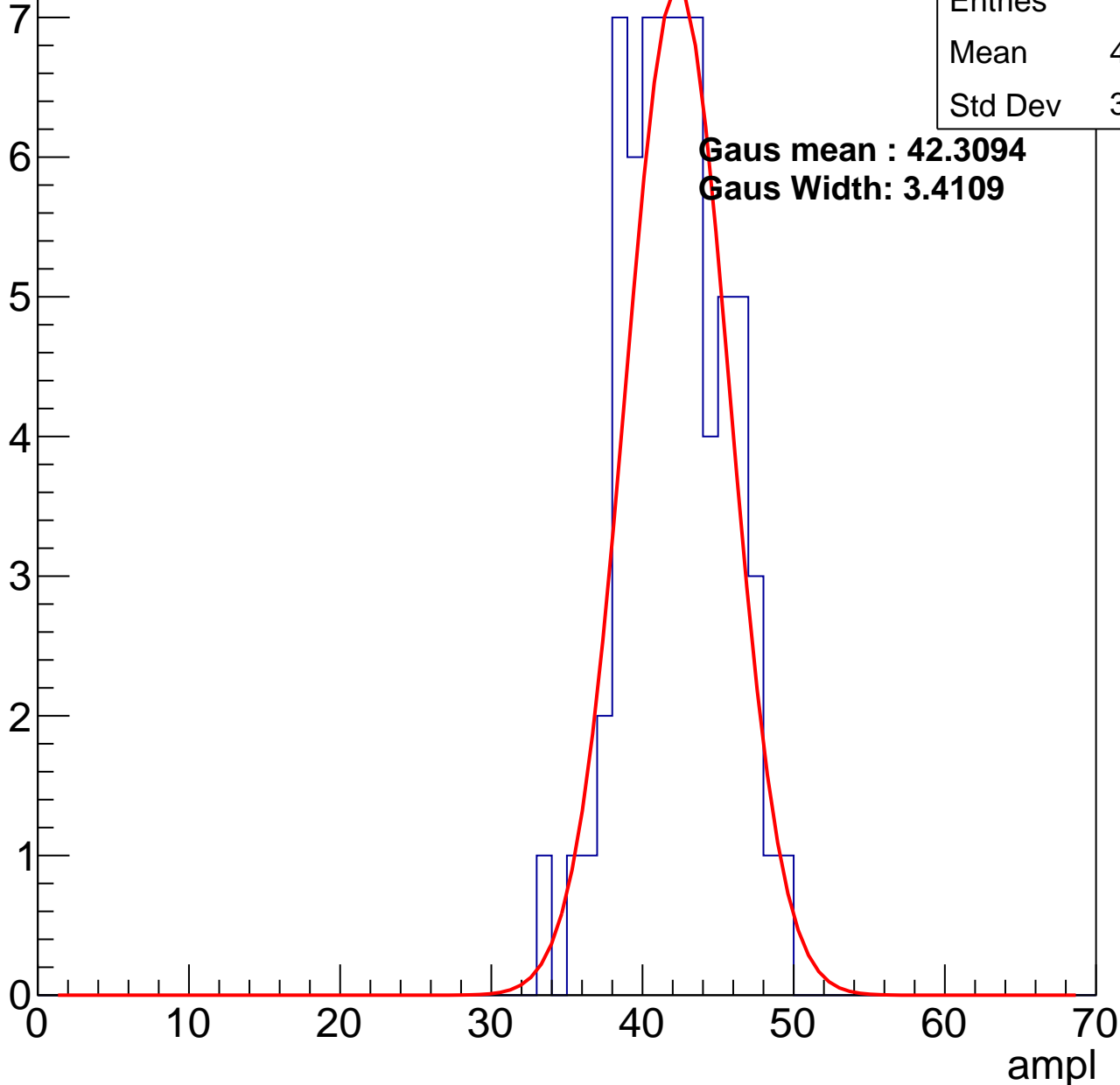
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.68
Std Dev	3.324

Gaus mean : 42.3094

Gaus Width: 3.4109

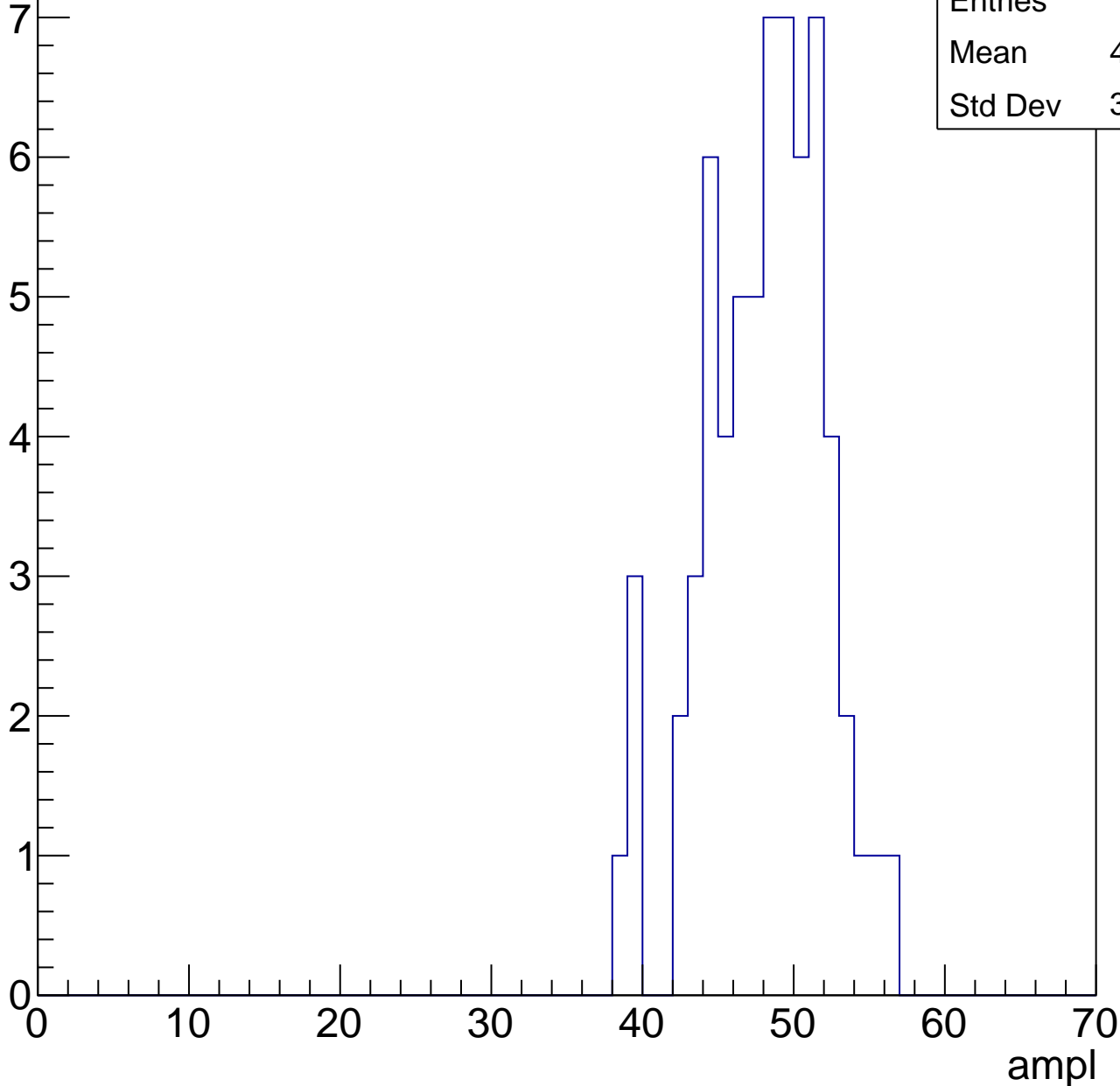


B1L101S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	47.57
Std Dev	3.906

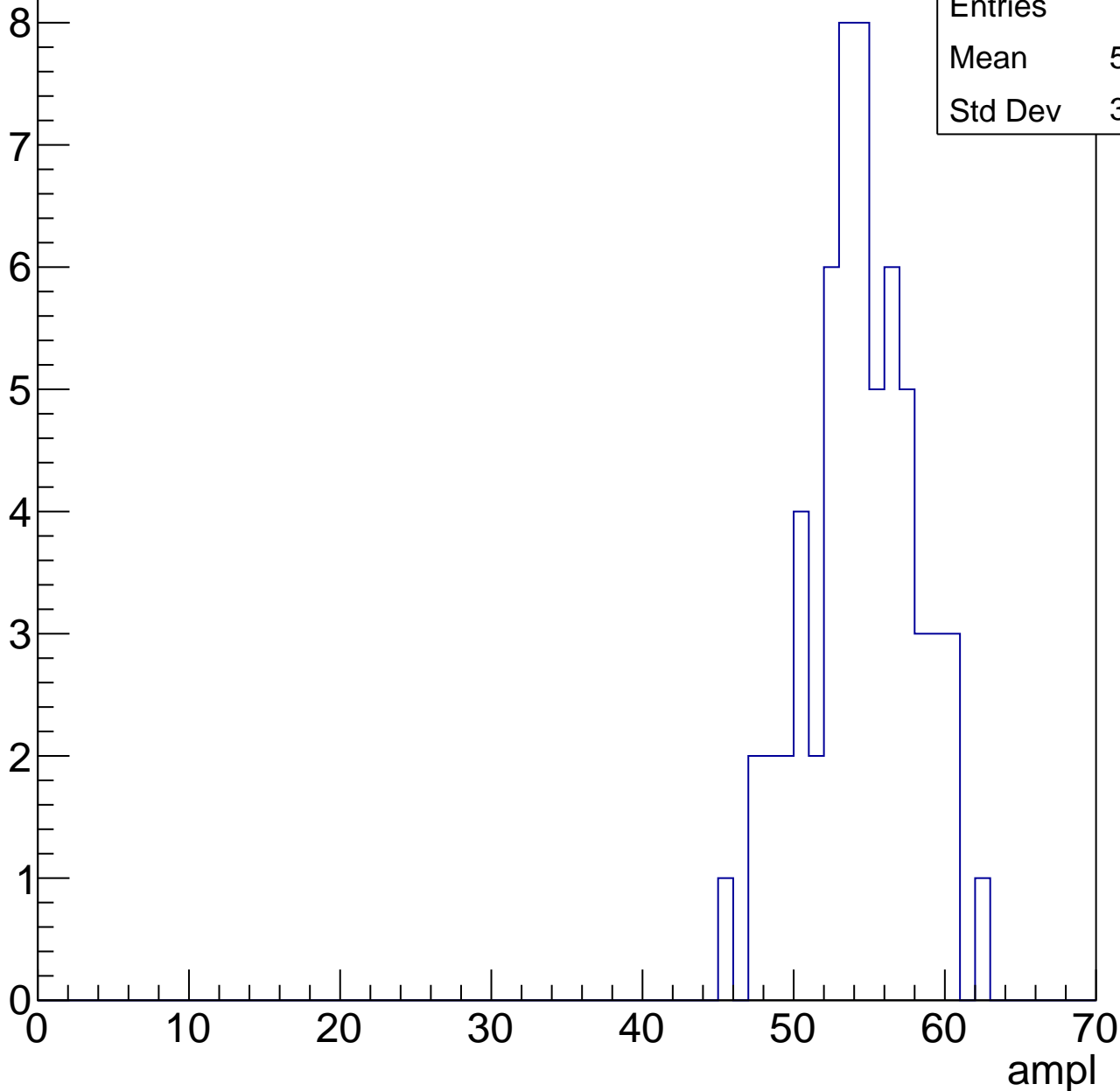


B1L101S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	53.97
Std Dev	3.585

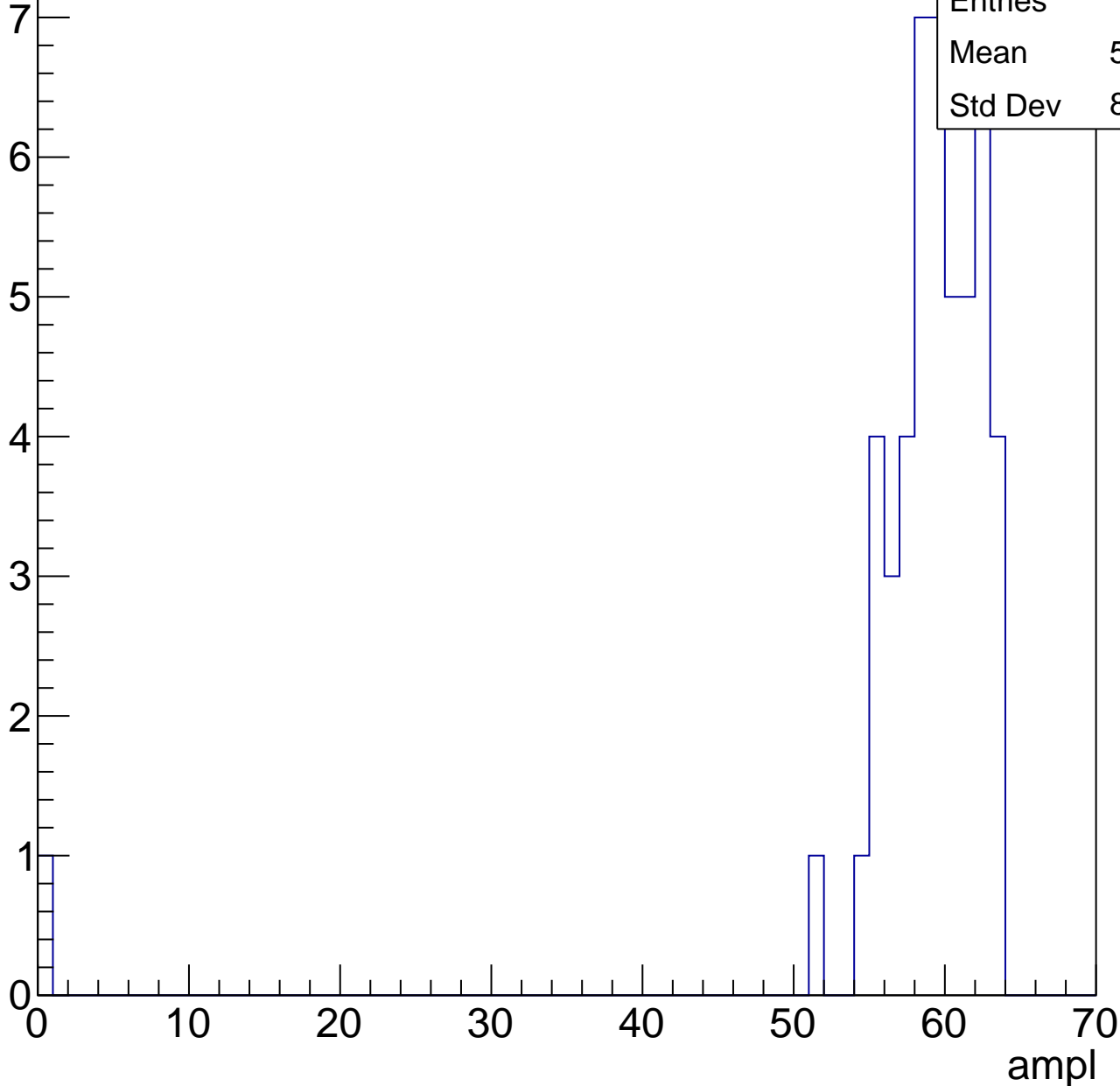


B1L101S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.78
Std Dev	8.763

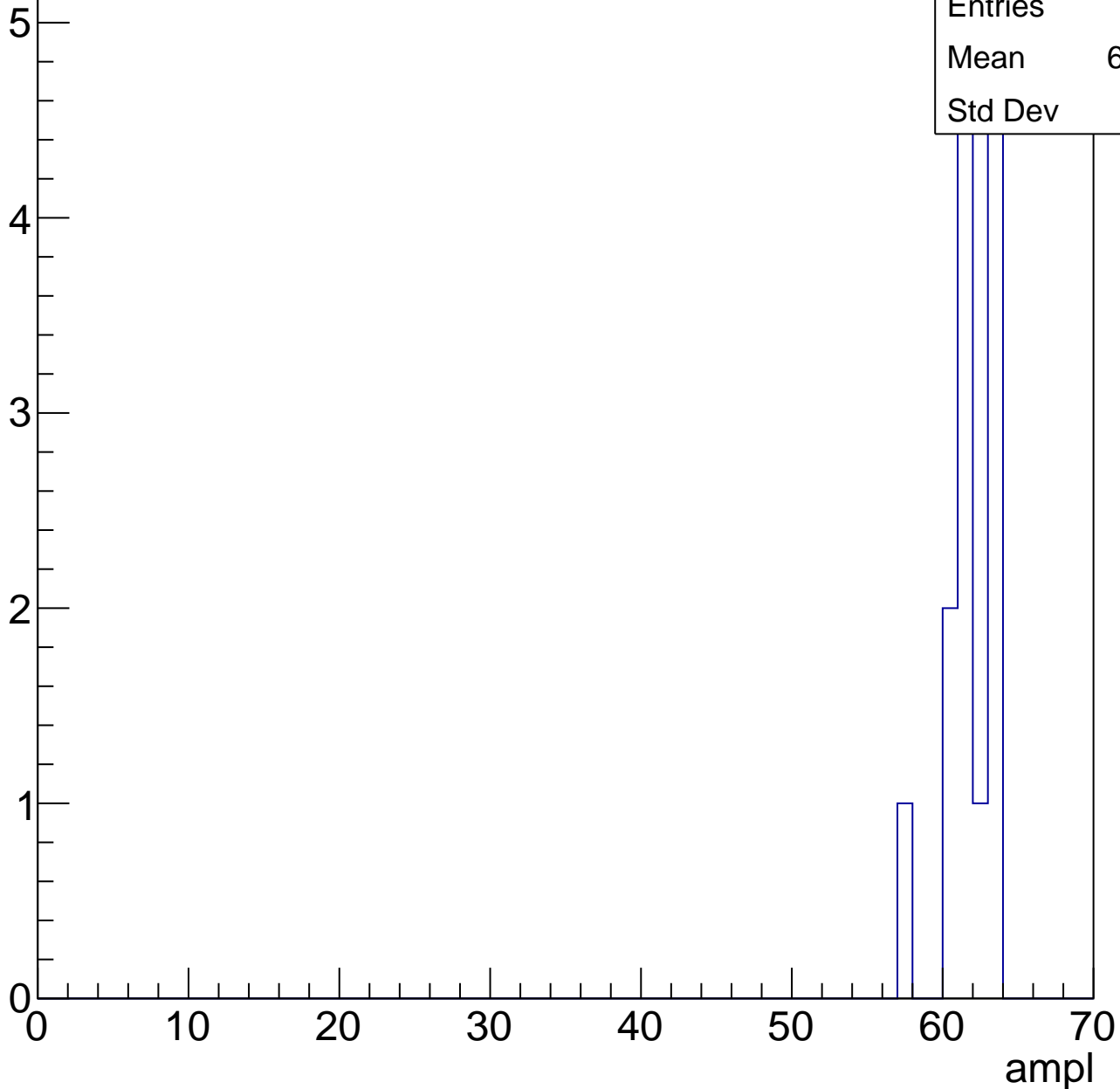


B1L101S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.36
Std Dev	1.63



B1L101S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch119, adc0

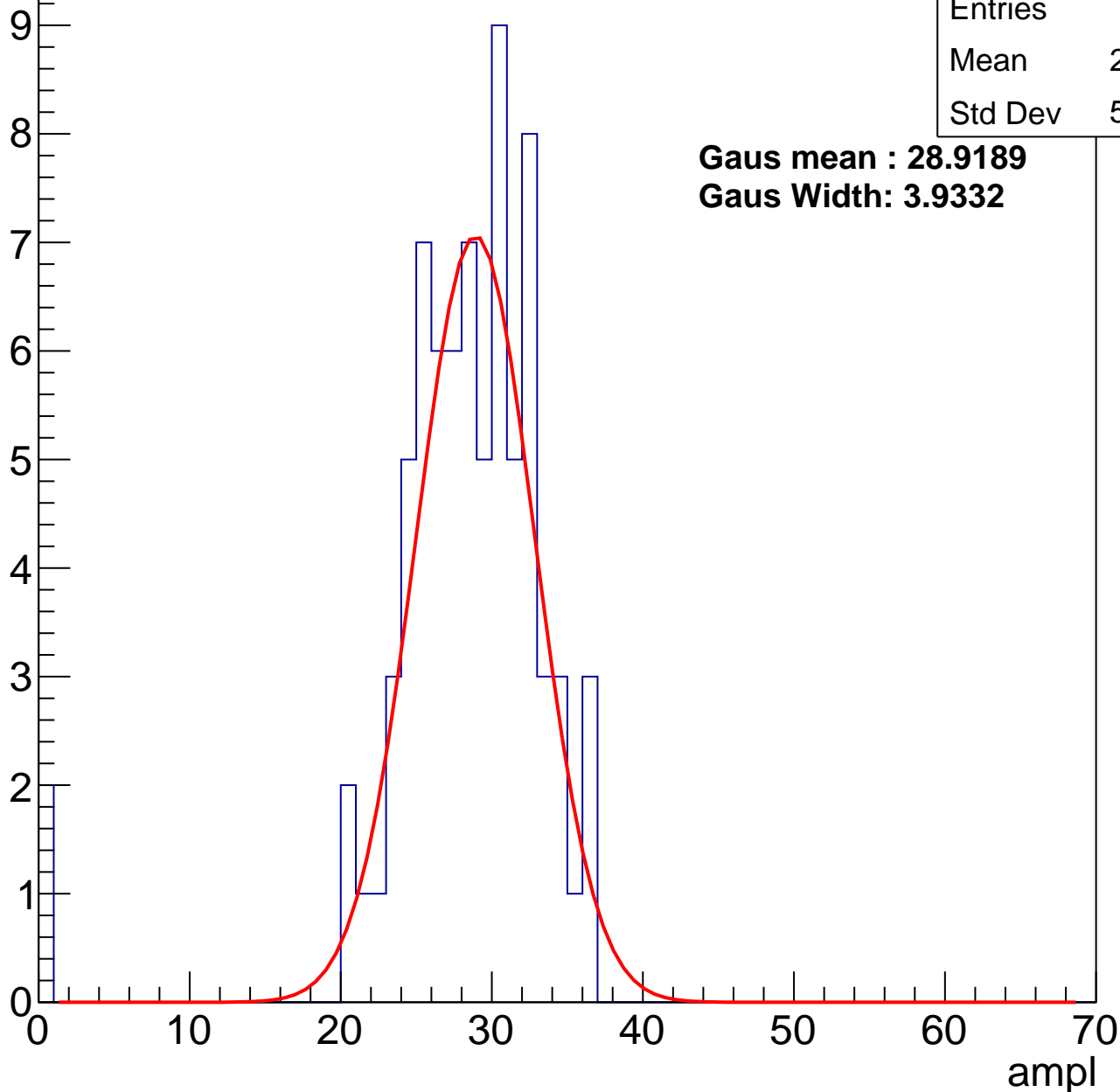
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	27.68
Std Dev	5.876

Gaus mean : 28.9189

Gaus Width: 3.9332



B1L101S, U14-ch119, adc1

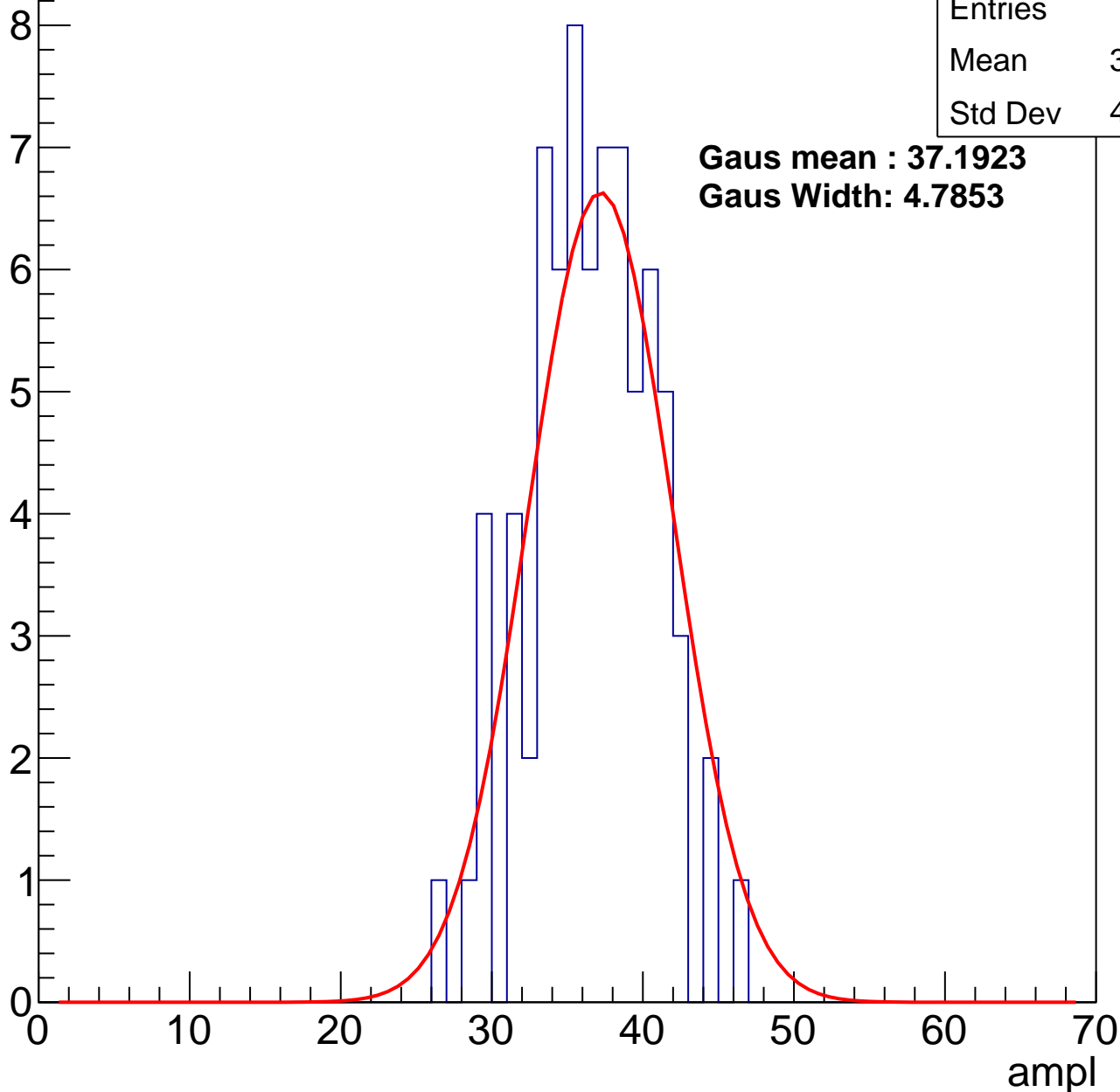
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.19
Std Dev	4.035

Gaus mean : 37.1923

Gaus Width: 4.7853



B1L101S, U14-ch119, adc2

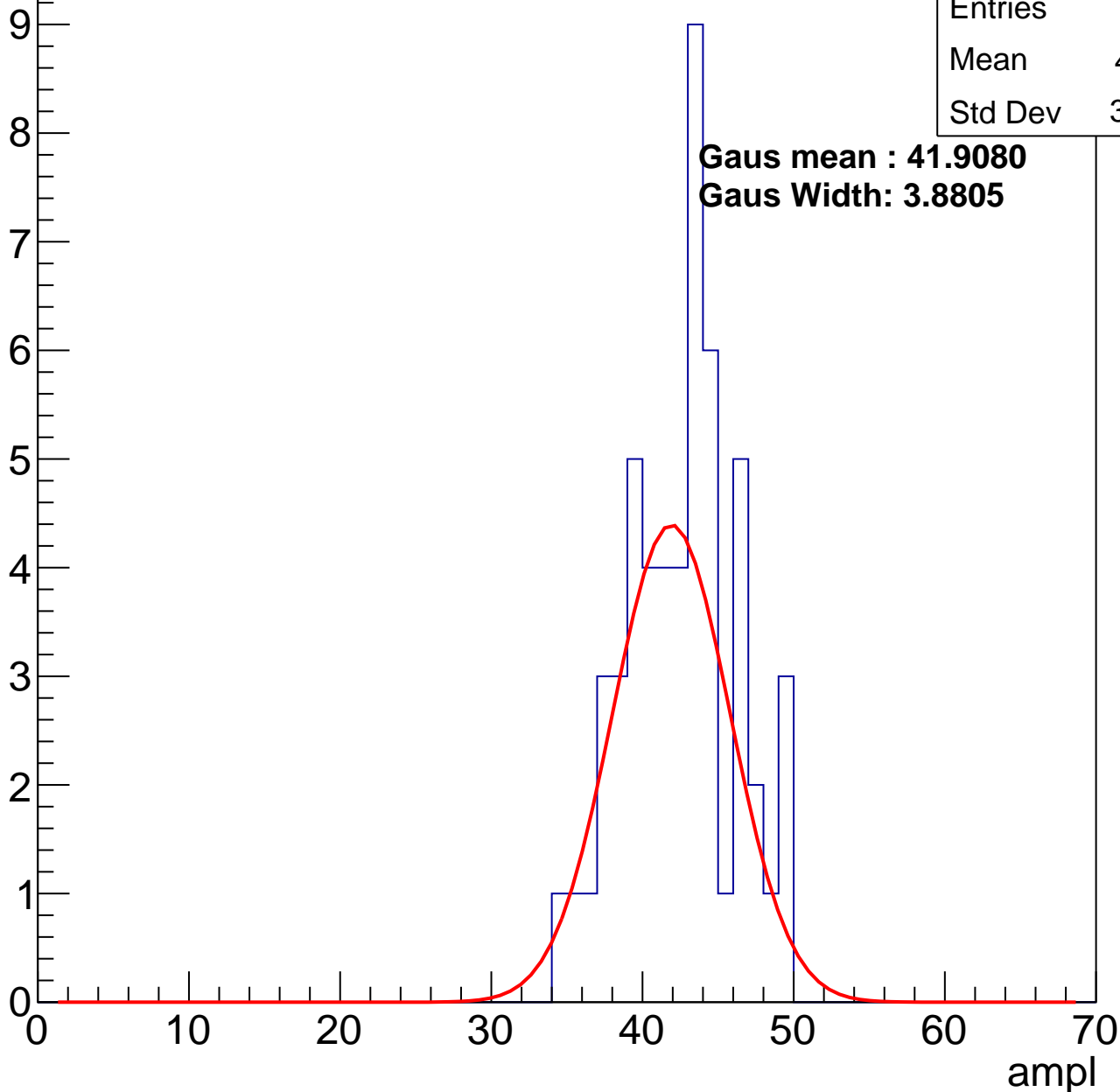
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.11
Std Dev	3.612

Gaus mean : 41.9080

Gaus Width: 3.8805

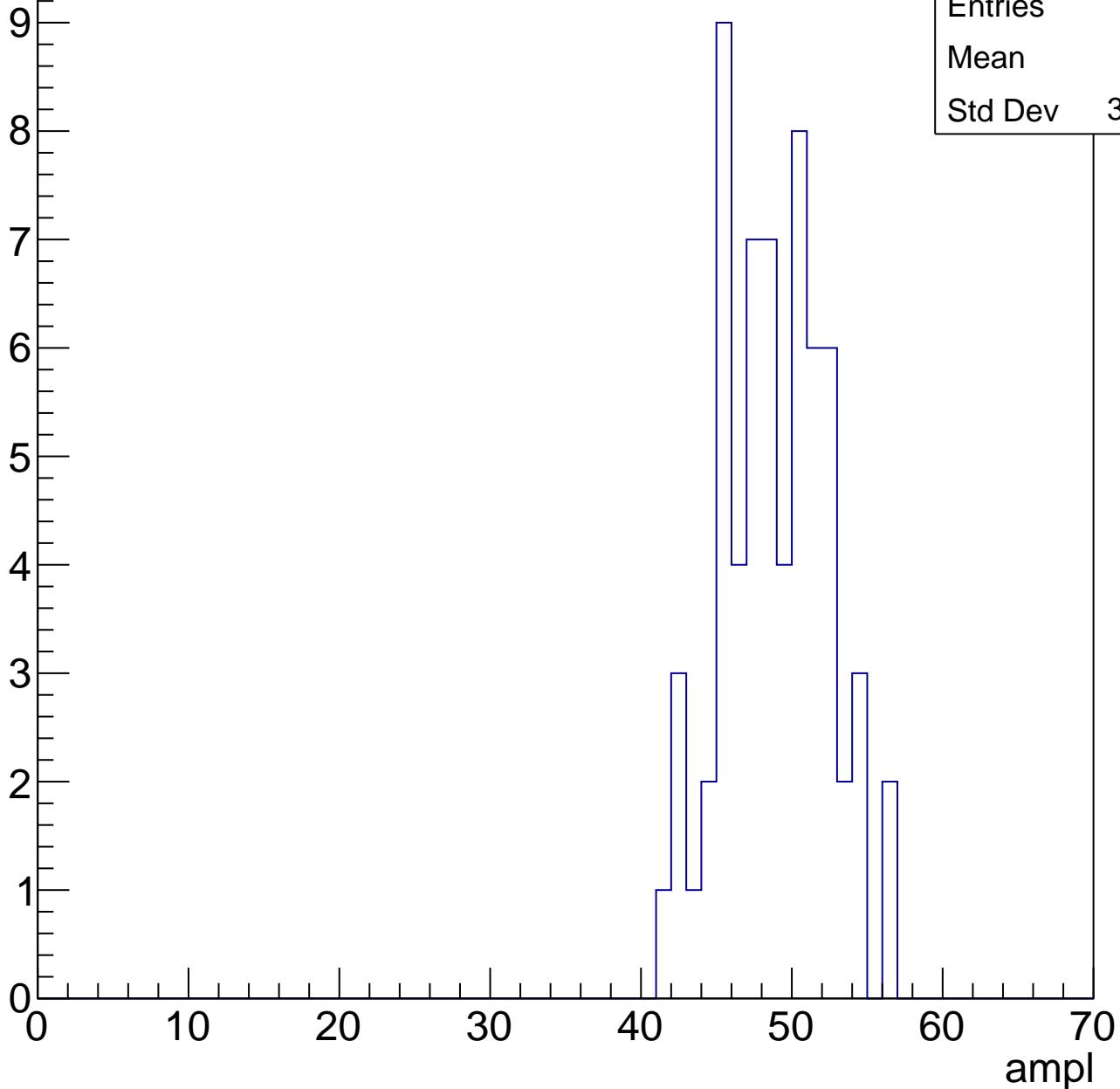


B1L101S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.4
Std Dev	3.463

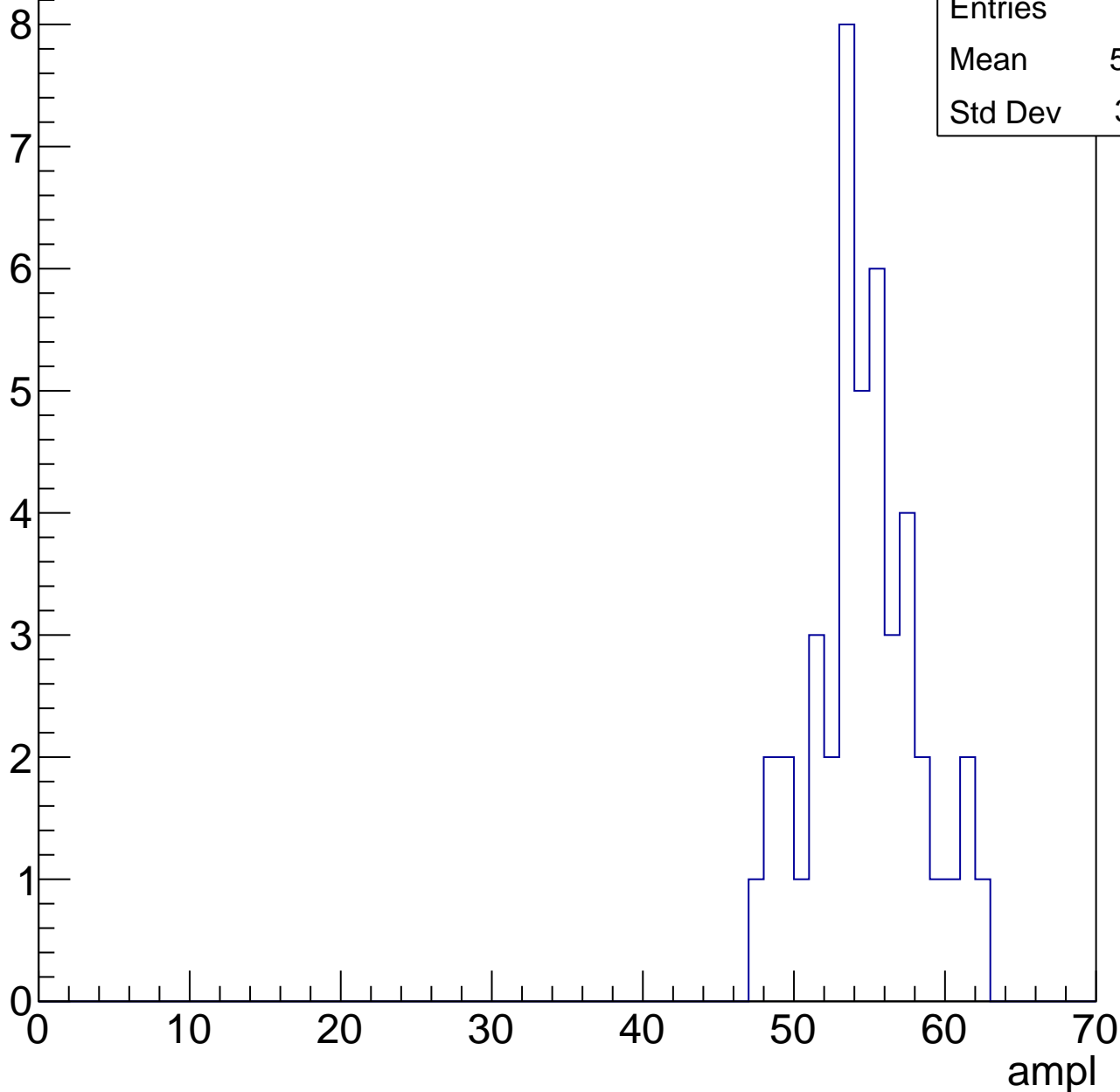


B1L101S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	54.25
Std Dev	3.491

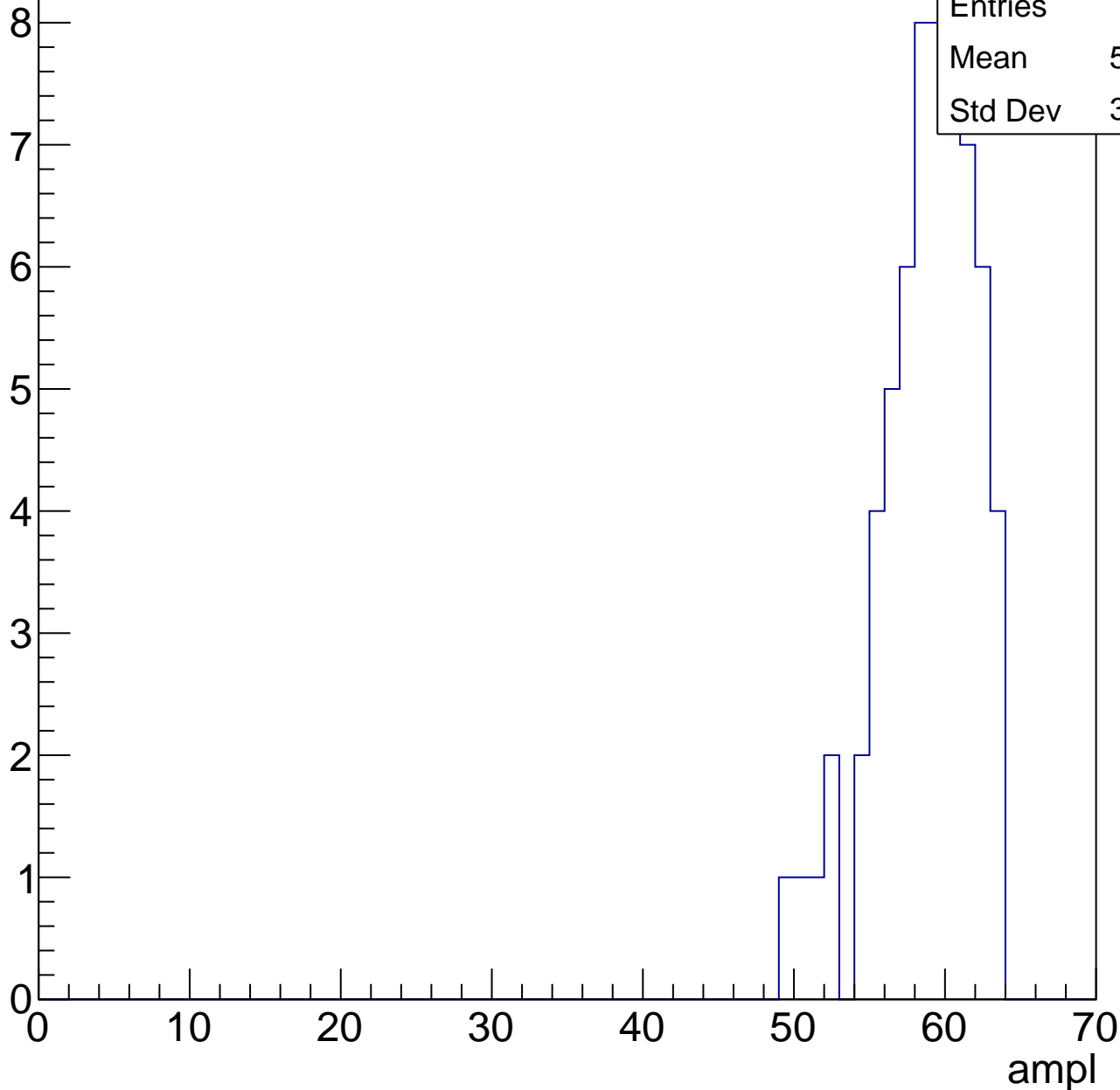


B1L101S, U14-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	58.27
Std Dev	3.223

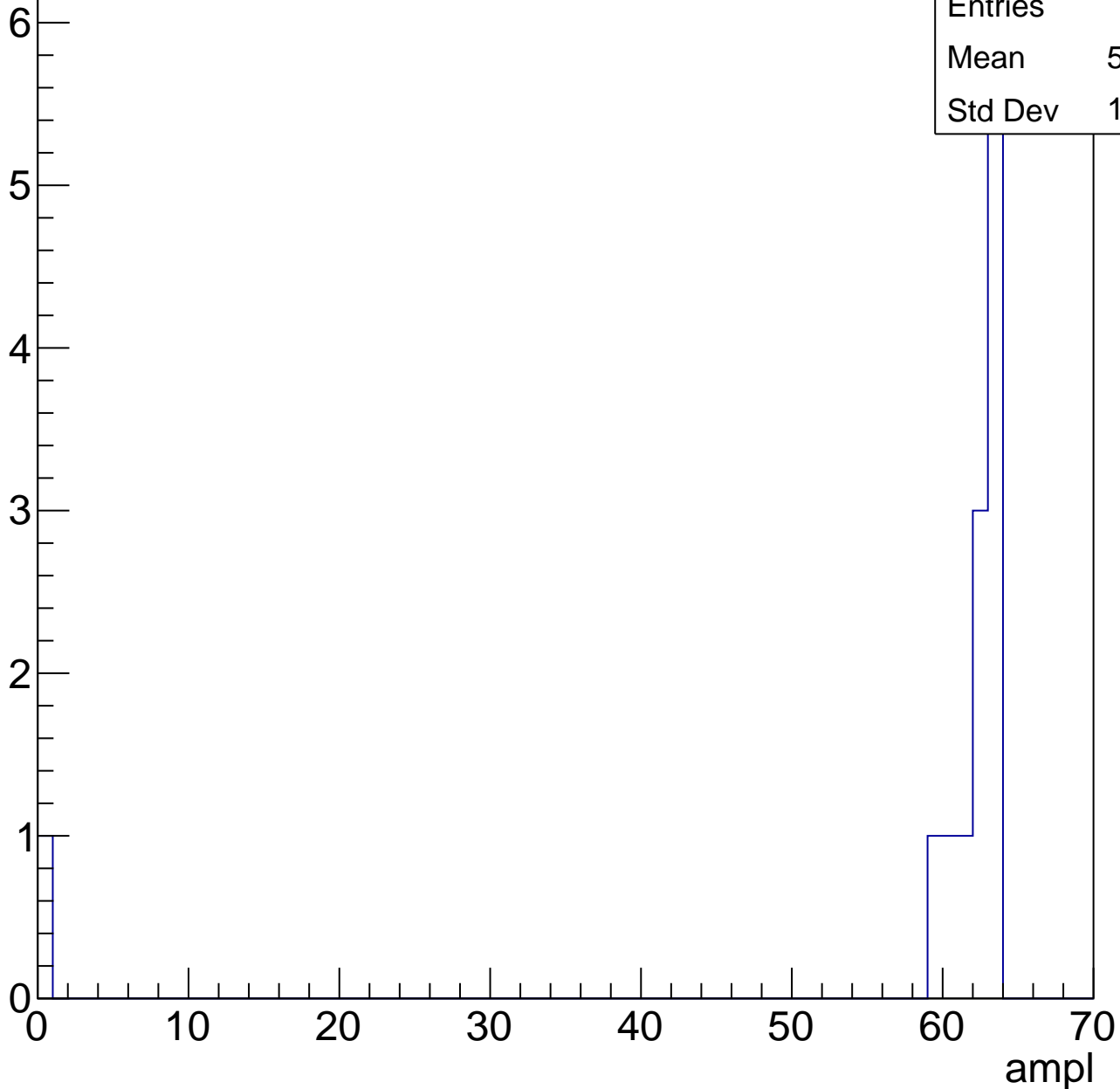


B1L101S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	57.23
Std Dev	16.57



B1L101S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch120, adc0

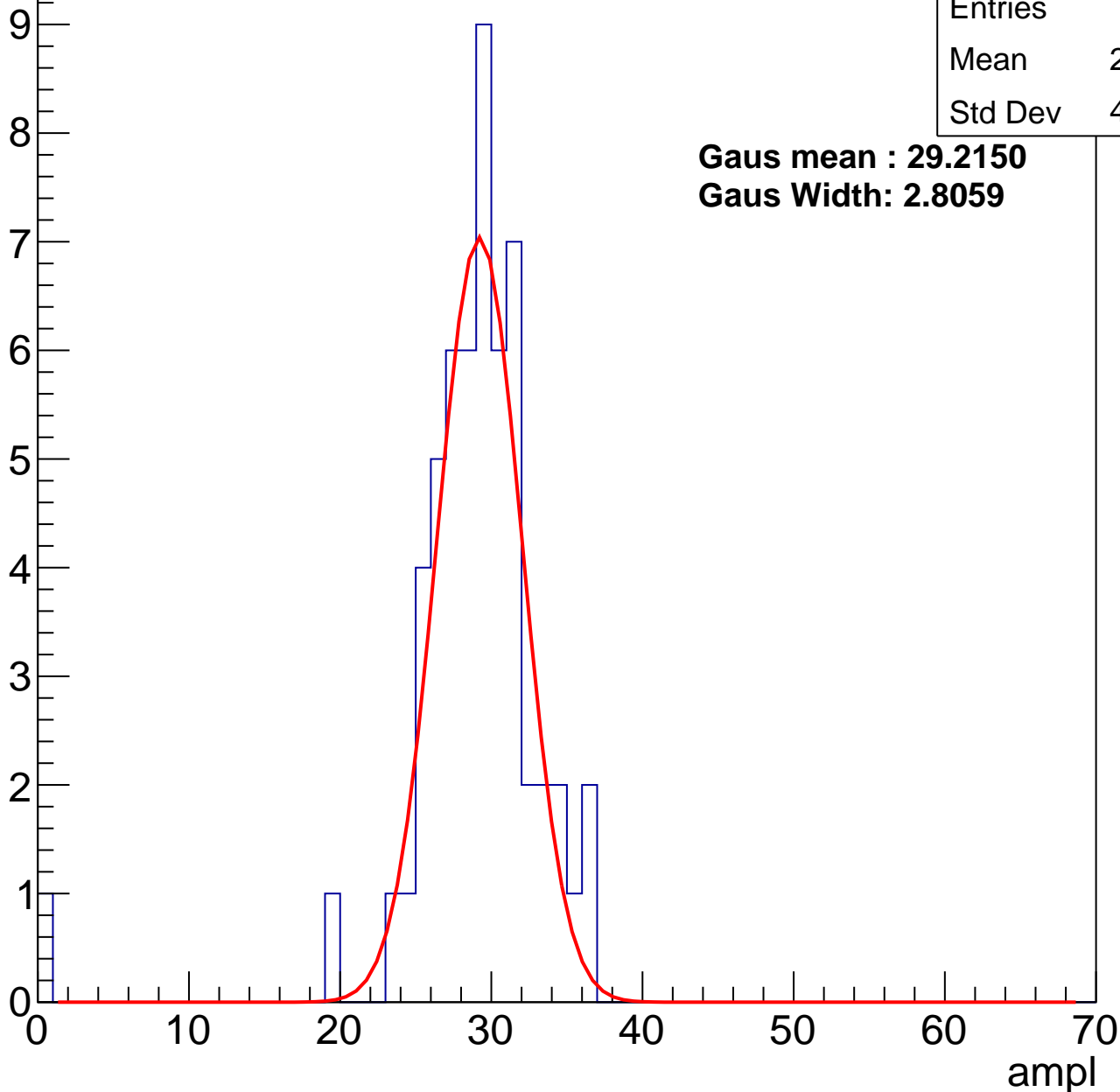
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.38
Std Dev	4.977

Gaus mean : 29.2150

Gaus Width: 2.8059



B1L101S, U14-ch120, adc1

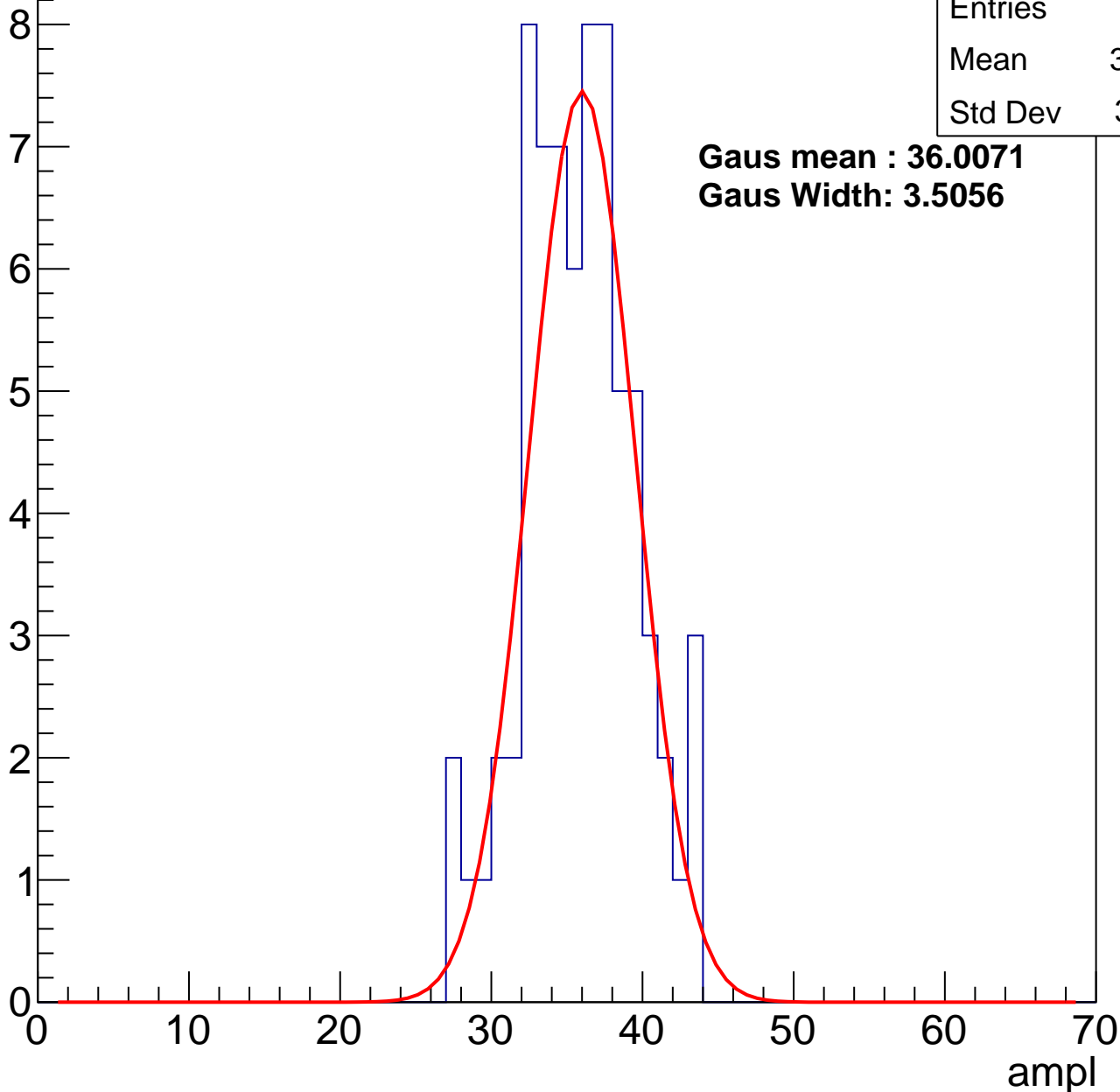
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.35
Std Dev	3.651

Gaus mean : 36.0071

Gaus Width: 3.5056



B1L101S, U14-ch120, adc2

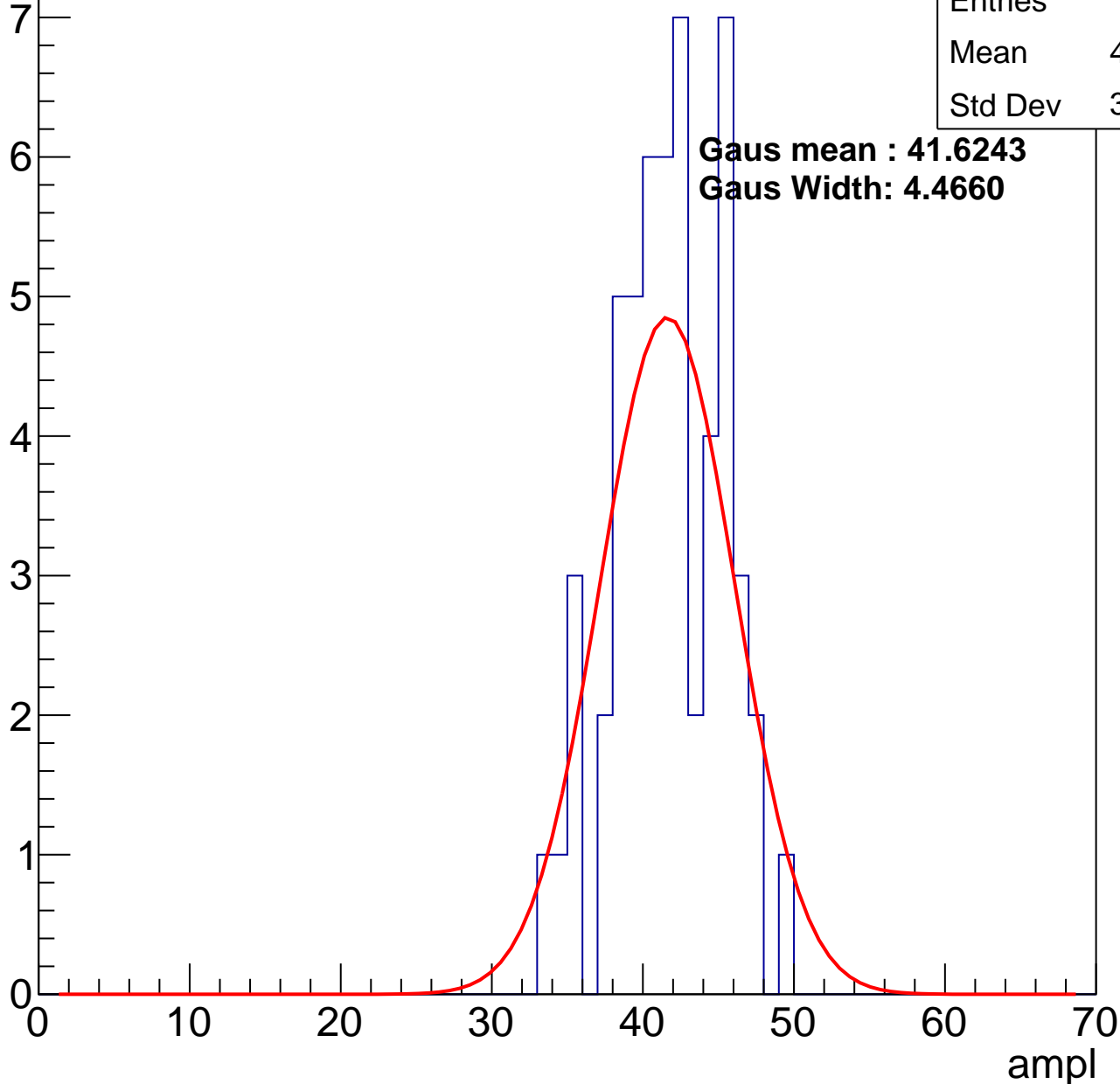
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.25
Std Dev	3.553

Gaus mean : 41.6243

Gaus Width: 4.4660

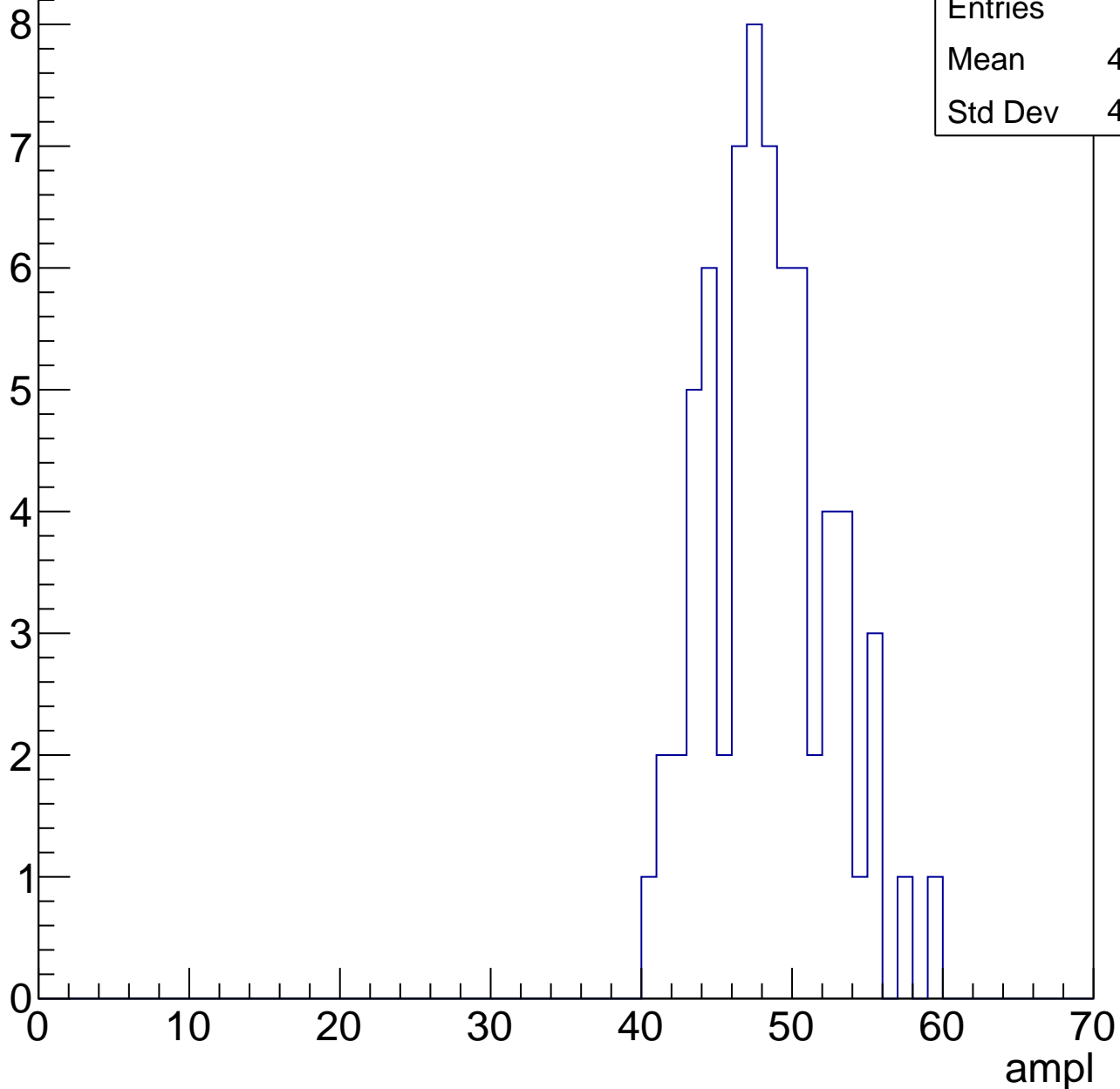


B1L101S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	47.94
Std Dev	4.033

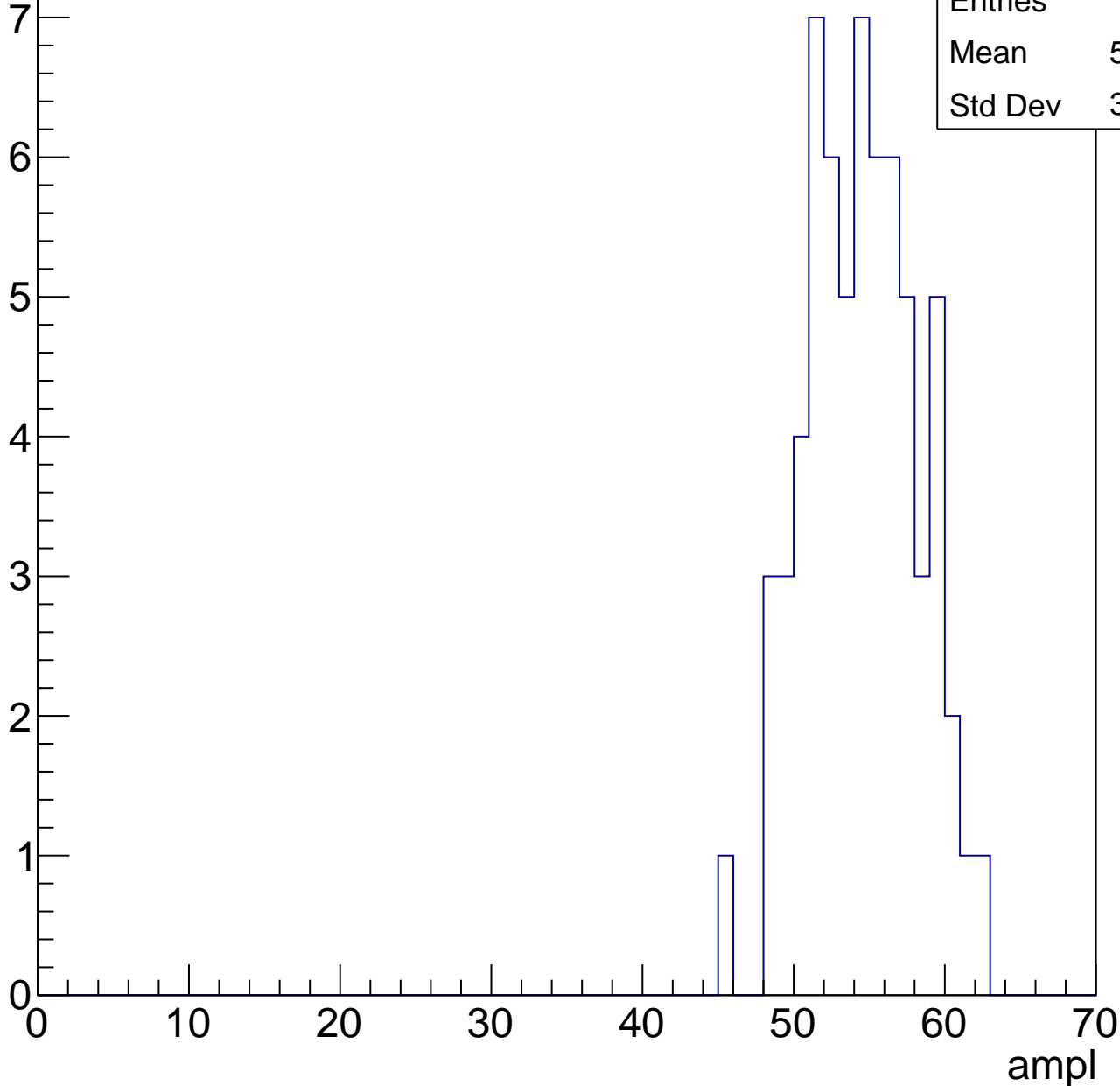


B1L101S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.02
Std Dev	3.623

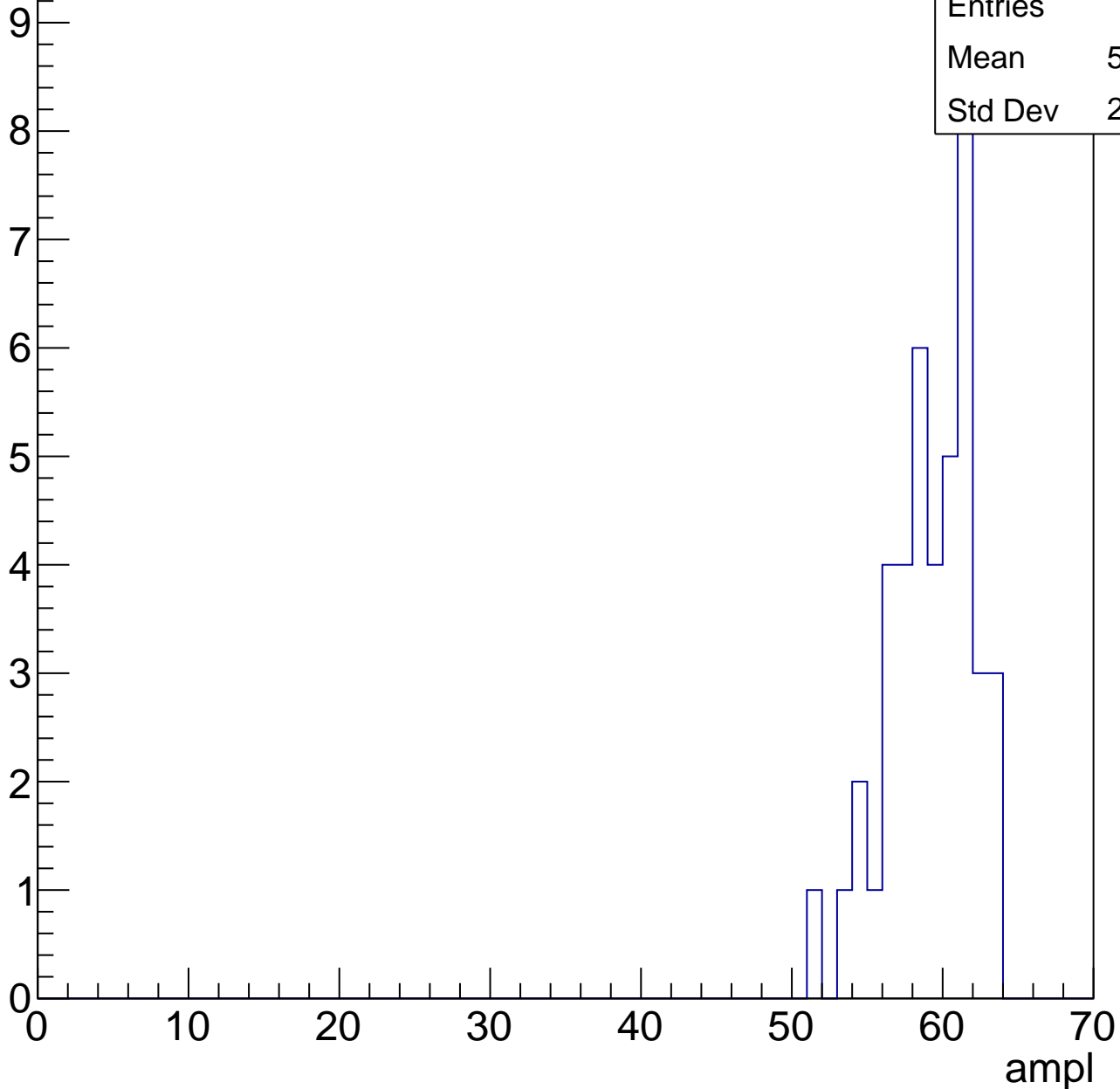


B1L101S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.77
Std Dev	2.802

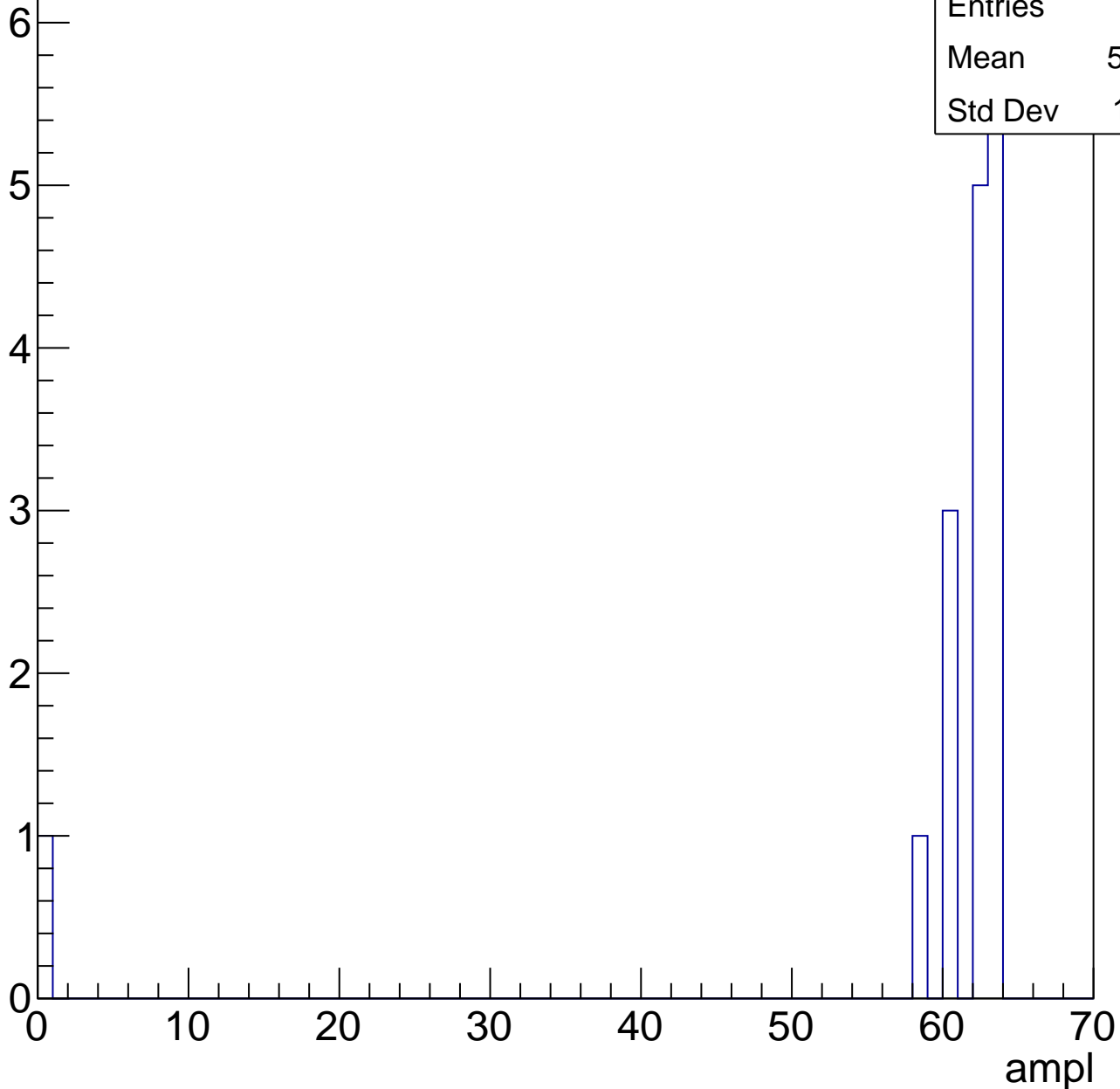


B1L101S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.88
Std Dev	15.01



B1L101S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch121, adc0

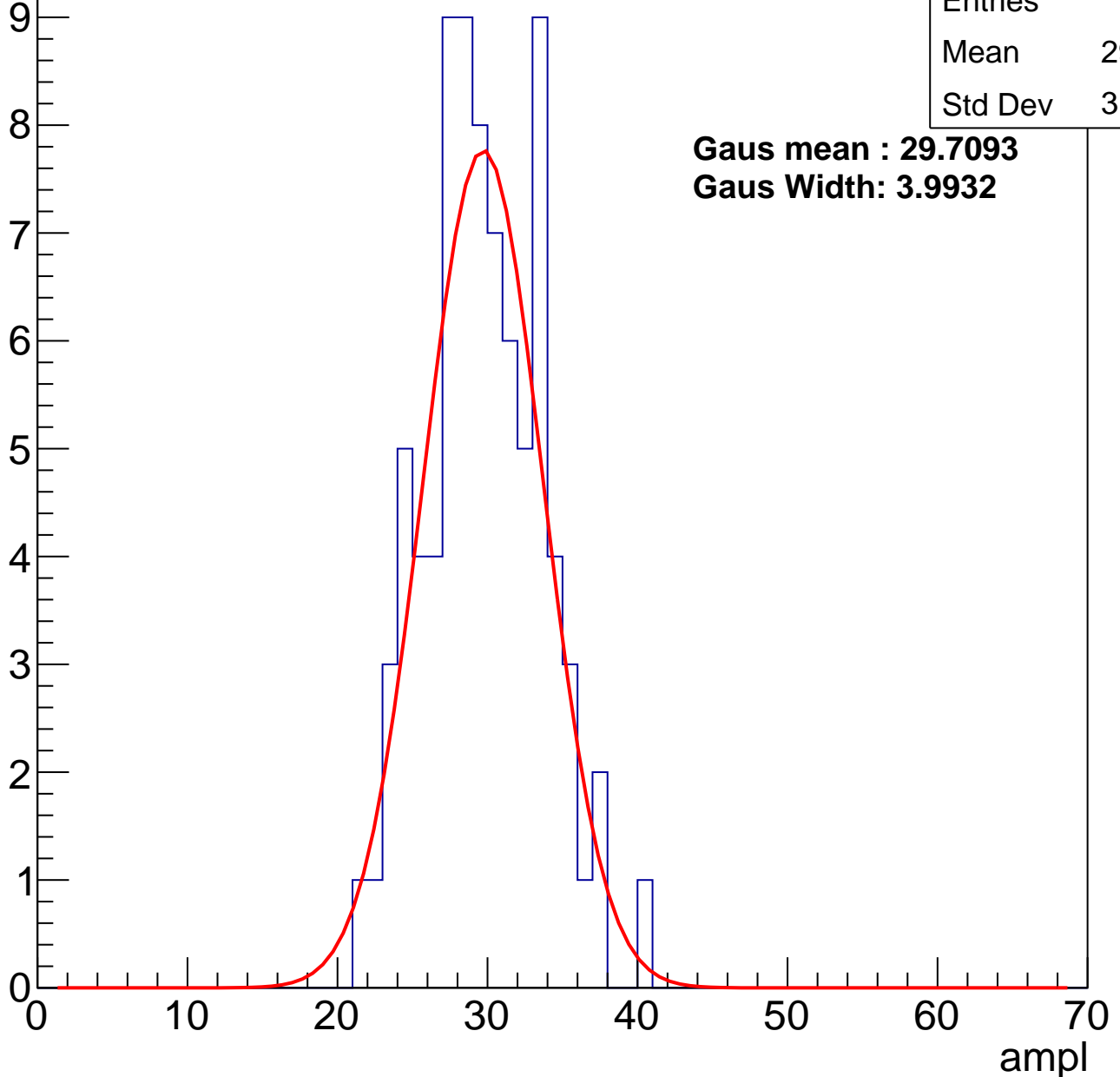
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	29.35
Std Dev	3.836

Gaus mean : 29.7093

Gaus Width: 3.9932



B1L101S, U14-ch121, adc1

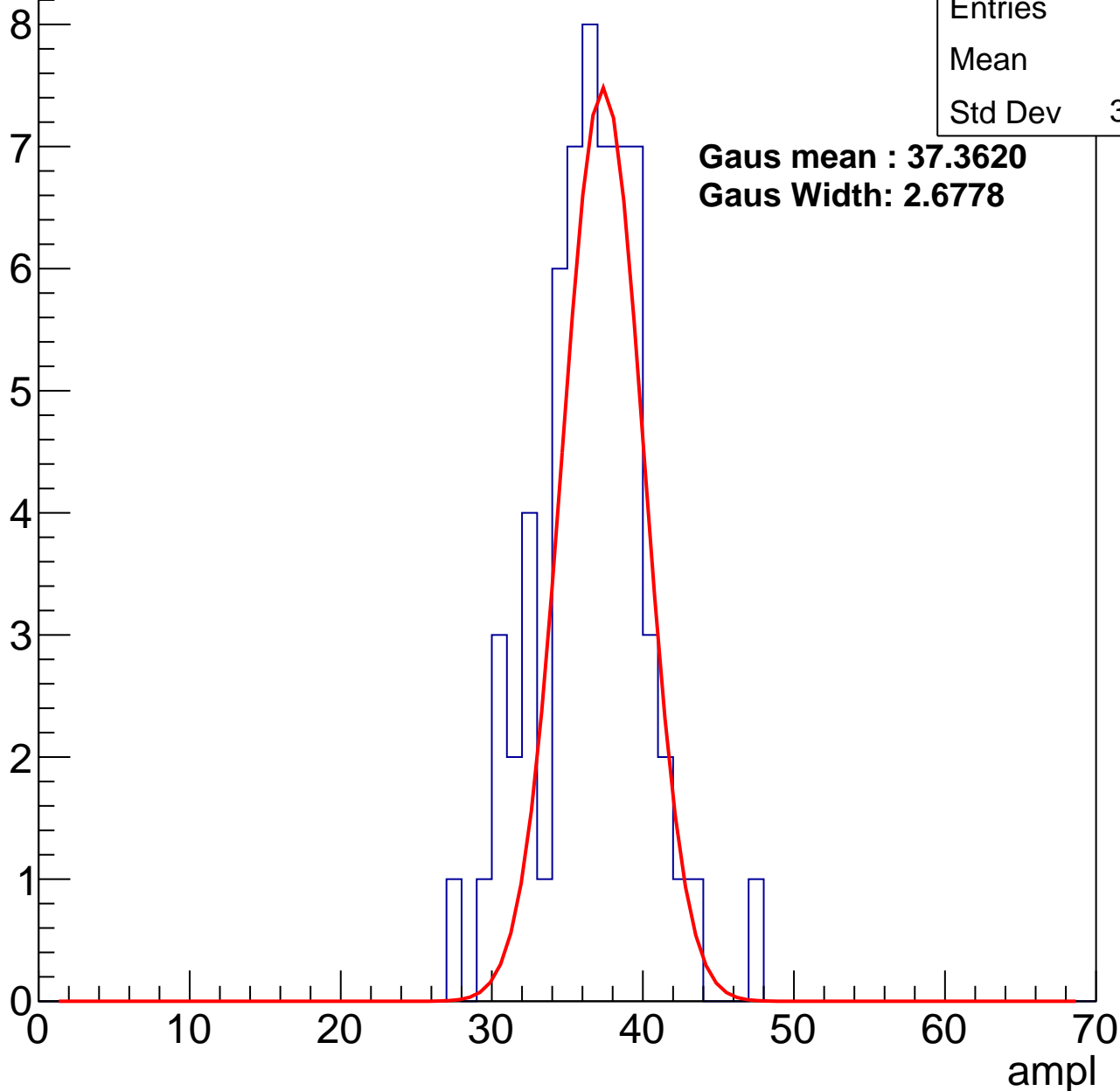
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.1
Std Dev	3.577

Gaus mean : 37.3620

Gaus Width: 2.6778



B1L101S, U14-ch121, adc2

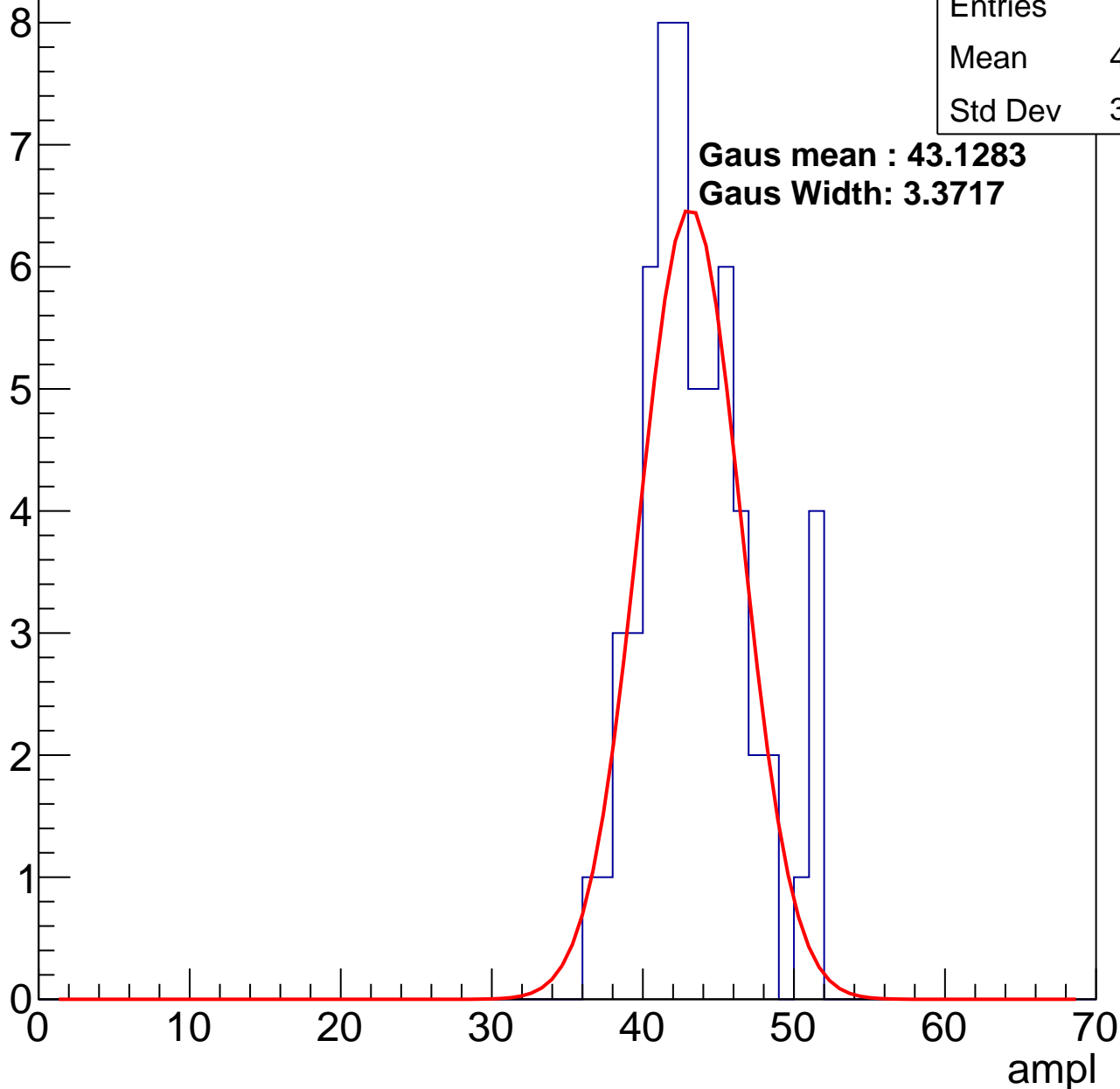
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.07
Std Dev	3.574

Gaus mean : 43.1283

Gaus Width: 3.3717

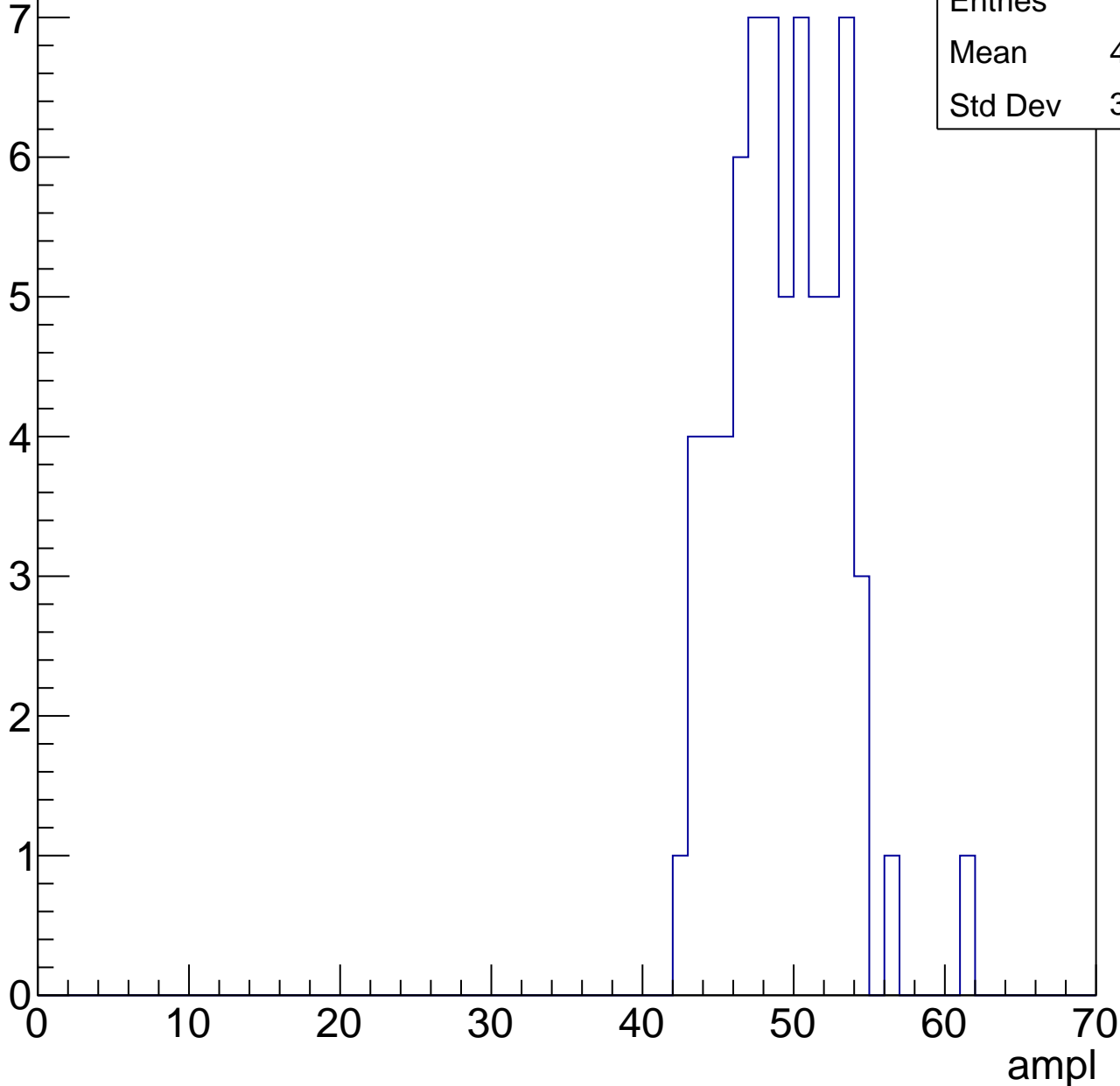


B1L101S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	48.82
Std Dev	3.657

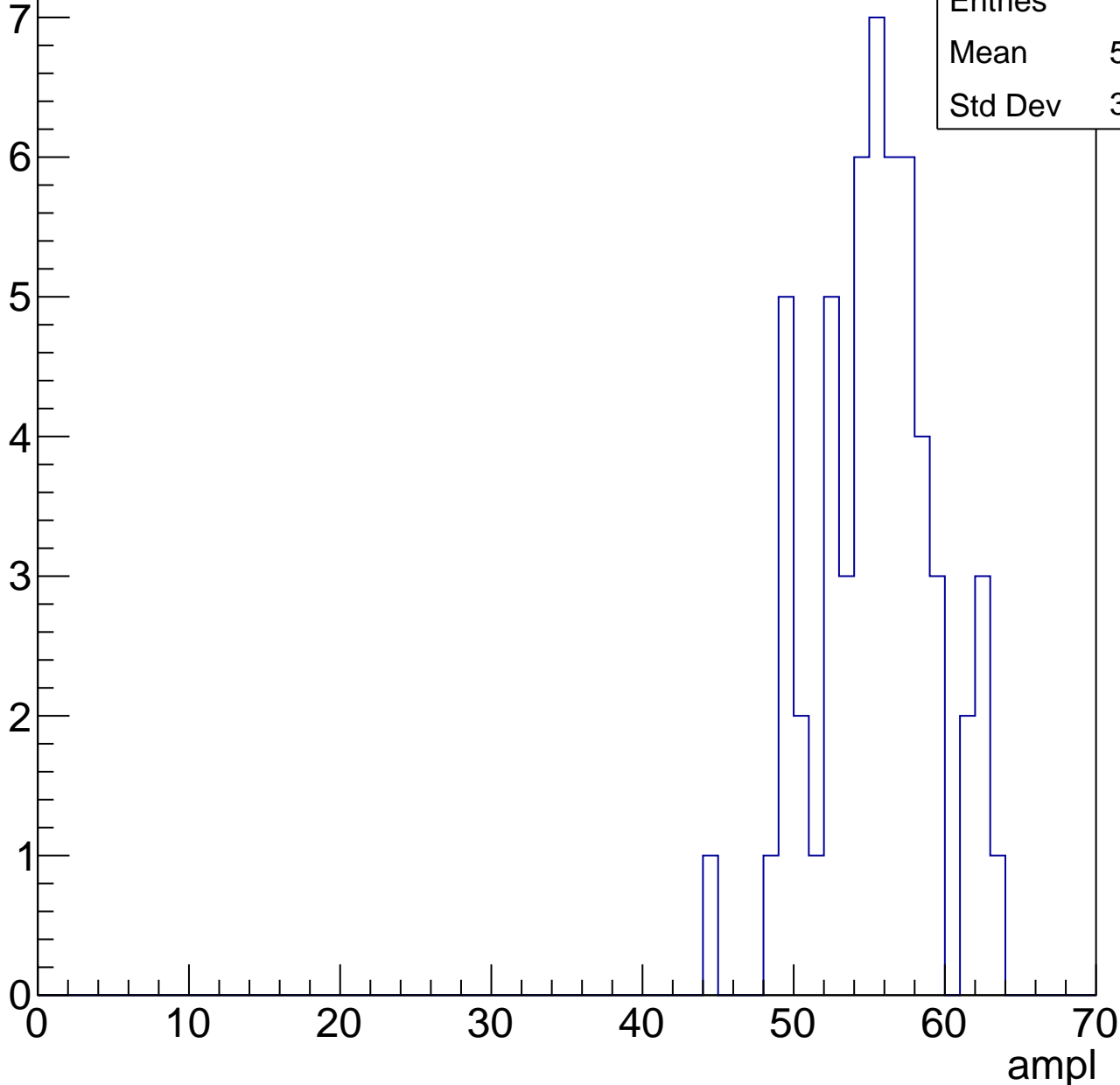


B1L101S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

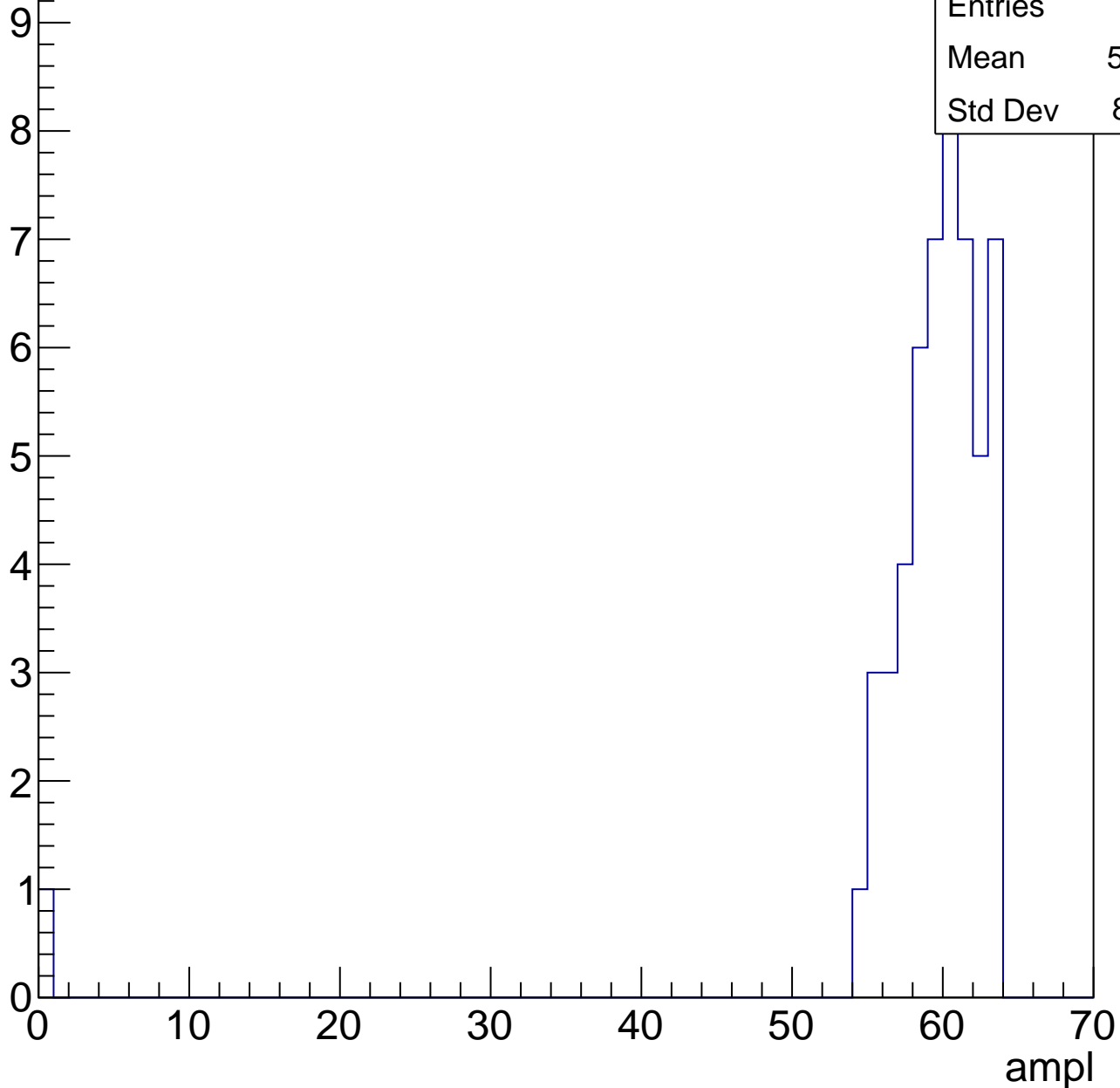
Entries	56
Mean	54.89
Std Dev	3.954



B1L101S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B1L101S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch122, adc0

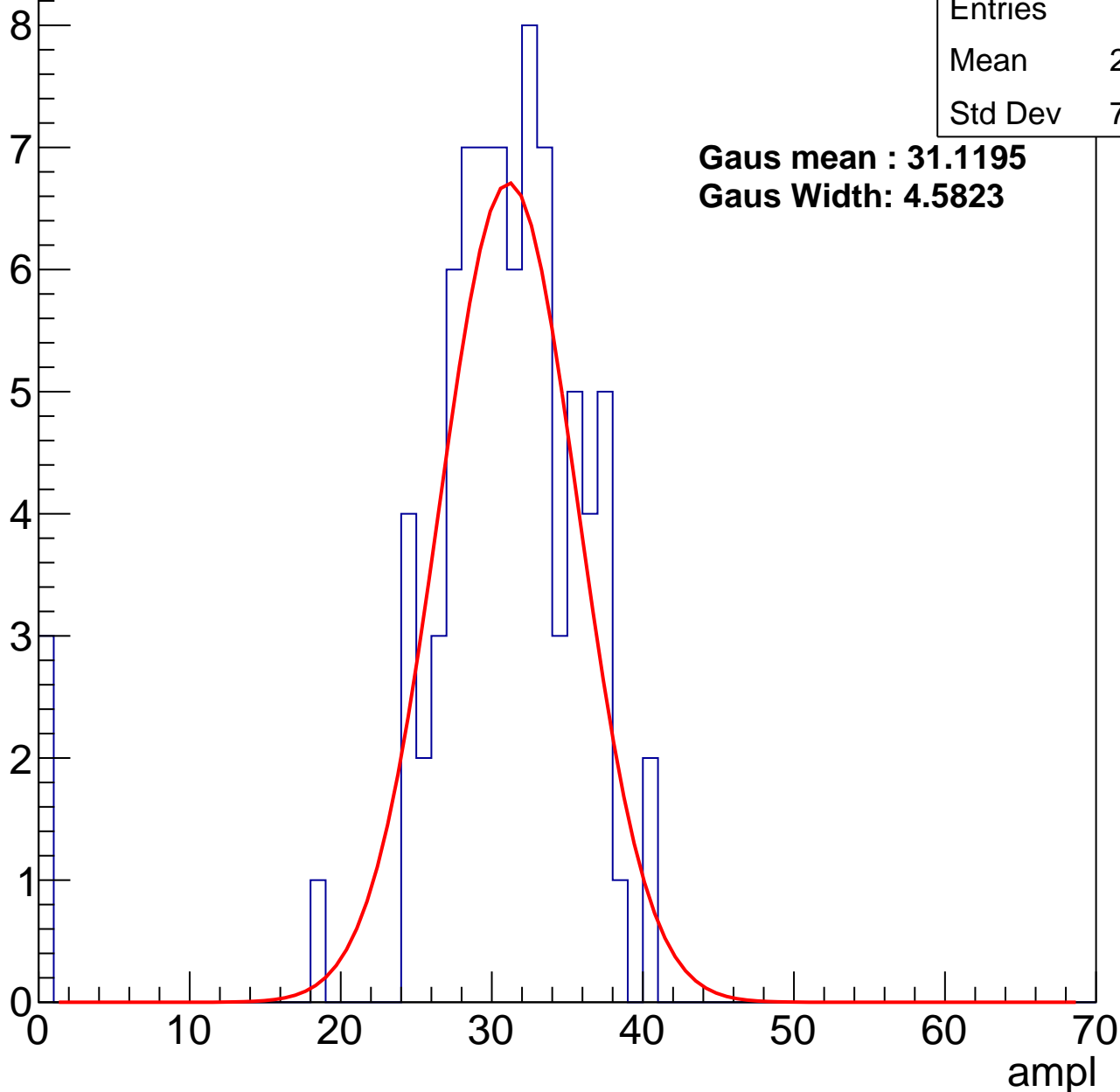
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	29.75
Std Dev	7.112

Gaus mean : 31.1195

Gaus Width: 4.5823



B1L101S, U14-ch122, adc1

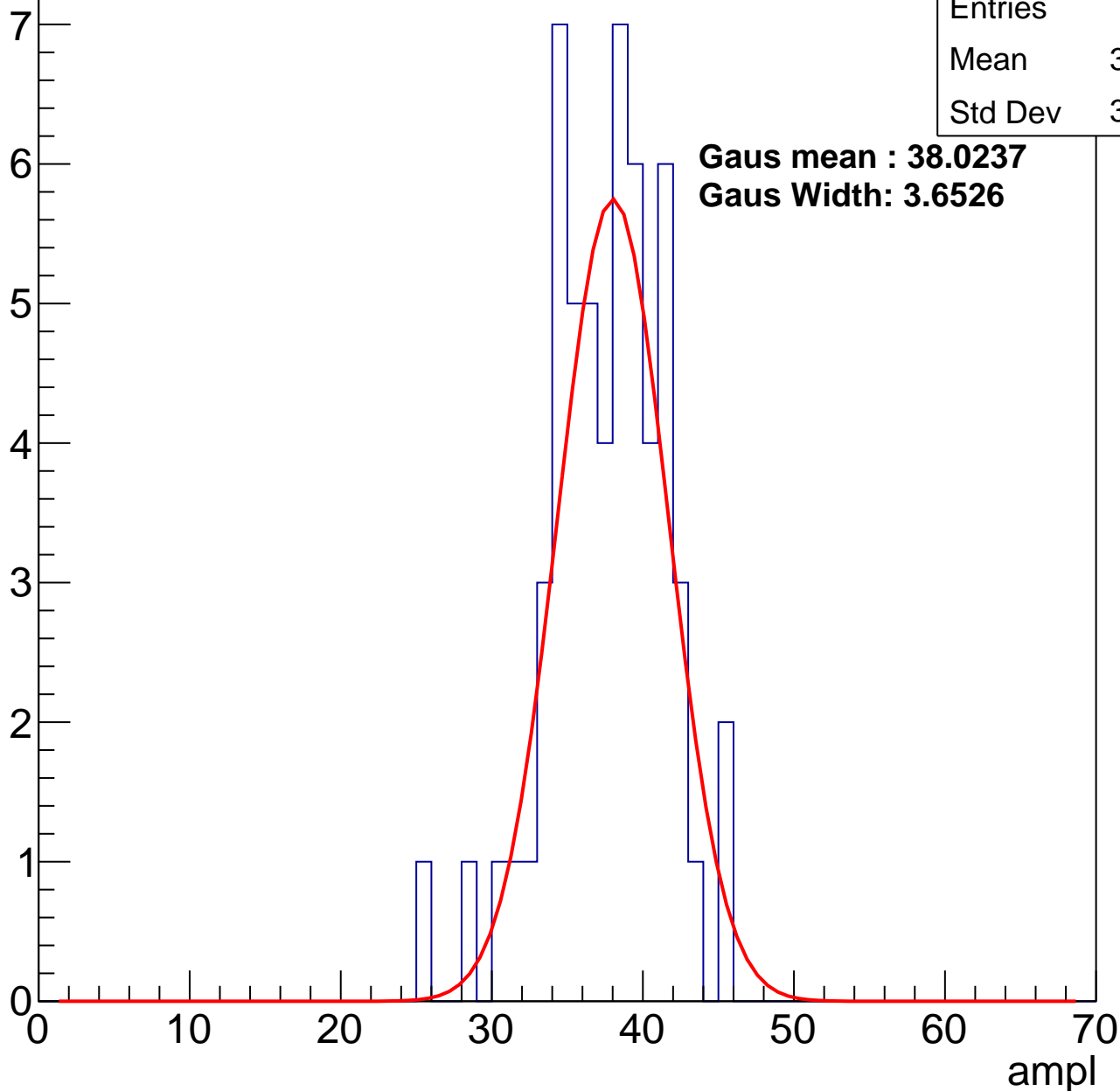
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.09
Std Dev	3.879

Gaus mean : 38.0237

Gaus Width: 3.6526



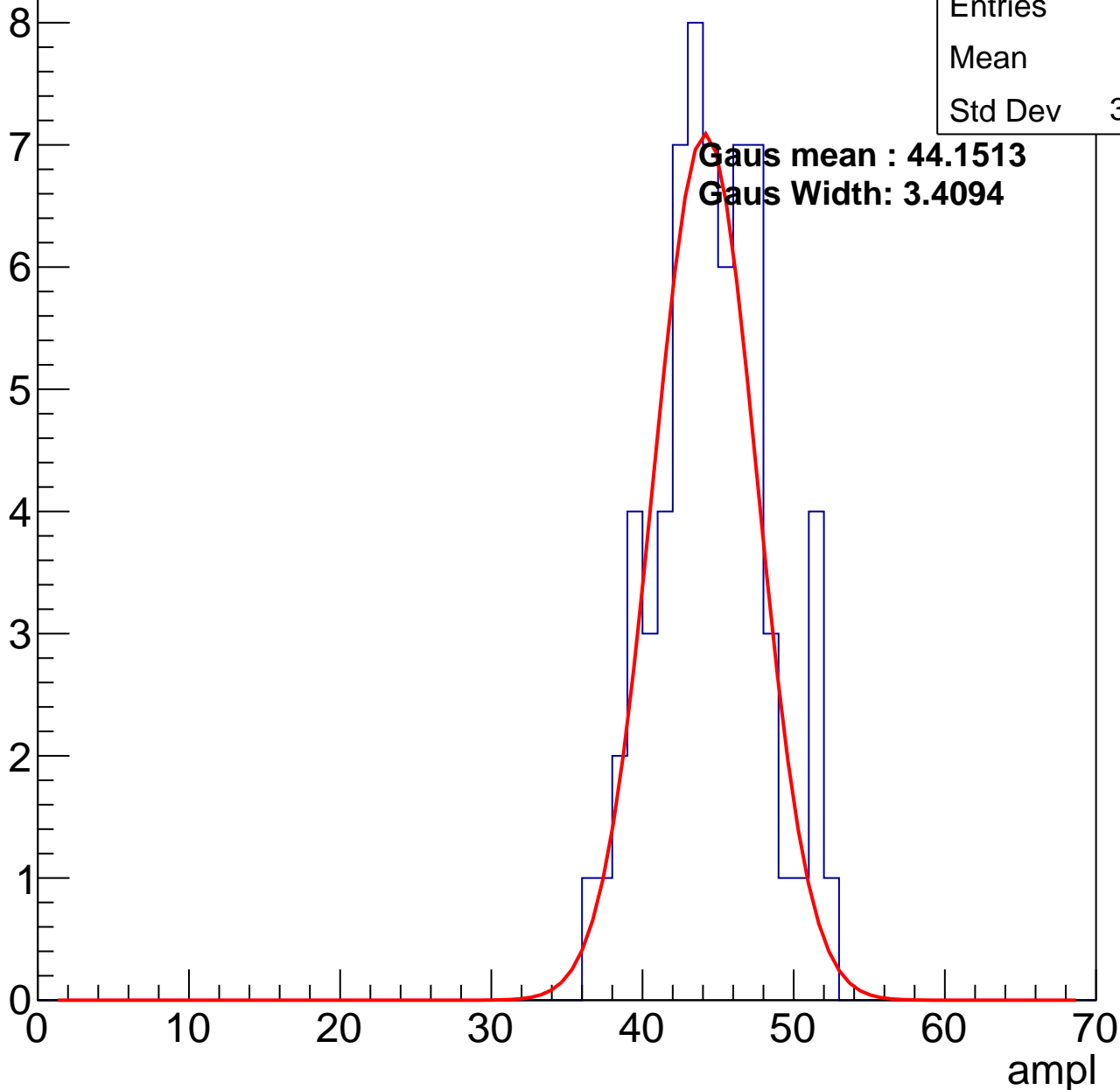
B1L101S, U14-ch122, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.1
Std Dev	3.604

Gaus mean : 44.1513
Gaus Width: 3.4094

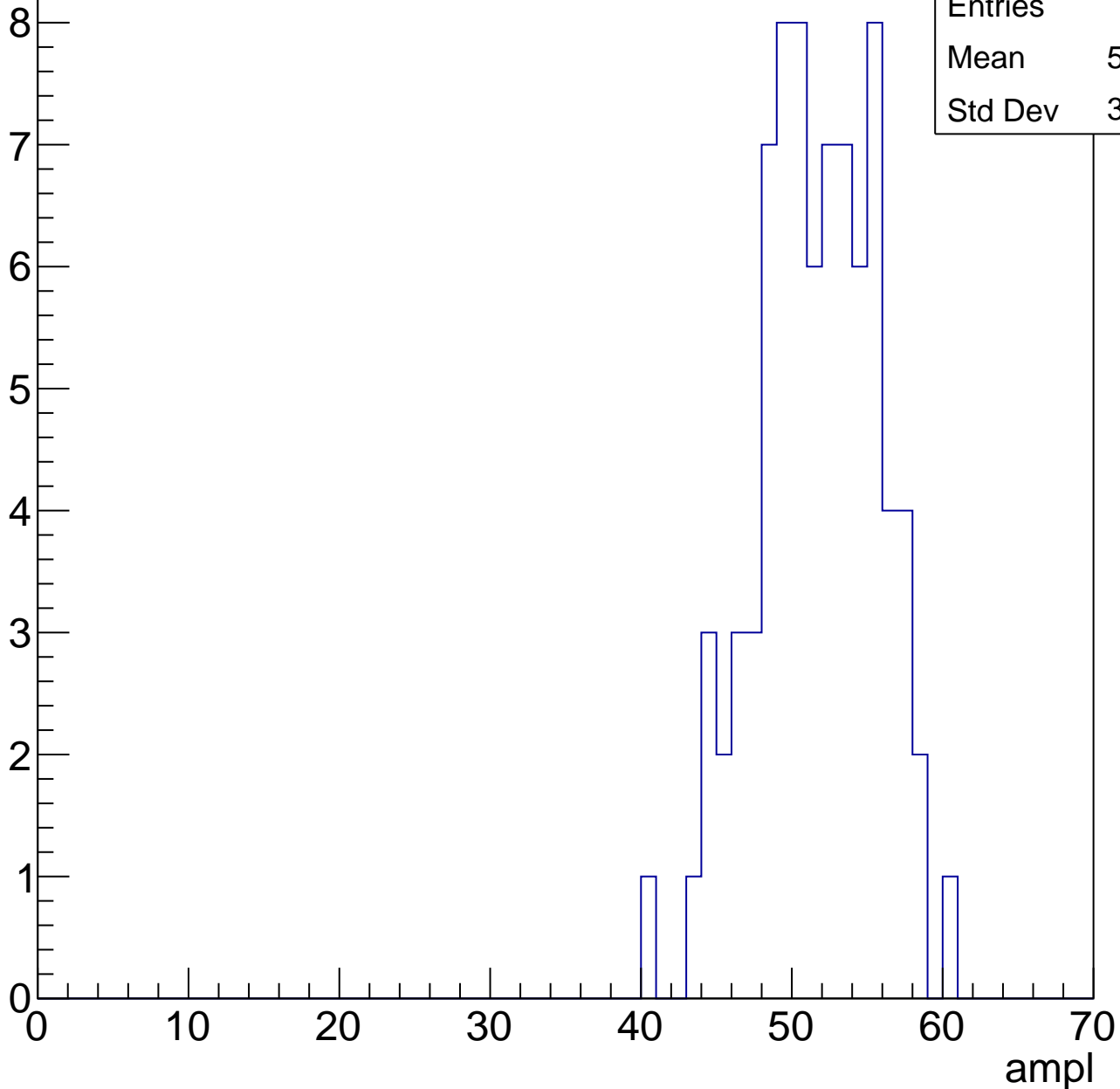


B1L101S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	51.17
Std Dev	3.956

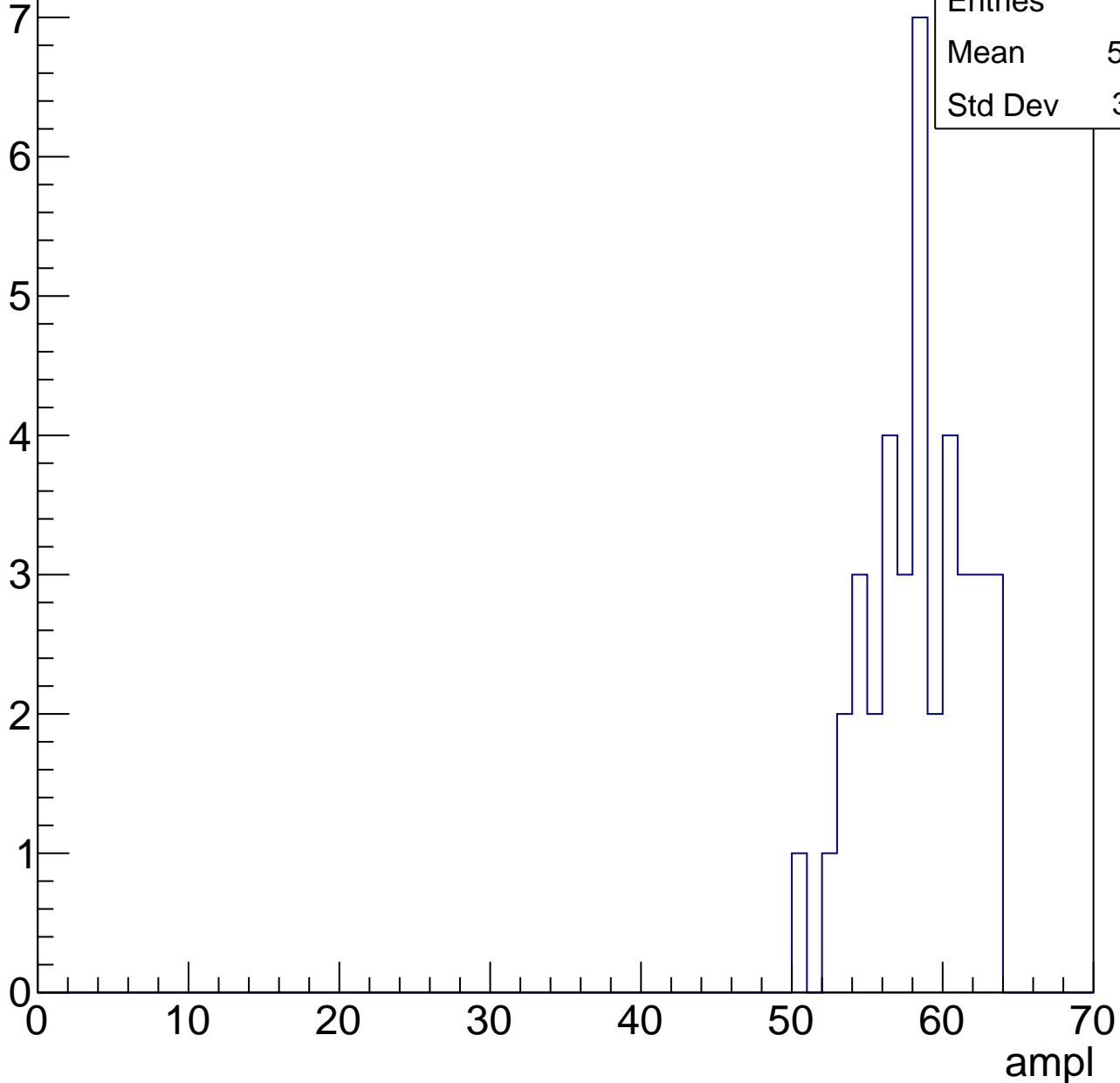


B1L101S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

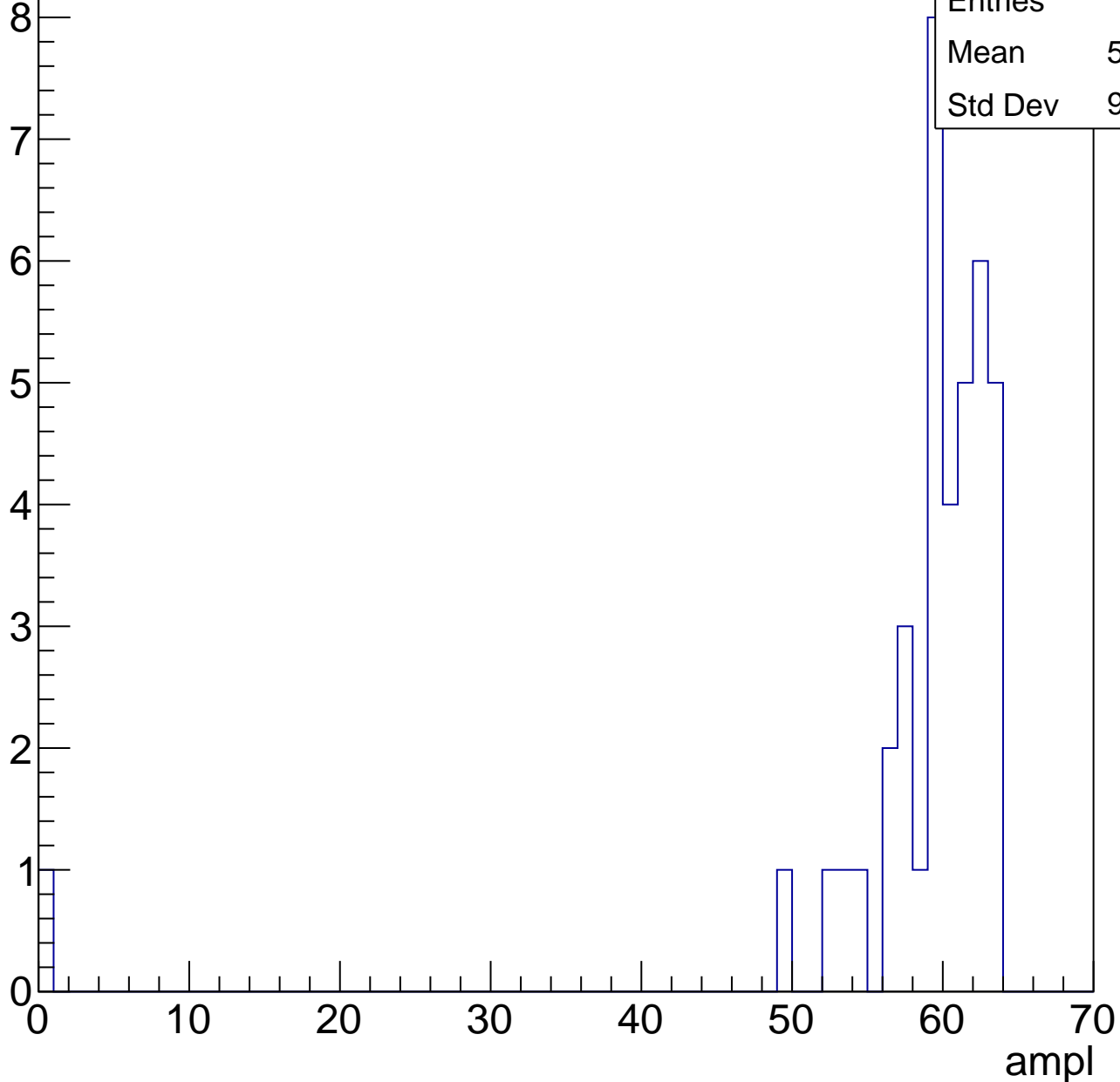
Entries	38
Mean	57.82
Std Dev	3.251



B1L101S, U14-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch122, adc6

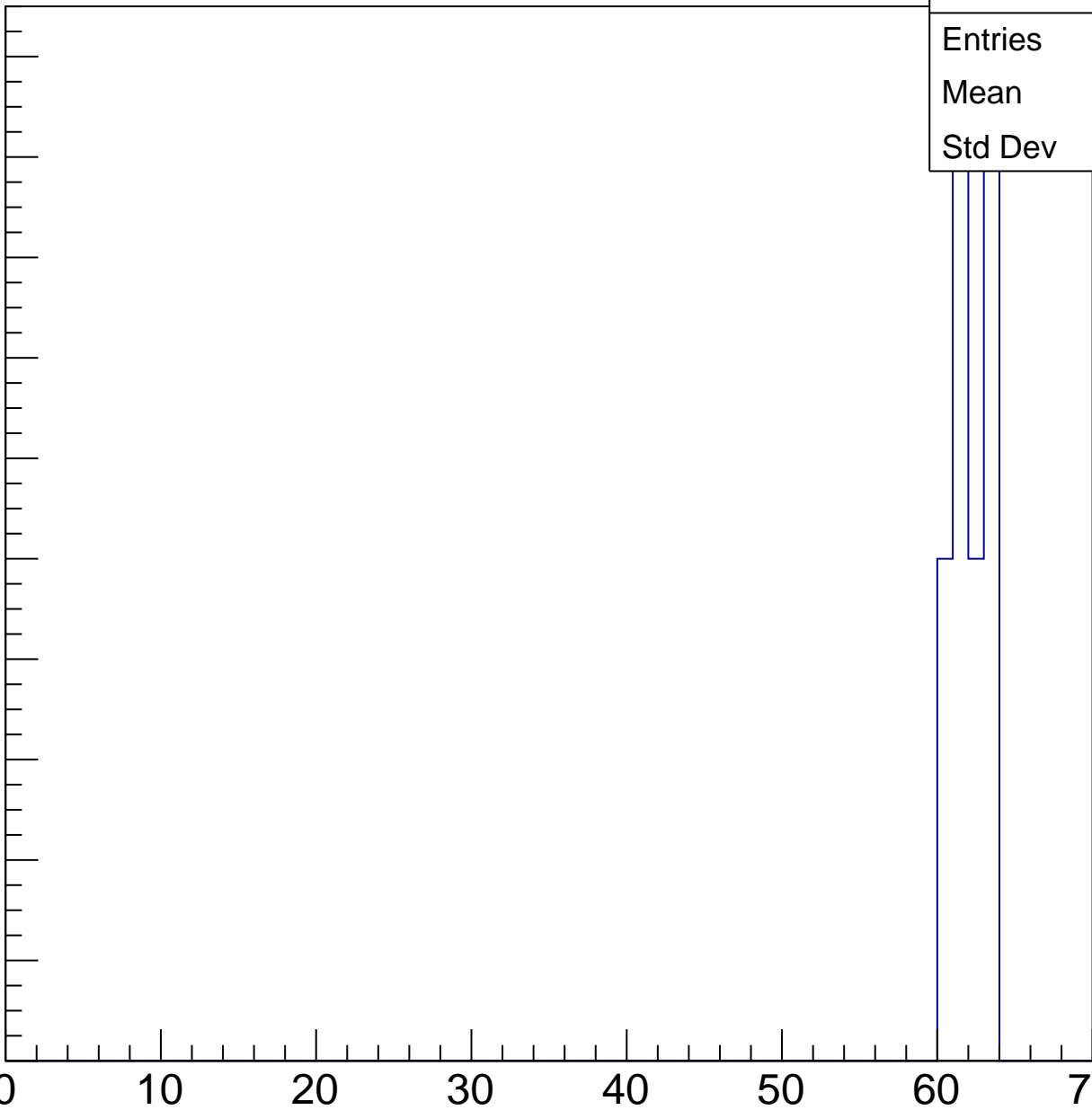
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl



B1L101S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch123, adc0

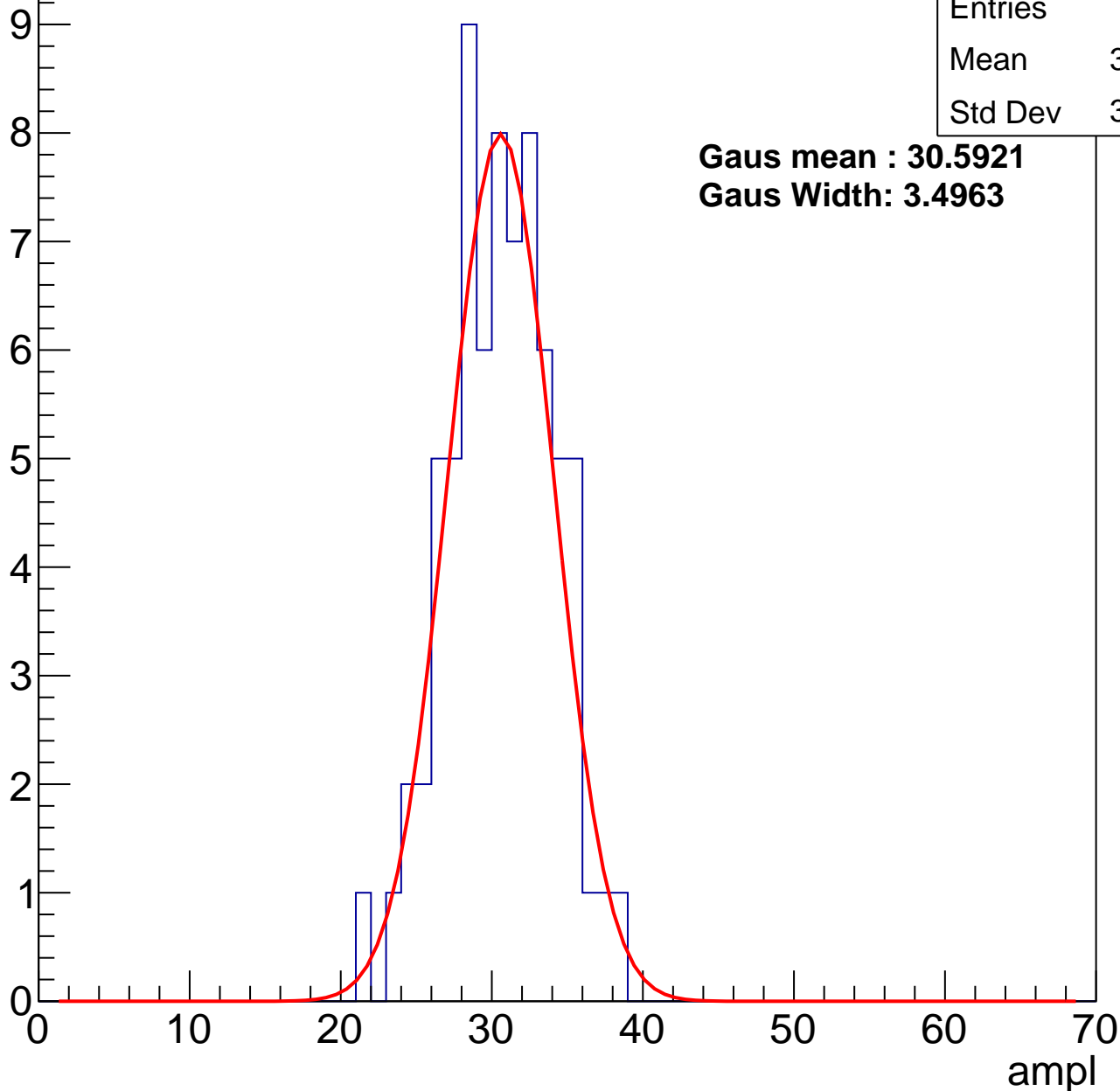
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.14
Std Dev	3.442

Gaus mean : 30.5921

Gaus Width: 3.4963



B1L101S, U14-ch123, adc1

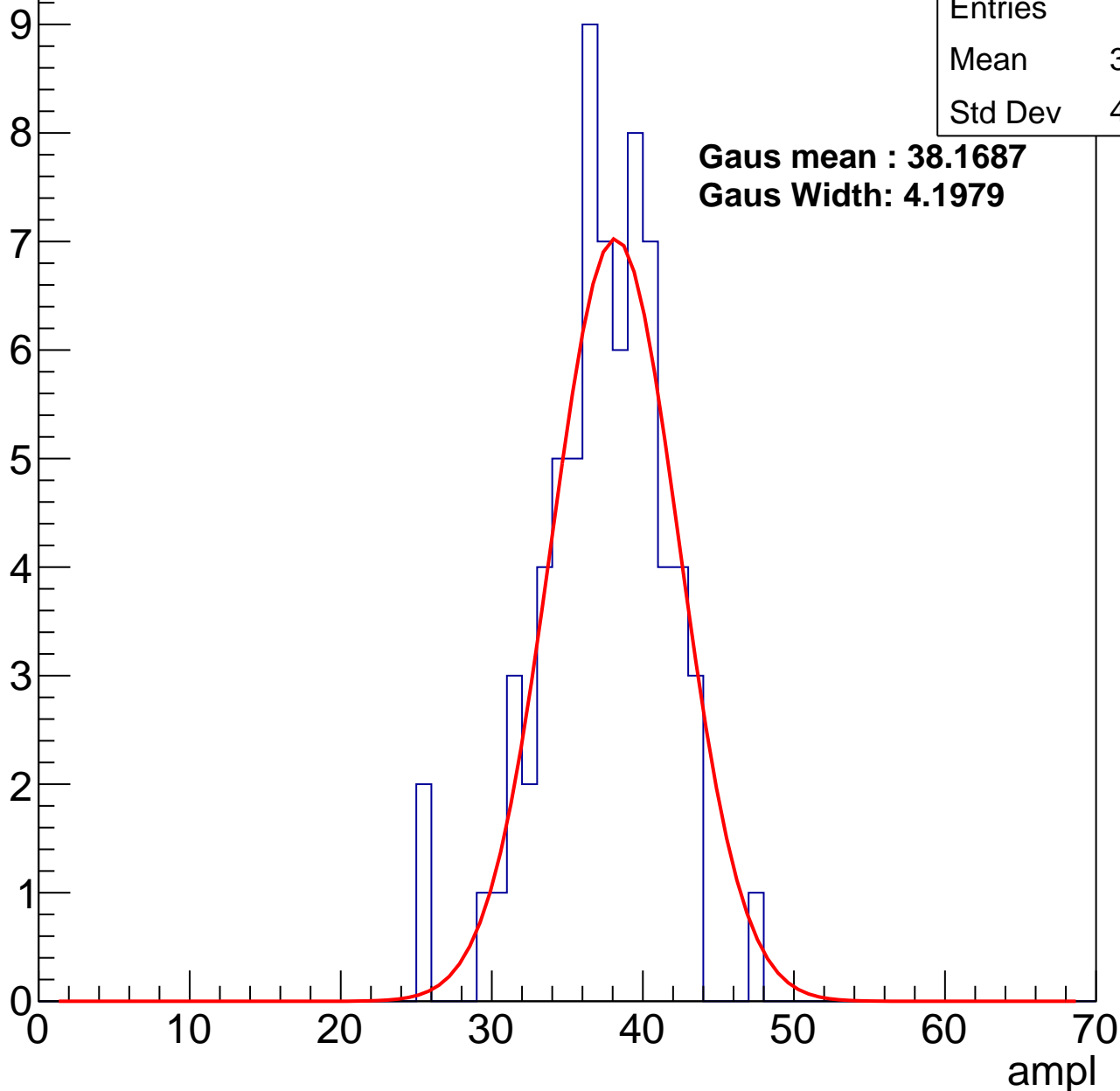
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.86
Std Dev	4.029

Gaus mean : 38.1687

Gaus Width: 4.1979



B1L101S, U14-ch123, adc2

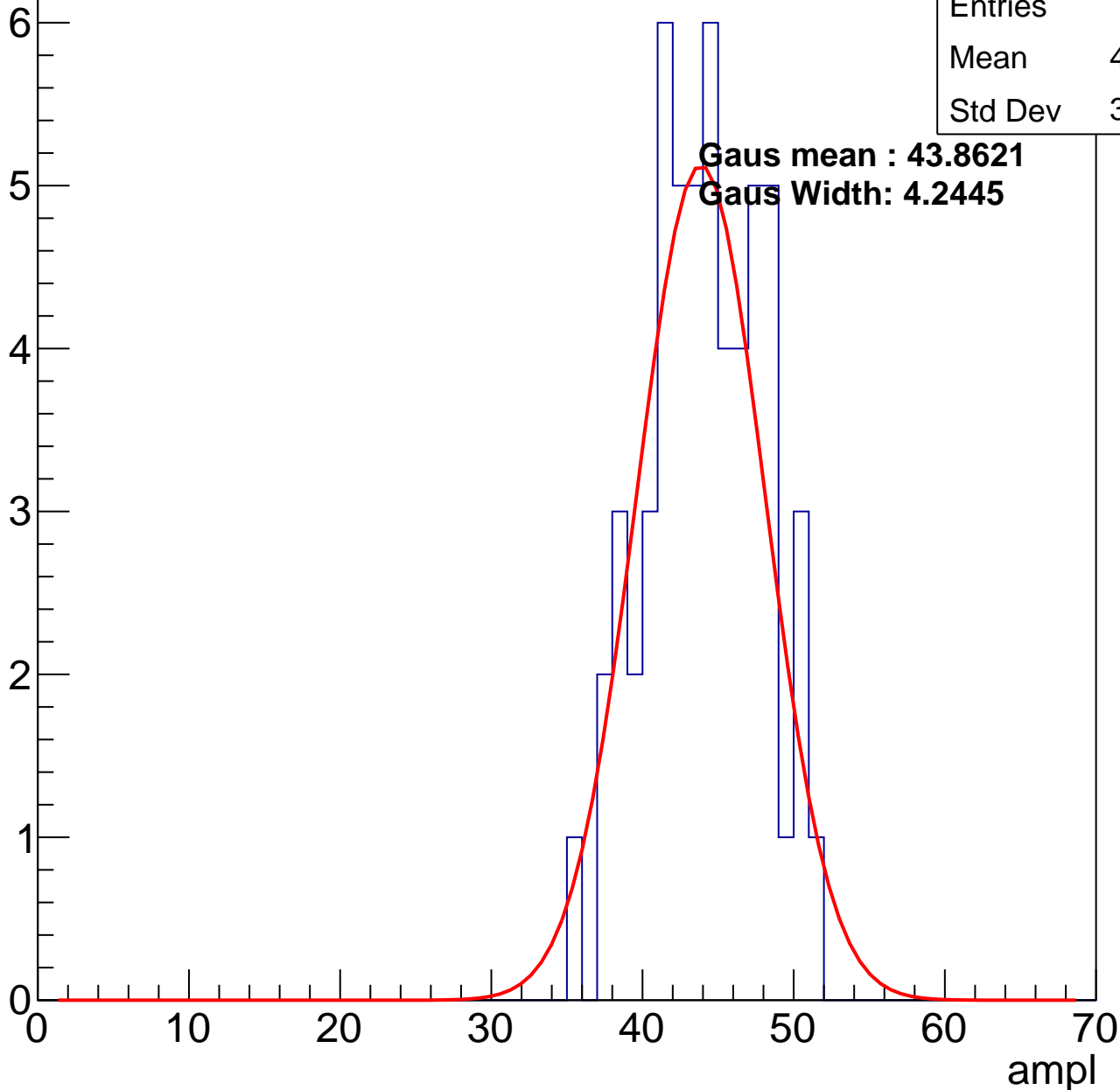
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.66
Std Dev	3.743

Gaus mean : 43.8621

Gaus Width: 4.2445

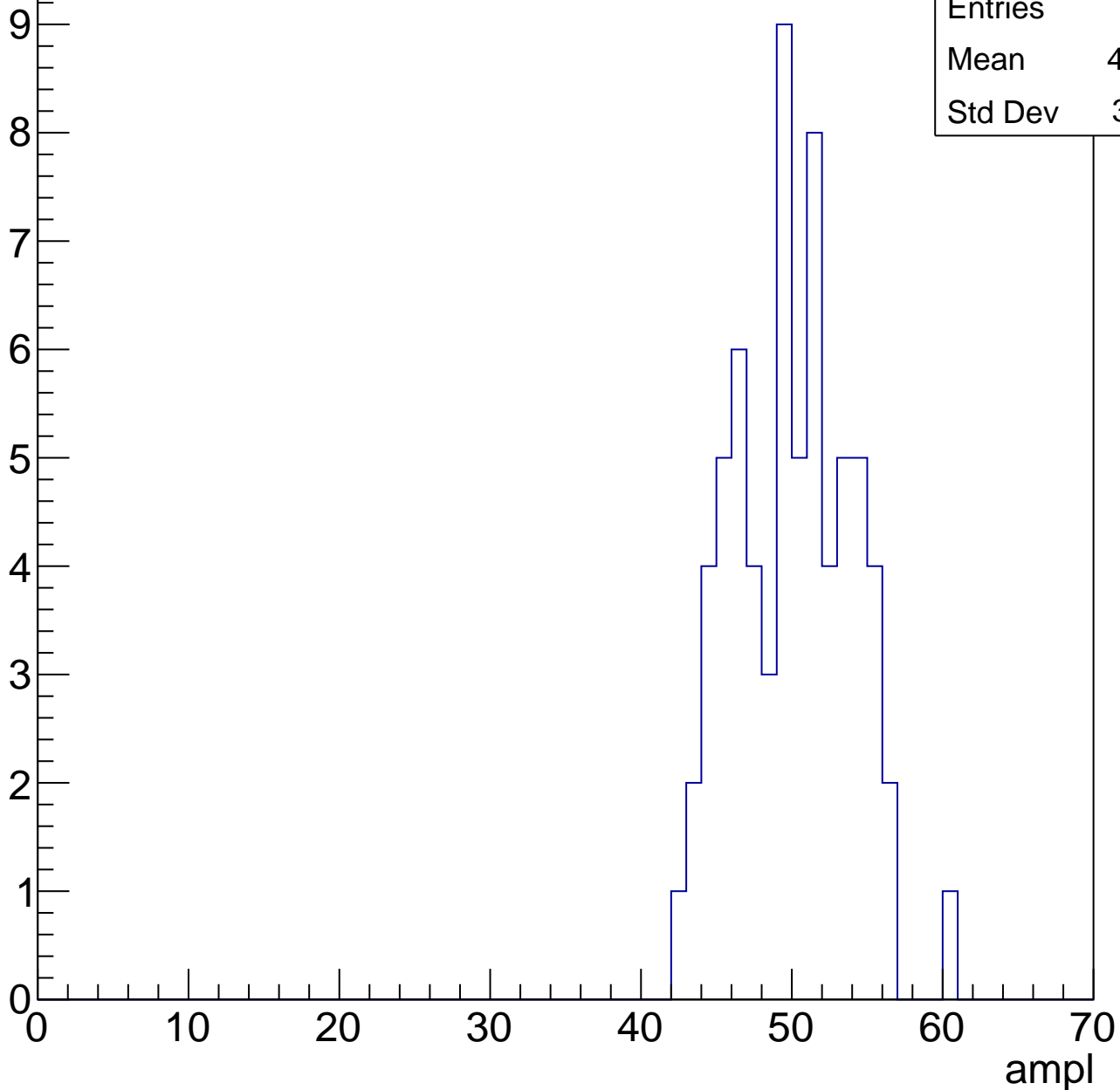


B1L101S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.57
Std Dev	3.821

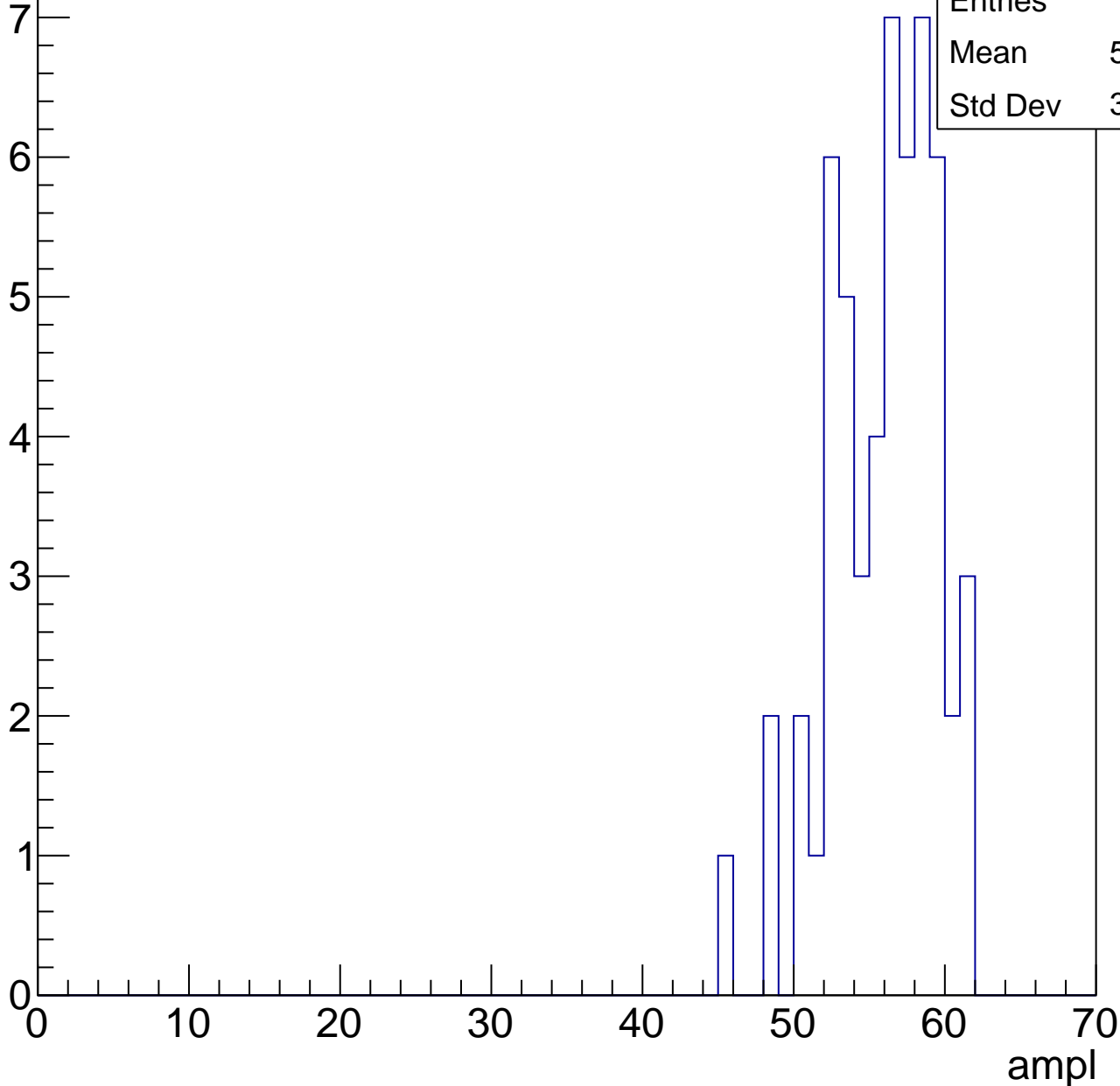


B1L101S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.42
Std Dev	3.515

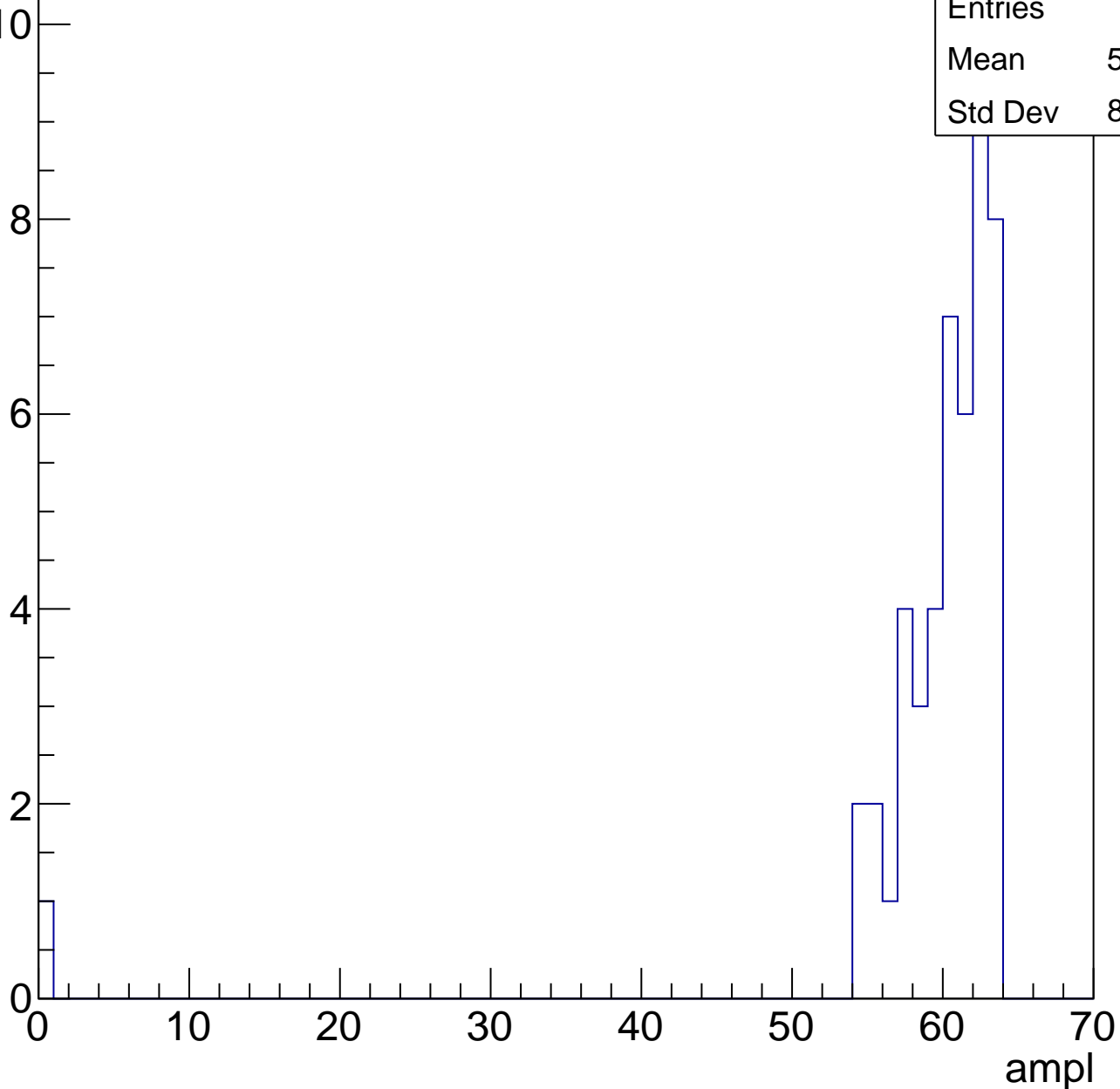


B1L101S, U14-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.79
Std Dev	8.942



B1L101S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

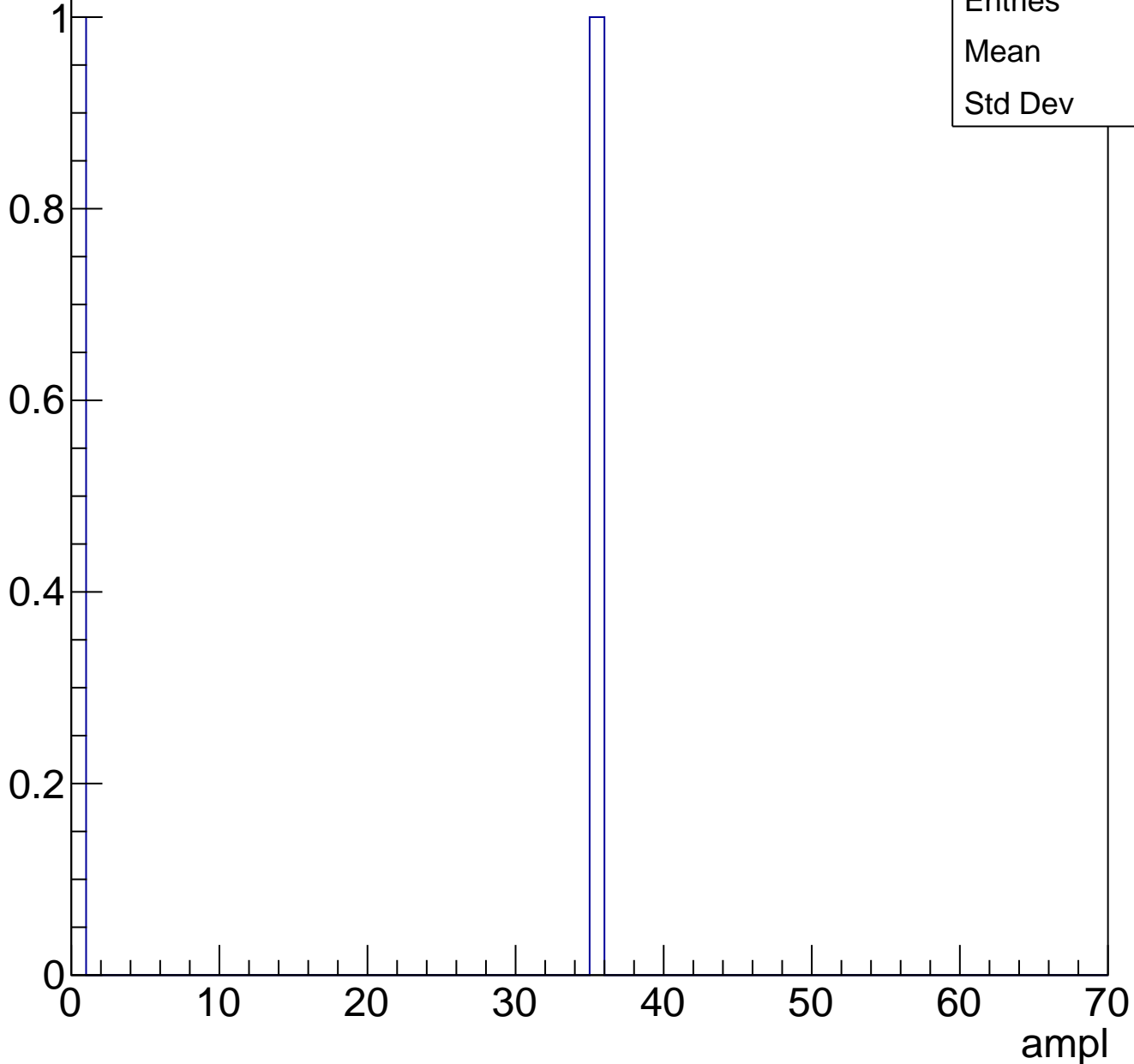
Entry



B1L101S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch124, adc0

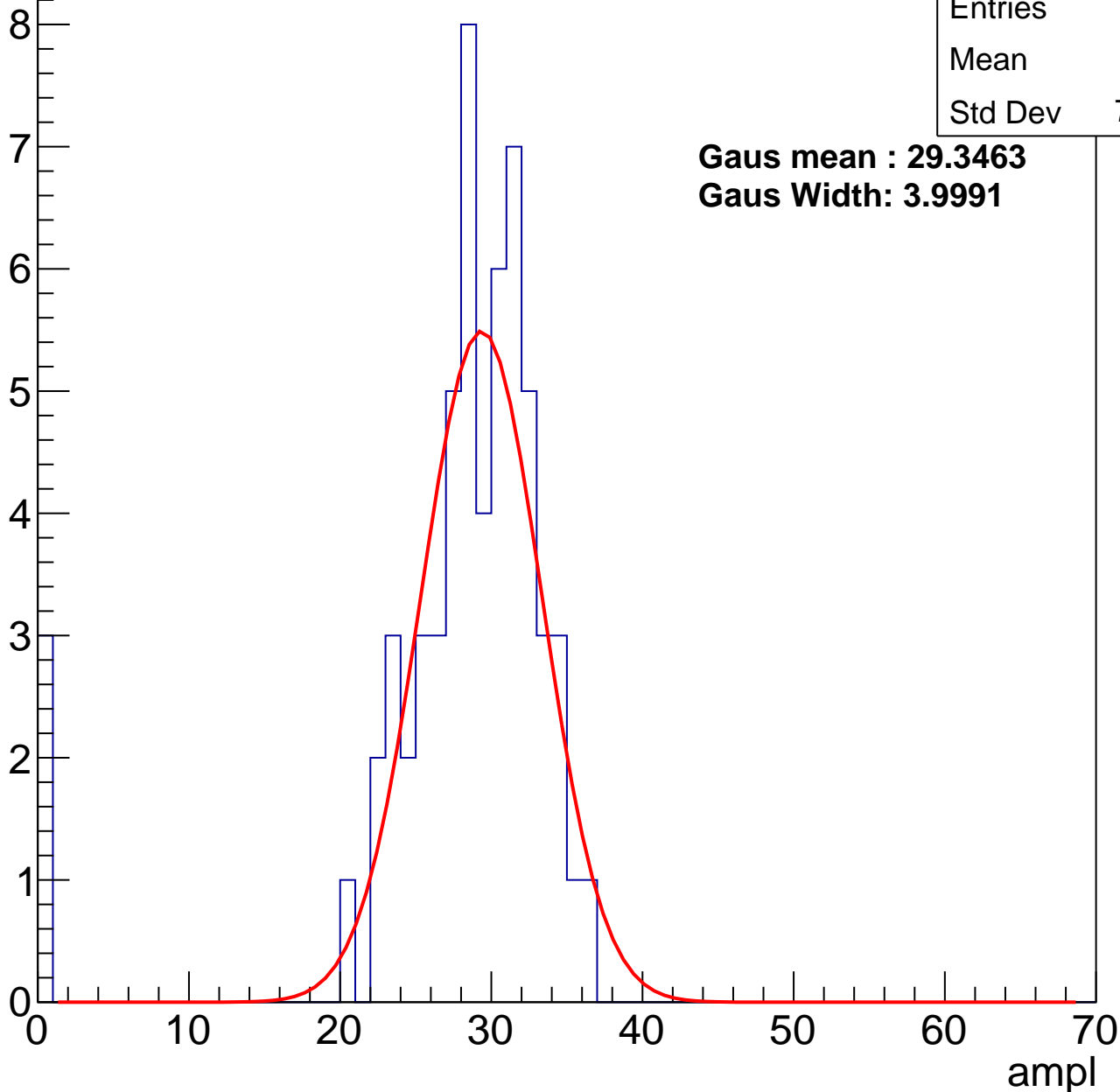
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	27.3
Std Dev	7.161

Gaus mean : 29.3463

Gaus Width: 3.9991



B1L101S, U14-ch124, adc1

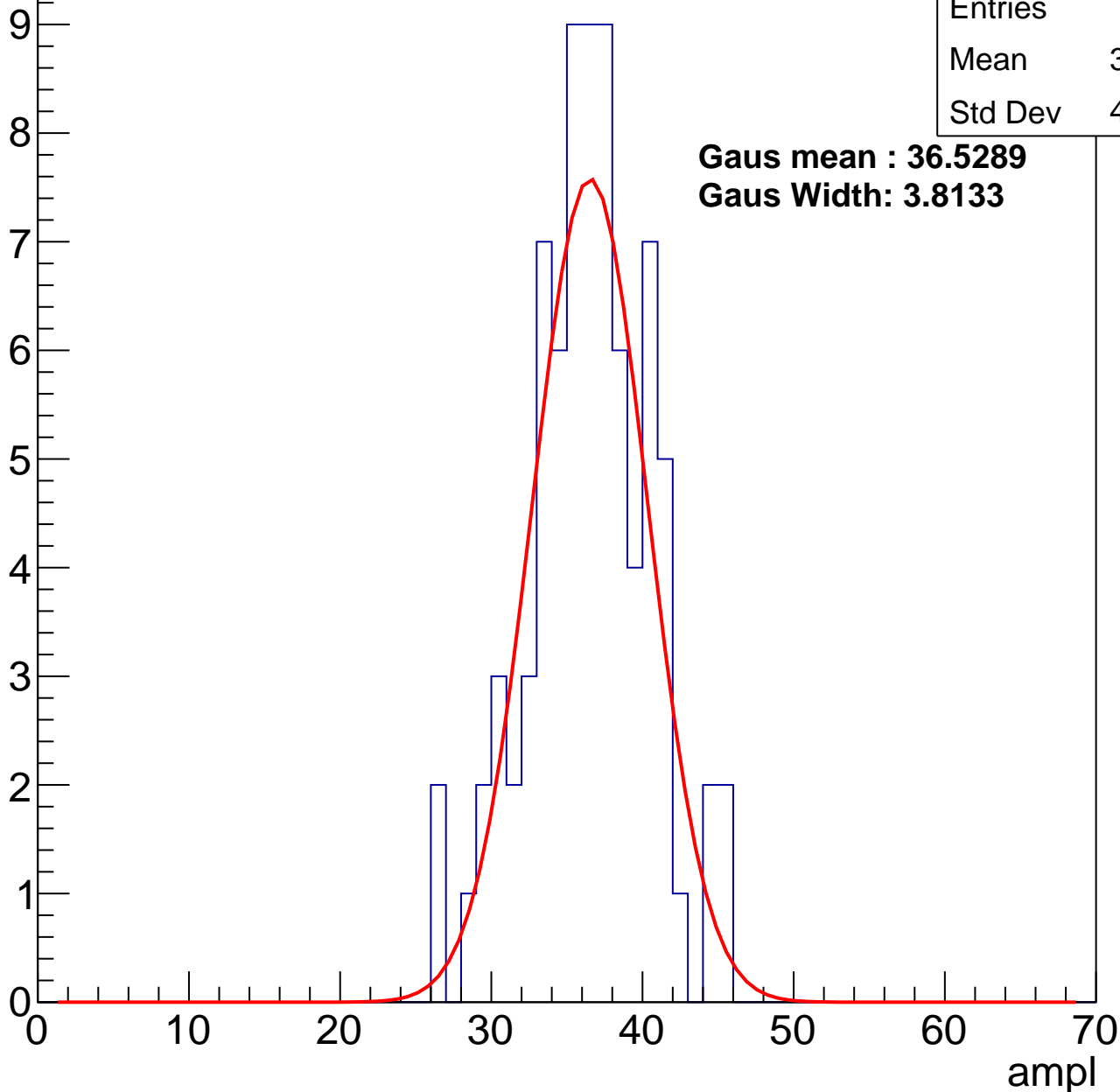
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.02
Std Dev	4.025

Gaus mean : 36.5289

Gaus Width: 3.8133



B1L101S, U14-ch124, adc2

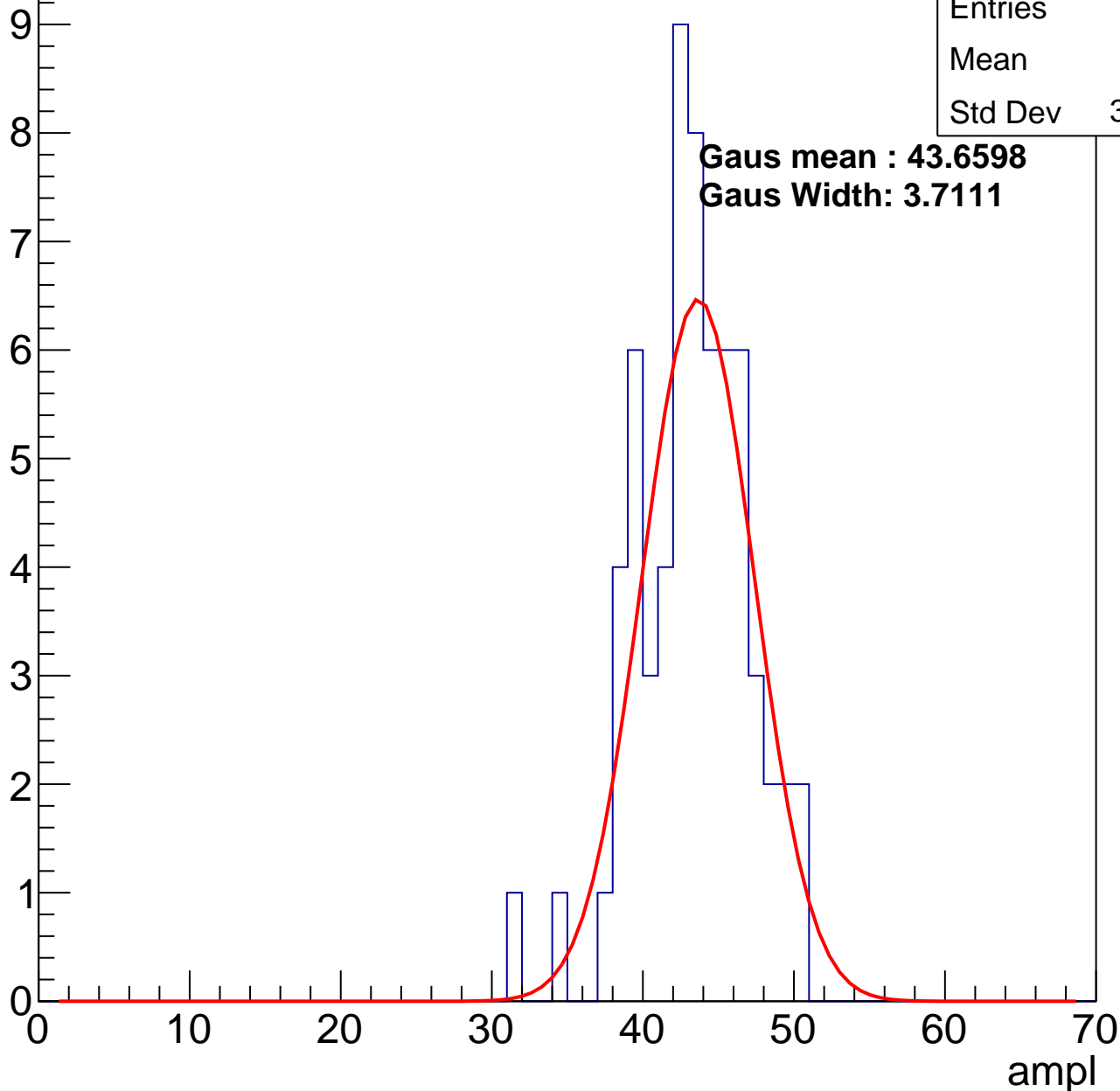
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.8
Std Dev	3.662

Gaus mean : 43.6598

Gaus Width: 3.7111

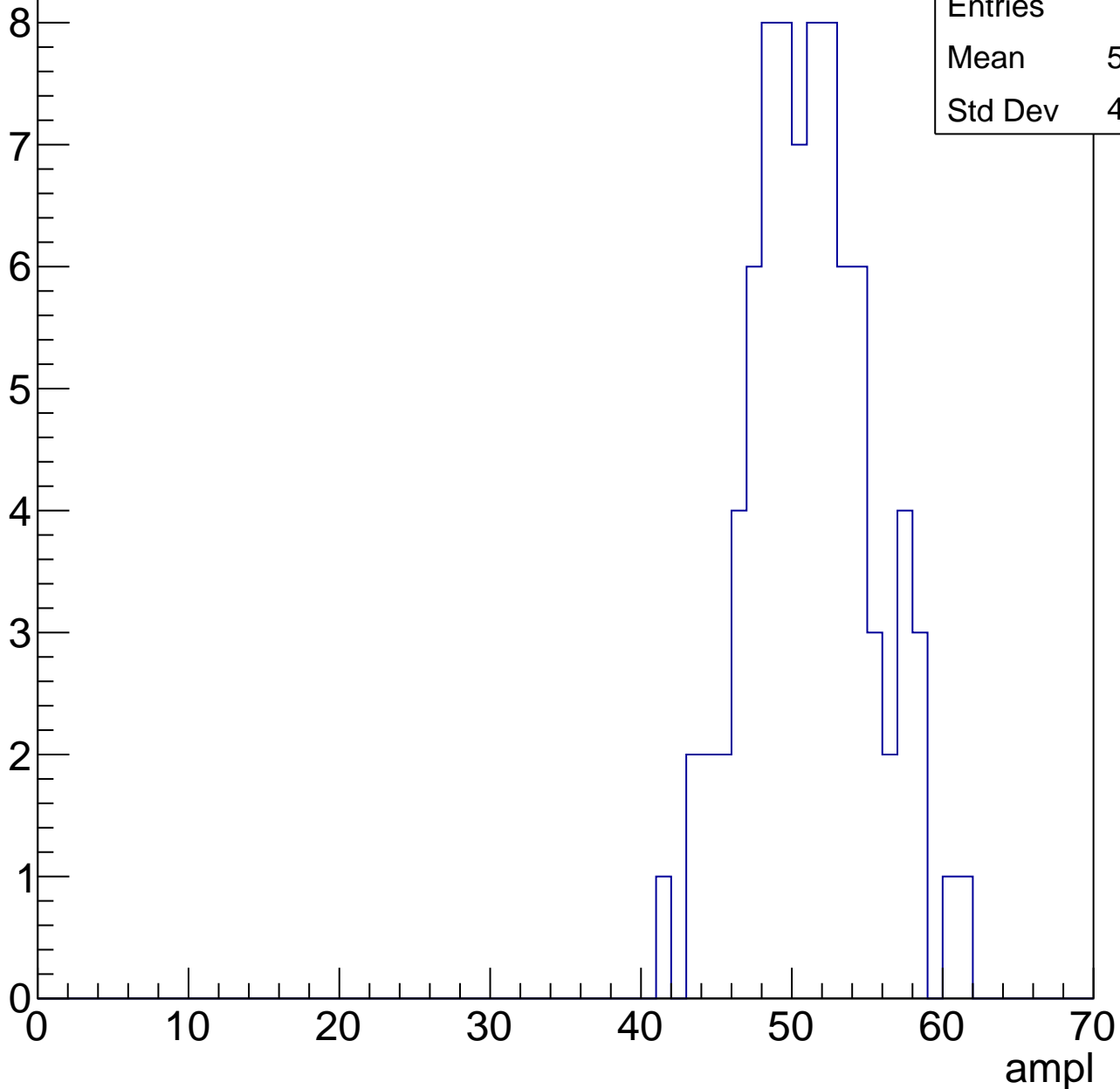


B1L101S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	50.77
Std Dev	4.067

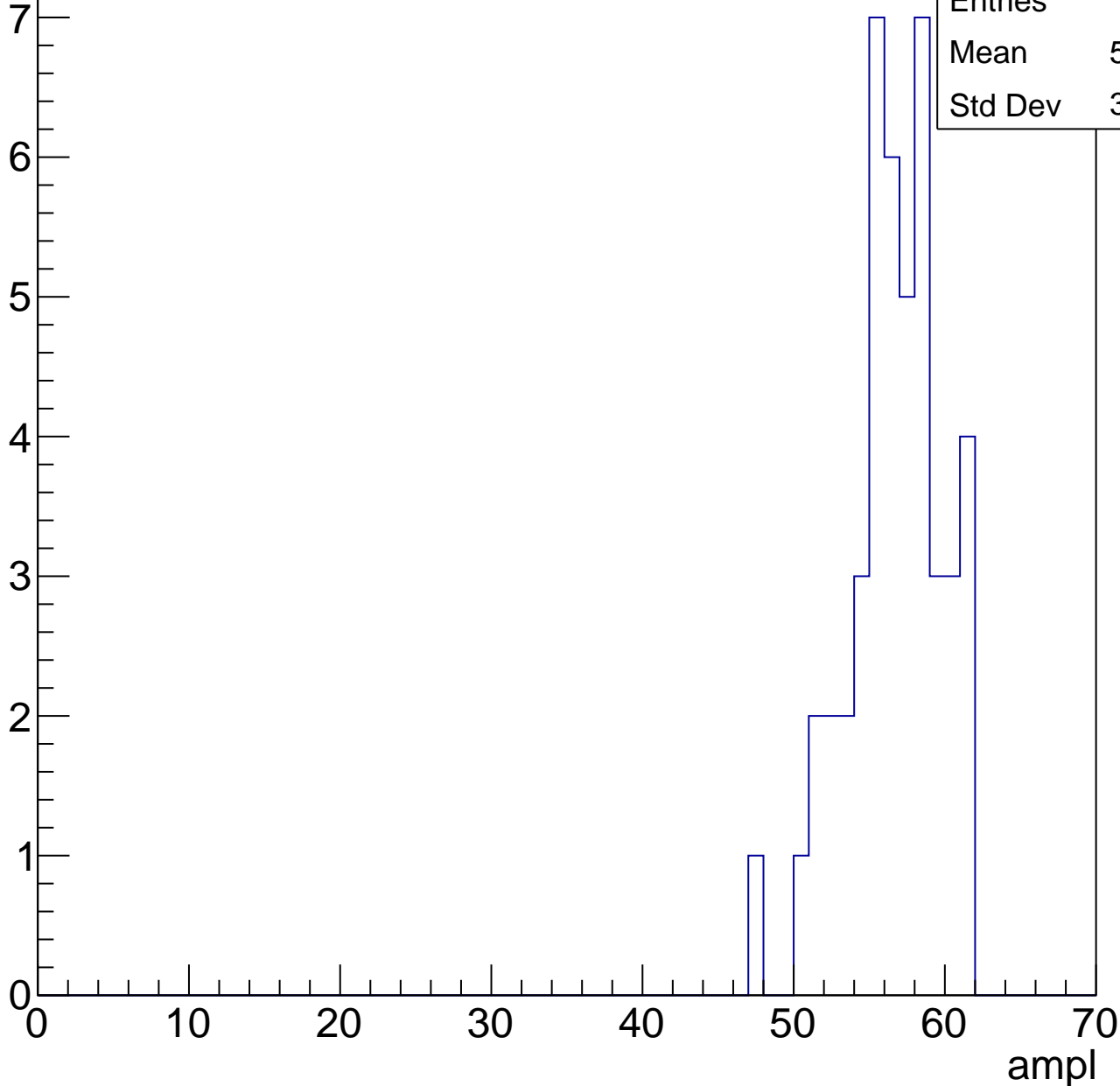


B1L101S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	56.17
Std Dev	3.102

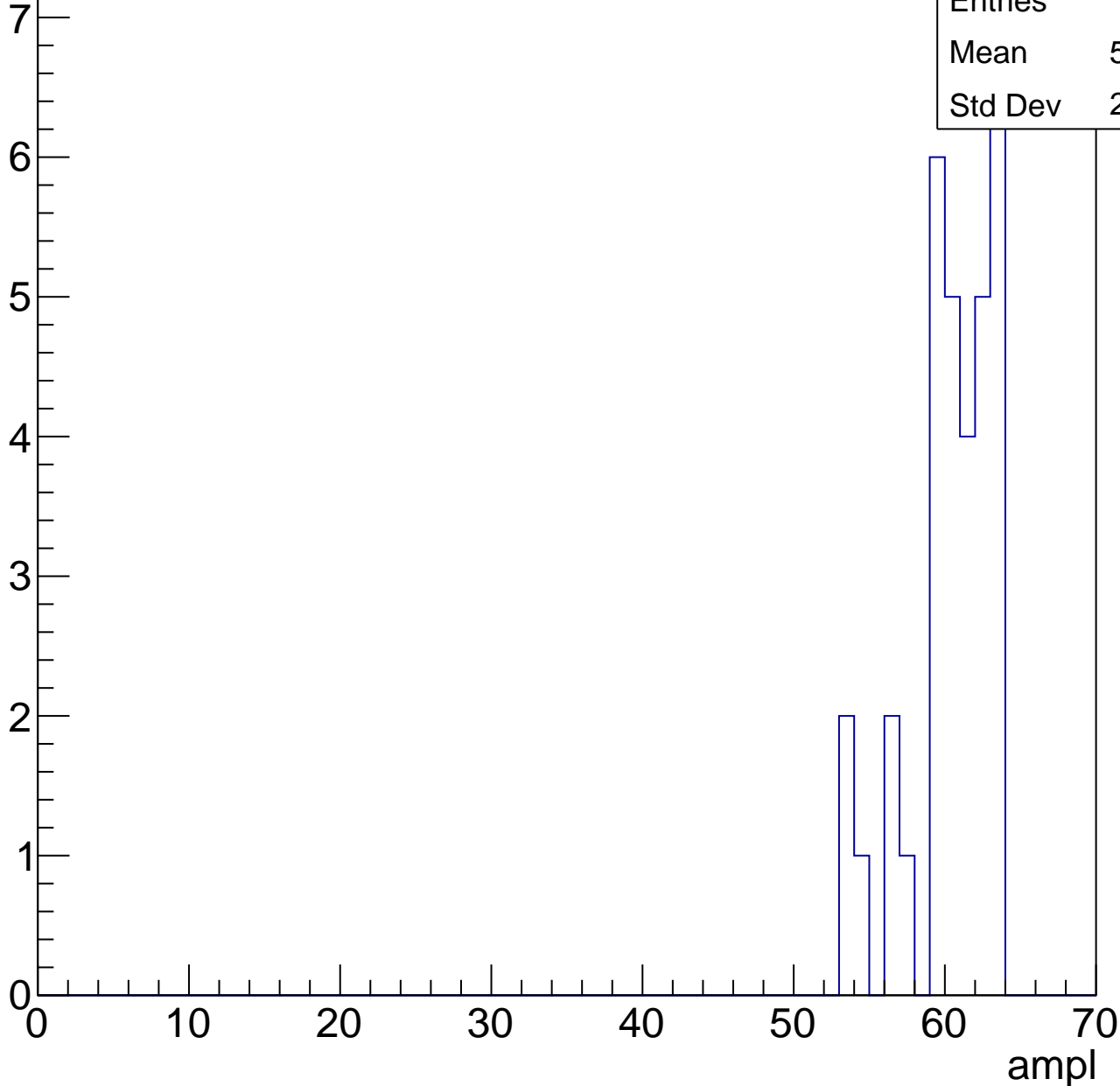


B1L101S, U14-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	59.94
Std Dev	2.849

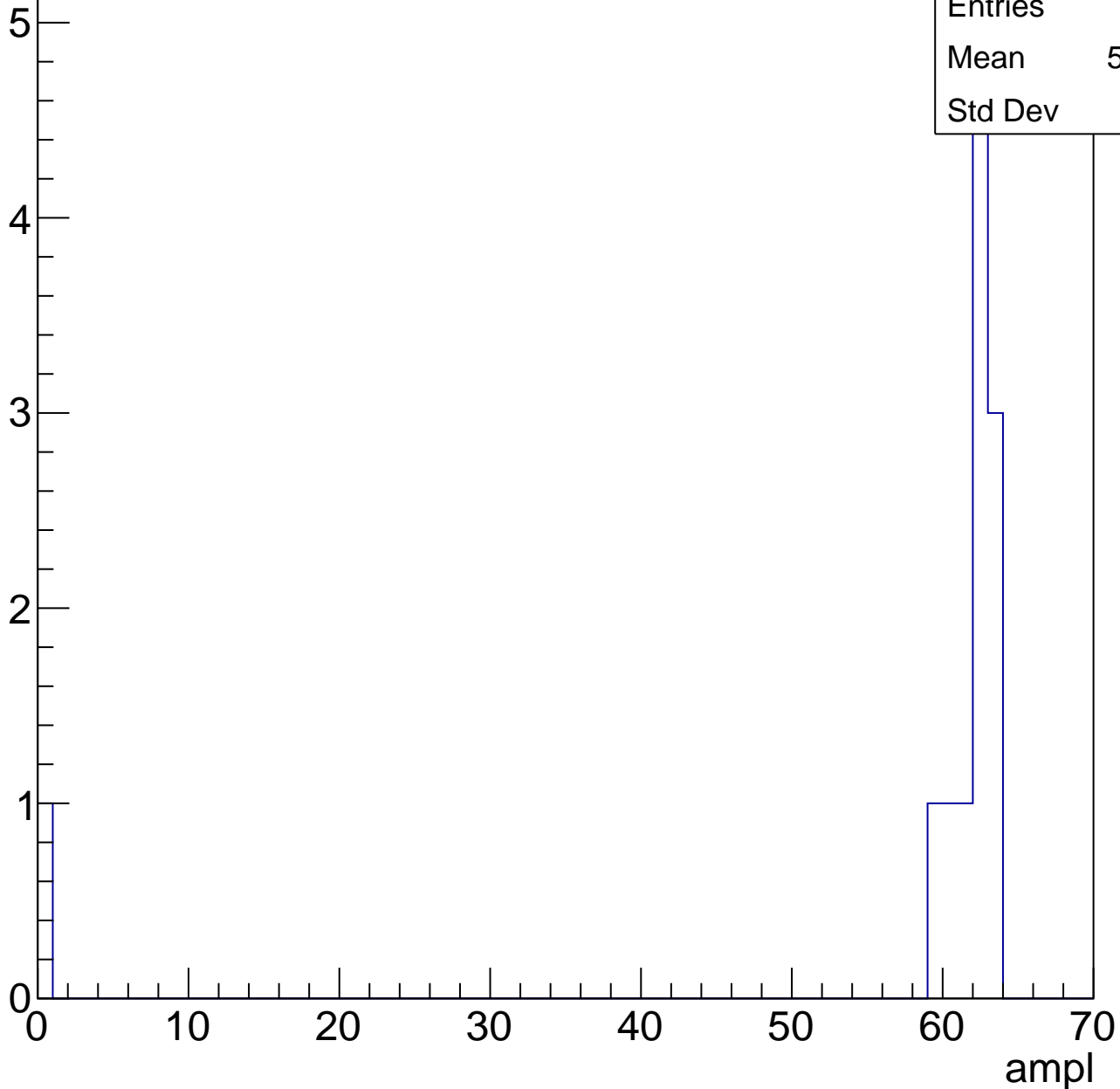


B1L101S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	56.58
Std Dev	17.1



B1L101S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U14-ch125, adc0

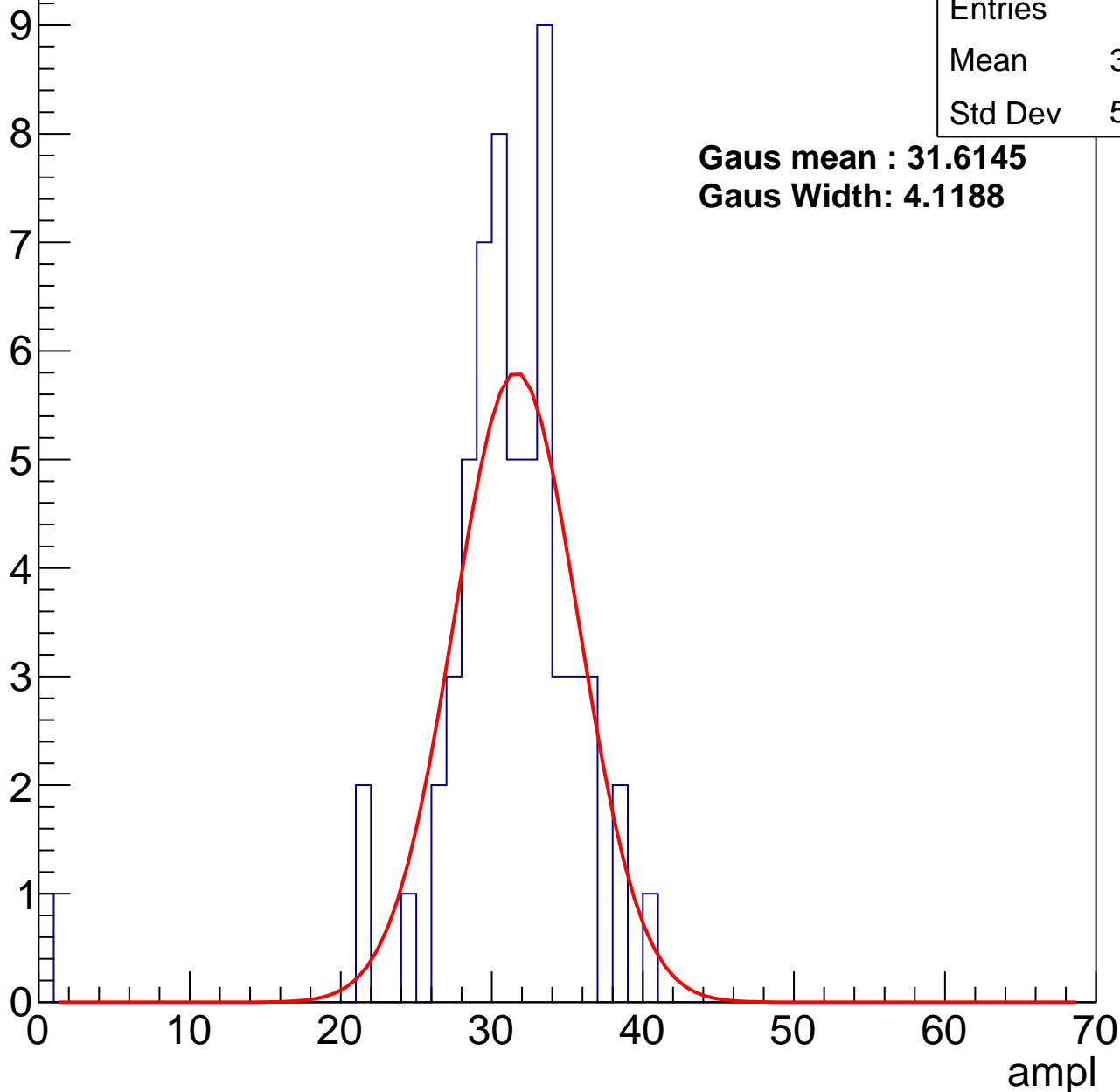
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.42
Std Dev	5.383

Gaus mean : 31.6145

Gaus Width: 4.1188



B1L101S, U14-ch125, adc1

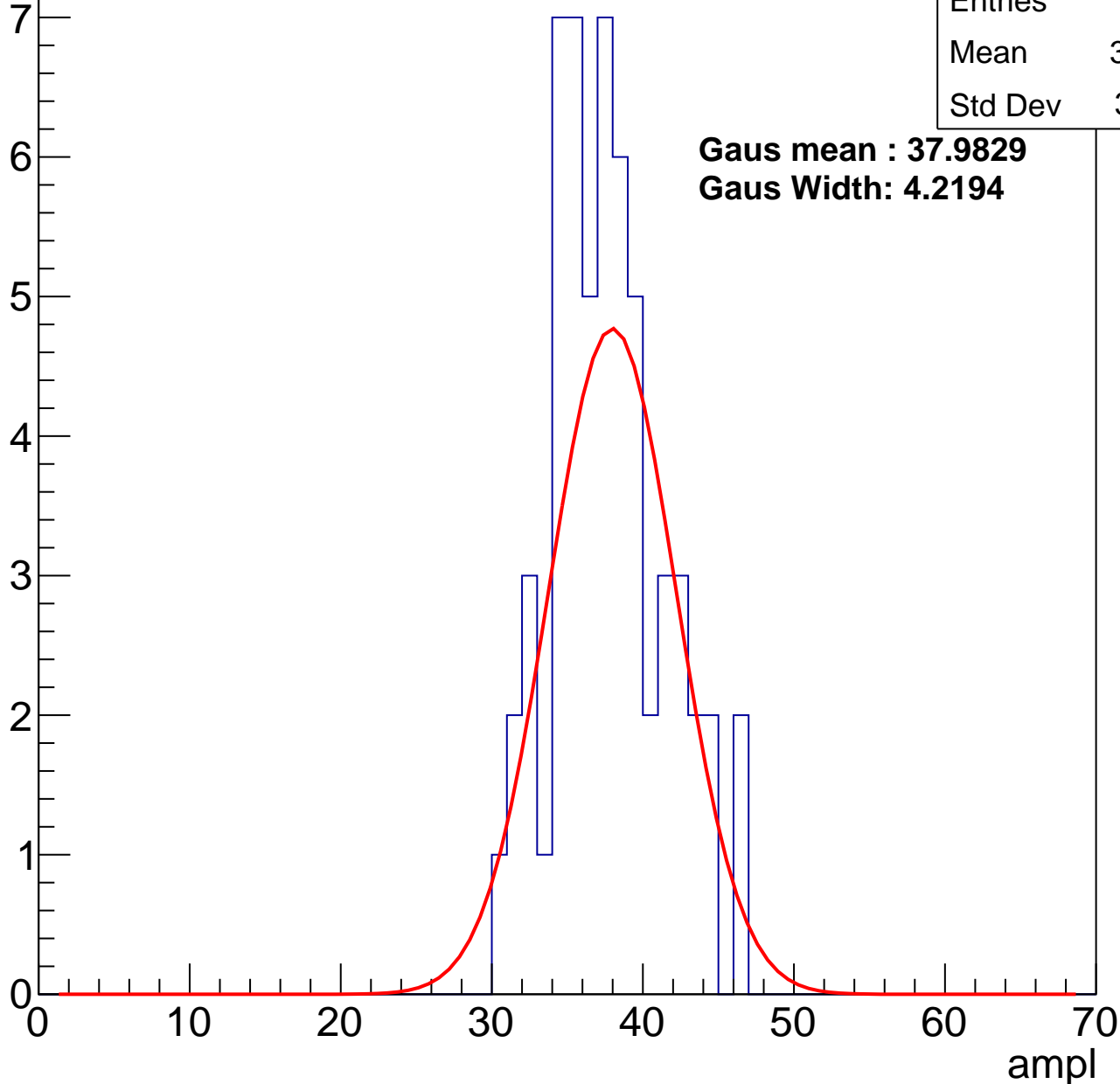
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.26
Std Dev	3.721

Gaus mean : 37.9829

Gaus Width: 4.2194



B1L101S, U14-ch125, adc2

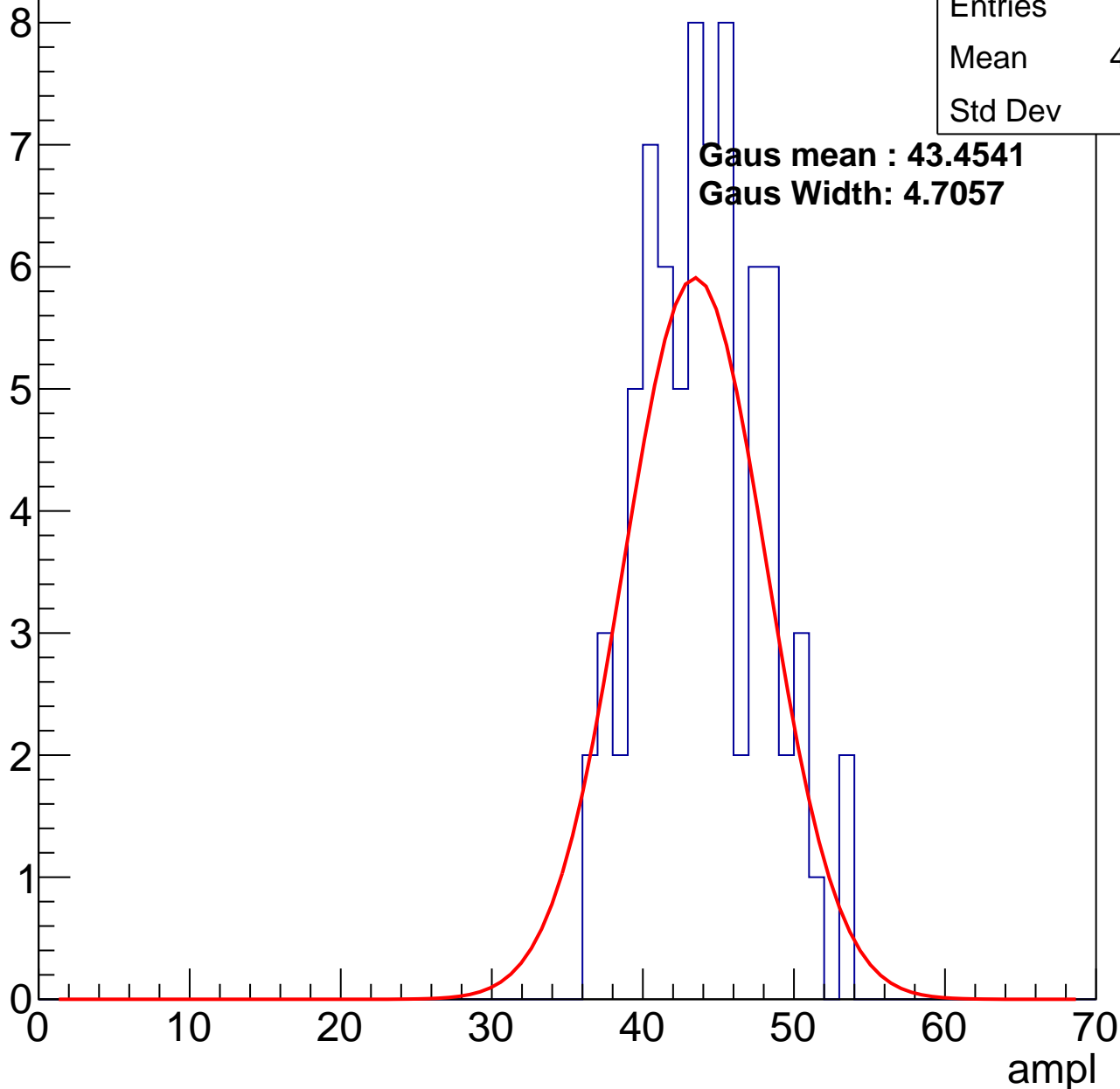
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.59
Std Dev	3.98

Gaus mean : 43.4541

Gaus Width: 4.7057

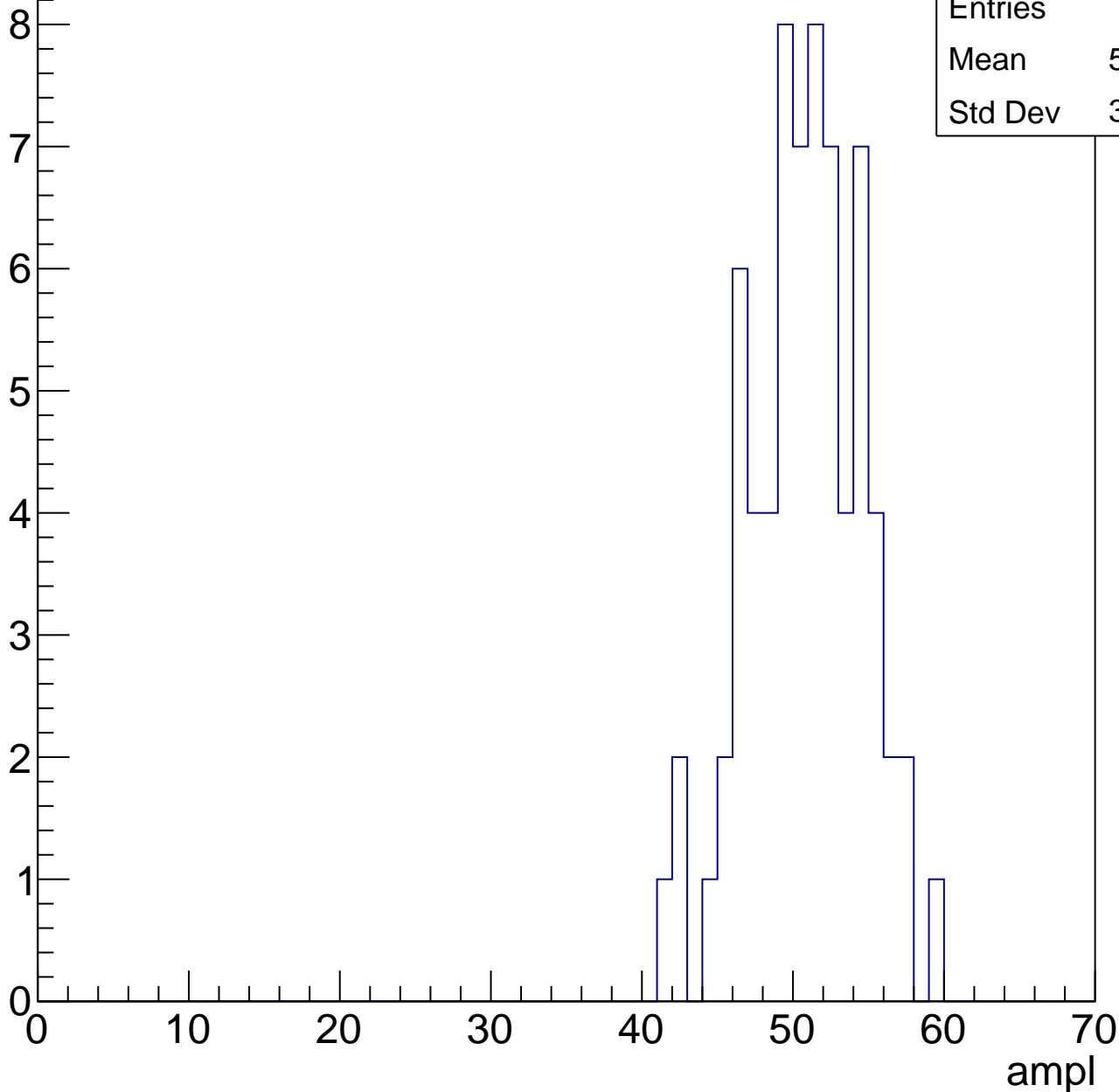


B1L101S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.34
Std Dev	3.737

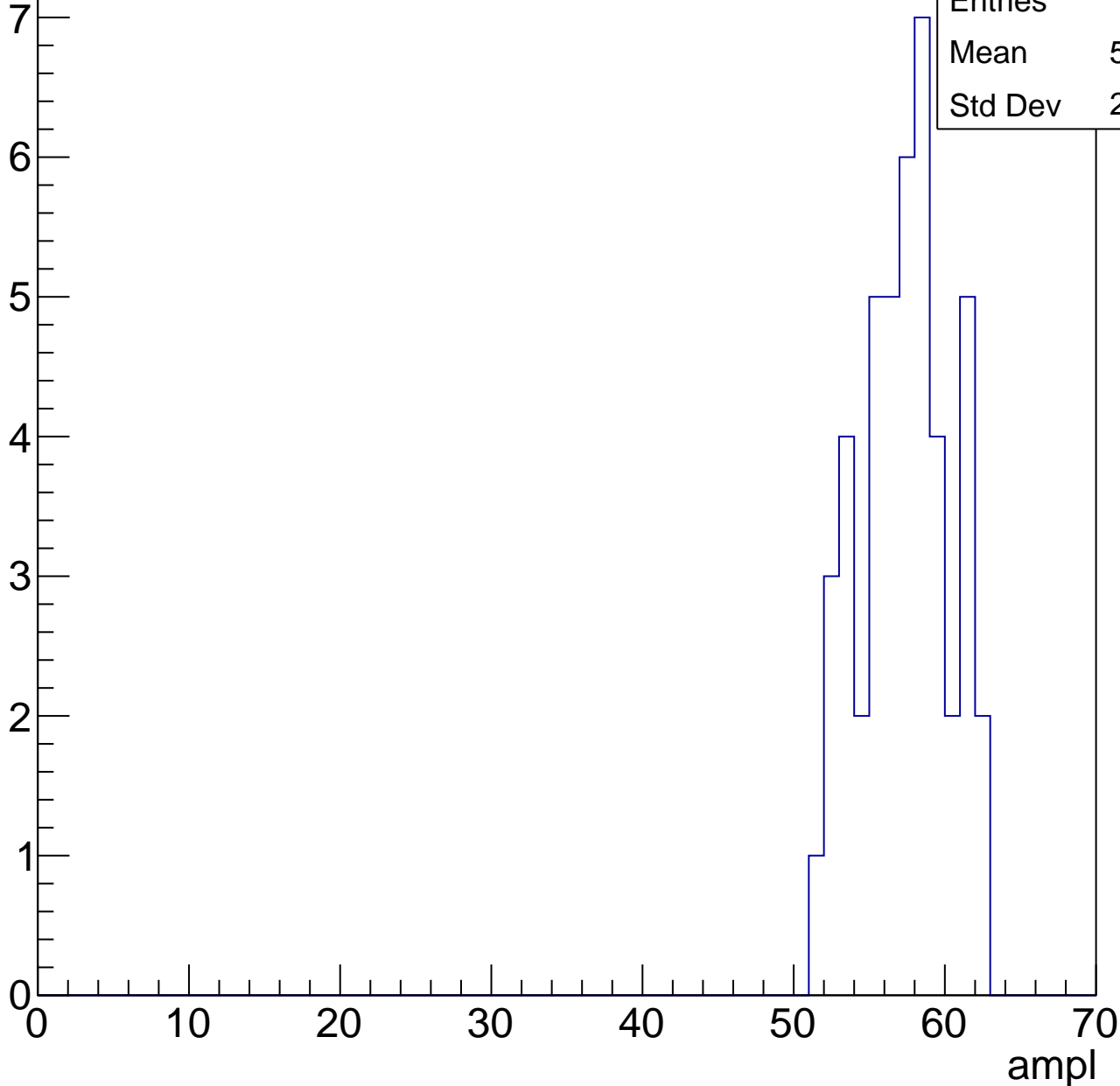


B1L101S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	56.85
Std Dev	2.904

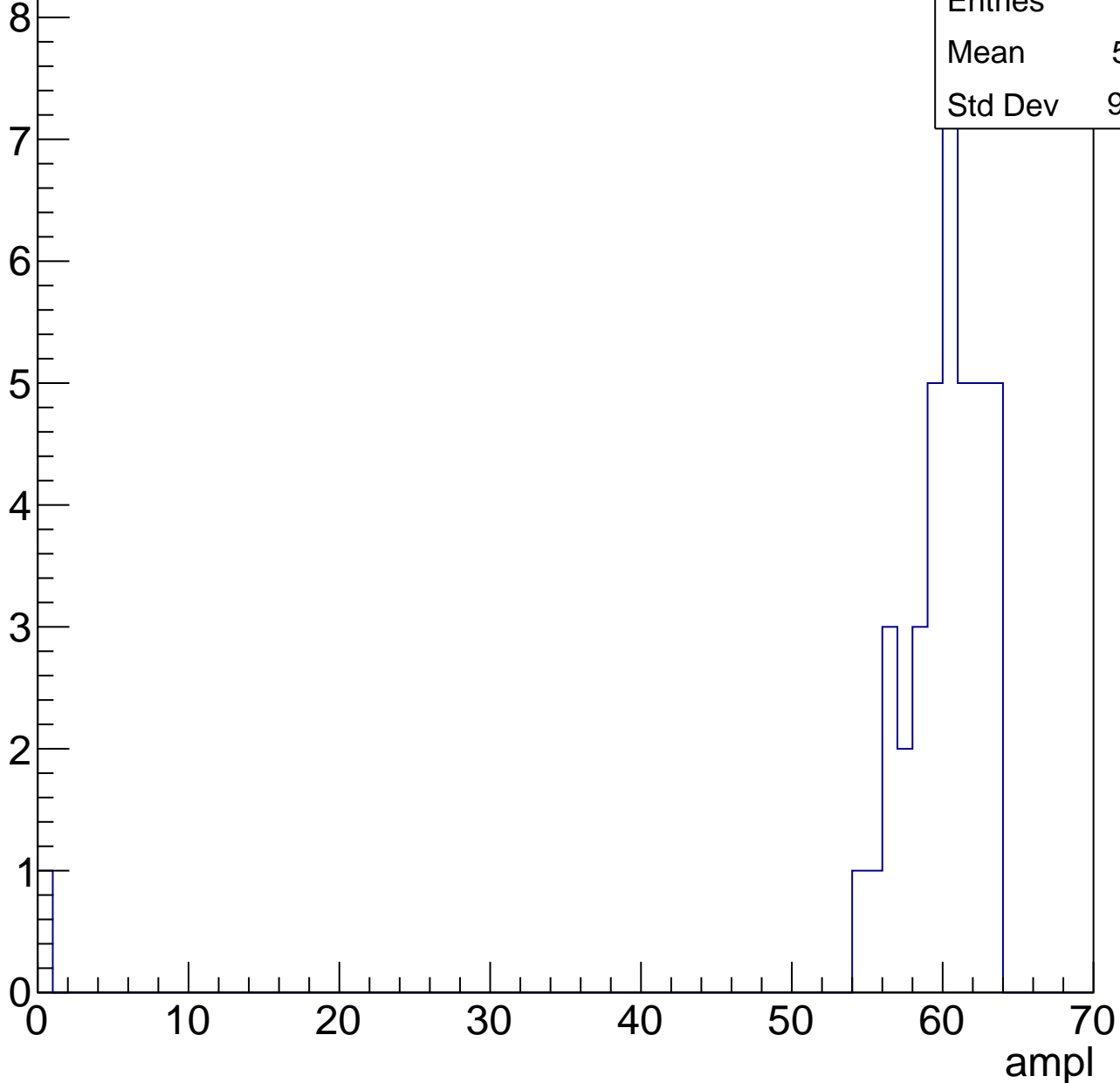


B1L101S, U14-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.21
Std Dev	9.725

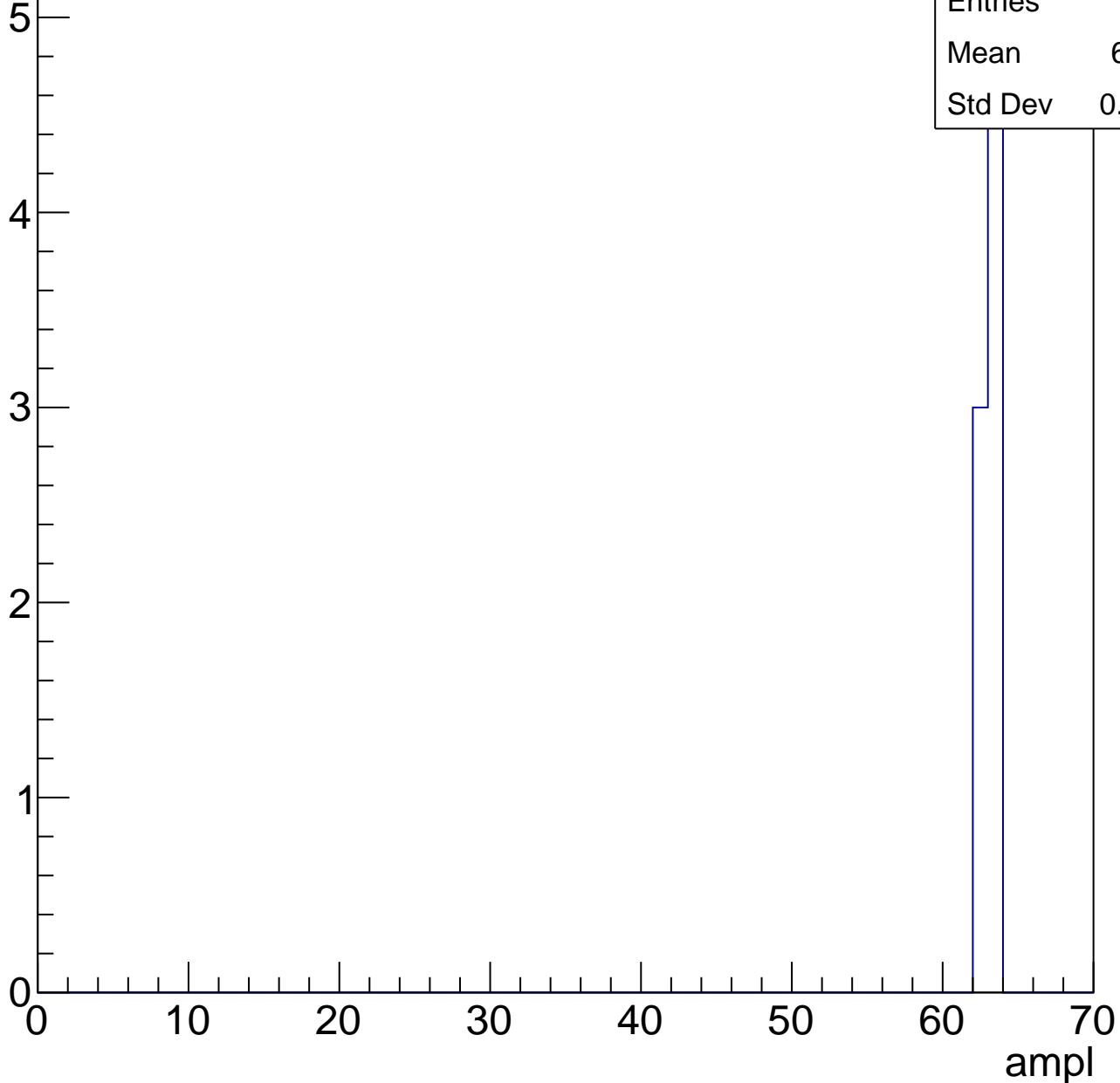


B1L101S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.62
Std Dev	0.4841



B1L101S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U14-ch126, adc0

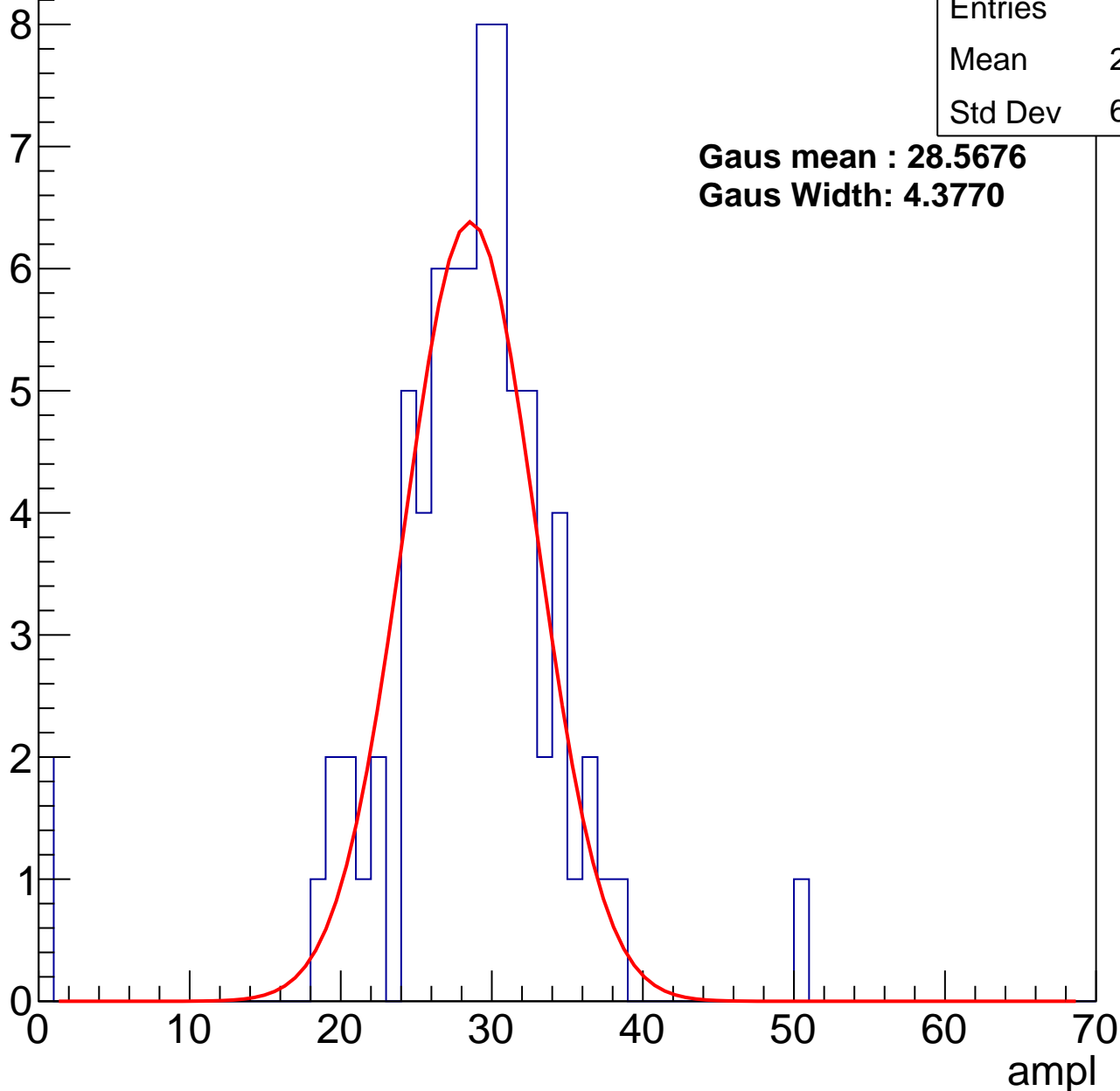
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	27.84
Std Dev	6.757

Gaus mean : 28.5676

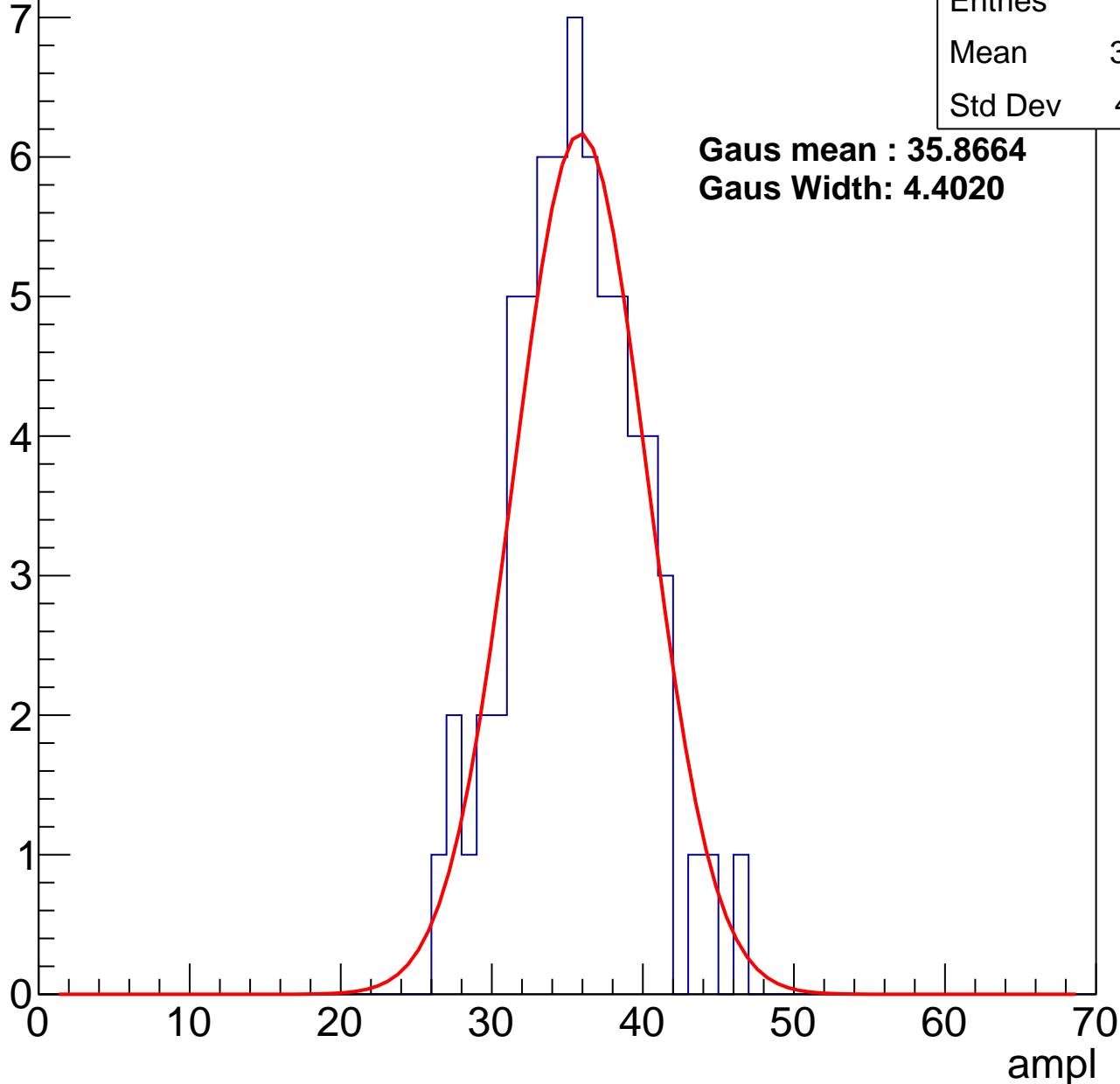
Gaus Width: 4.3770



B1L101S, U14-ch126, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	67
Mean	35.09
Std Dev	4.131

B1L101S, U14-ch126, adc2

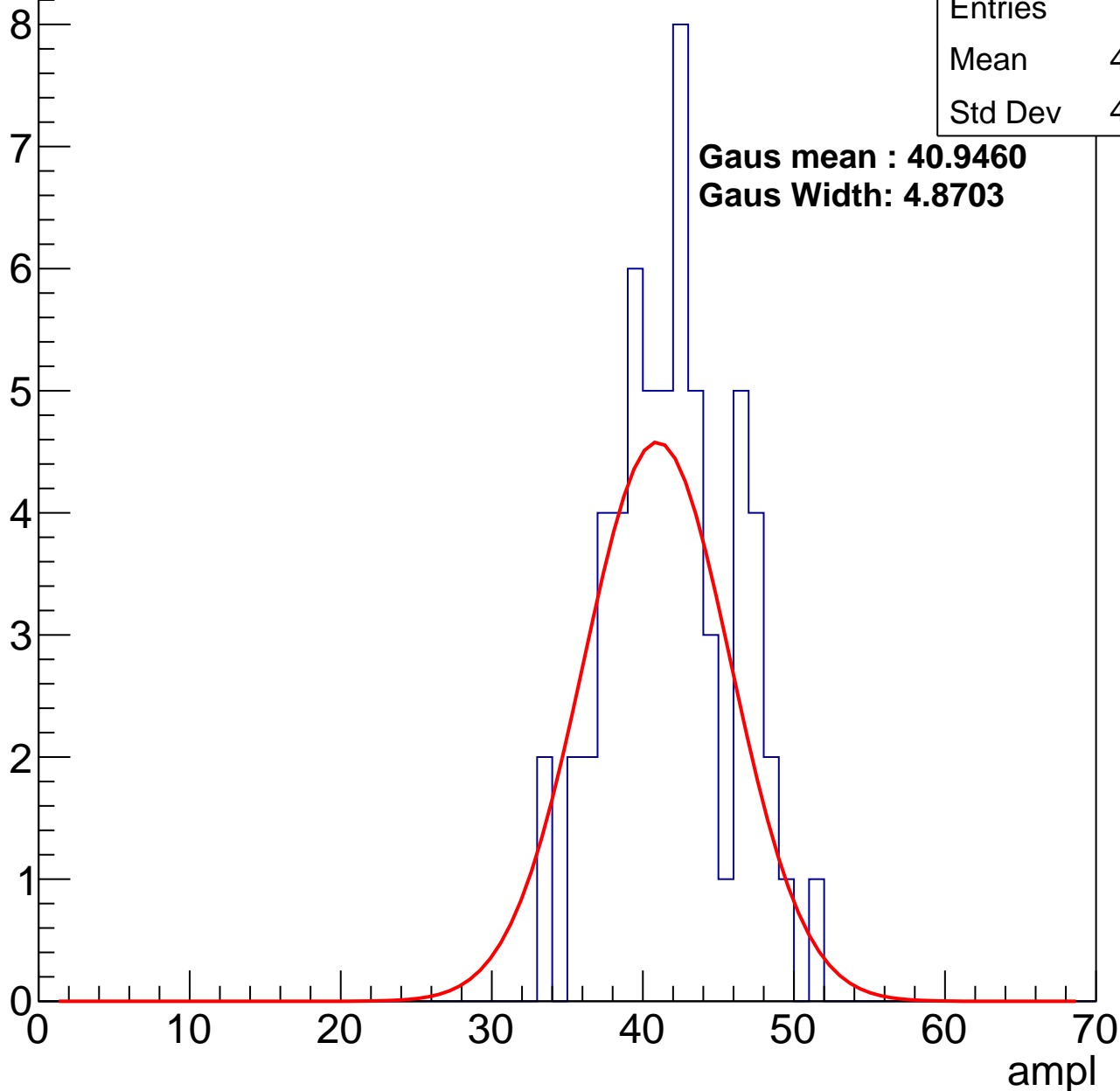
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.48
Std Dev	4.015

Gaus mean : 40.9460

Gaus Width: 4.8703

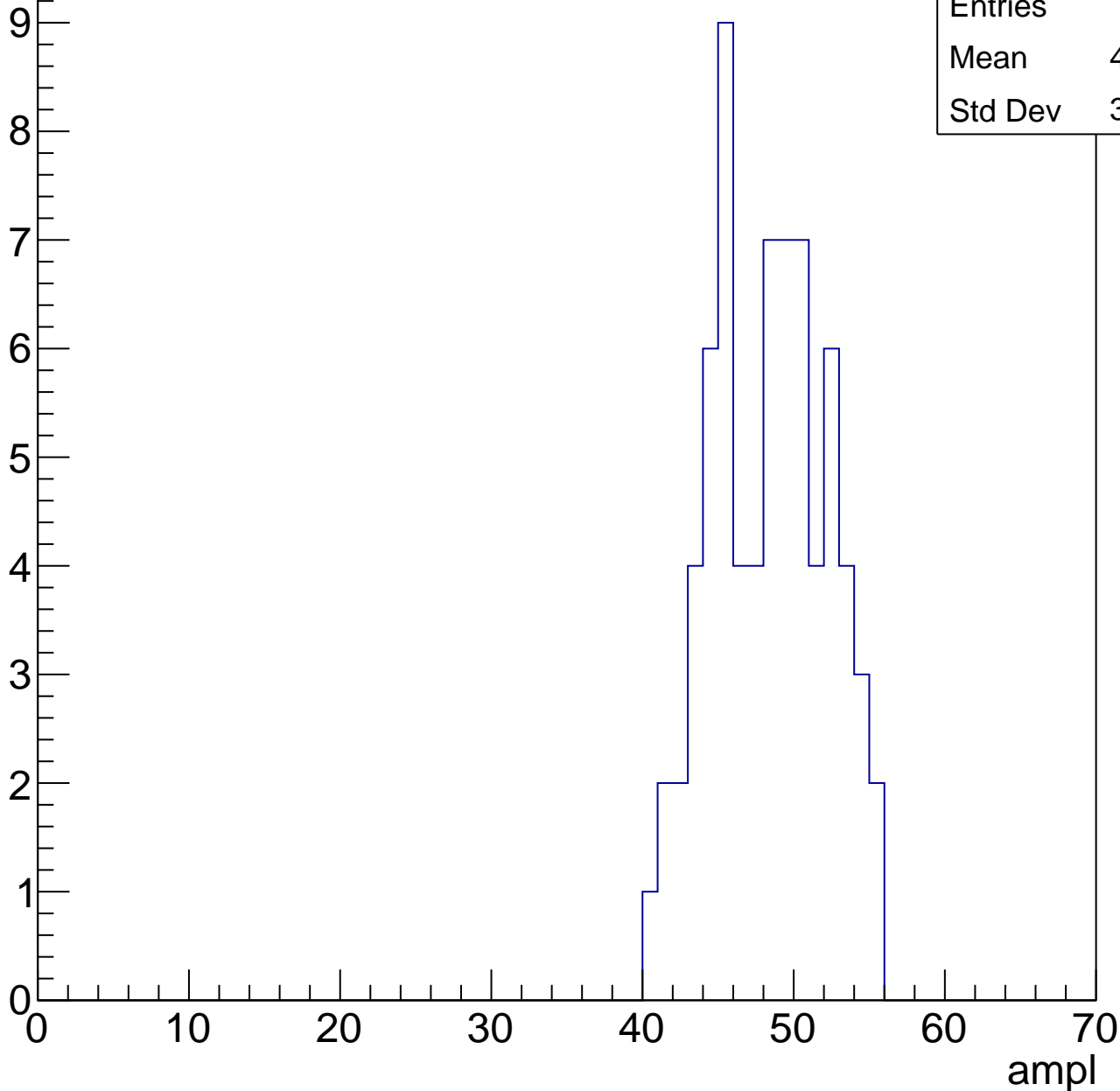


B1L101S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	47.89
Std Dev	3.733

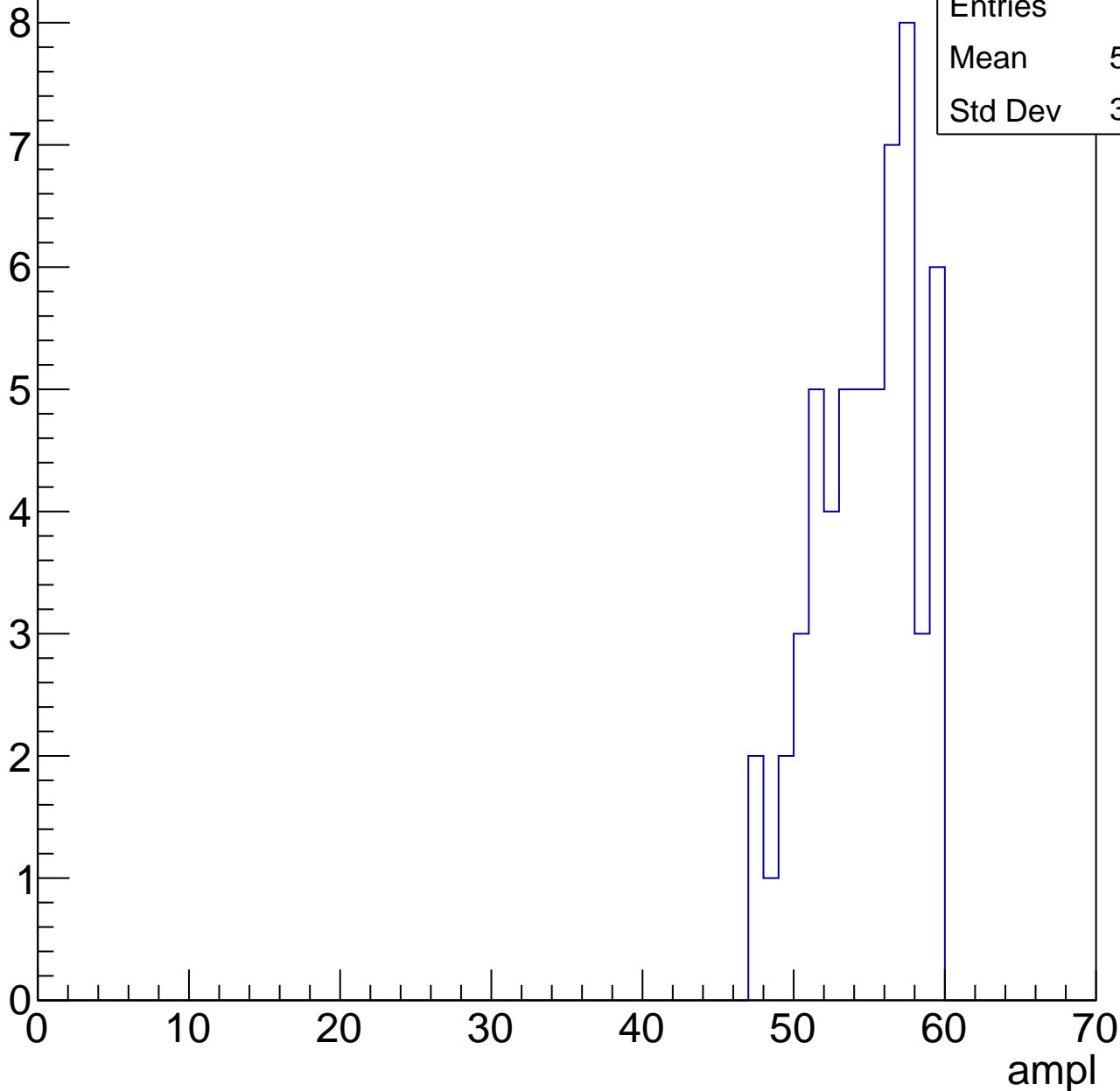


B1L101S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	54.27
Std Dev	3.265

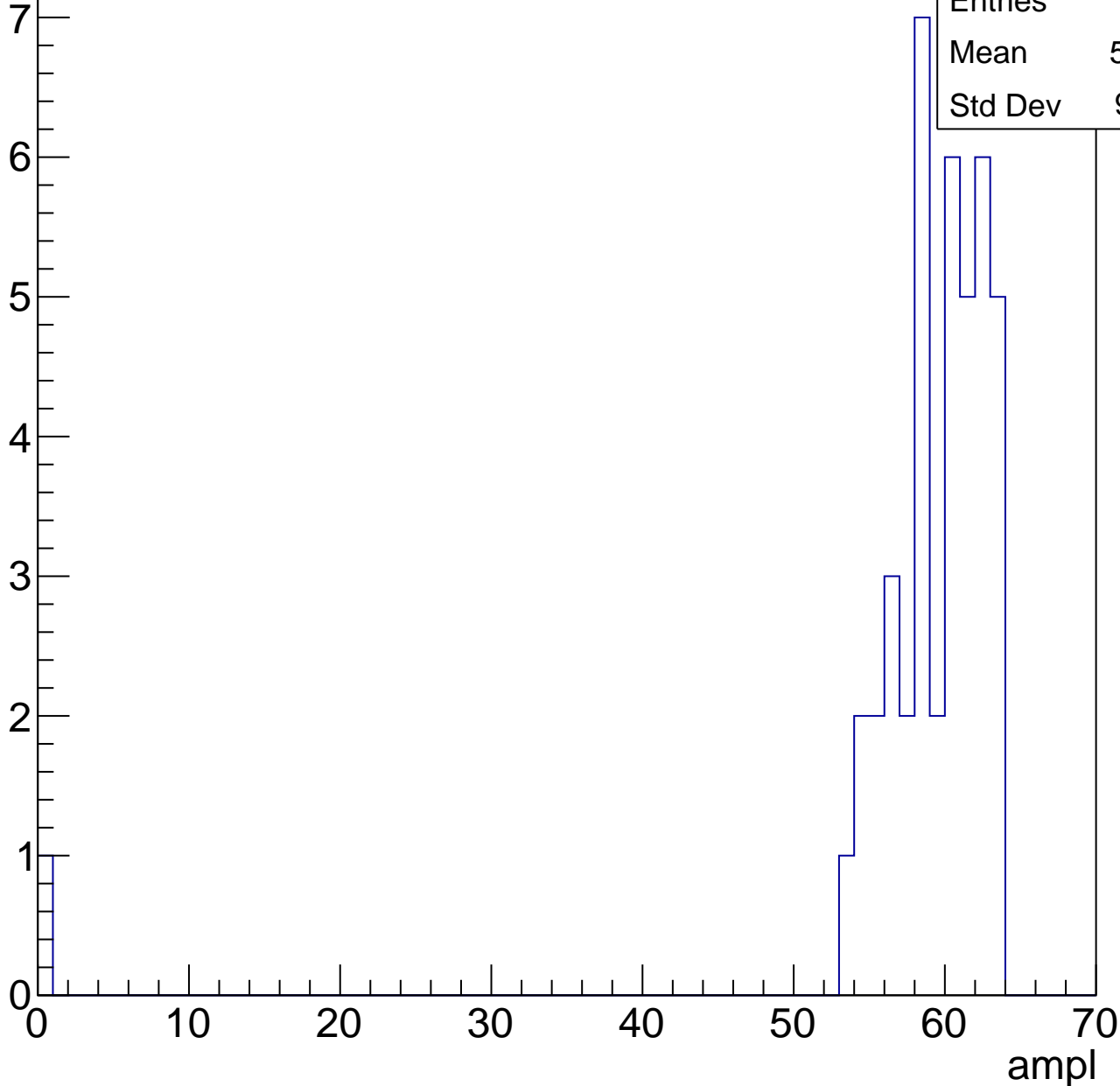


B1L101S, U14-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	57.83
Std Dev	9.441

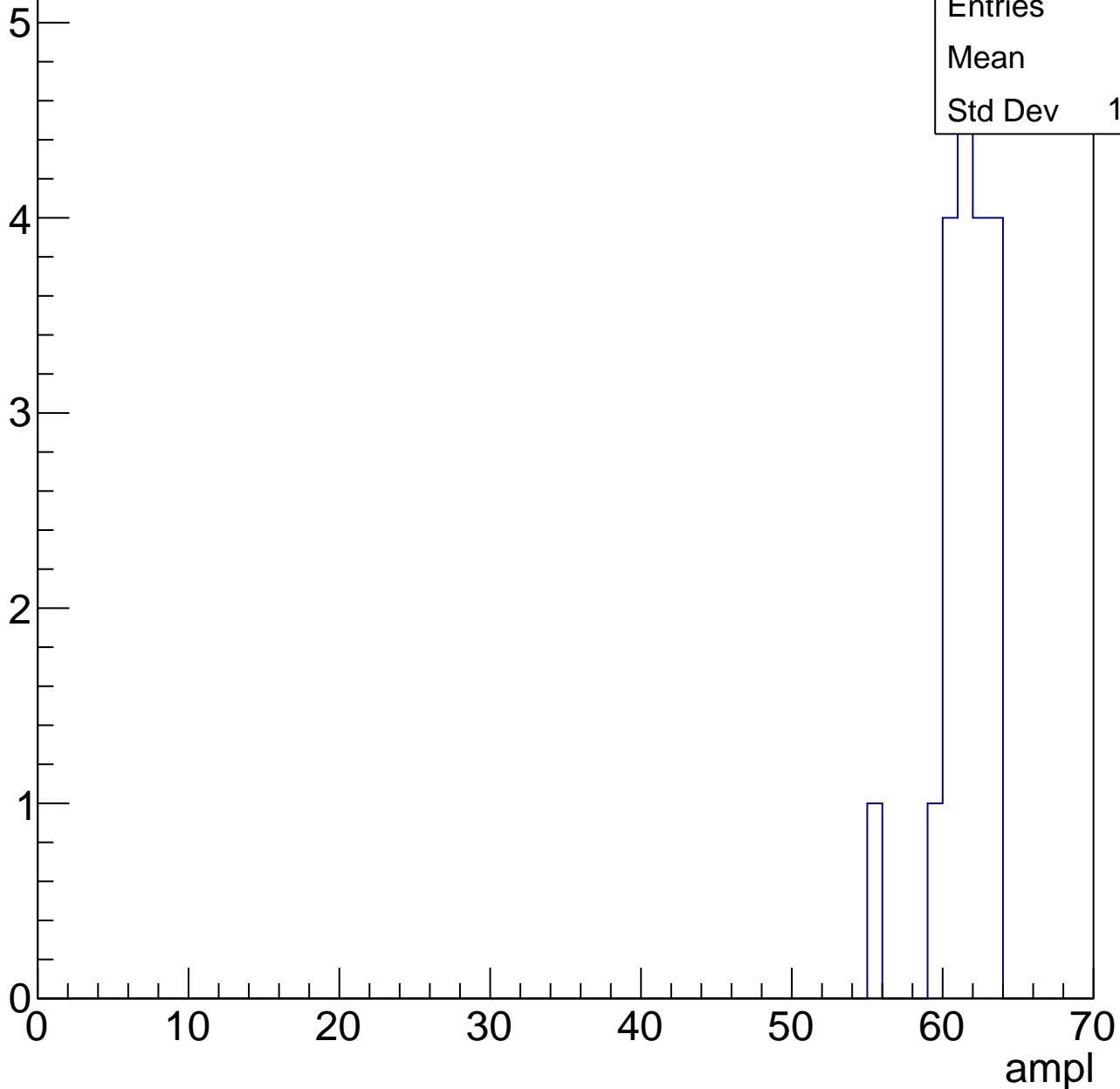


B1L101S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61
Std Dev	1.835



B1L101S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

61

Std Dev

2

B1L101S, U14-ch127, adc0

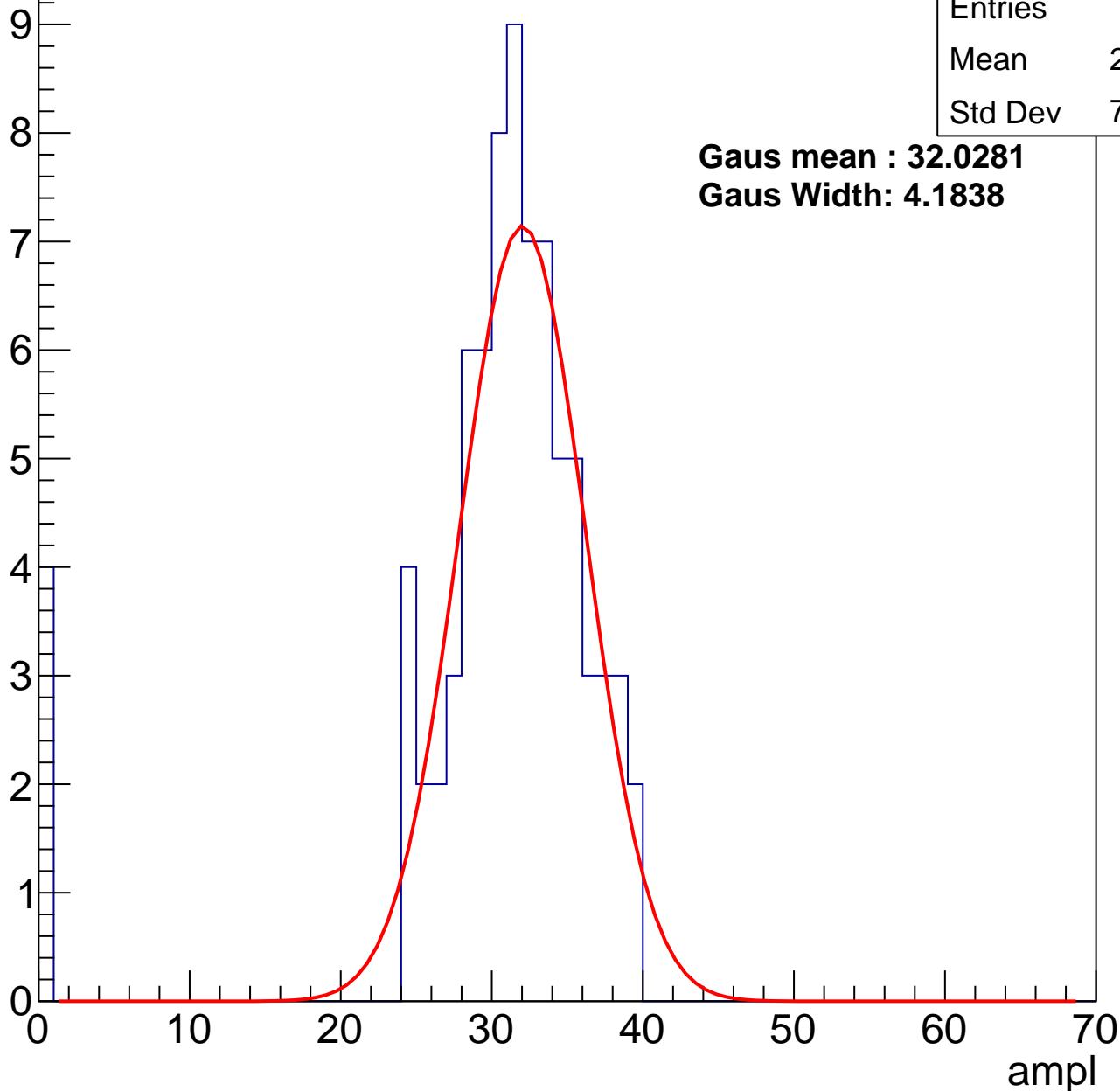
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.76
Std Dev	7.789

Gaus mean : 32.0281

Gaus Width: 4.1838



B1L101S, U14-ch127, adc1

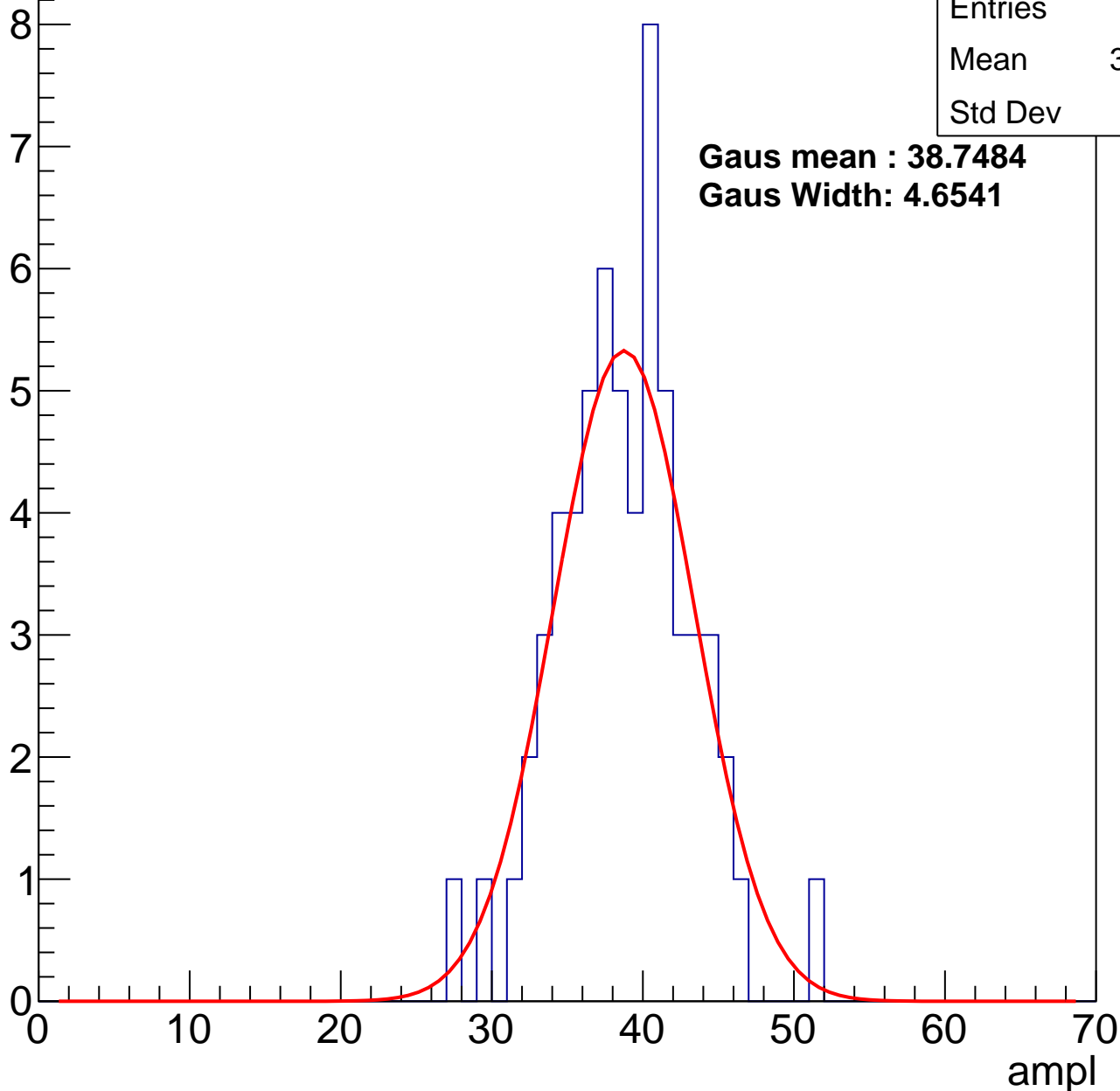
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	38.27
Std Dev	4.33

Gaus mean : 38.7484

Gaus Width: 4.6541



B1L101S, U14-ch127, adc2

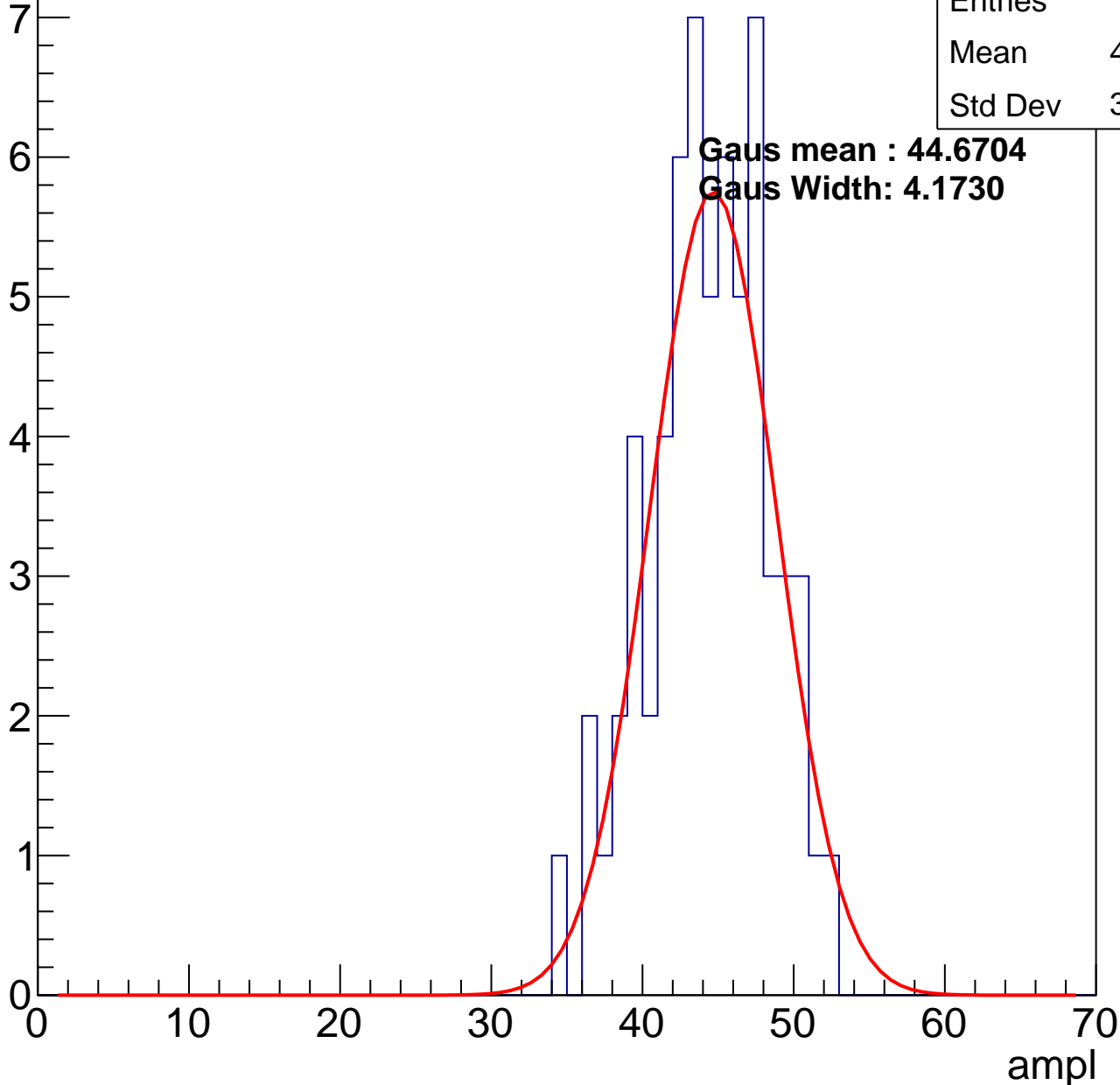
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.89
Std Dev	3.945

Gaus mean : 44.6704

Gaus Width: 4.1730

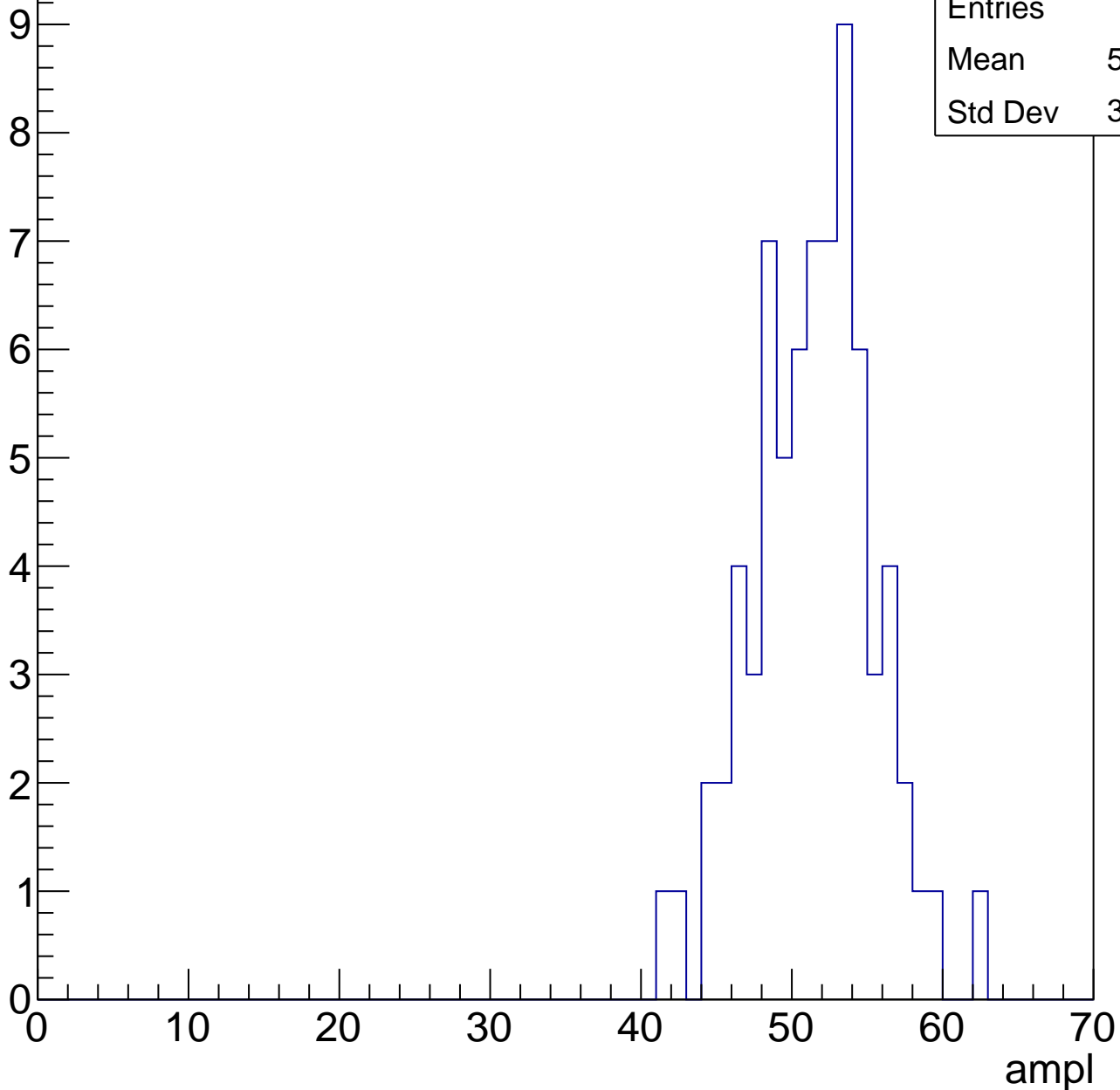


B1L101S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

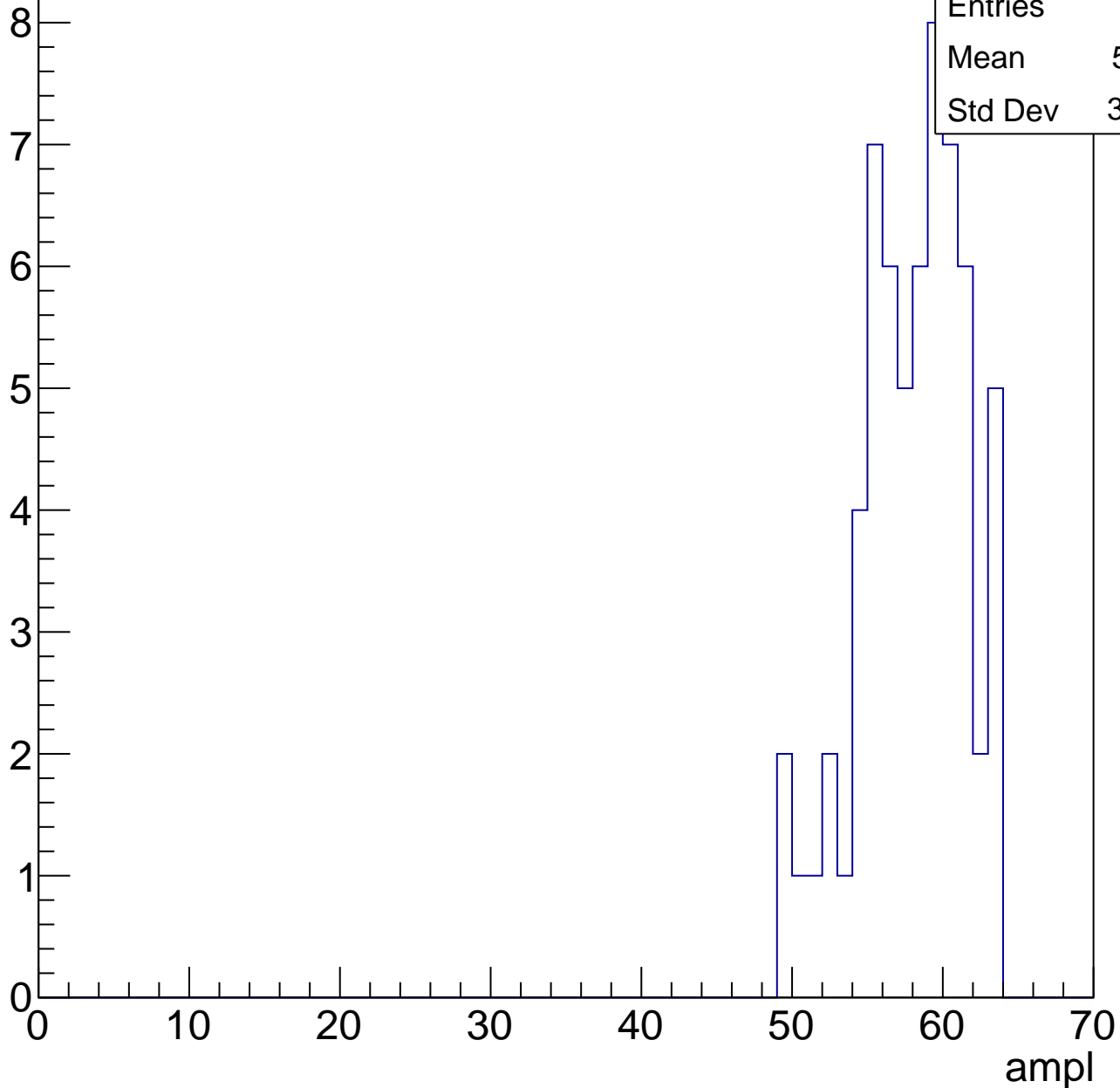
Entries	72
Mean	50.99
Std Dev	3.977



B1L101S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



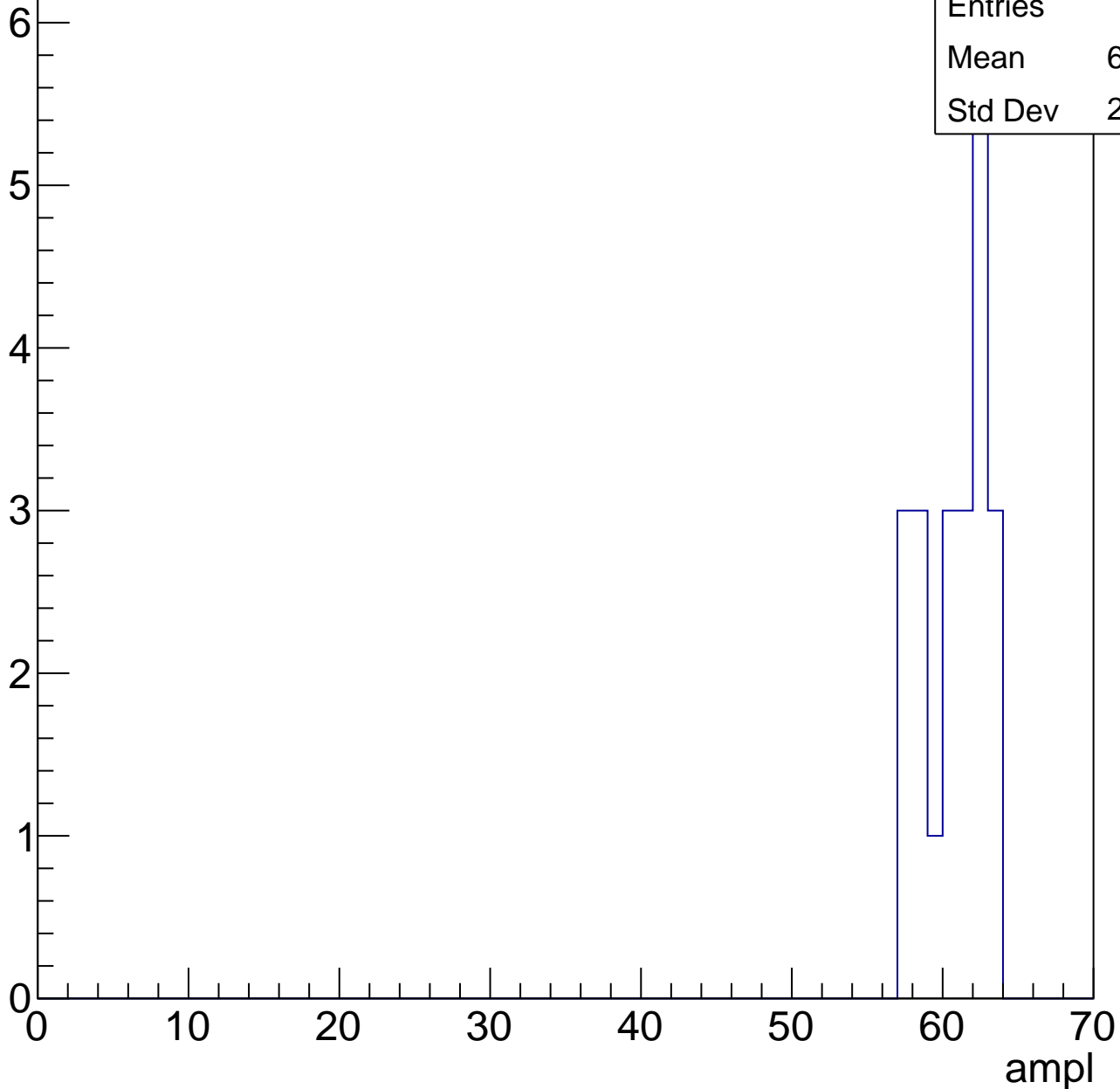
Entries	63
Mean	57.51
Std Dev	3.473

B1L101S, U14-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	60.36
Std Dev	2.035



B1L101S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

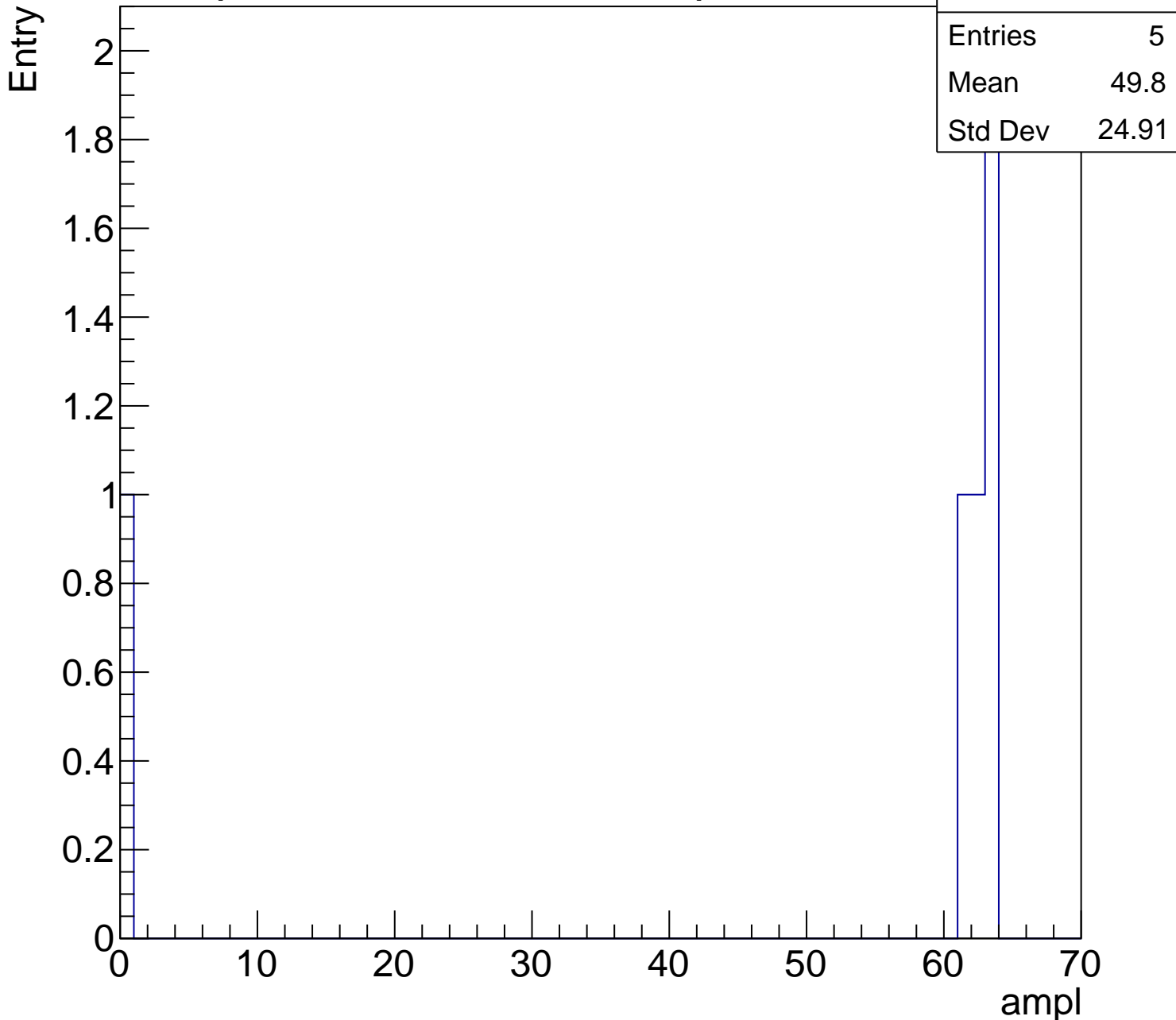
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.8
Std Dev	24.91

0 10 20 30 40 50 60 70

ampl



B1L101S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0