

B0L101S, U20-ch0, adc0

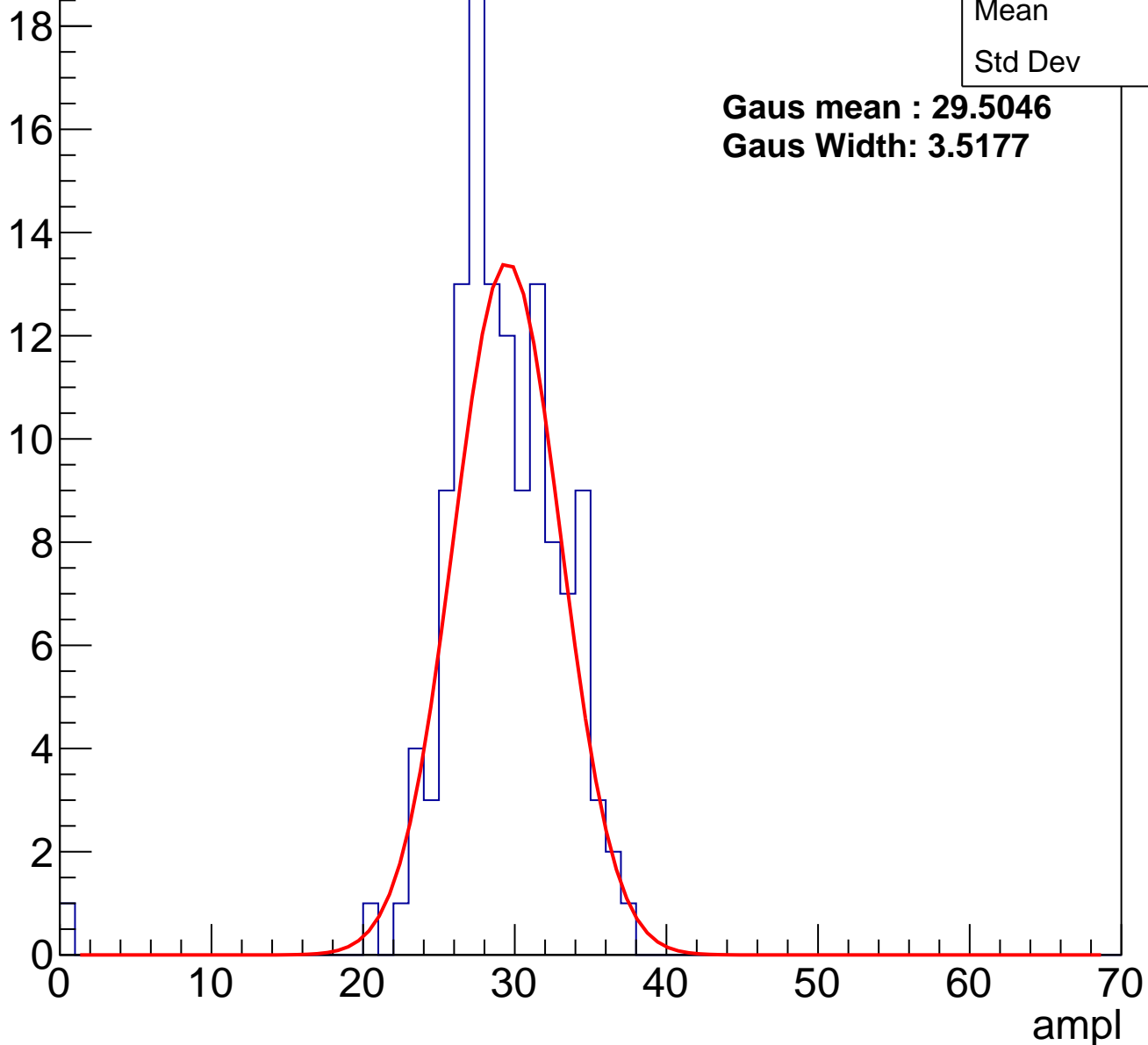
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	28.7
Std Dev	4.21

Gaus mean : 29.5046

Gaus Width: 3.5177

Entry



B0L101S, U20-ch0, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

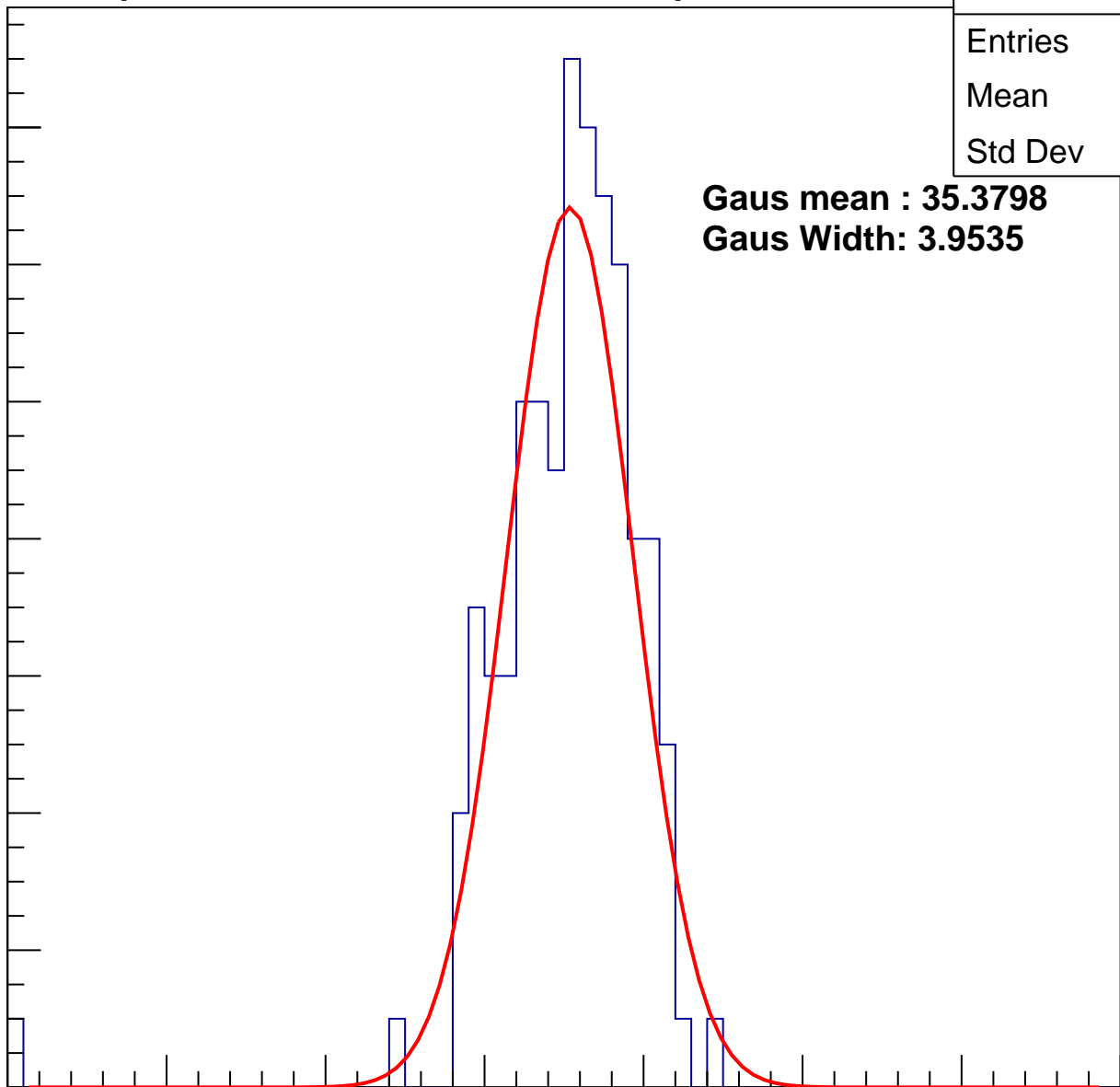
Entries	131
Mean	34.73
Std Dev	4.771

Gaus mean : 35.3798

Gaus Width: 3.9535

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

145

Mean

41.77

Std Dev

3.918

Gaus mean : 42.6419

Gaus Width: 3.7706

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

B0L101S, U20-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

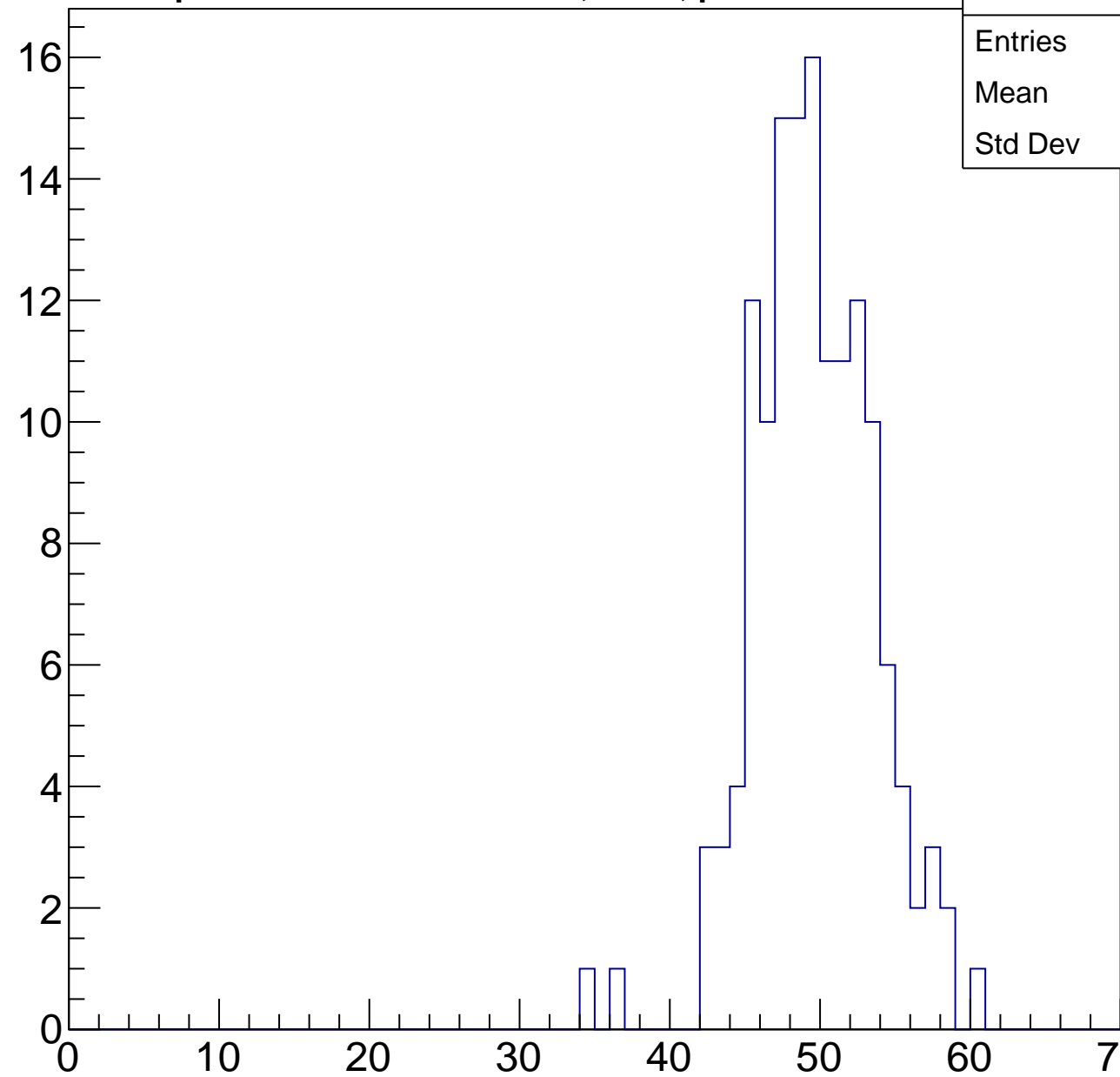
Entries	142
Mean	49.14
Std Dev	4.019

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

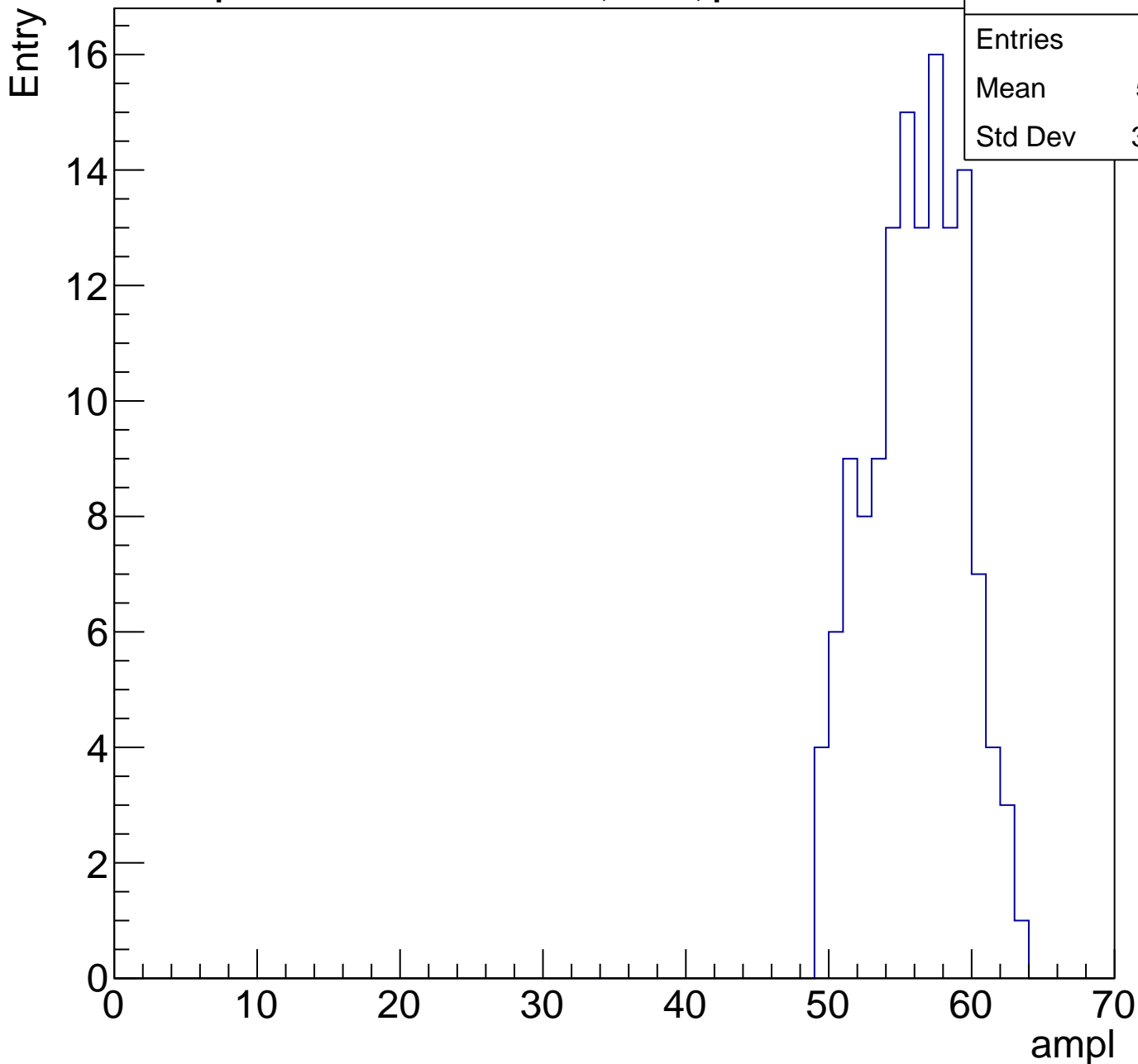
ampl



B0L101S, U20-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	55.61
Std Dev	3.284



B0L101S, U20-ch0, adc5

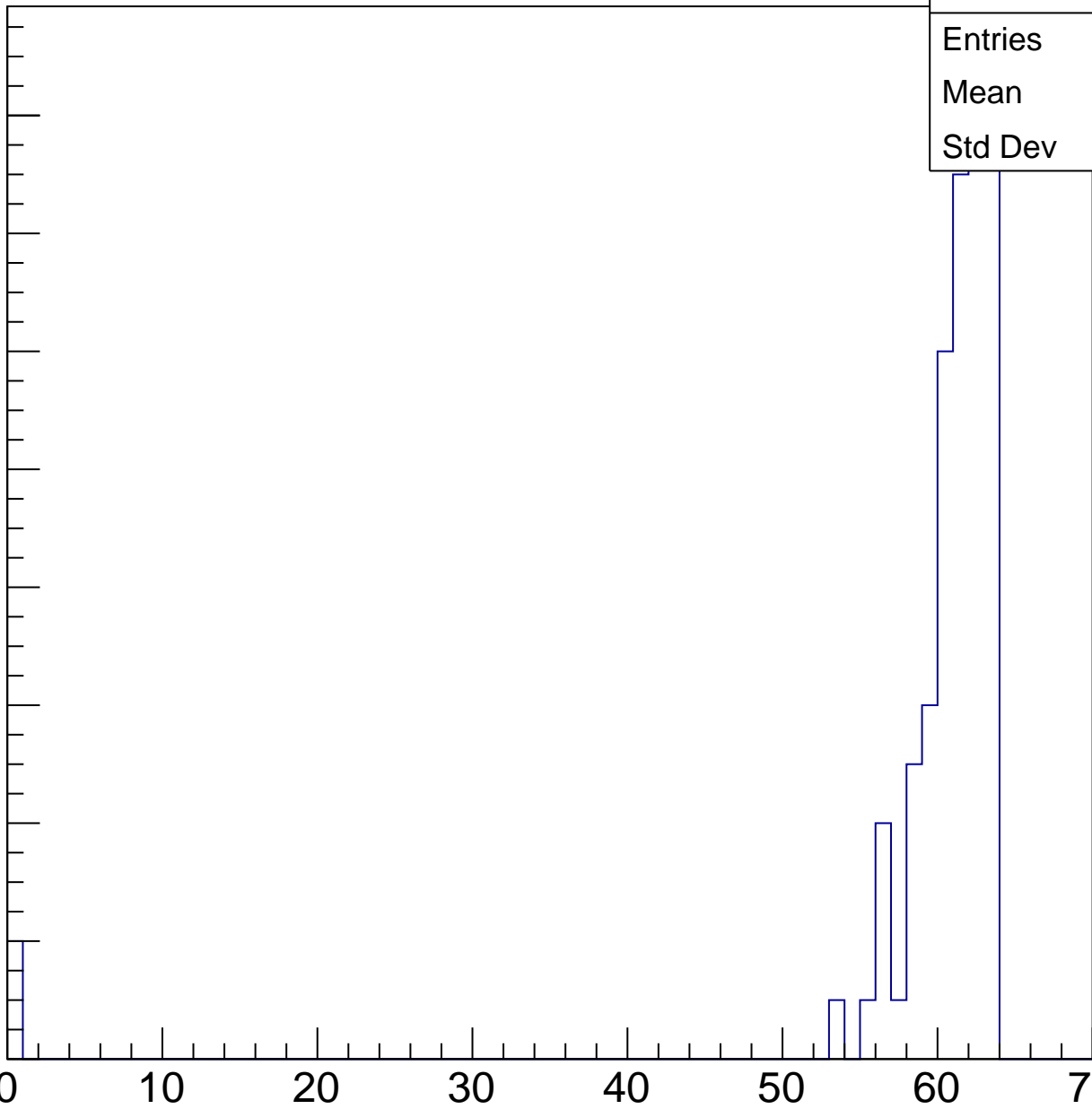
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.14
Std Dev	9.712

ampl



B0L101S, U20-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

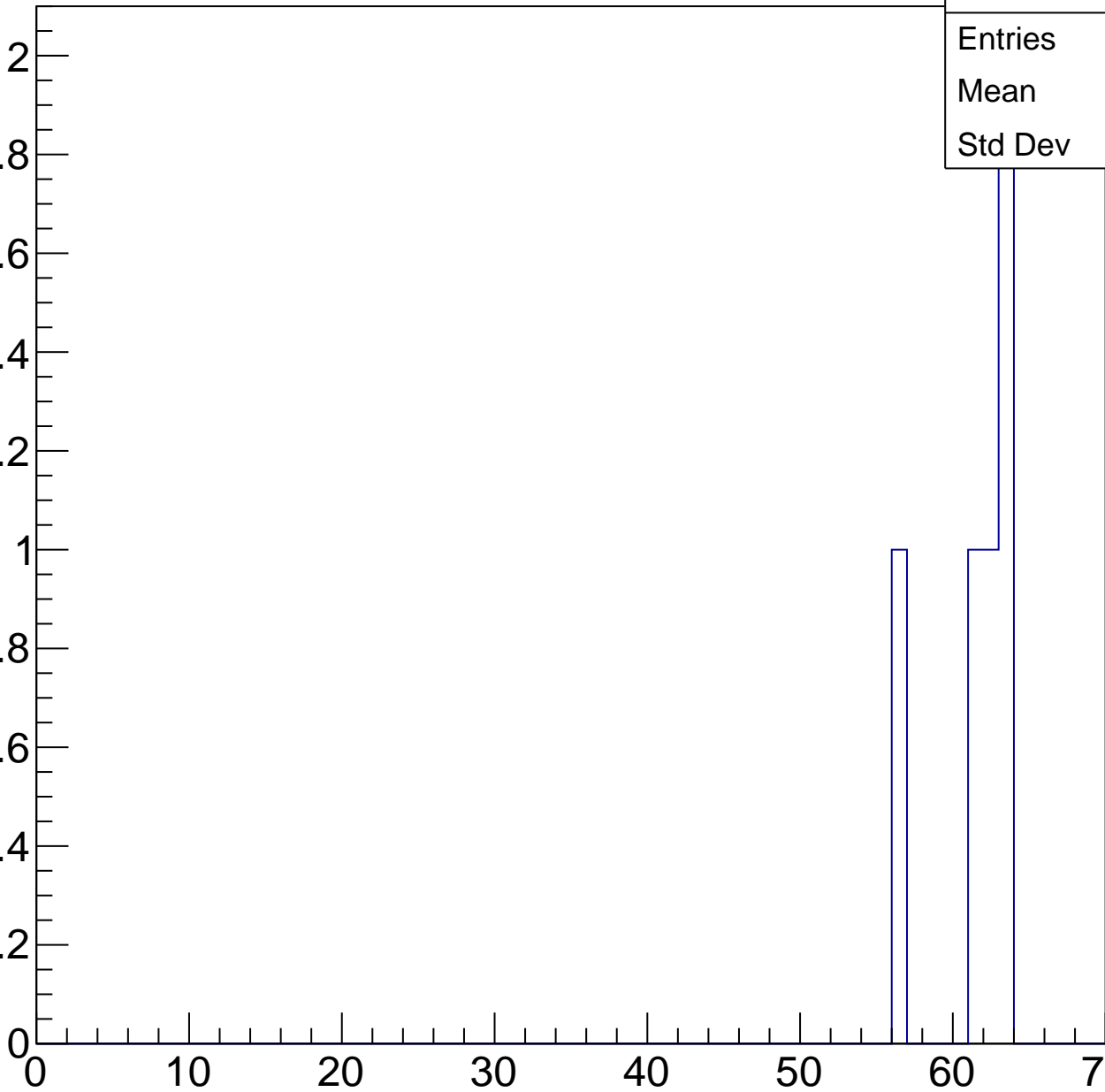
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	2.608

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch1, adc0

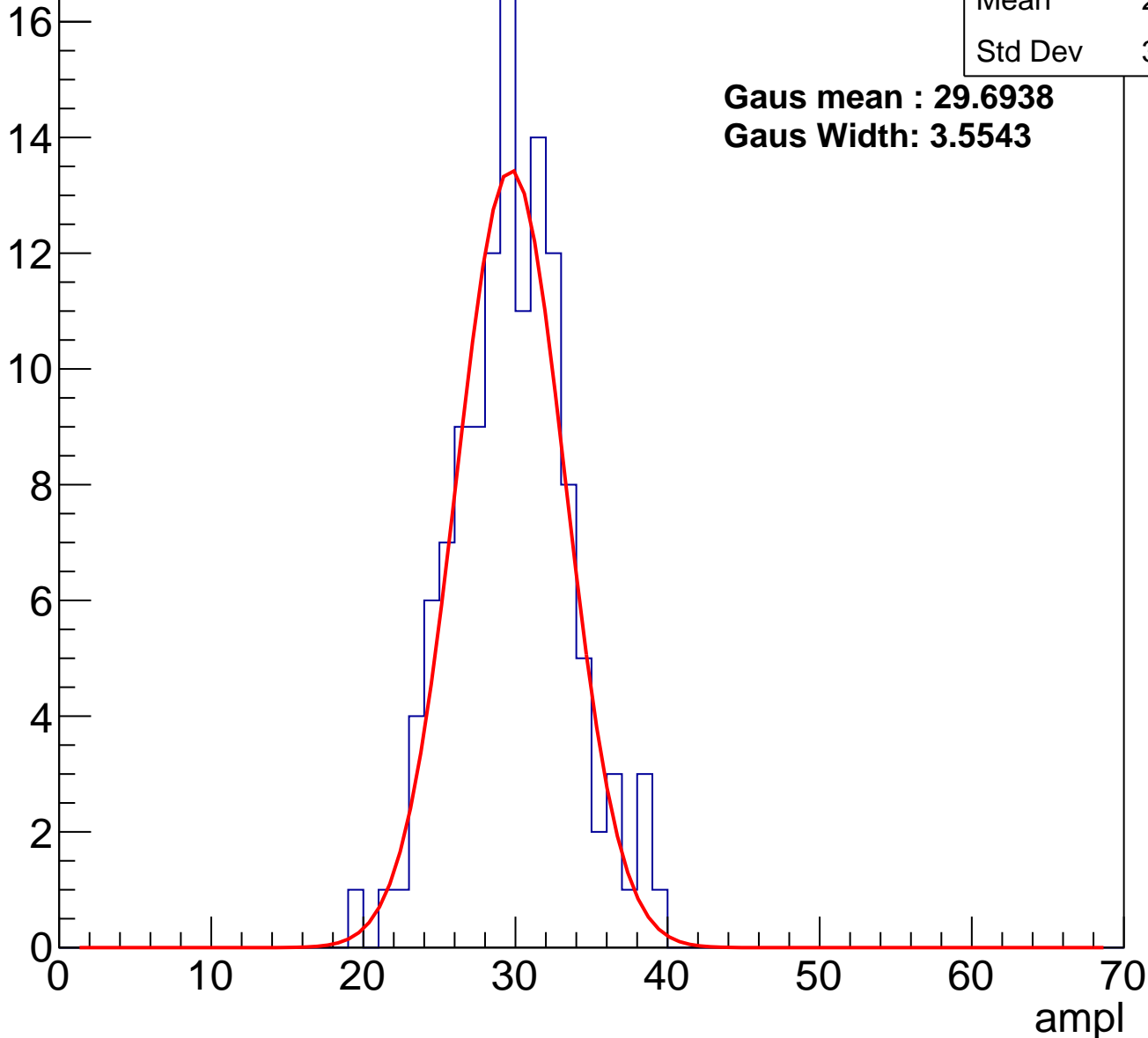
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	127
Mean	29.36
Std Dev	3.749

Gaus mean : 29.6938

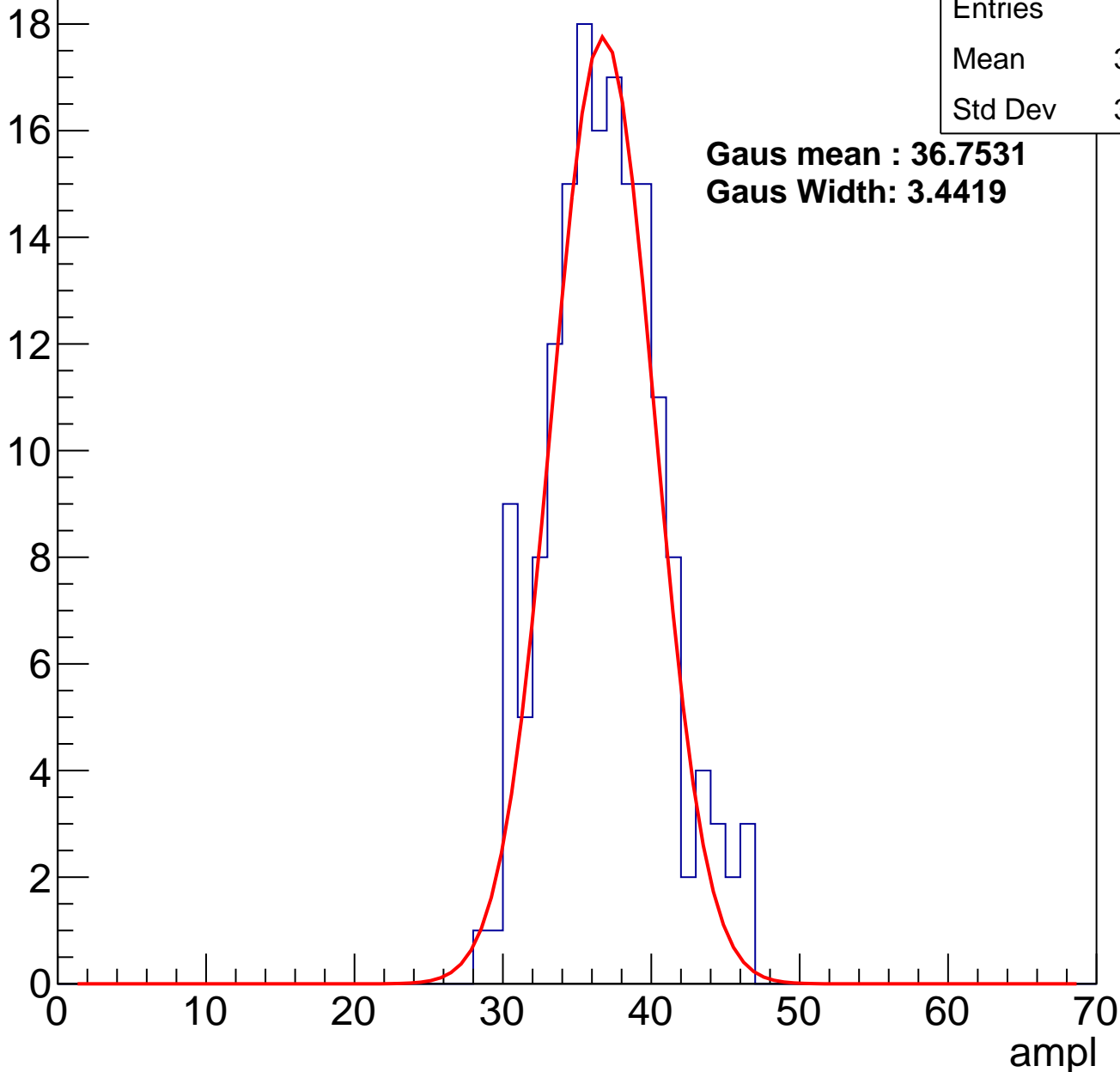
Gaus Width: 3.5543



B0L101S, U20-ch1, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

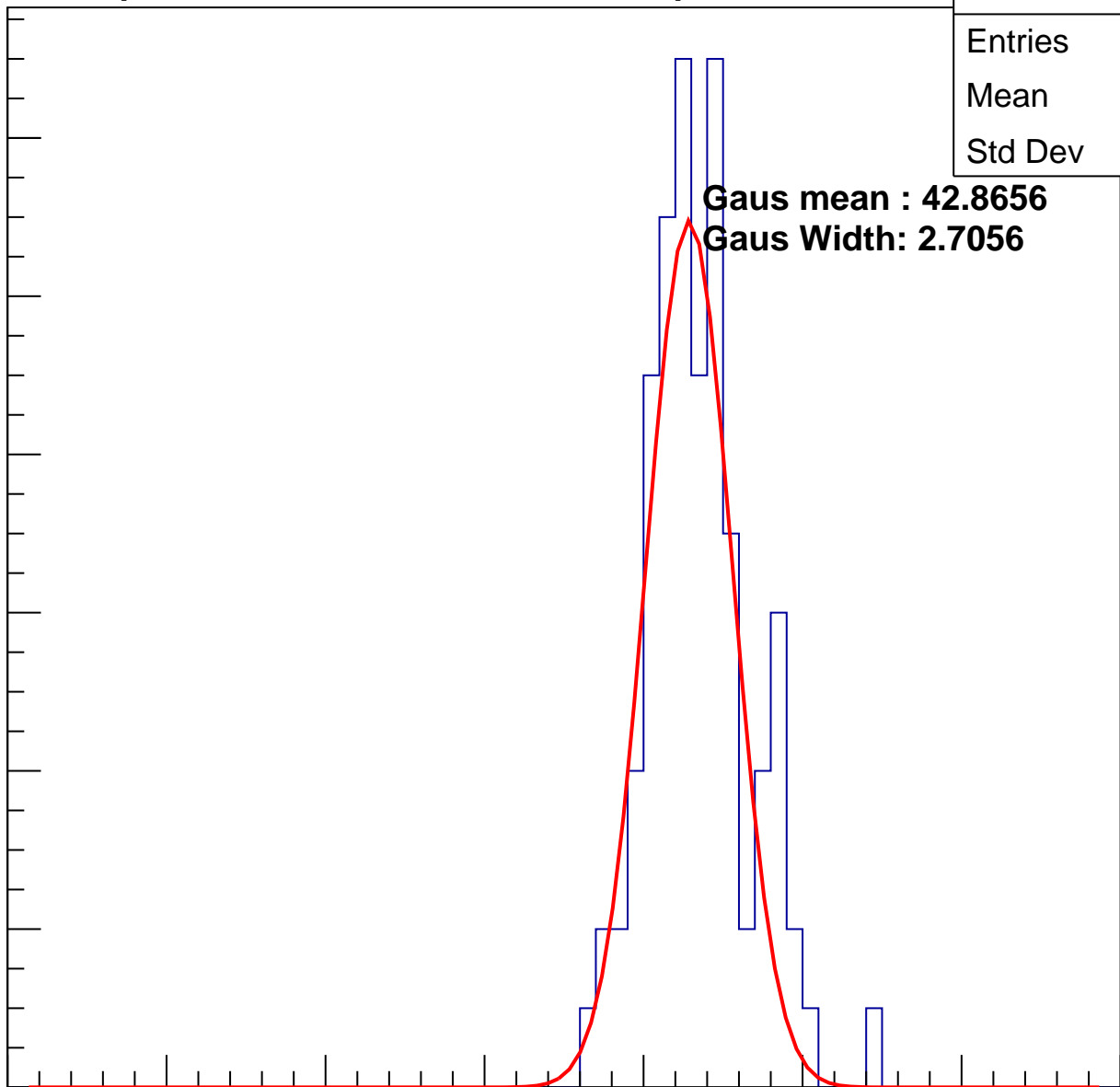
Entries	87
Mean	43.02
Std Dev	3.195

Gaus mean : 42.8656

Gaus Width: 2.7056

0 10 20 30 40 50 60 70

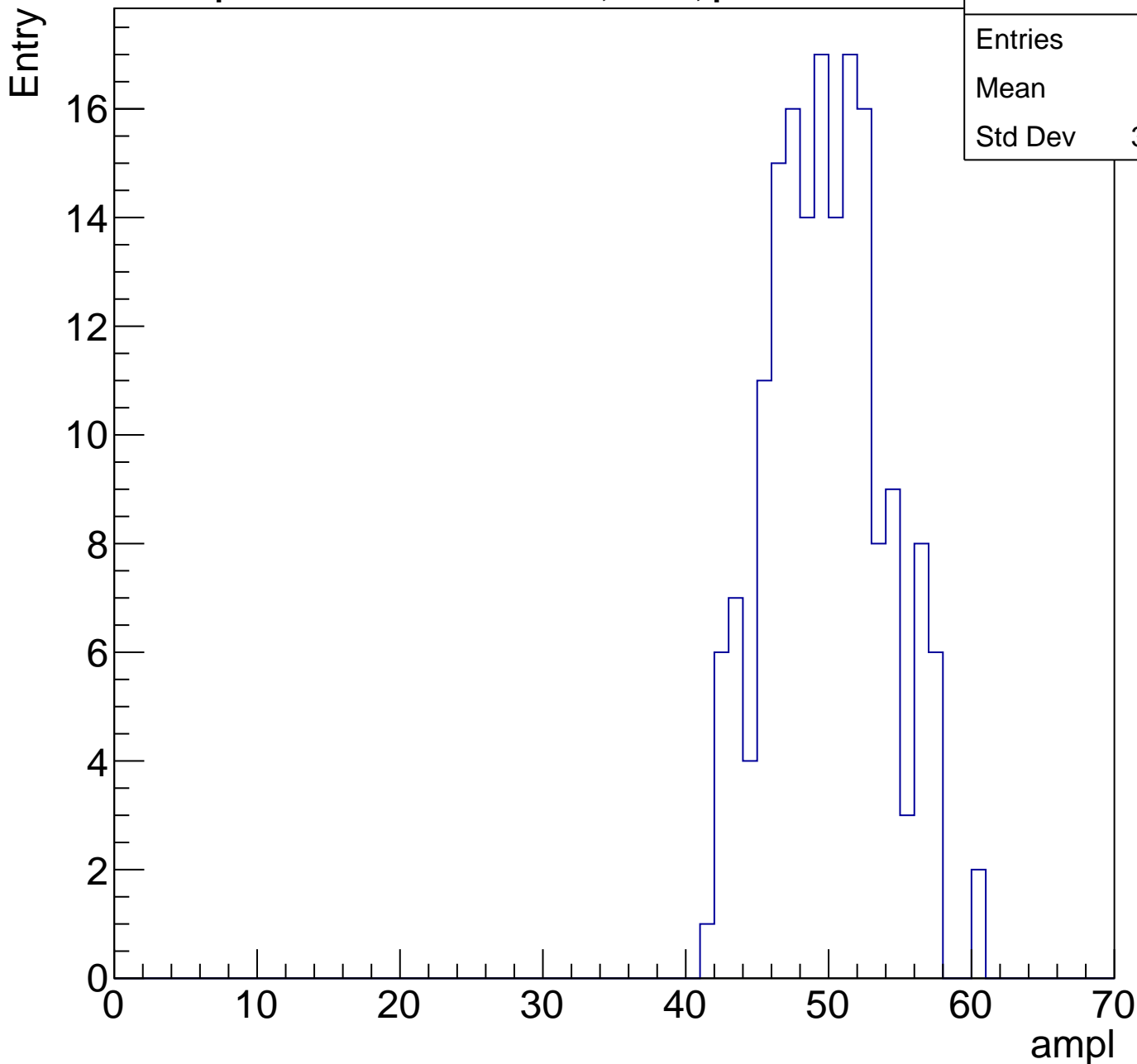
ampl



B0L101S, U20-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	49.4
Std Dev	3.994



B0L101S, U20-ch1, adc4

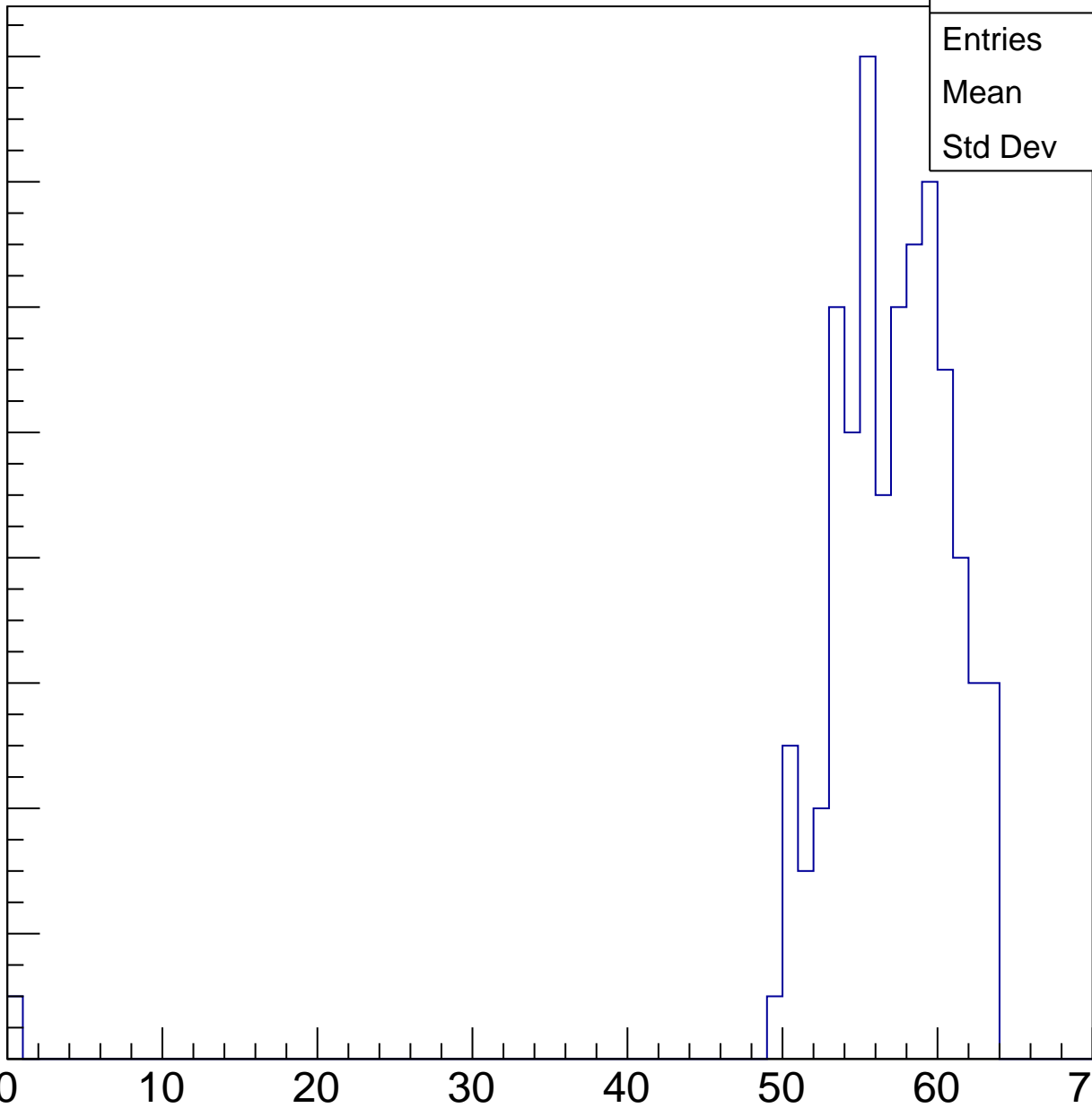
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	131
Mean	56.35
Std Dev	6.008

ampl



B0L101S, U20-ch1, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	60
Mean	59.35
Std Dev	8.004

ampl

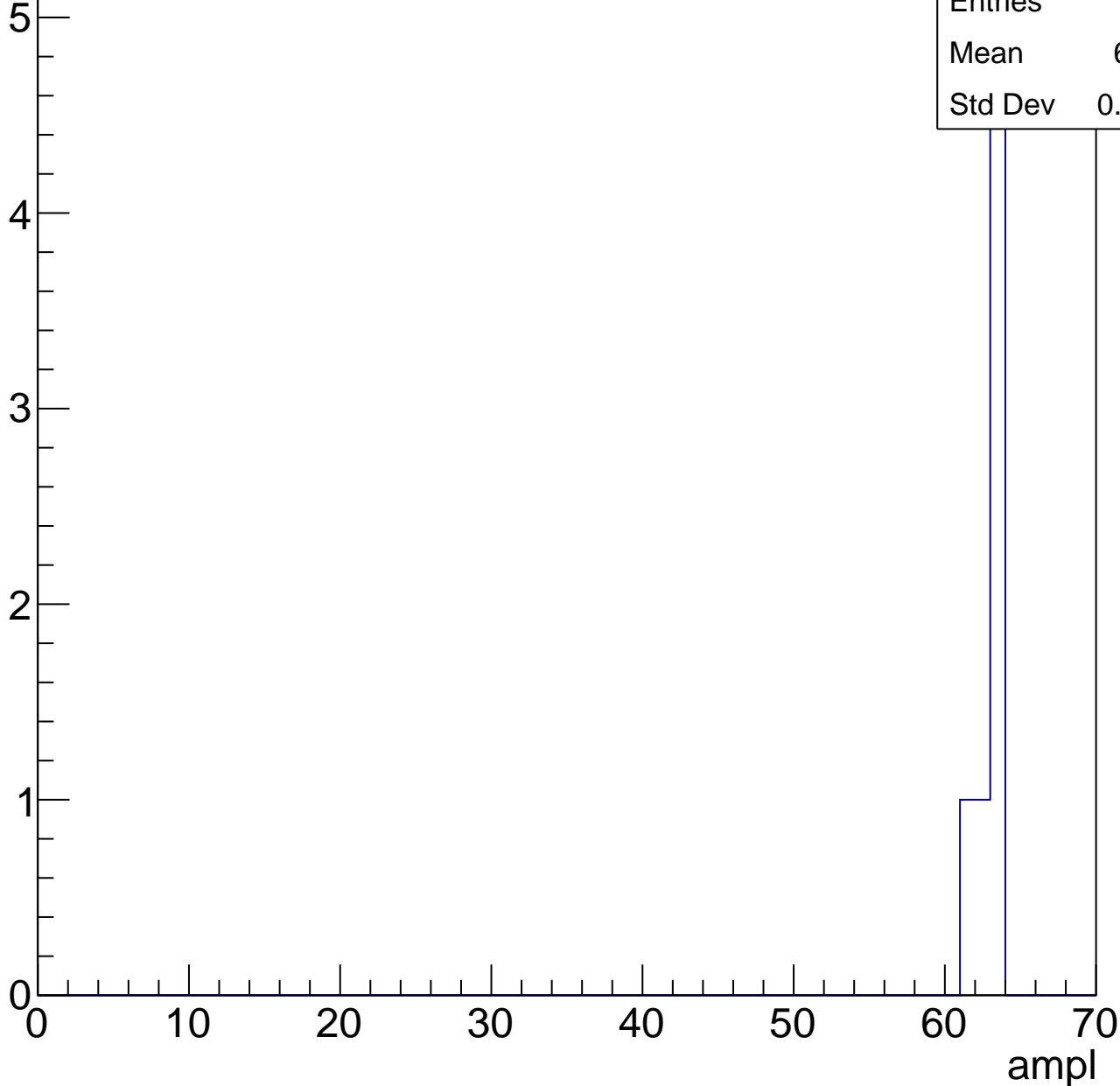
0 10 20 30 40 50 60 70

B0L101S, U20-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B0L101S, U20-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

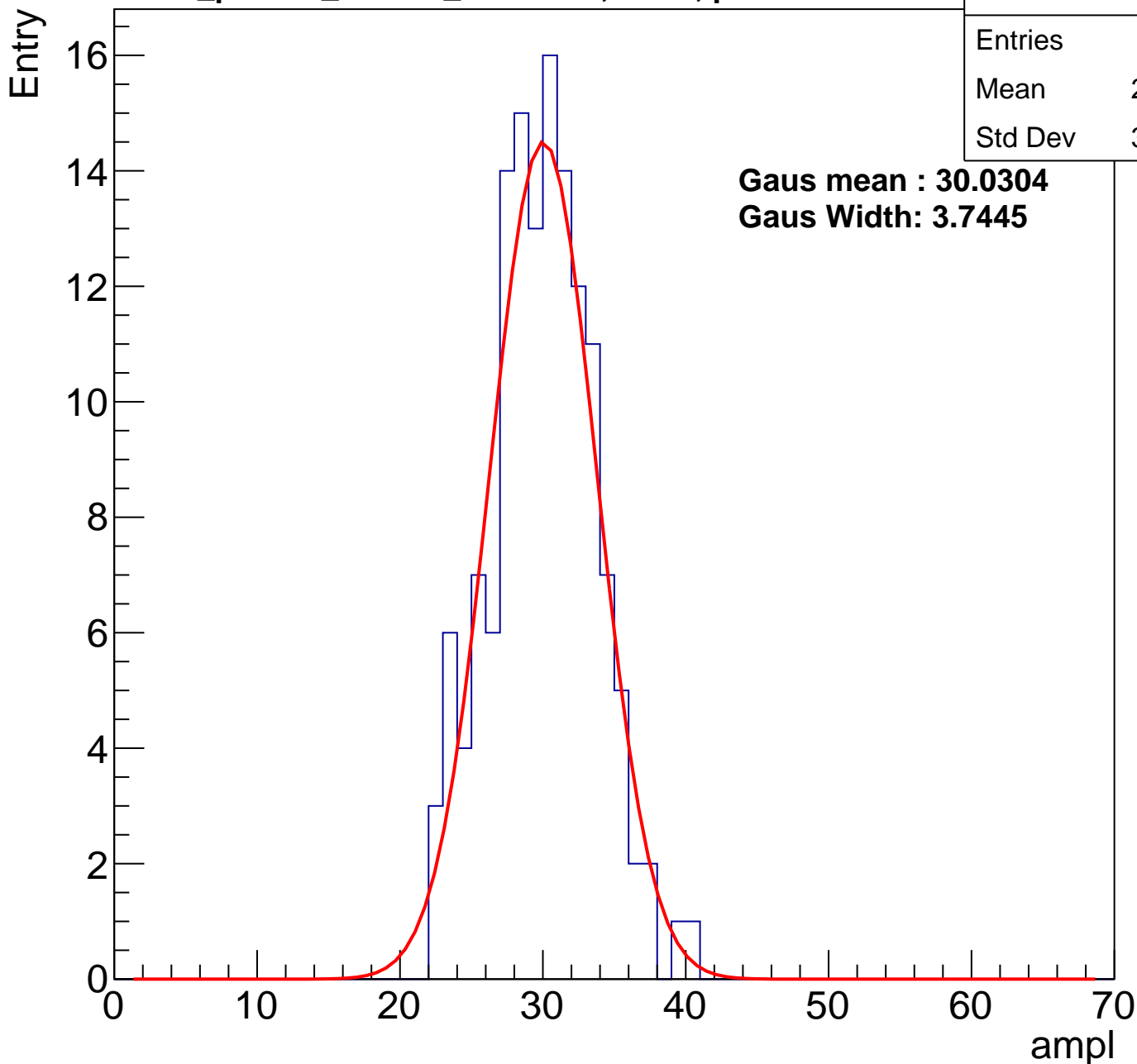
B0L101S, U20-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.53
Std Dev	3.617

Gaus mean : 30.0304

Gaus Width: 3.7445



B0L101S, U20-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	163
Mean	37.6
Std Dev	3.873

Gaus mean : 37.8515

Gaus Width: 3.9014

B0L101S, U20-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	116
Mean	44.21
Std Dev	3.782

Gaus mean : 44.4748

Gaus Width: 3.8737

0

10

20

30

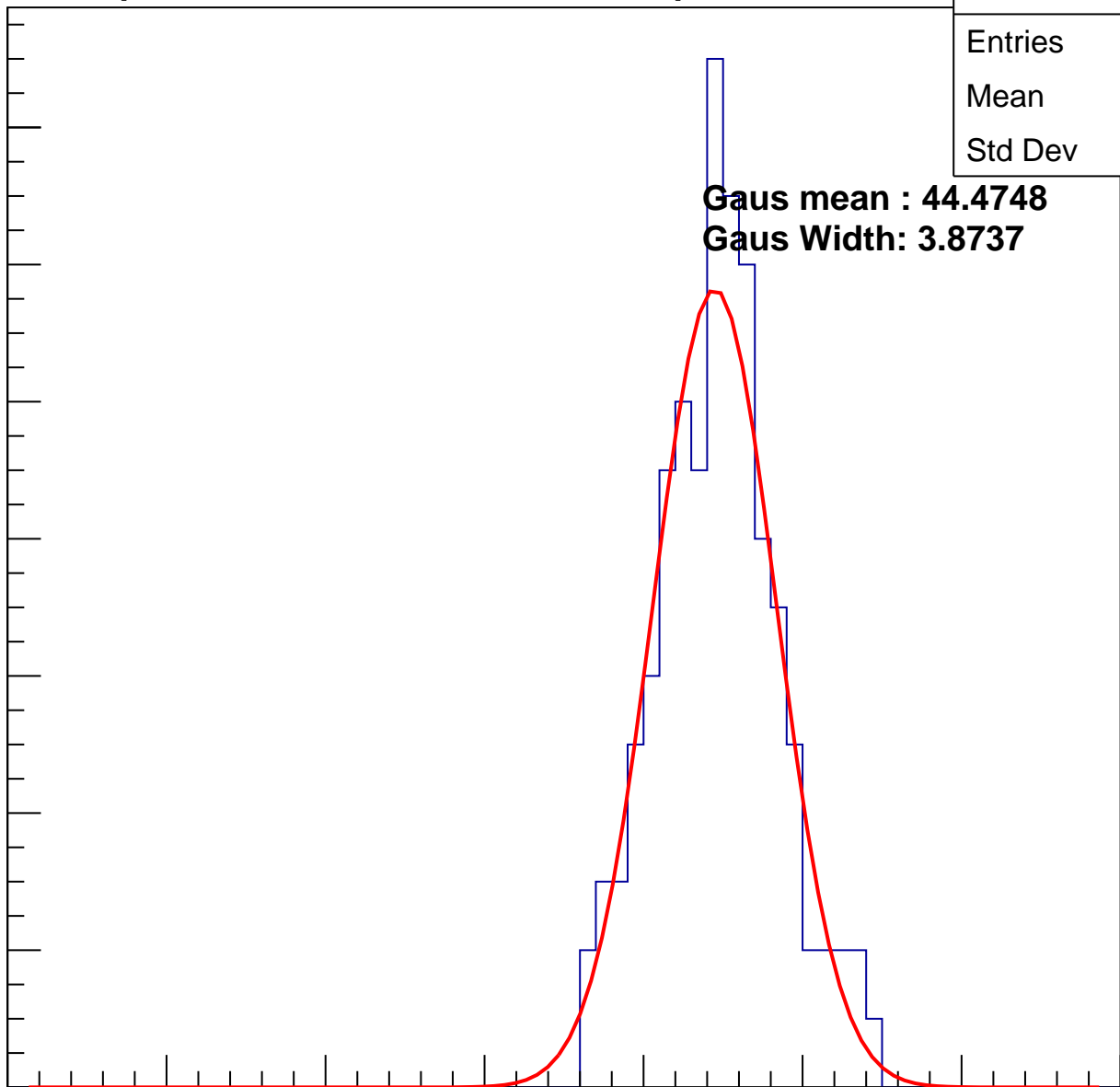
40

50

60

70

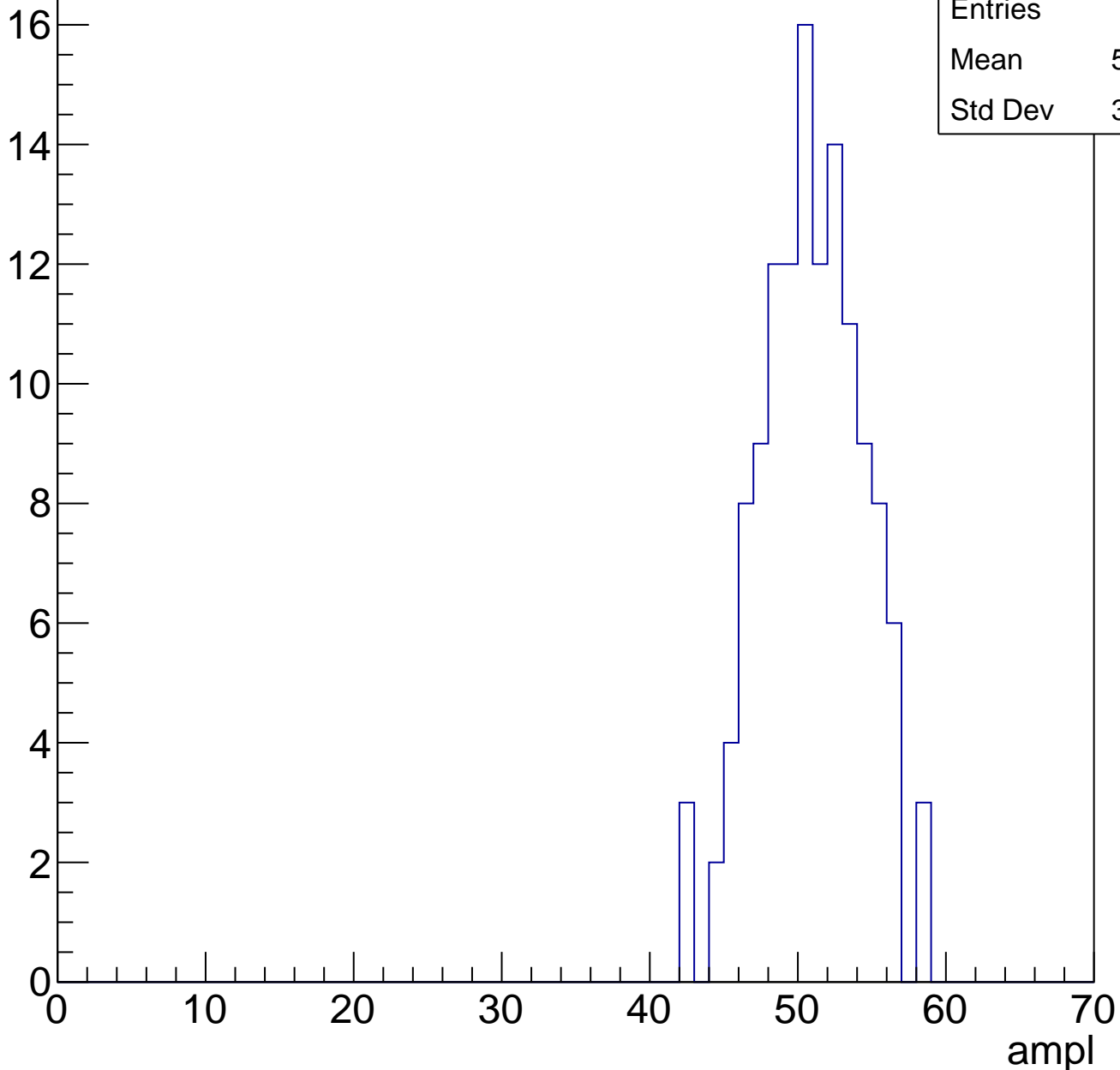
ampl



B0L101S, U20-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

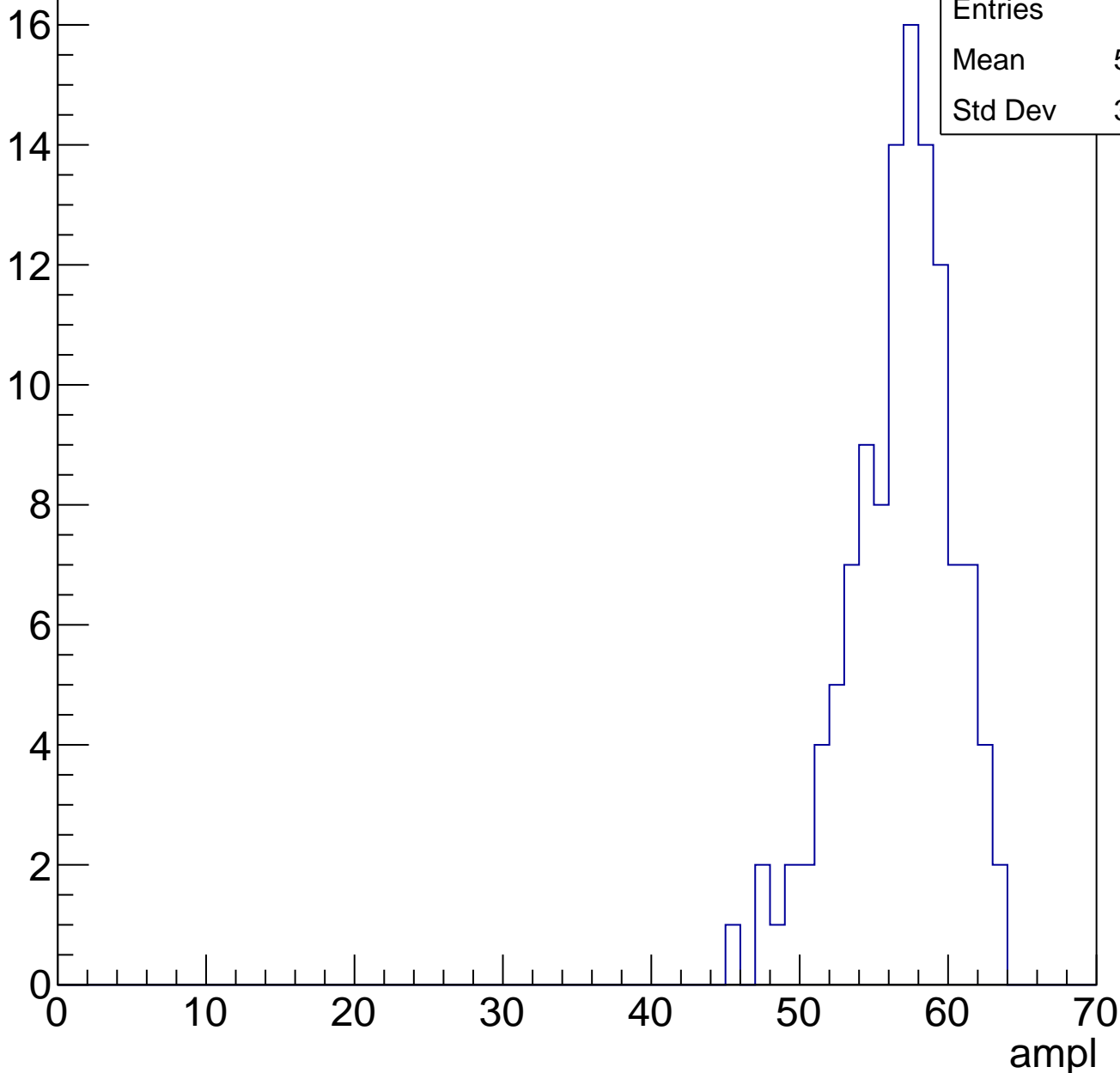


Entries	129
Mean	50.45
Std Dev	3.439

B0L101S, U20-ch2, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch2, adc5

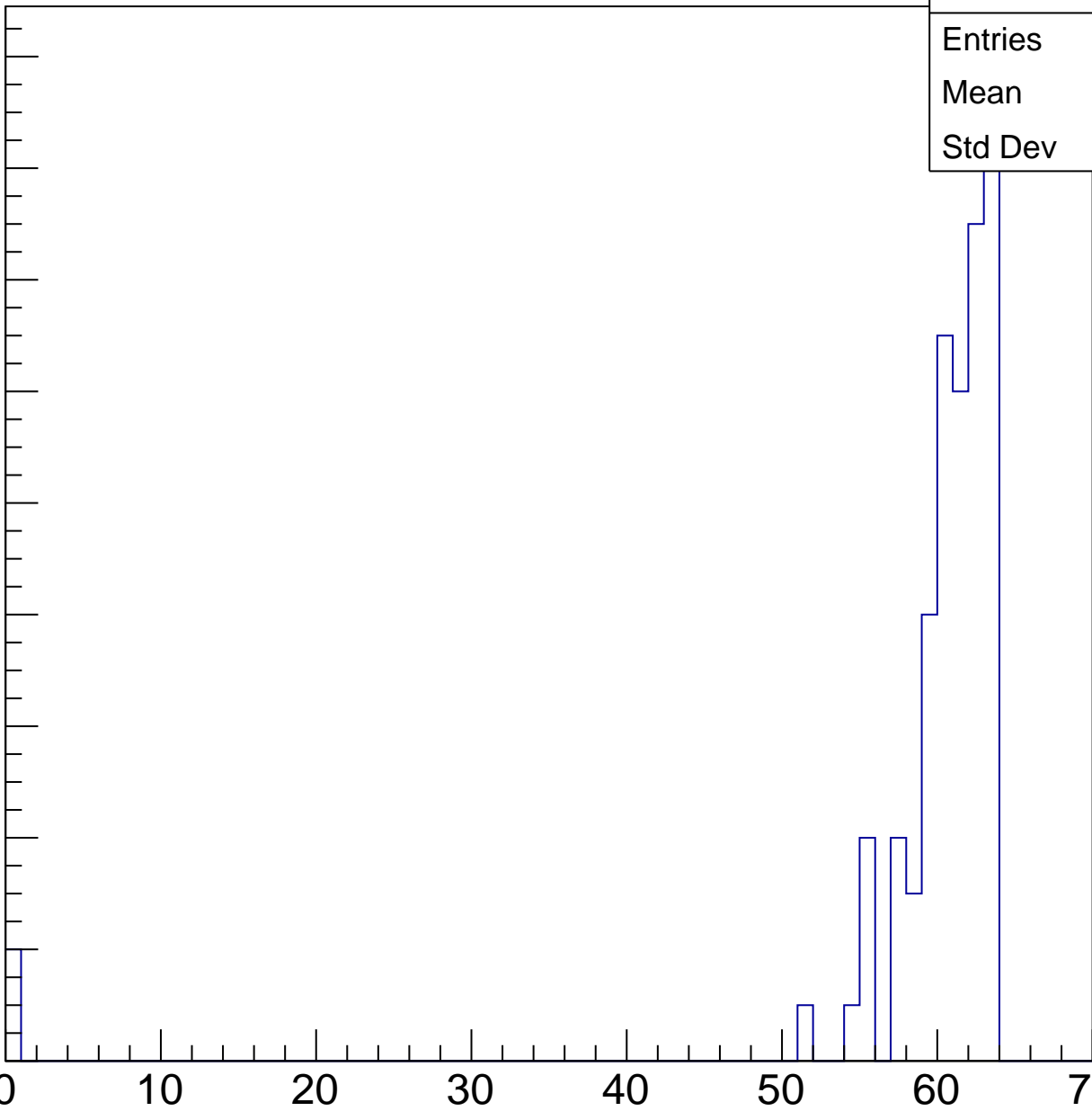
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.95
Std Dev	9.698

ampl



B0L101S, U20-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch3, adc0

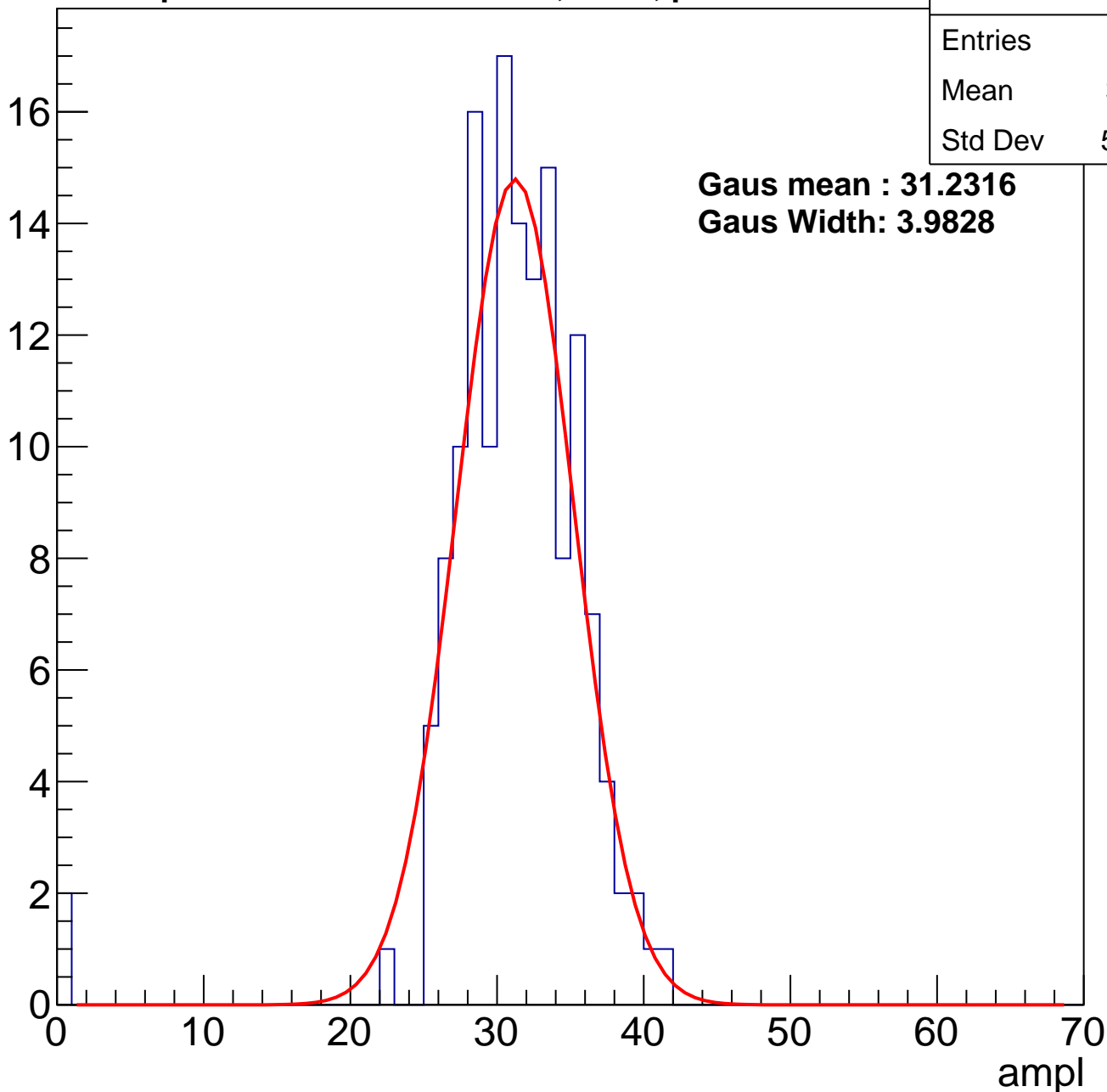
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	30.71
Std Dev	5.063

Gaus mean : 31.2316

Gaus Width: 3.9828

Entry



B0L101S, U20-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

115

Mean

37.37

Std Dev

3.147

Gaus mean : 37.4546

Gaus Width: 2.8319

0

10

20

30

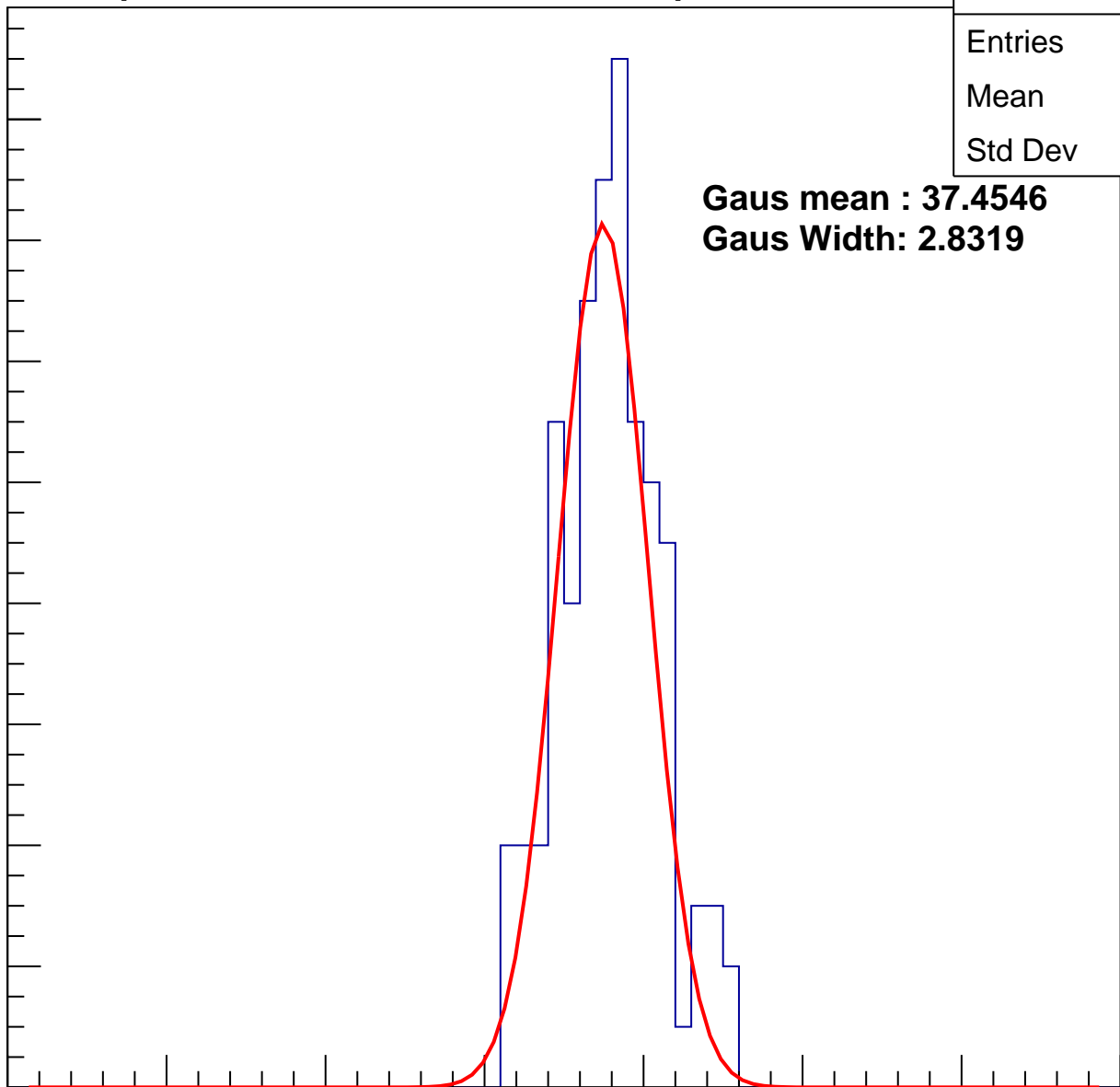
40

50

60

70

ampl



B0L101S, U20-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

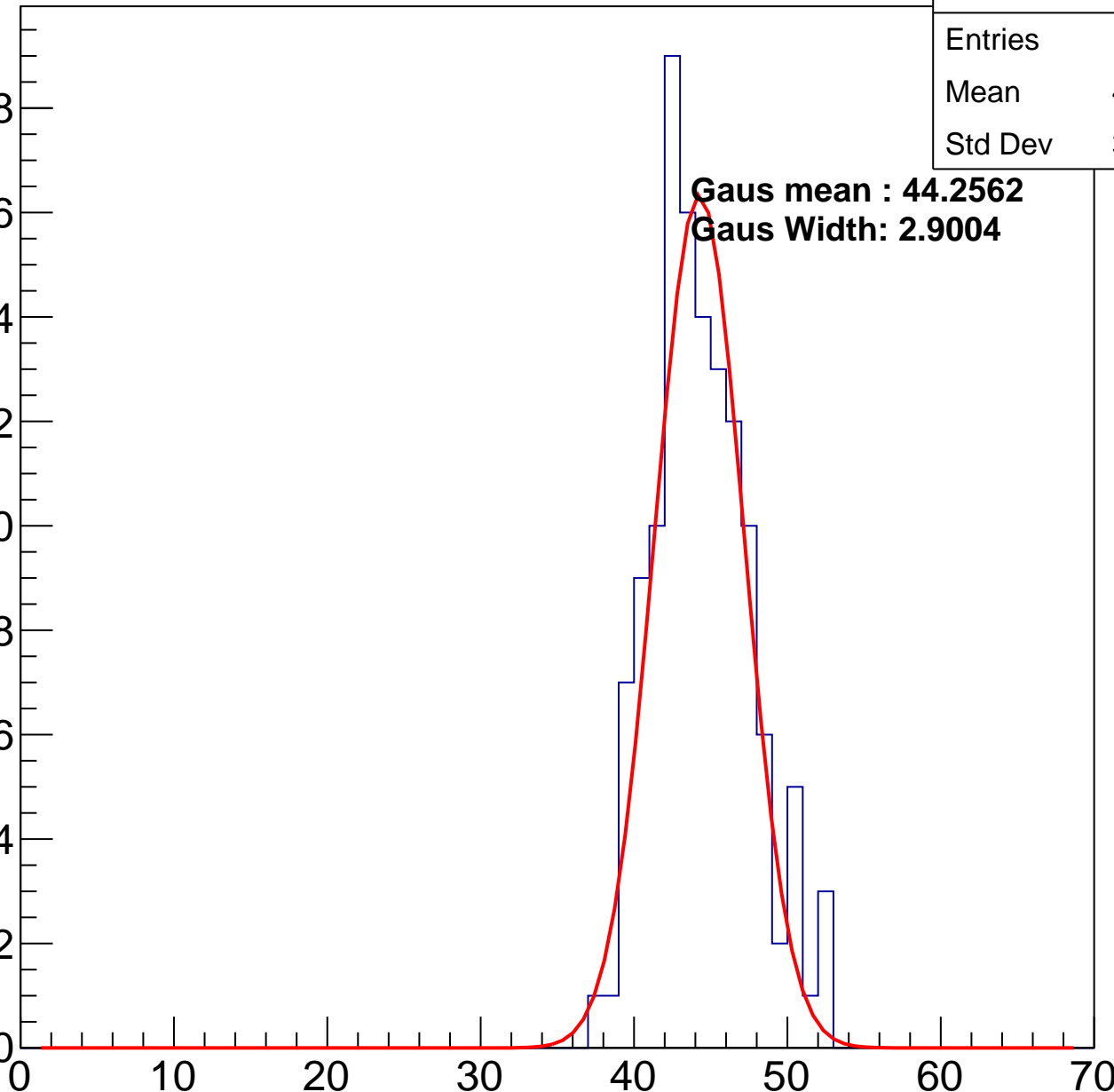
18
16
14
12
10
8
6
4
2
0

Entries	129
Mean	43.95
Std Dev	3.179

Gaus mean : 44.2562

Gaus Width: 2.9004

ampl

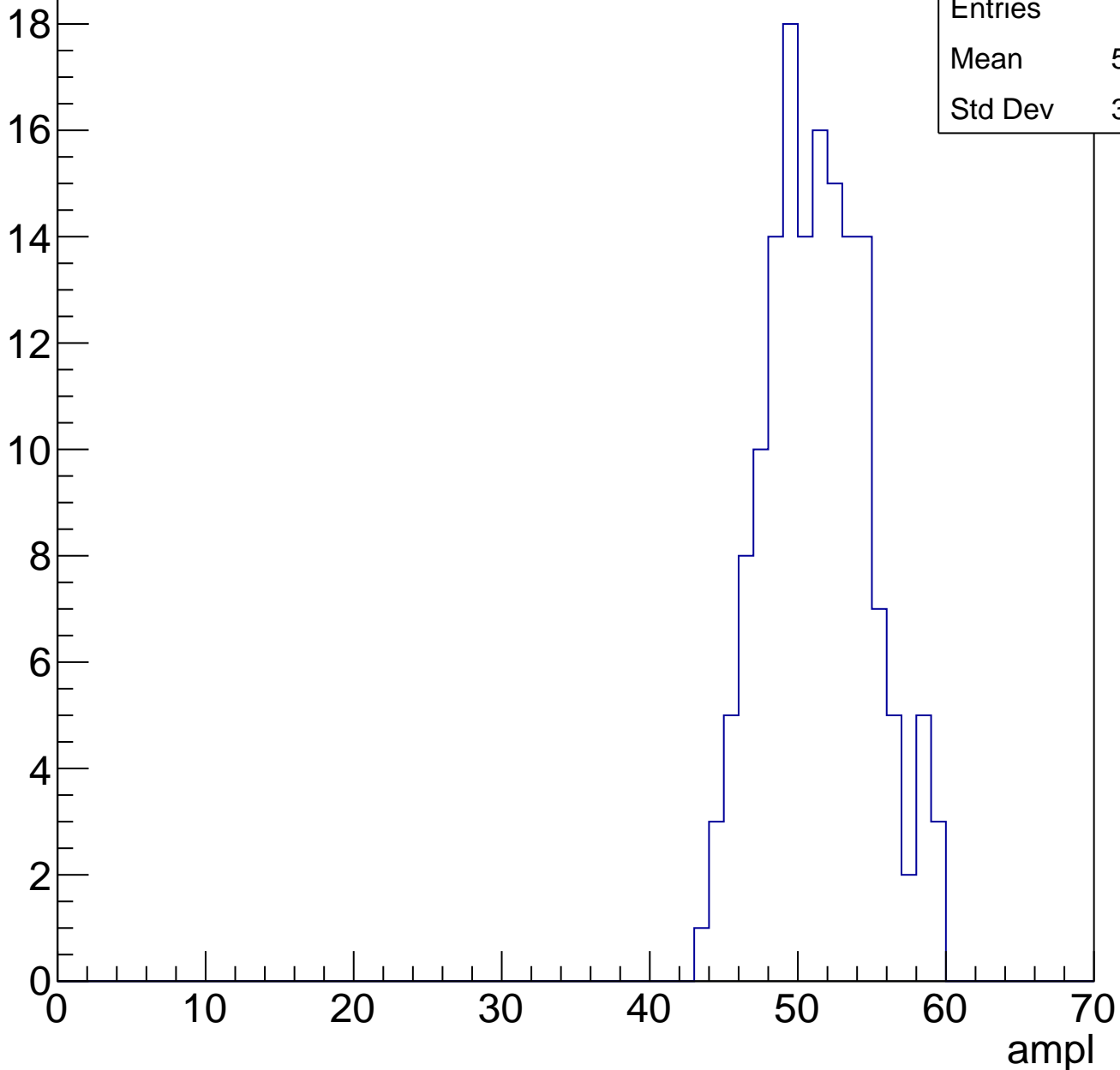


B0L101S, U20-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	50.86
Std Dev	3.515

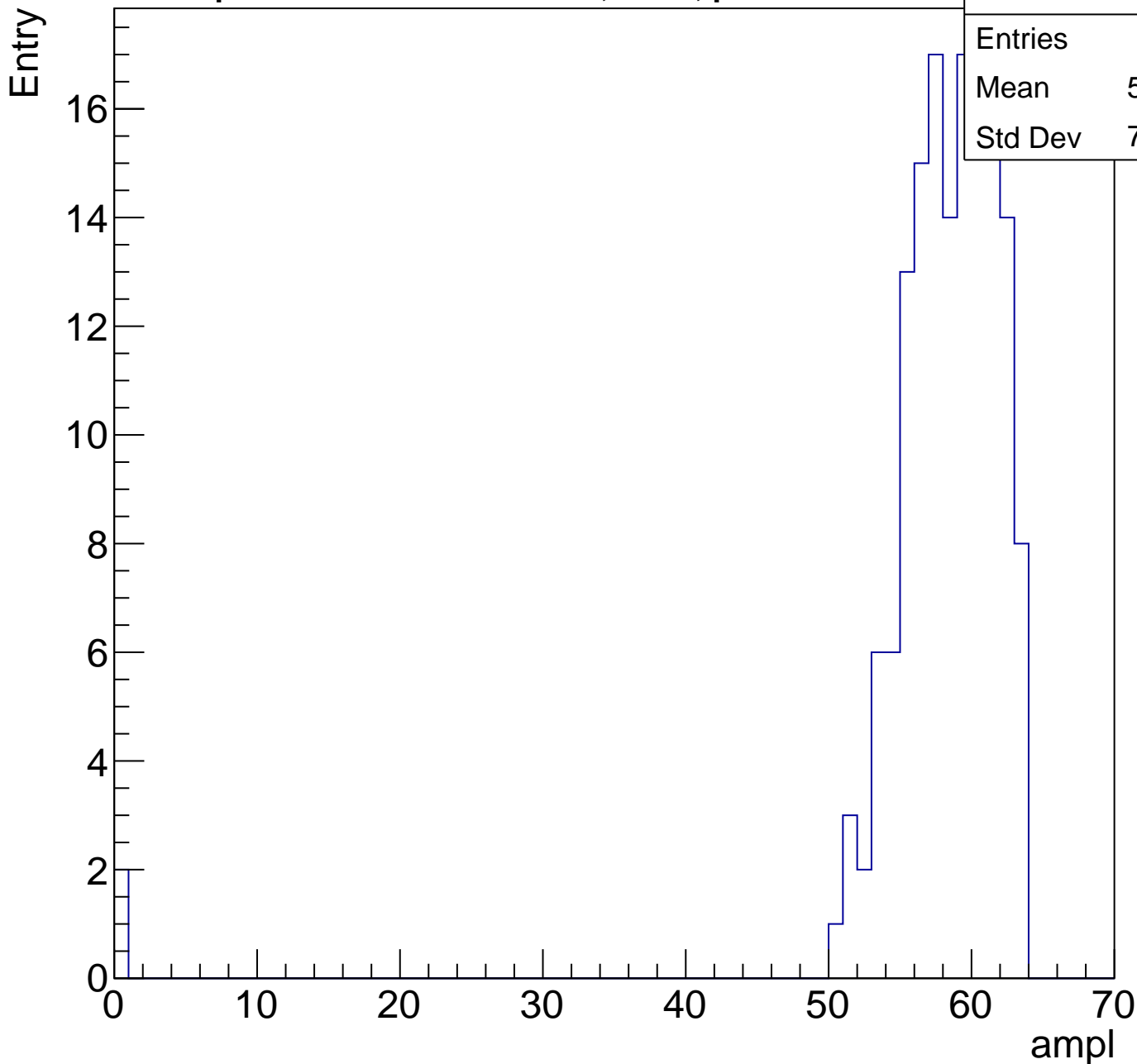
Entry



B0L101S, U20-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	57.33
Std Dev	7.298



B0L101S, U20-ch3, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	24
Mean	61.71
Std Dev	1.645

B0L101S, U20-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	1.247

ampl

0 10 20 30 40 50 60 70

B0L101S, U20-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

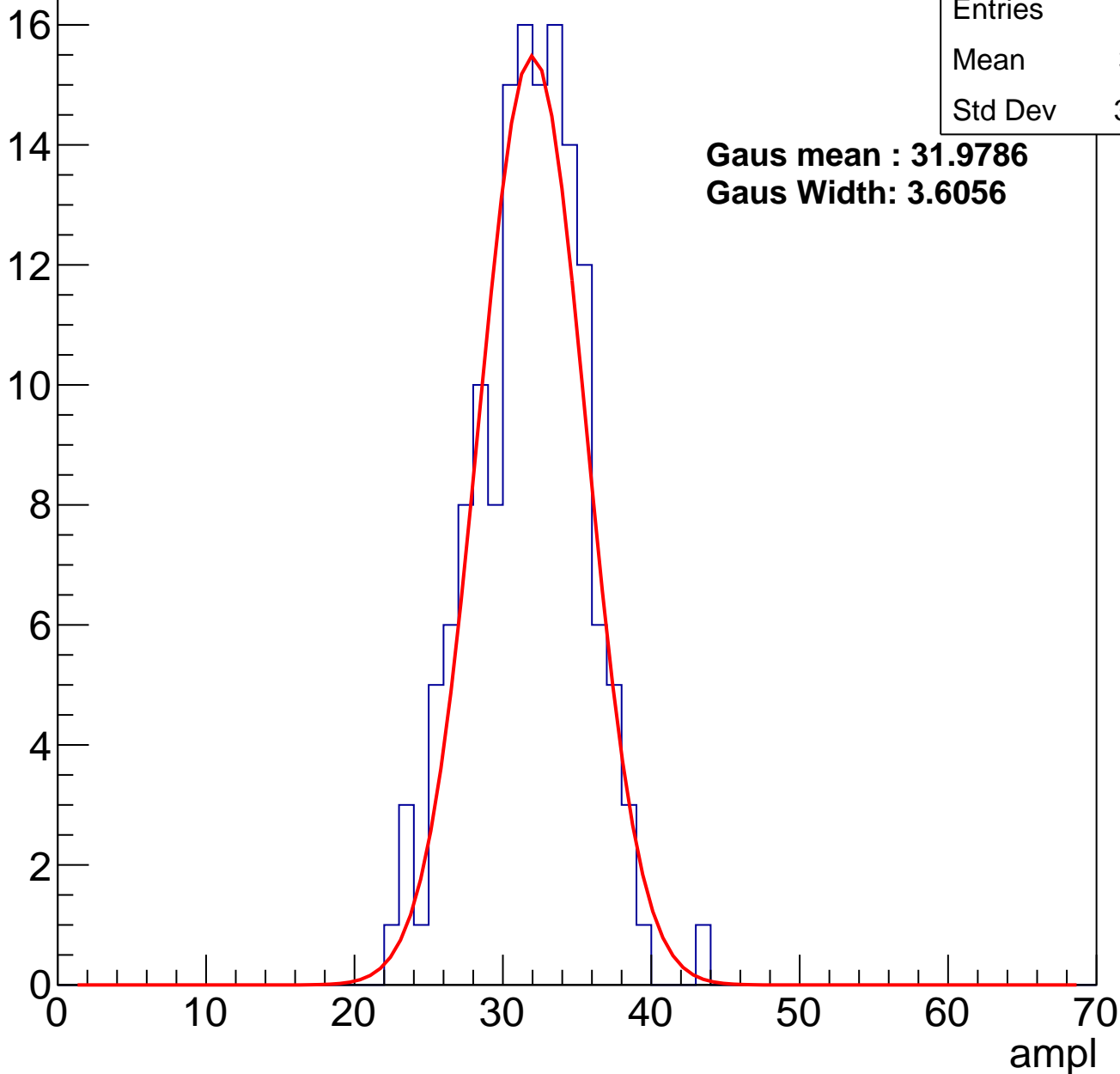
Entry



B0L101S, U20-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	38.42
Std Dev	3.405

Gaus mean : 39.0227

Gaus Width: 3.2807

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

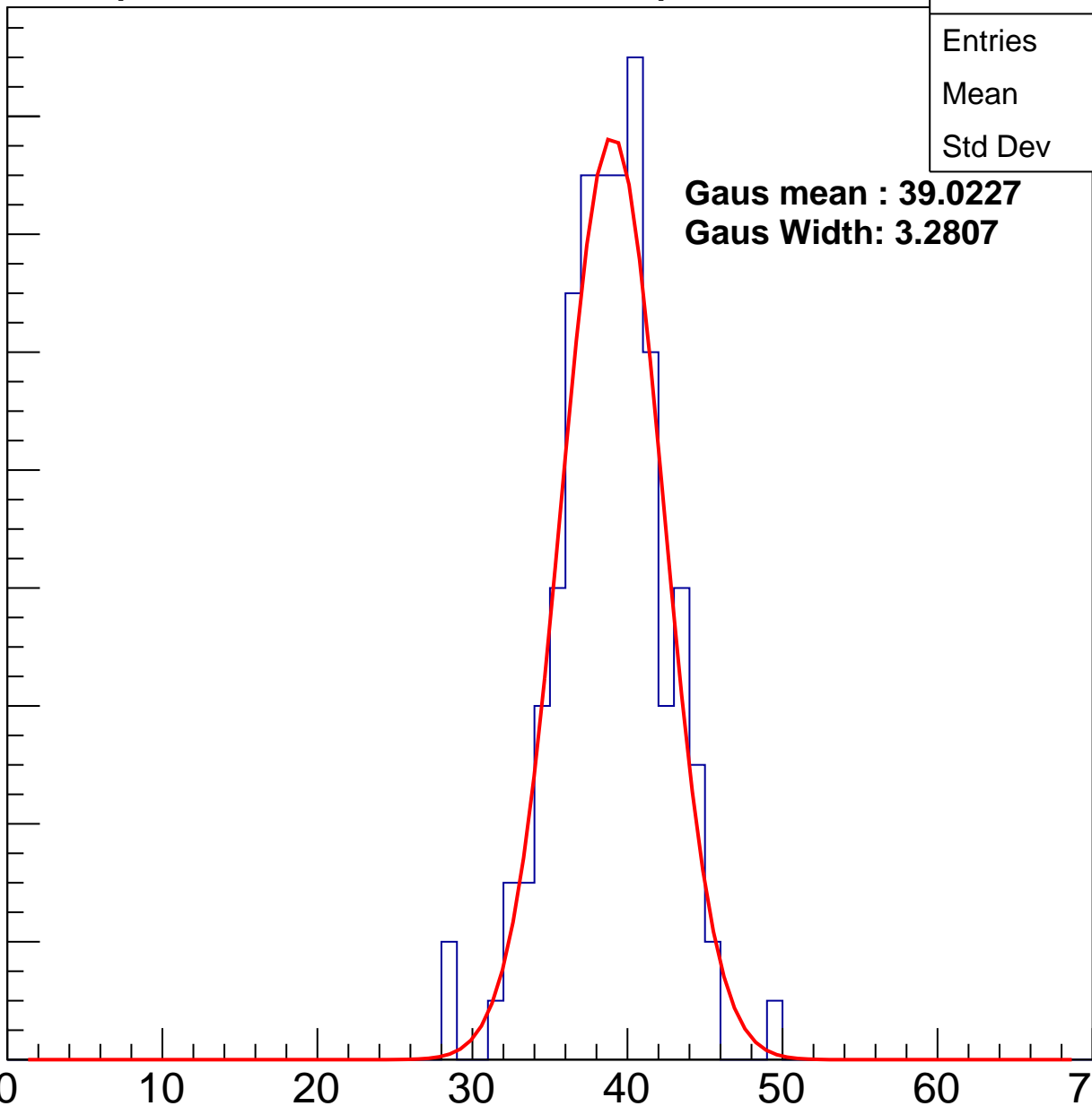
40

50

60

70

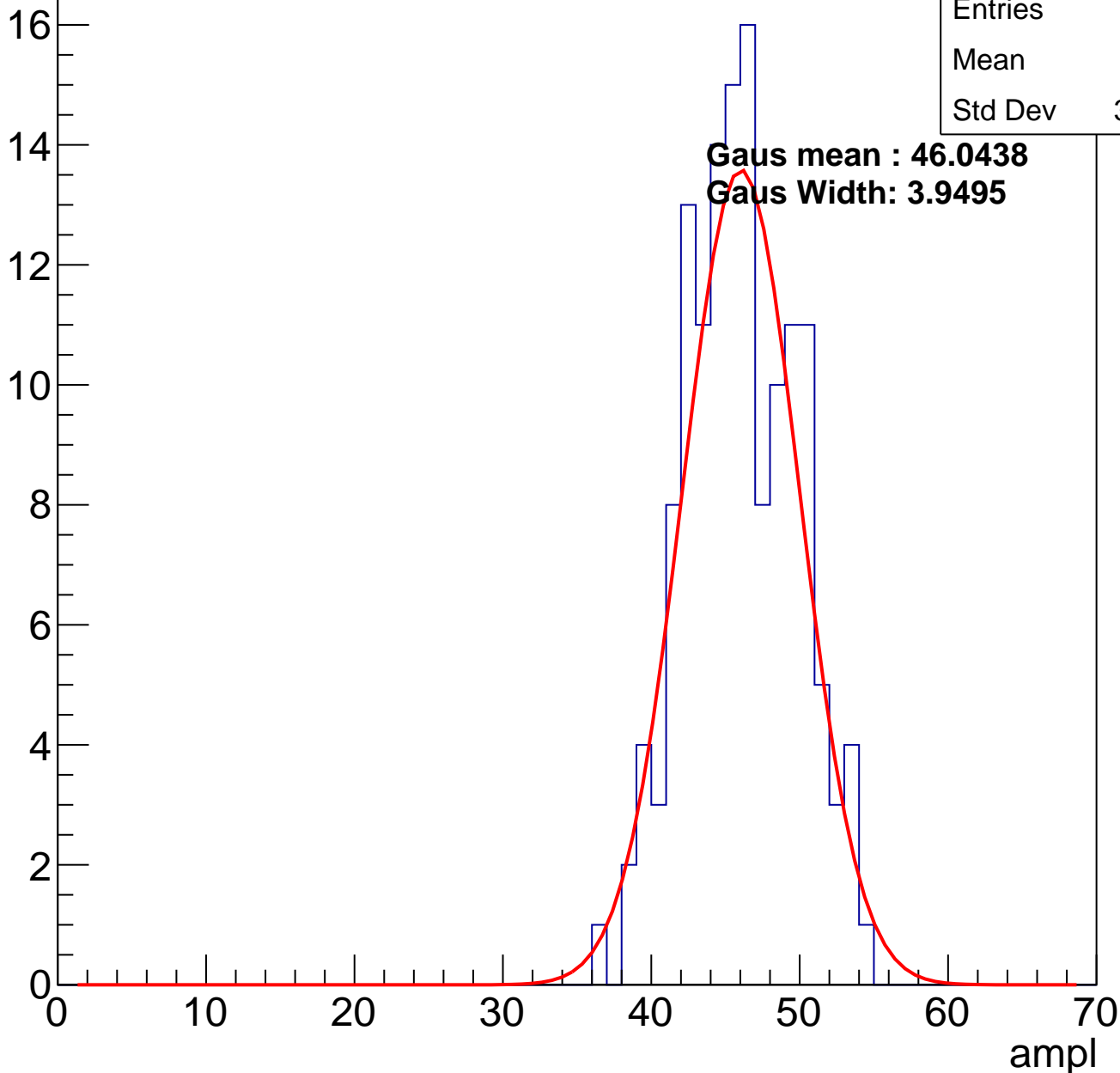
ampl



B0L101S, U20-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch4, adc3

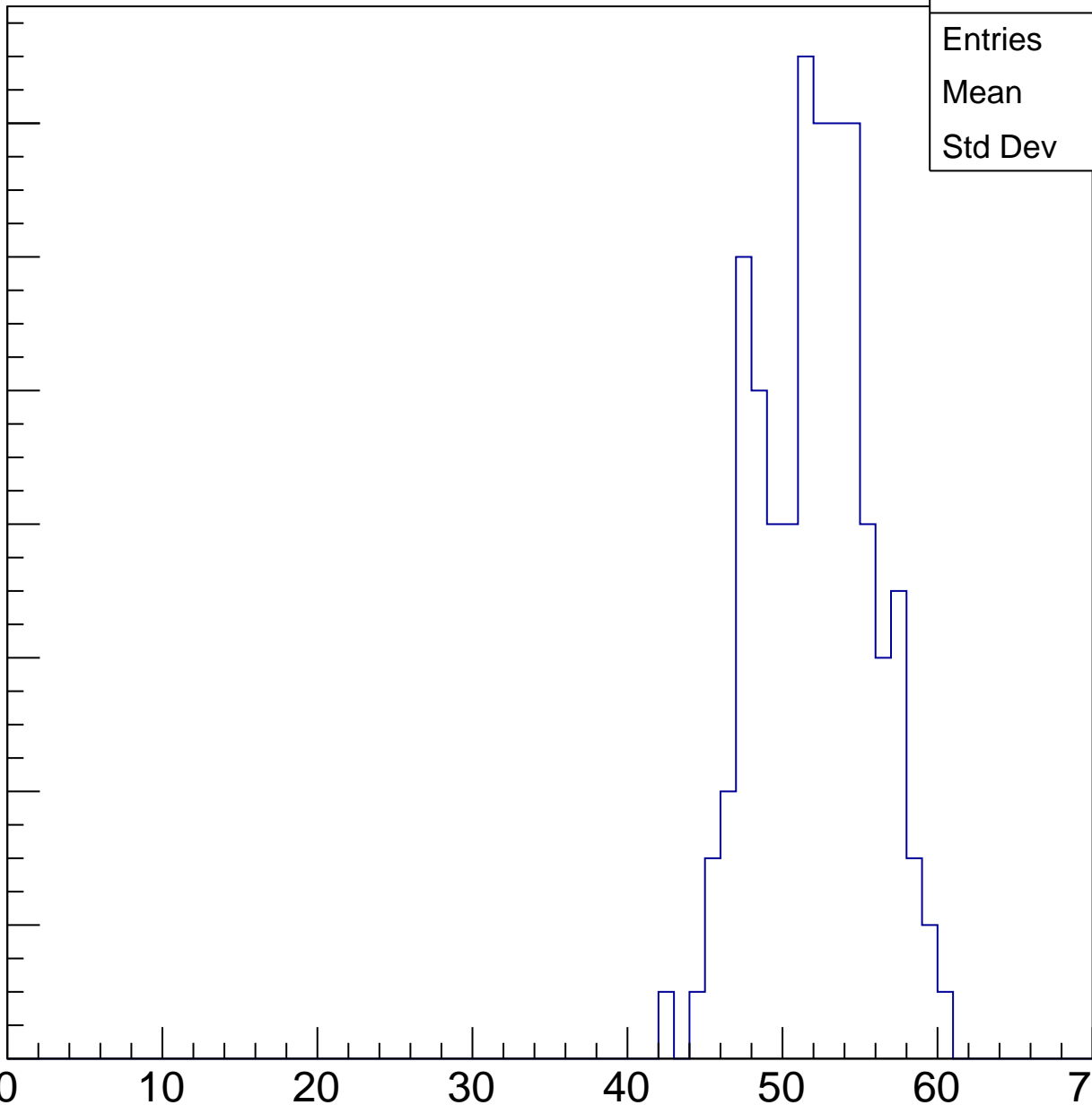
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	51.6
Std Dev	3.598

ampl



B0L101S, U20-ch4, adc4

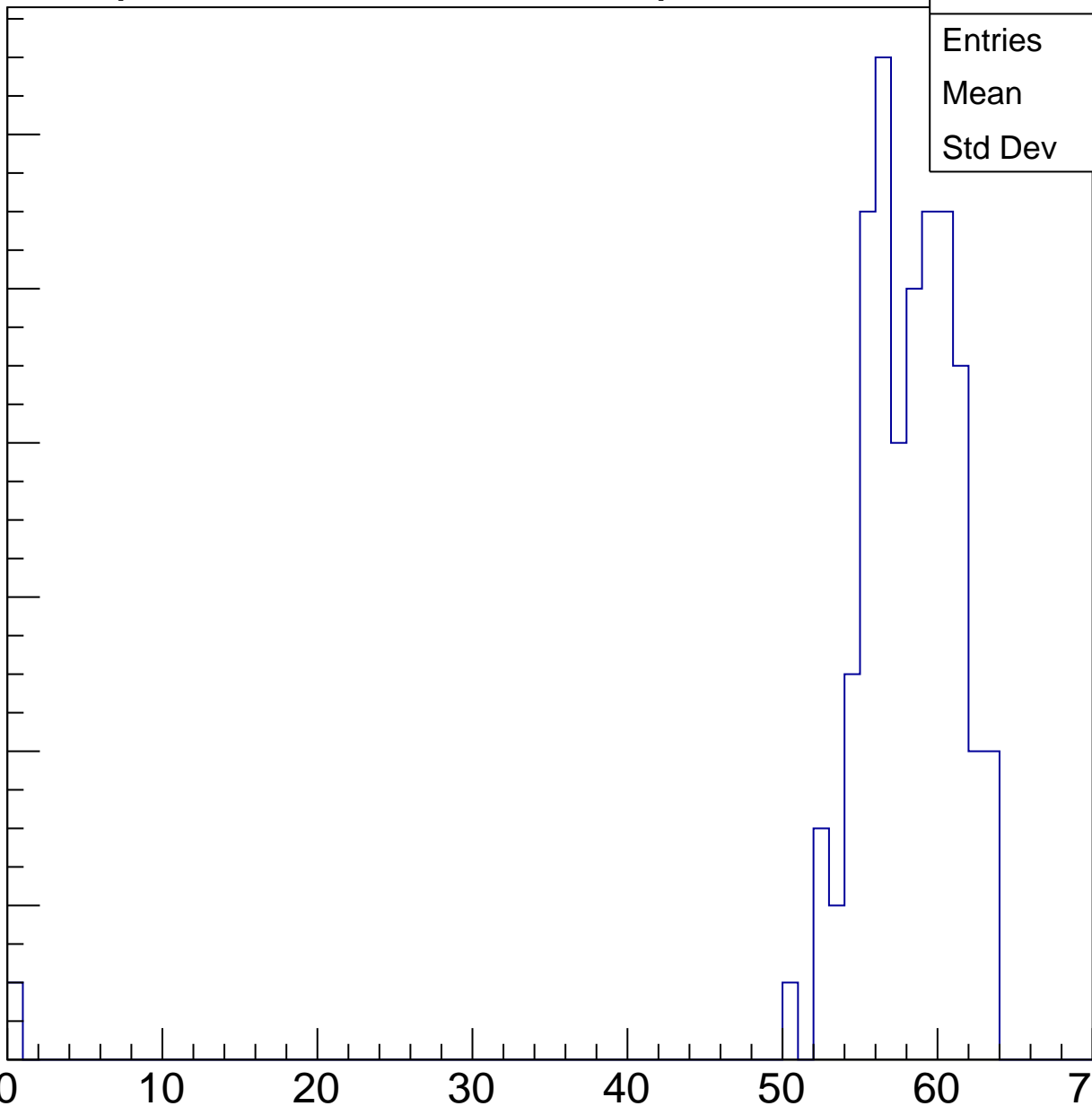
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	93
Mean	57.09
Std Dev	6.597

ampl



B0L101S, U20-ch4, adc5

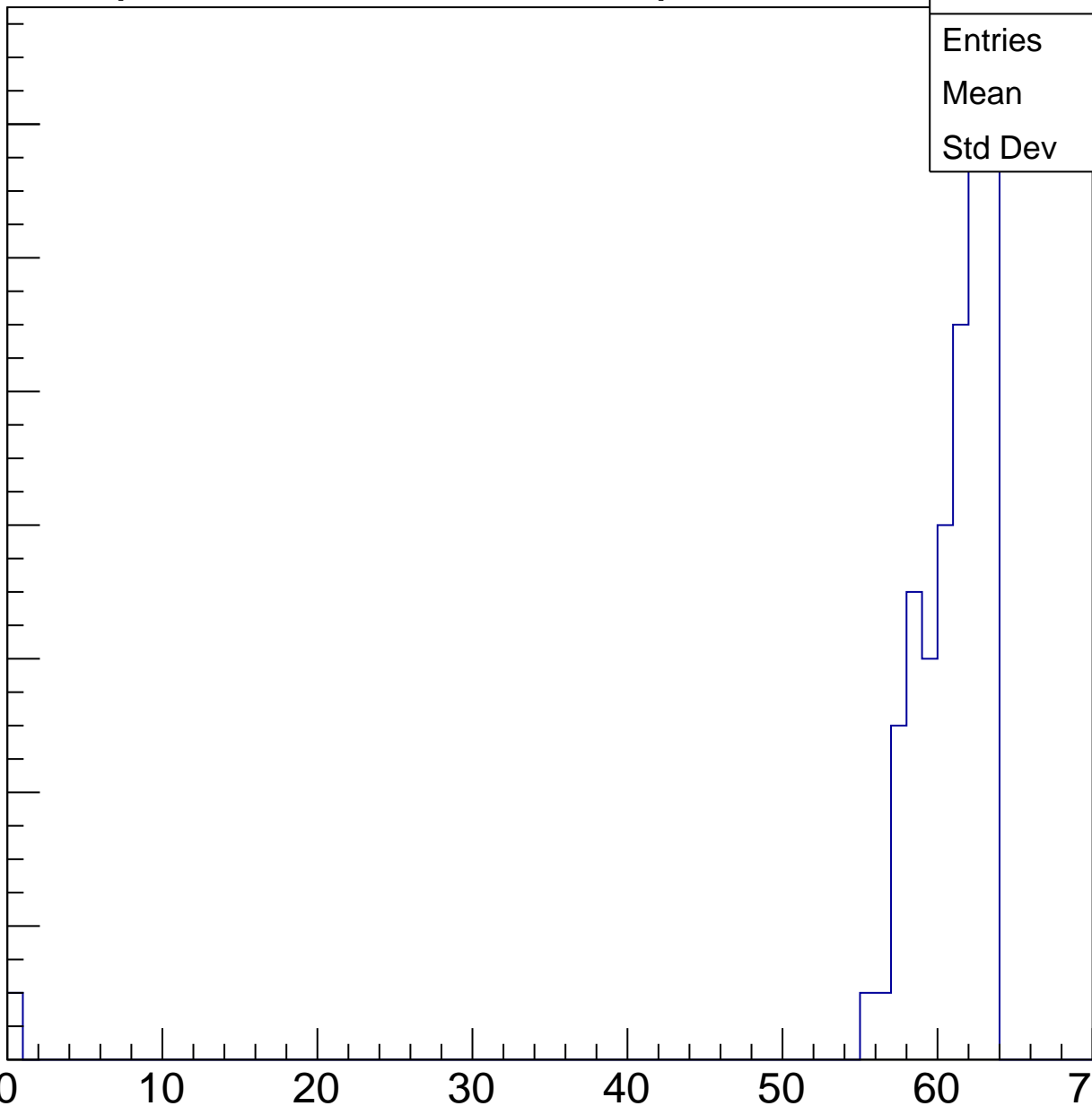
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	59.71
Std Dev	7.534

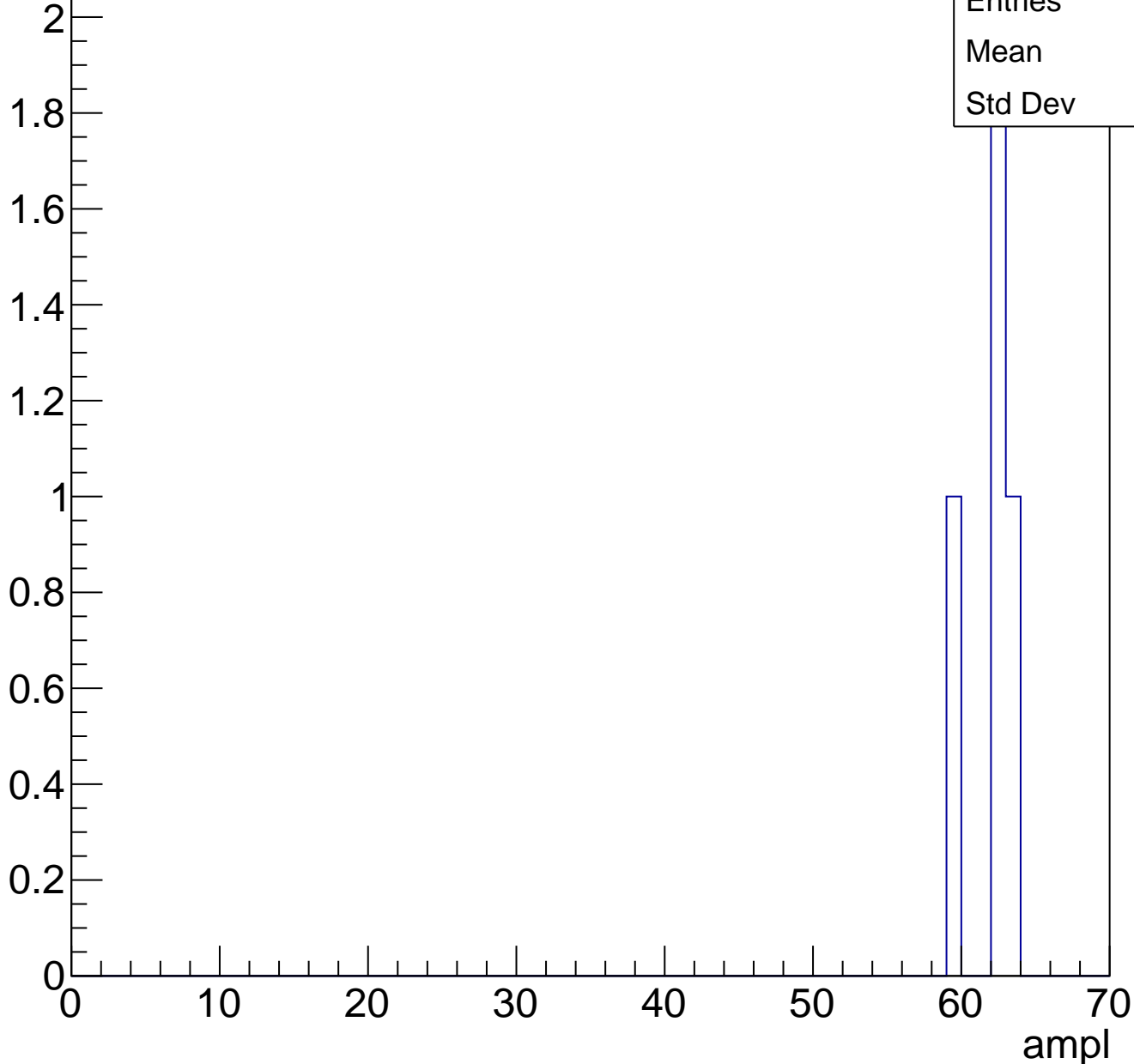
ampl



B0L101S, U20-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch5, adc0

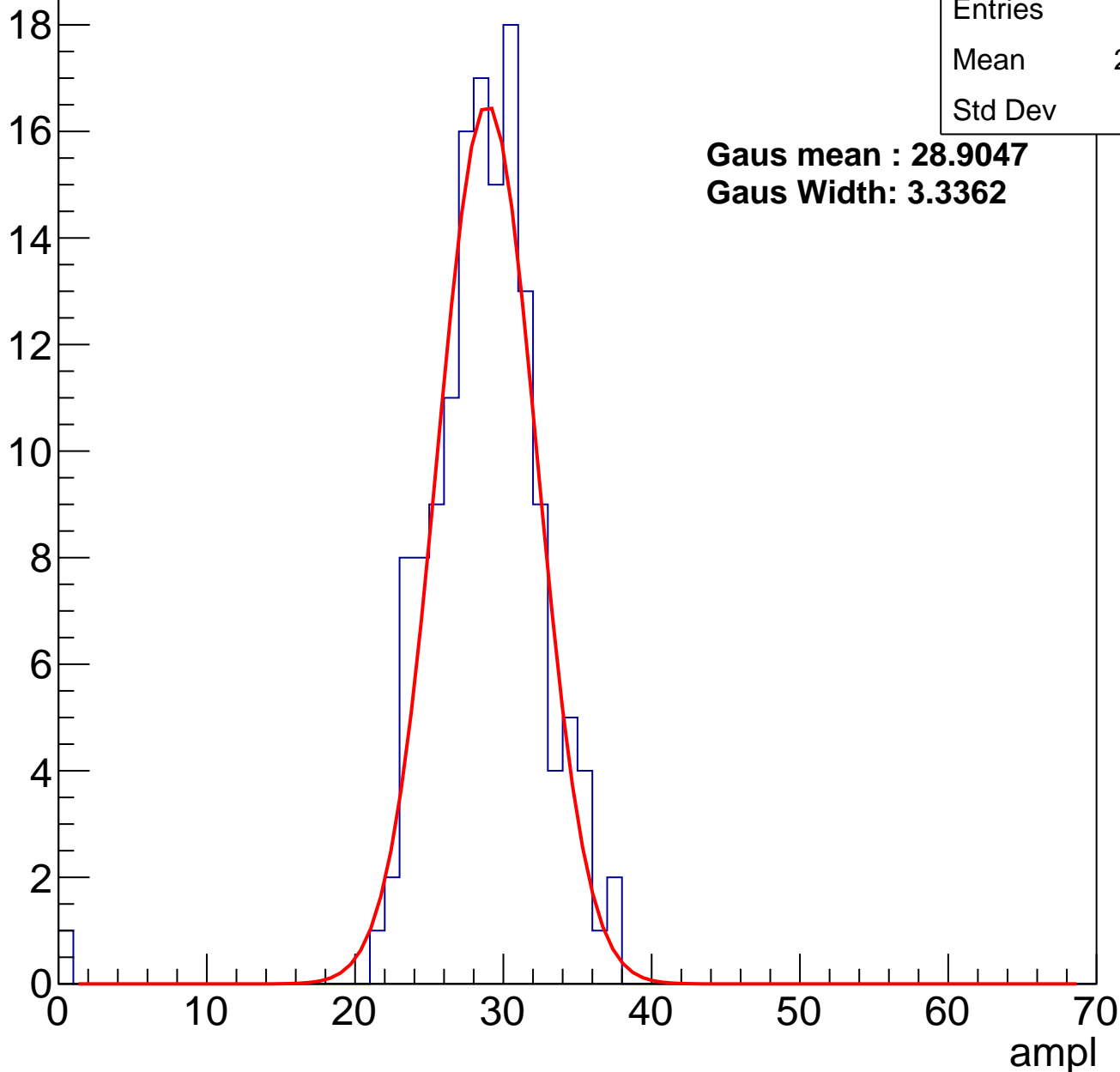
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.32
Std Dev	4.09

Gaus mean : 28.9047

Gaus Width: 3.3362

Entry



B0L101S, U20-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	35.47
Std Dev	3.747

Gaus mean : 36.4397

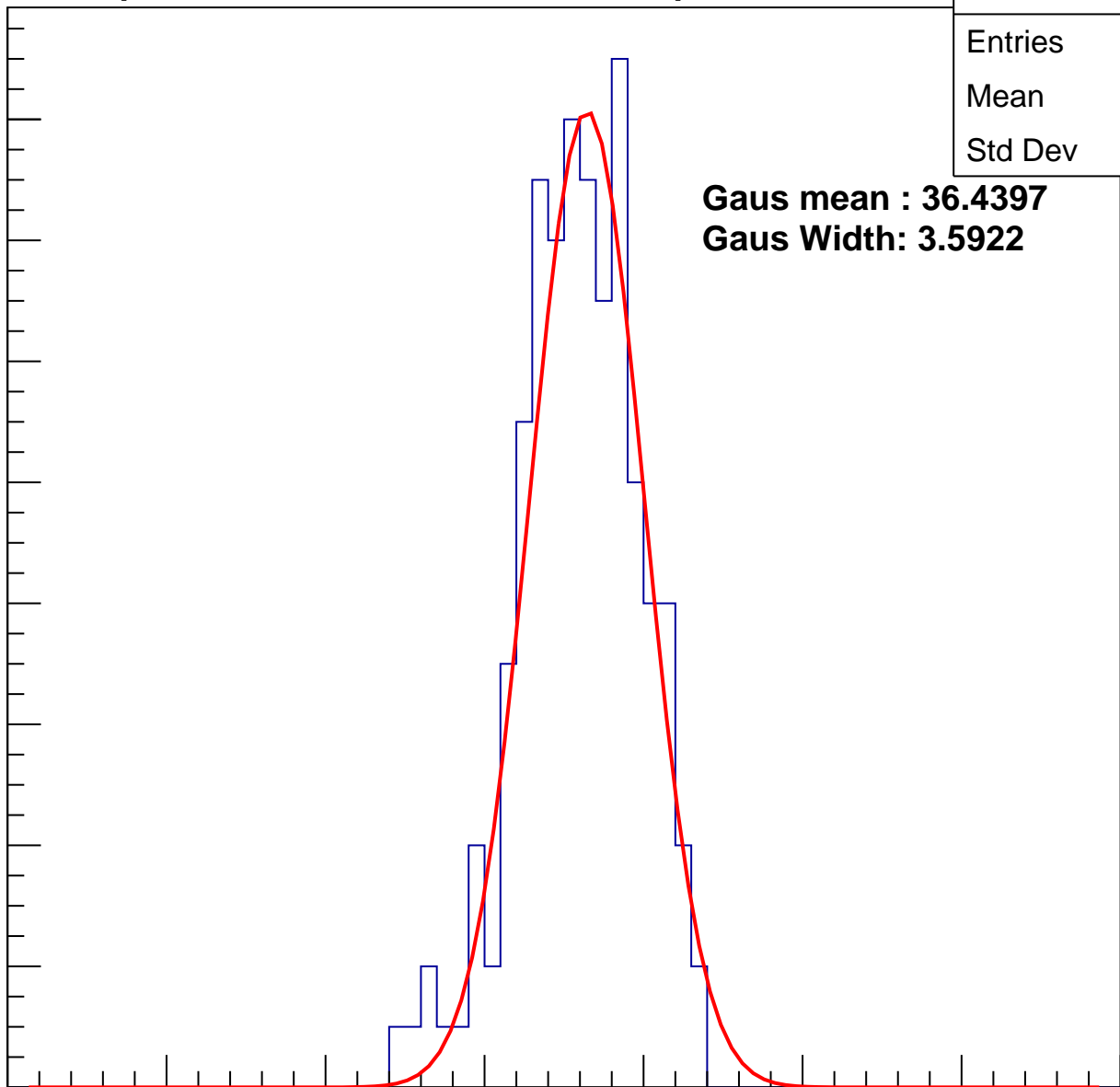
Gaus Width: 3.5922

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

121

Mean

42.52

Std Dev

3.493

Gaus mean : 42.4879

Gaus Width: 3.0060

0

10

20

30

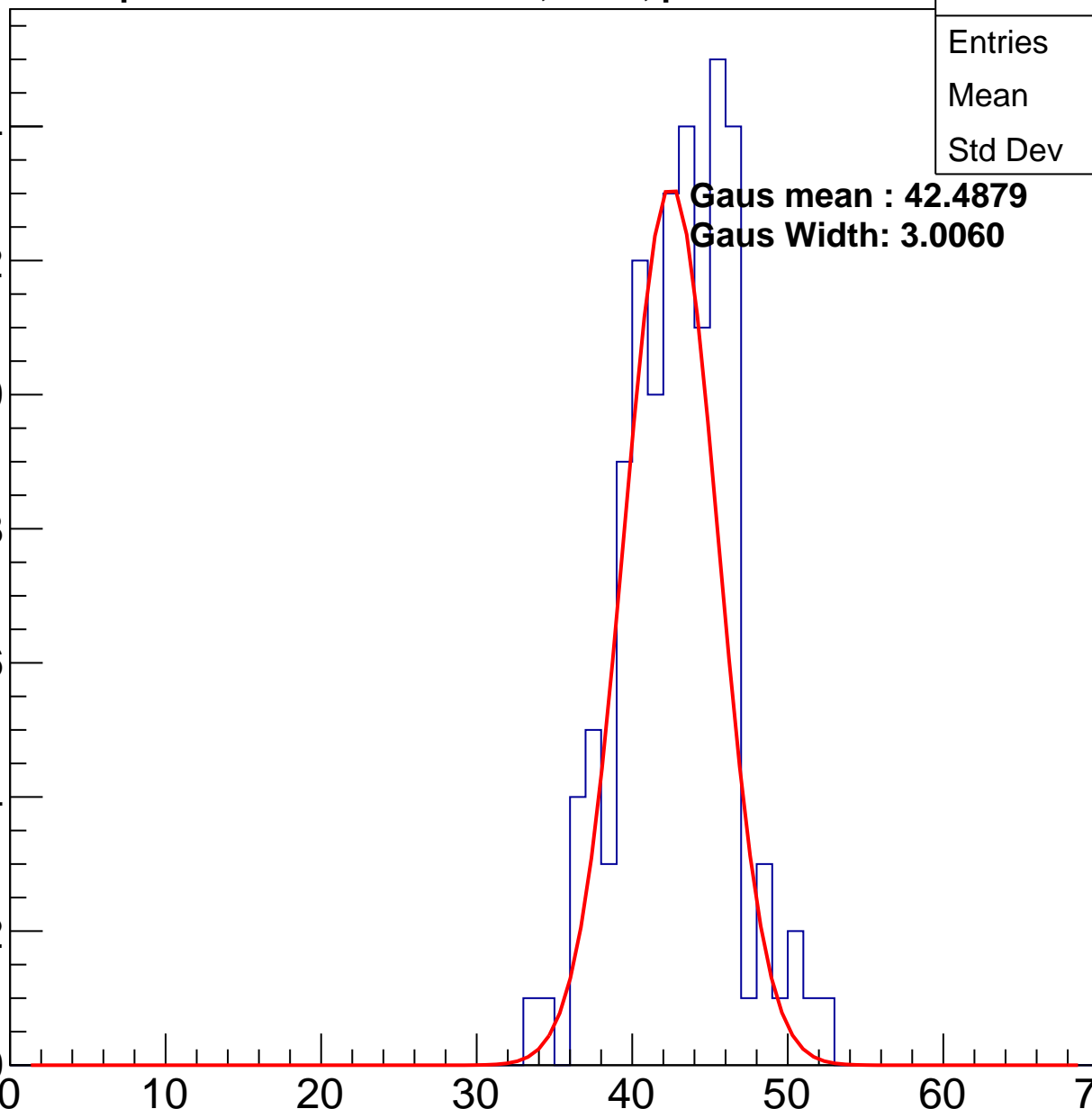
40

50

60

70

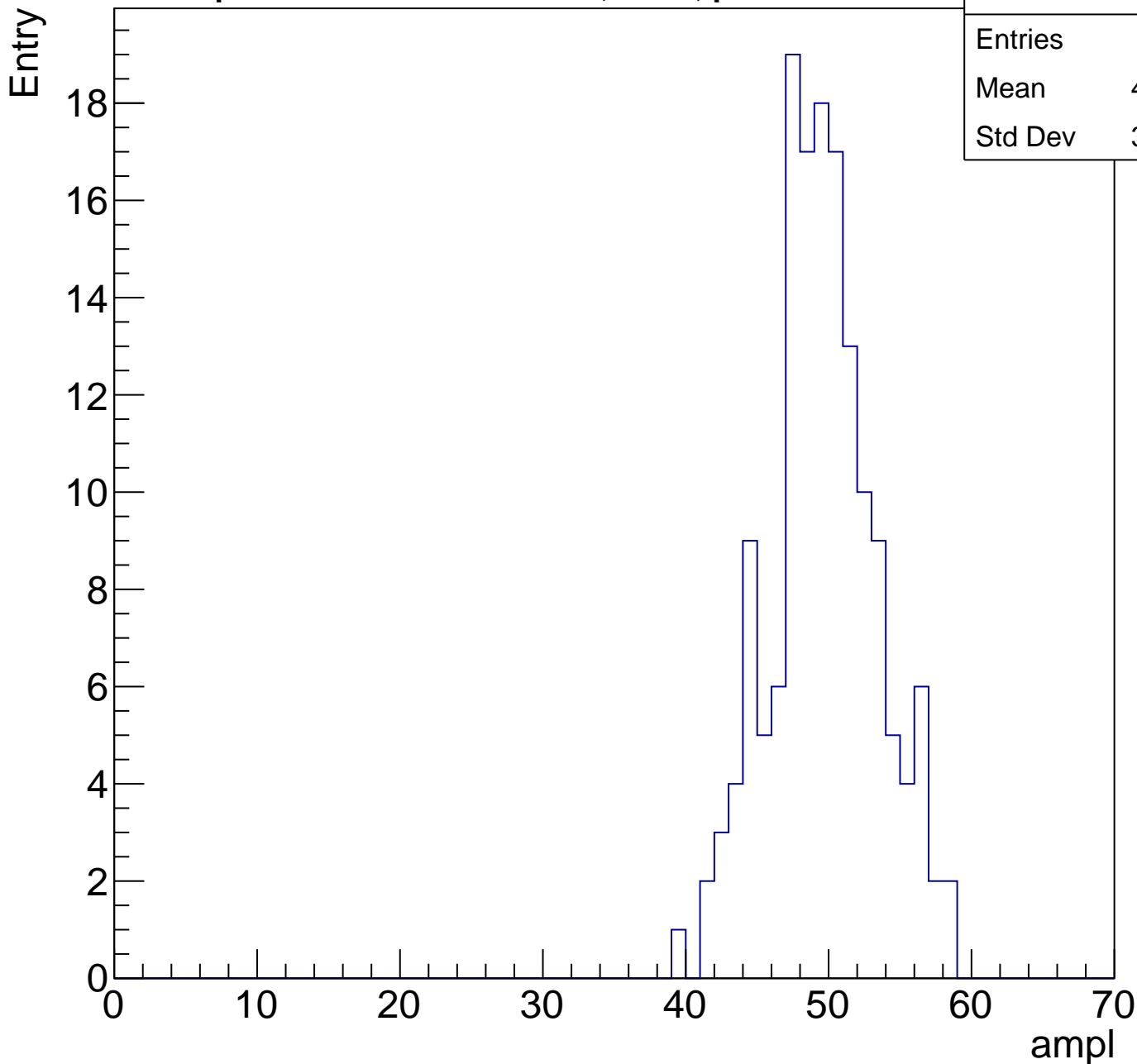
ampl



B0L101S, U20-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	49.16
Std Dev	3.737



B0L101S, U20-ch5, adc4

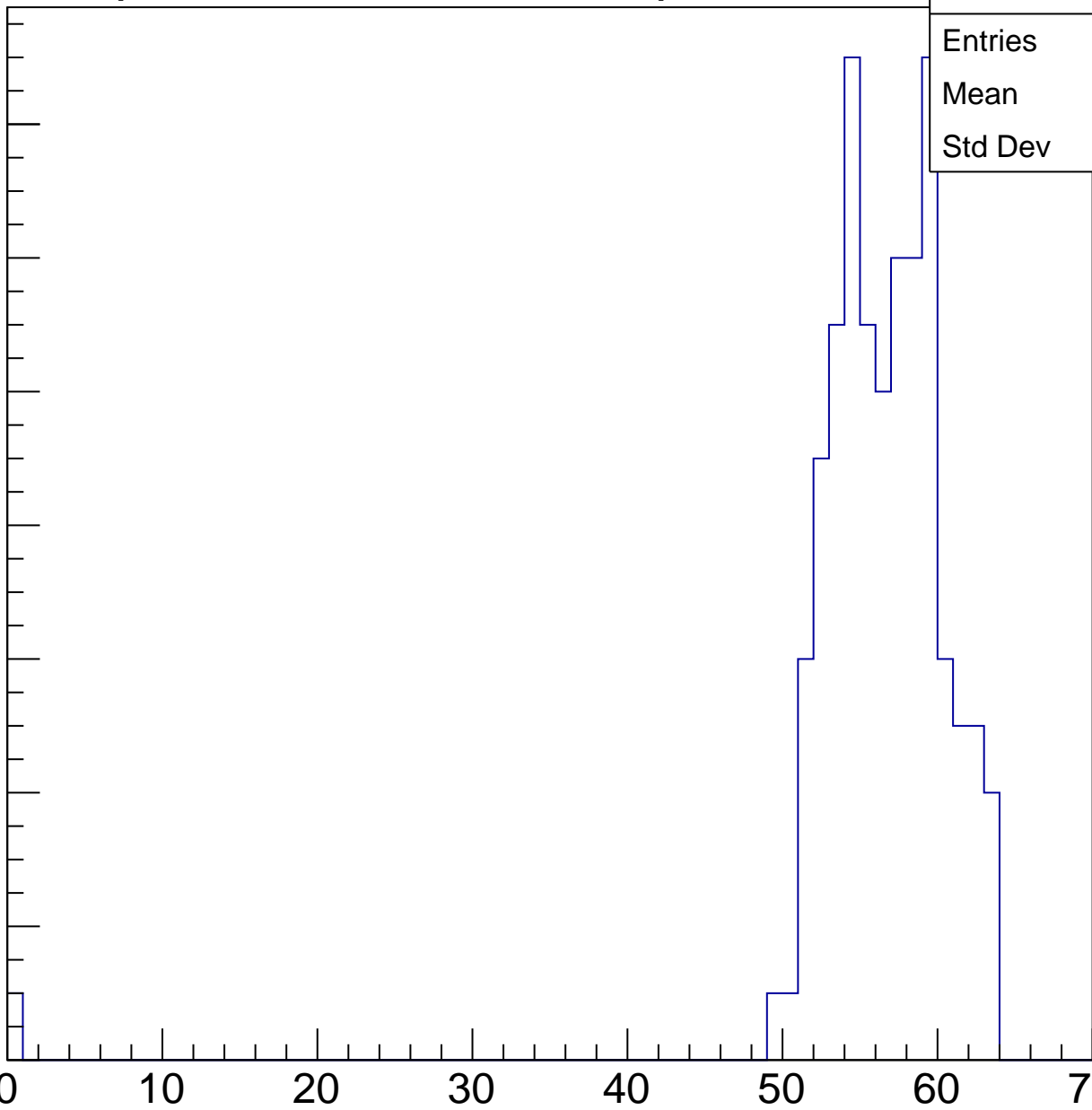
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	124
Mean	55.83
Std Dev	6.006

ampl



B0L101S, U20-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

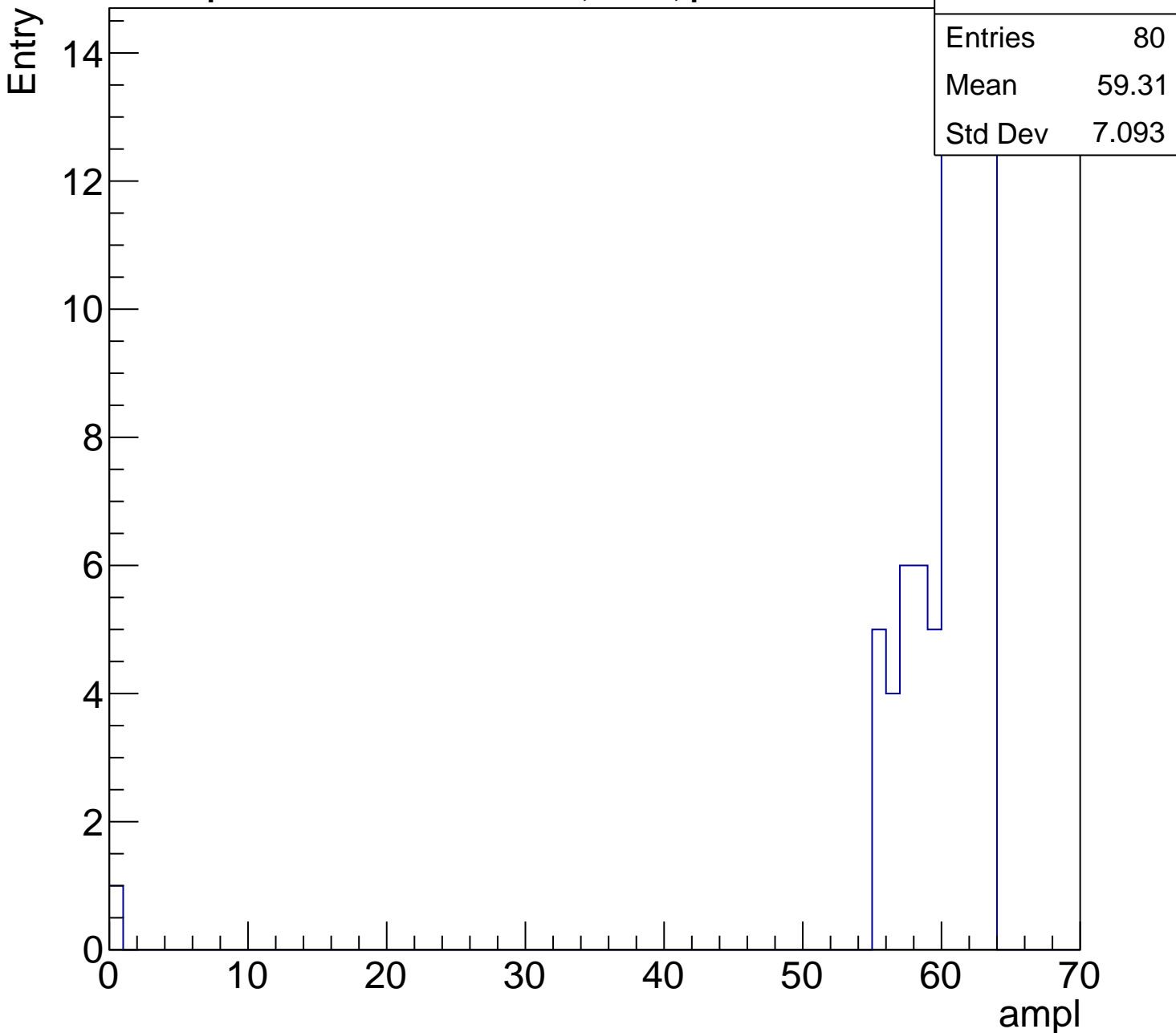
Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.31
Std Dev	7.093

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

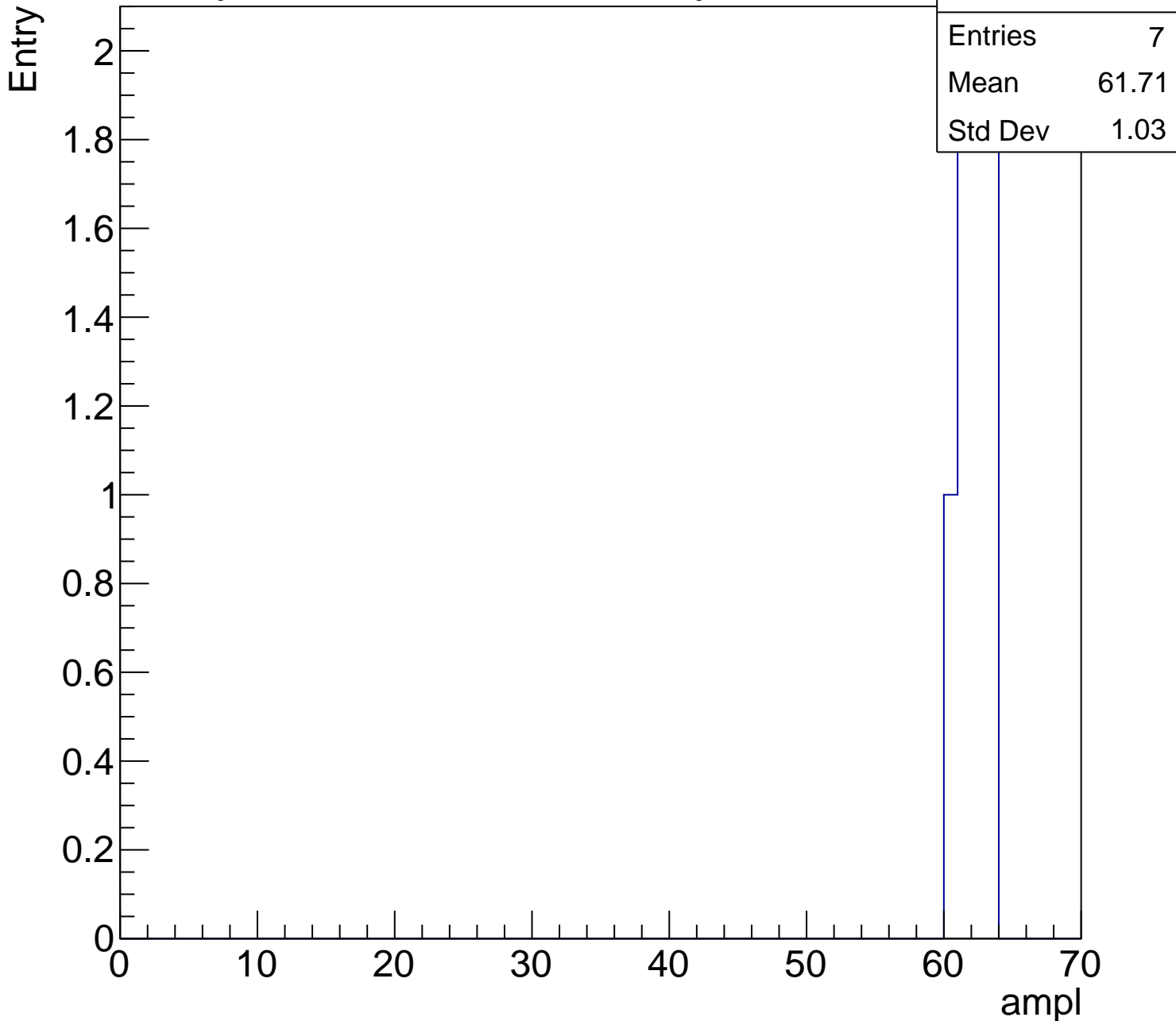
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.71
Std Dev	1.03

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

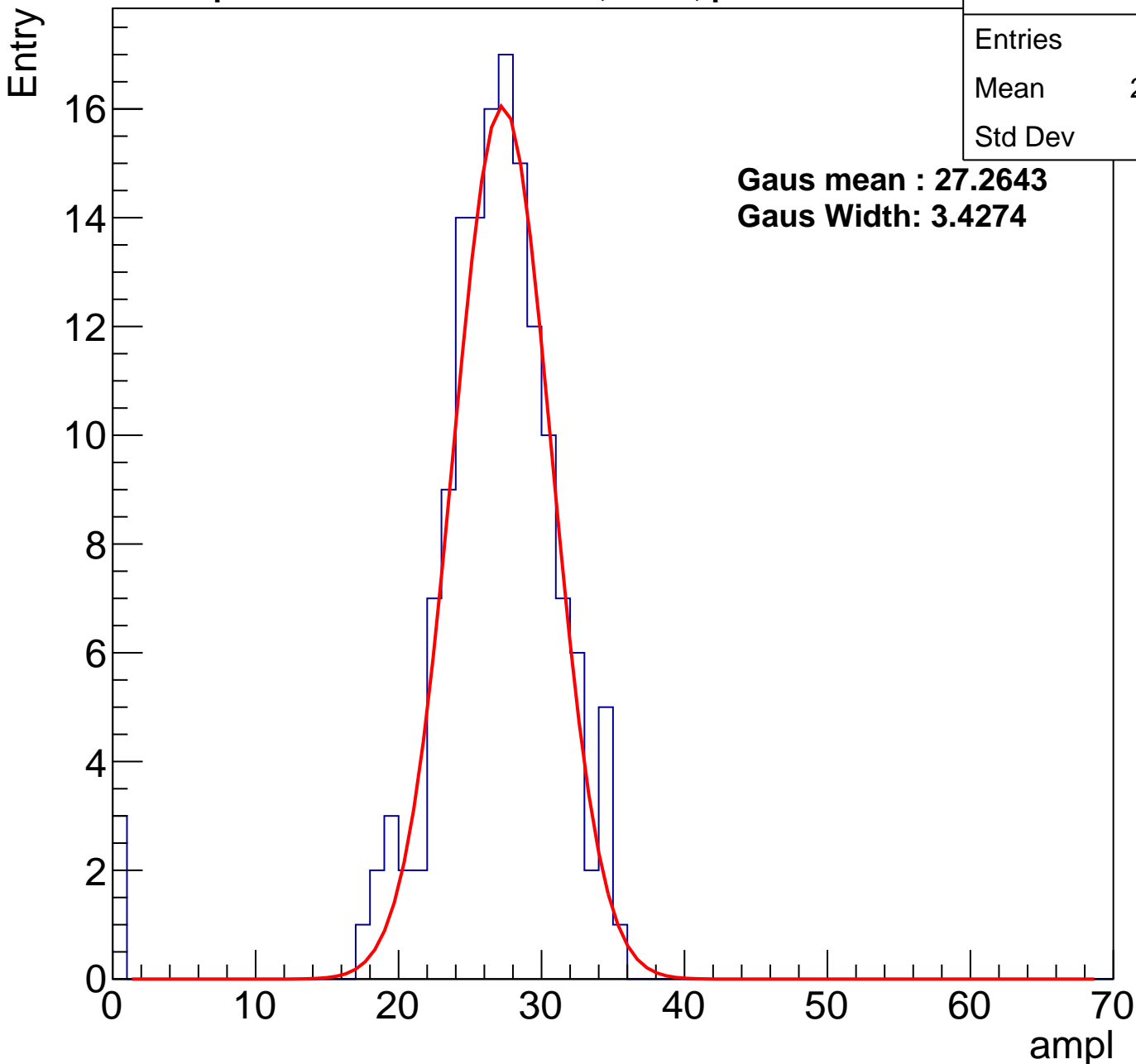
B0L101S, U20-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	26.09
Std Dev	5.2

Gaus mean : 27.2643

Gaus Width: 3.4274



B0L101S, U20-ch6, adc1

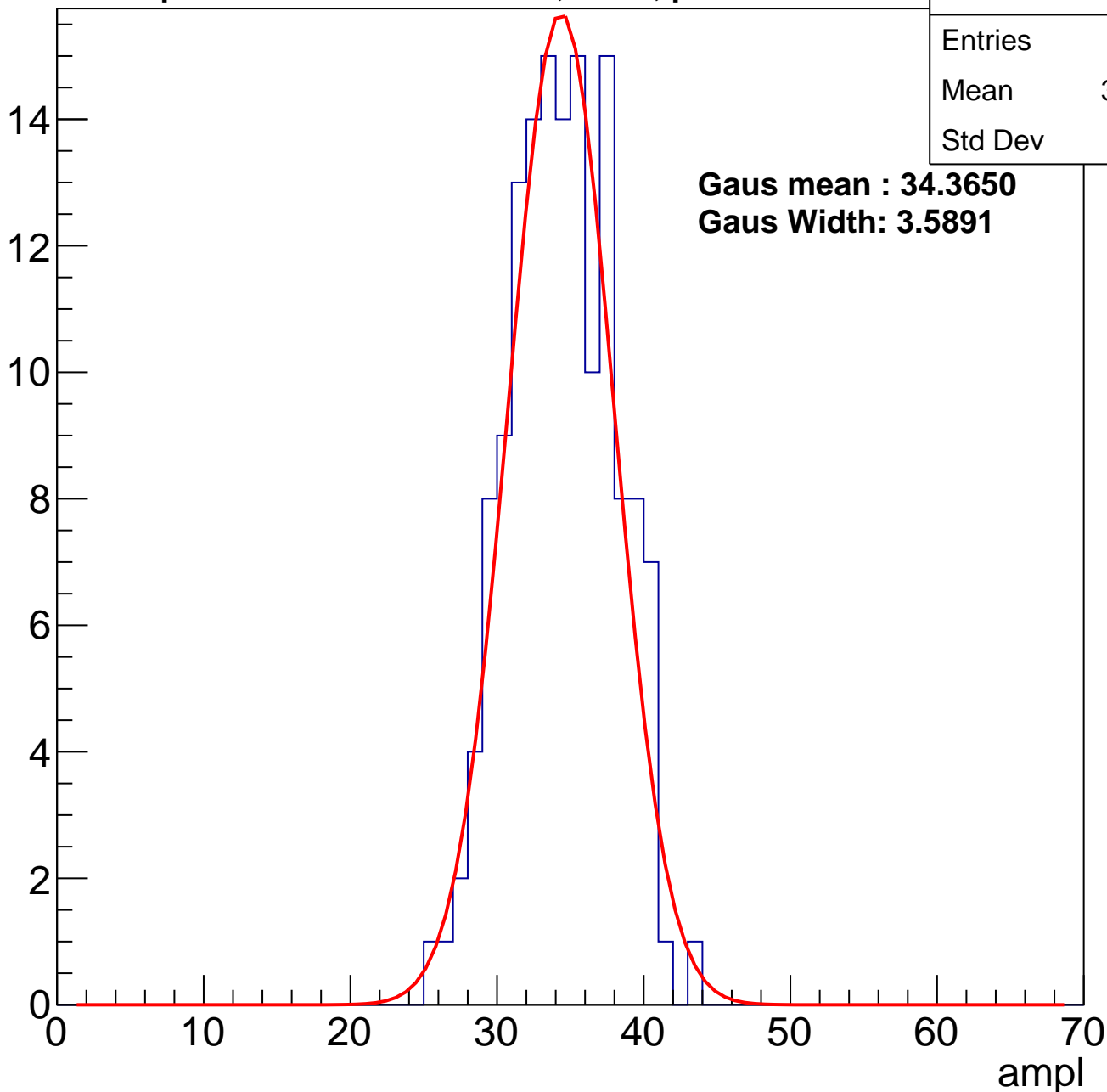
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	33.98
Std Dev	3.53

Gaus mean : 34.3650

Gaus Width: 3.5891

Entry



B0L101S, U20-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	41.06
Std Dev	3.622

Gaus mean : 41.5795

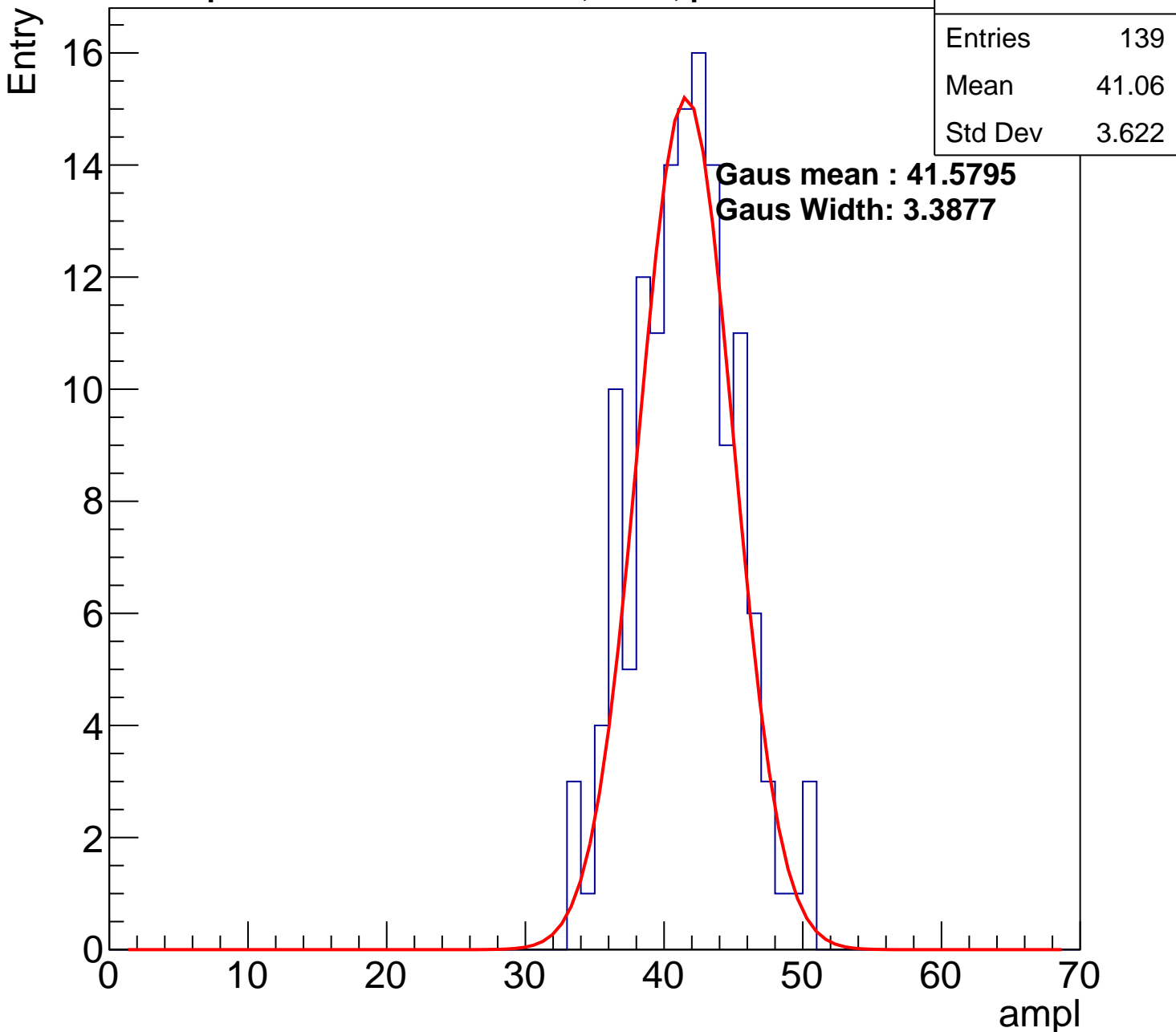
Gaus Width: 3.3877

Entry

16
14
12
10
8
6
4
2
0

ampl

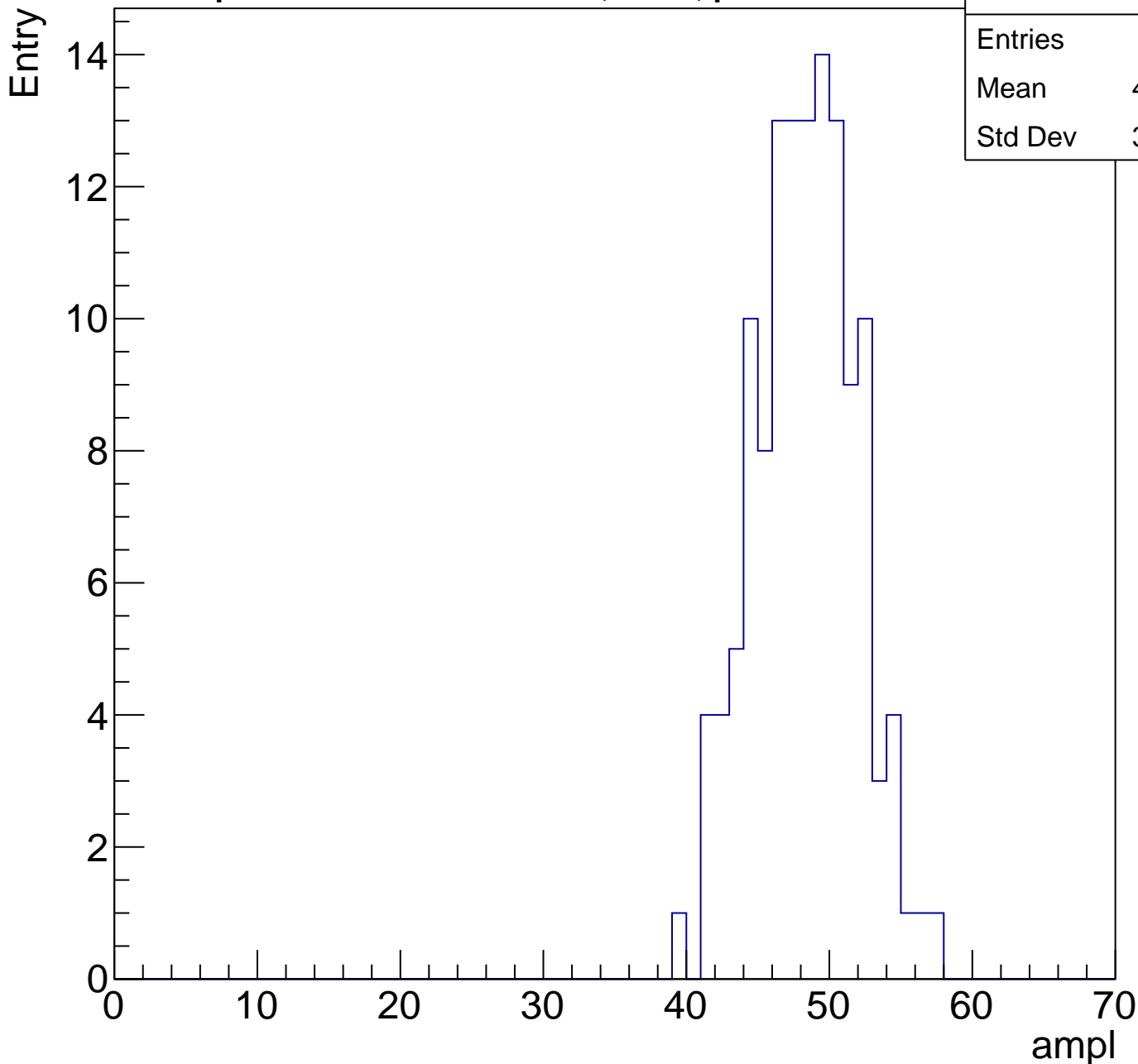
0 10 20 30 40 50 60 70



B0L101S, U20-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

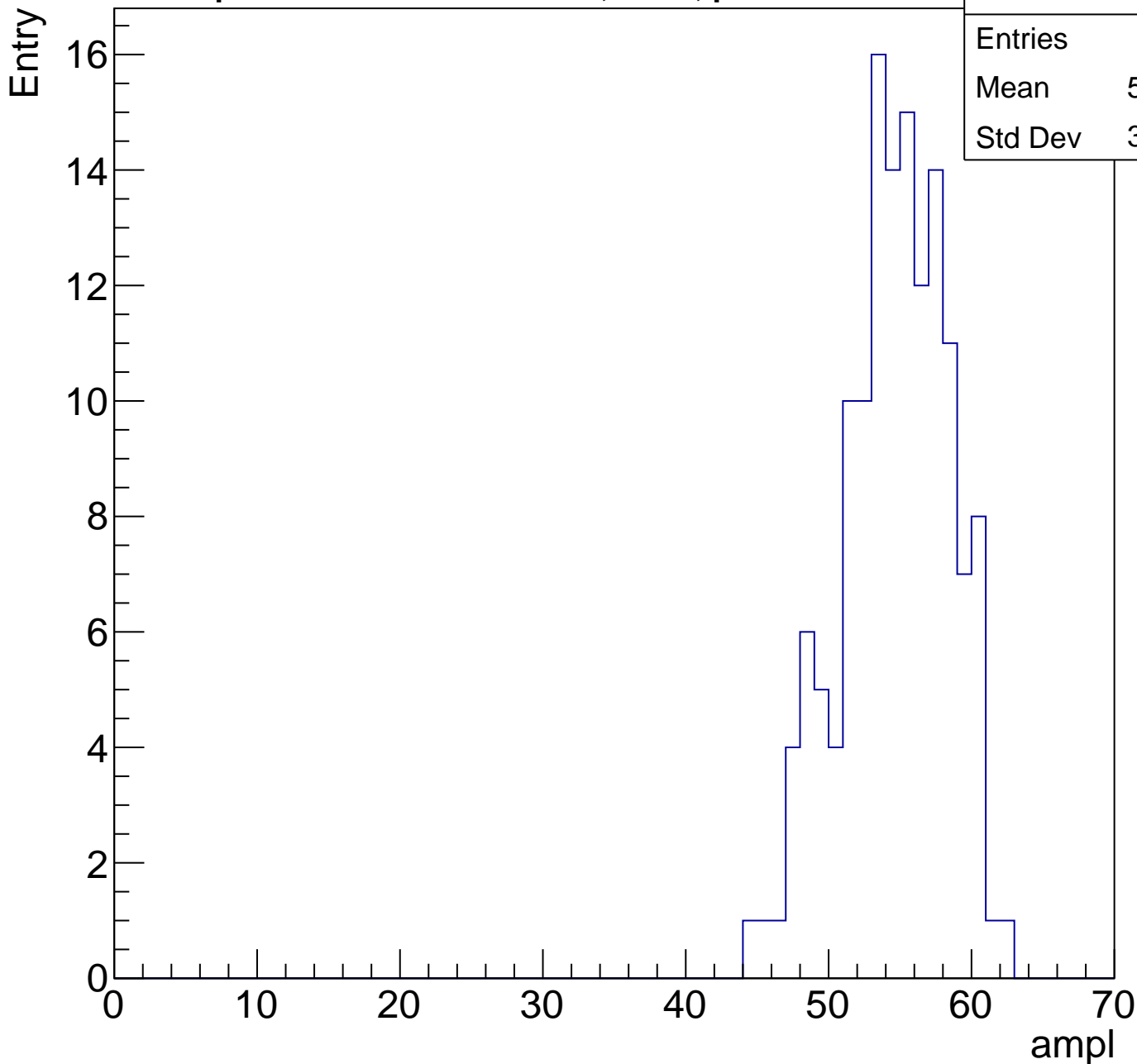
Entries	127
Mean	47.85
Std Dev	3.505



B0L101S, U20-ch6, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	54.18
Std Dev	3.698



B0L101S, U20-ch6, adc5

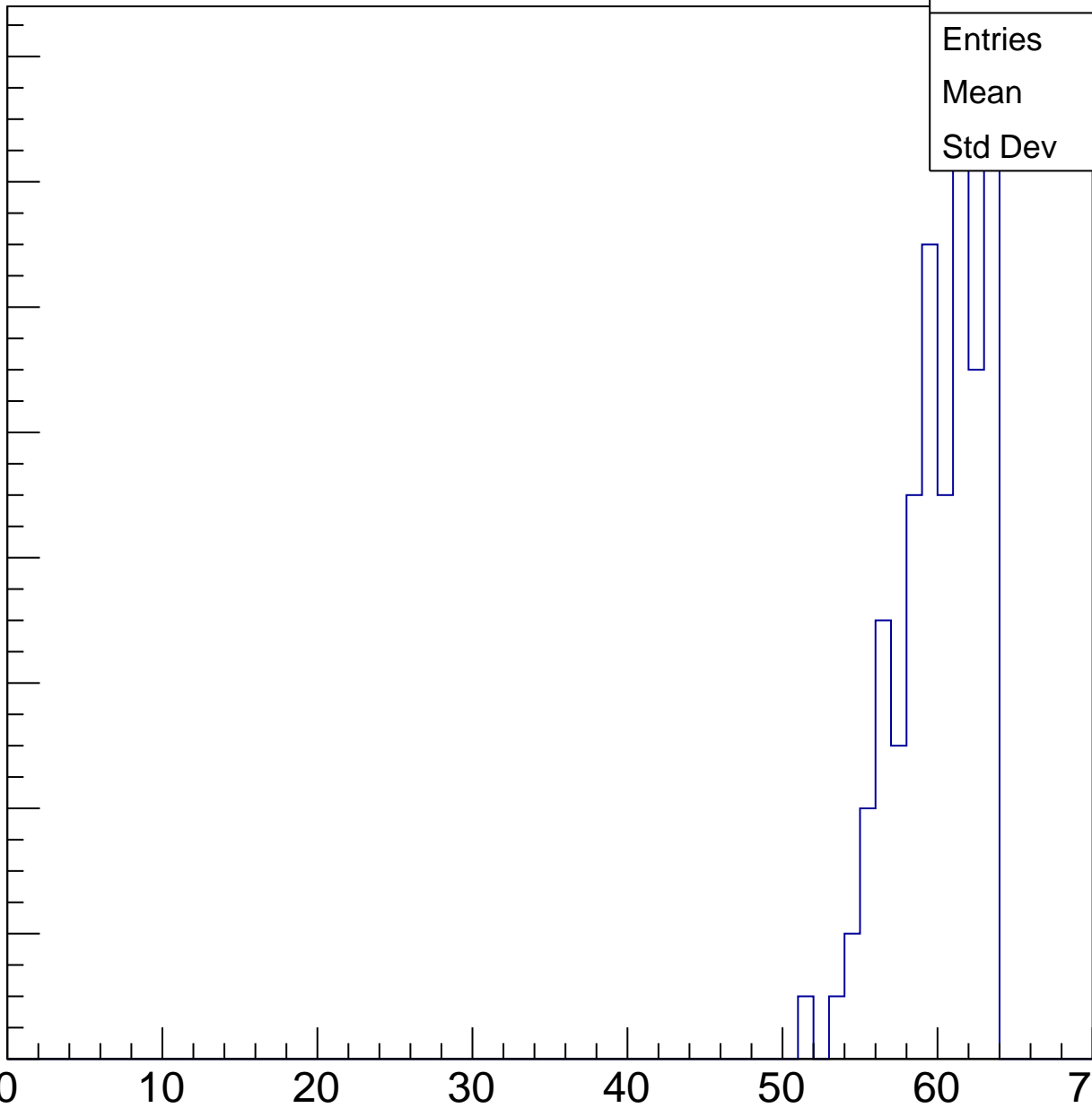
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.6
Std Dev	2.736

ampl

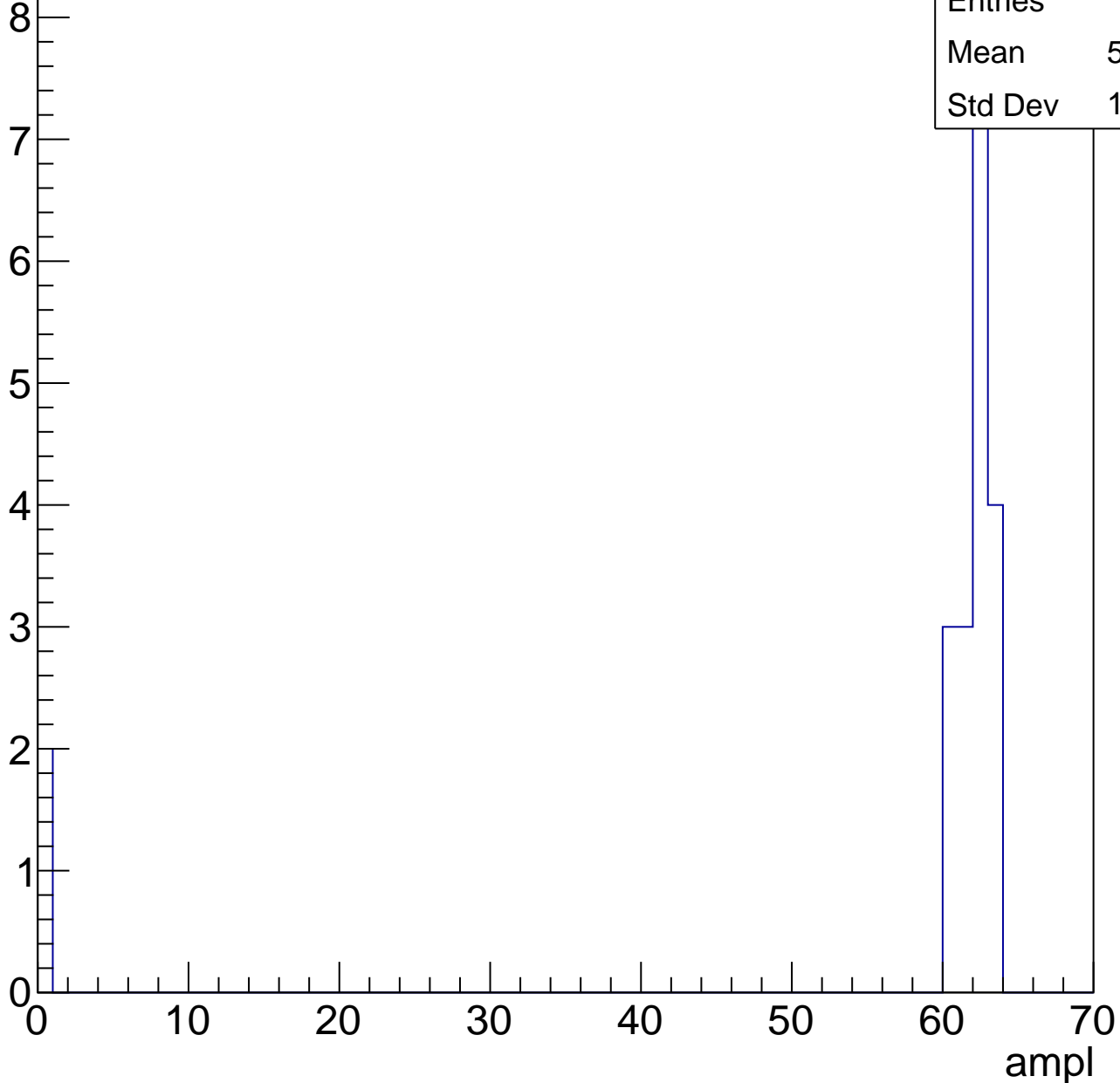


B0L101S, U20-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	55.55
Std Dev	18.54



B0L101S, U20-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	29
Std Dev	3.342

Gaus mean : 29.9341

Gaus Width: 3.1725

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

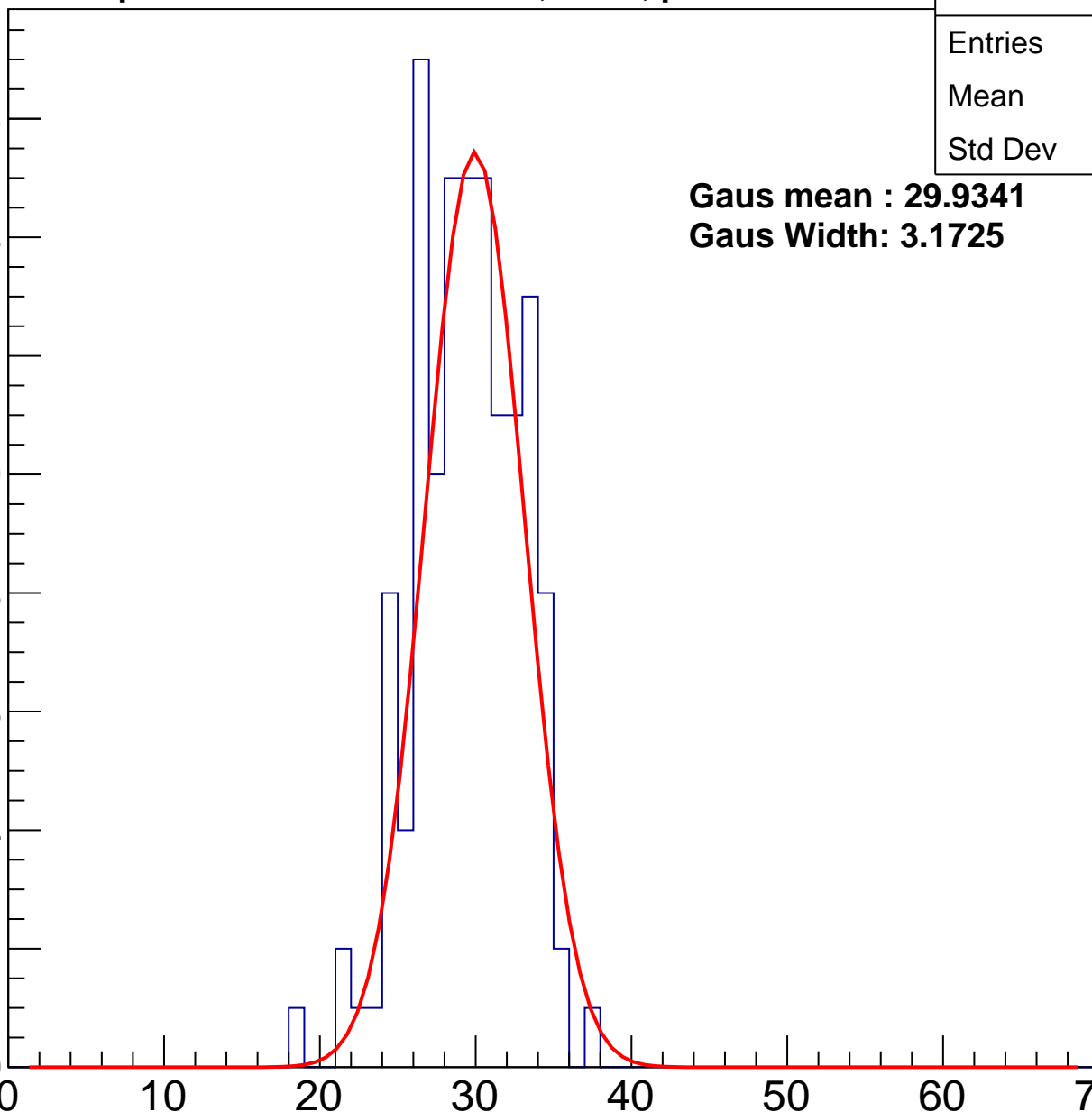
40

50

60

70

ampl



B0L101S, U20-ch7, adc1

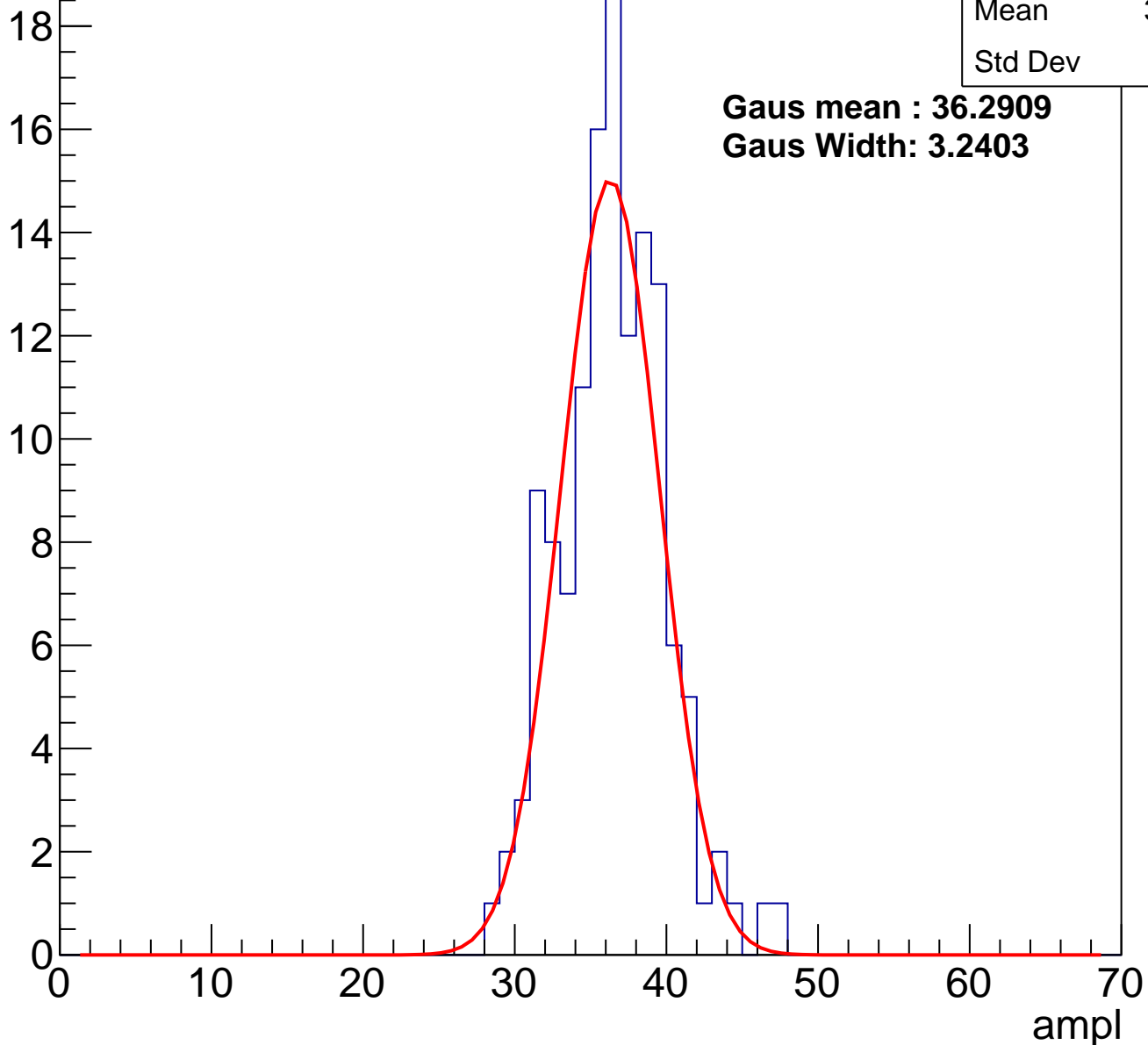
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	36.01
Std Dev	3.45

Gaus mean : 36.2909

Gaus Width: 3.2403

Entry



B0L101S, U20-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

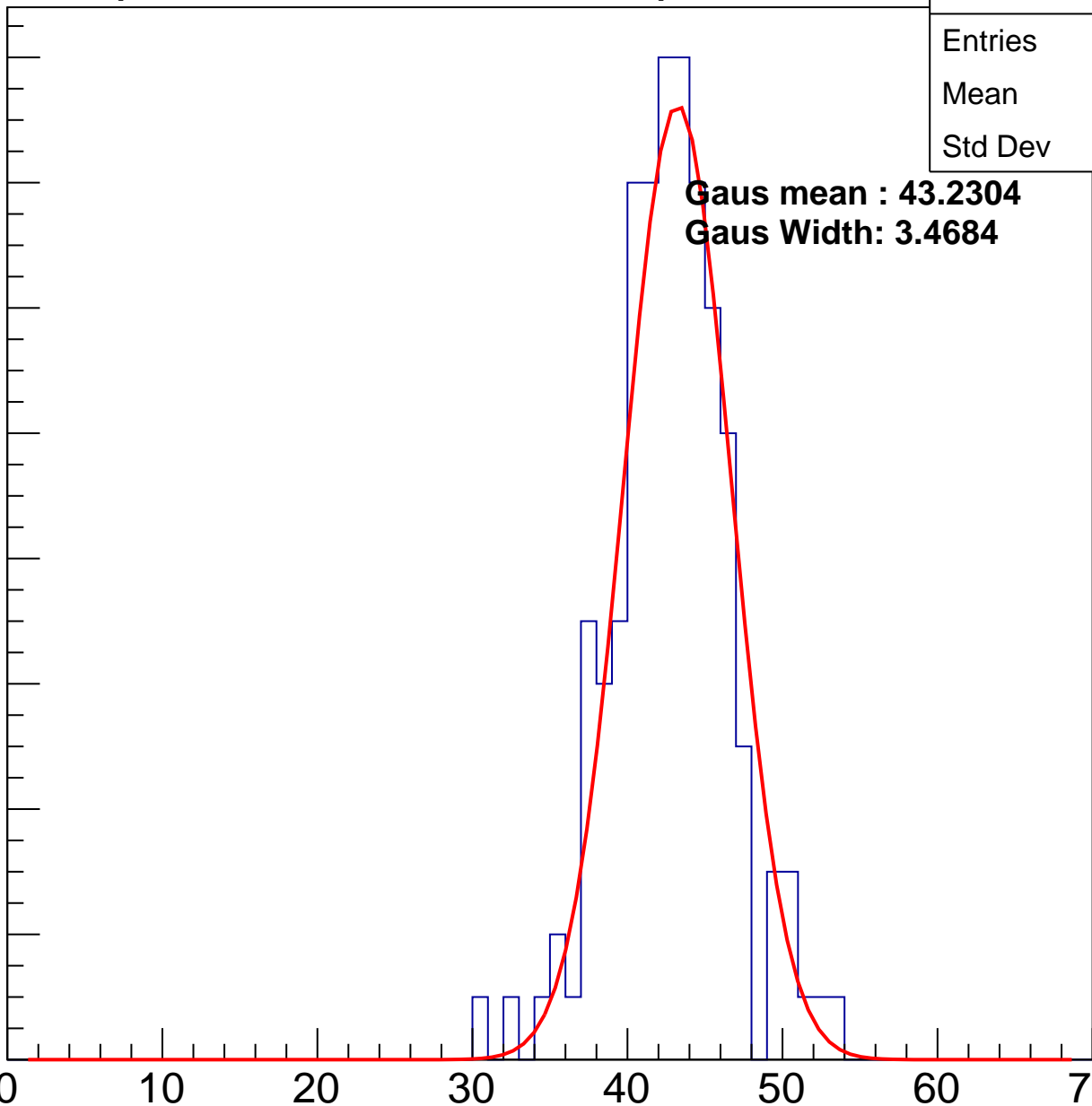
16
14
12
10
8
6
4
2
0

Entries	136
Mean	42.35
Std Dev	3.772

Gaus mean : 43.2304

Gaus Width: 3.4684

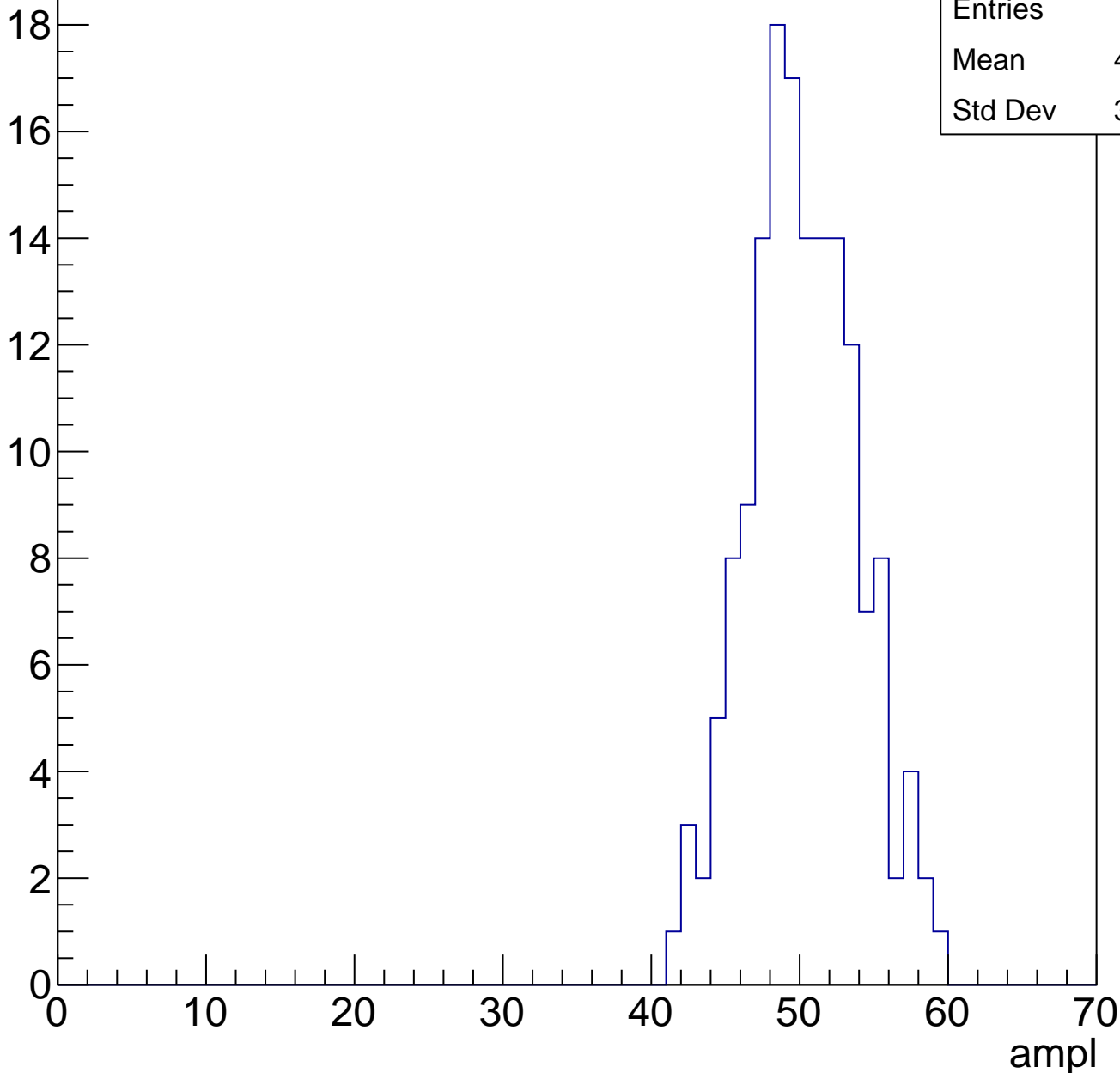
ampl



B0L101S, U20-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

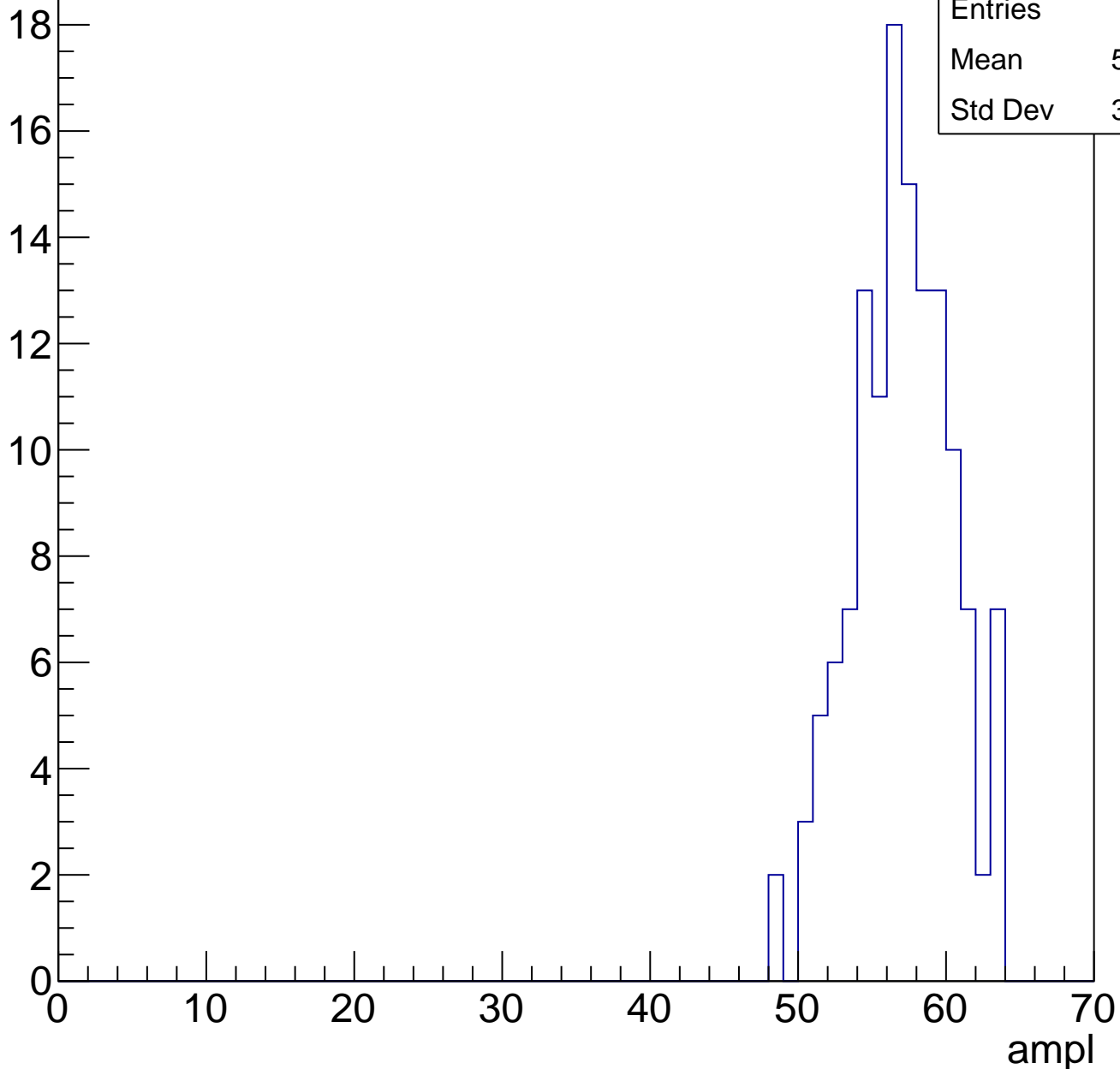
Entry



B0L101S, U20-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch7, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

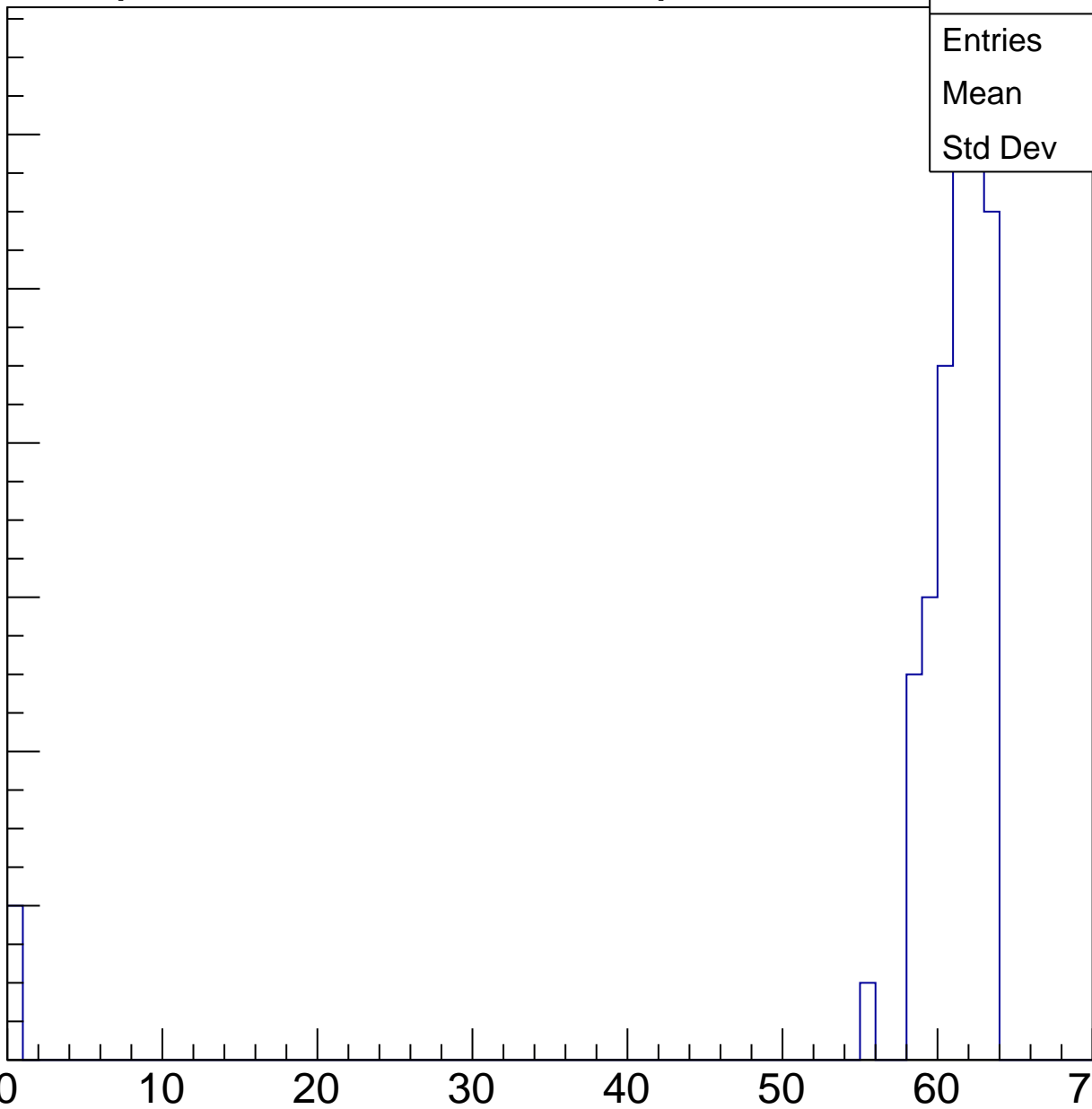
0

Entries	59
---------	----

Mean	58.81
------	-------

Std Dev	11.15
---------	-------

ampl

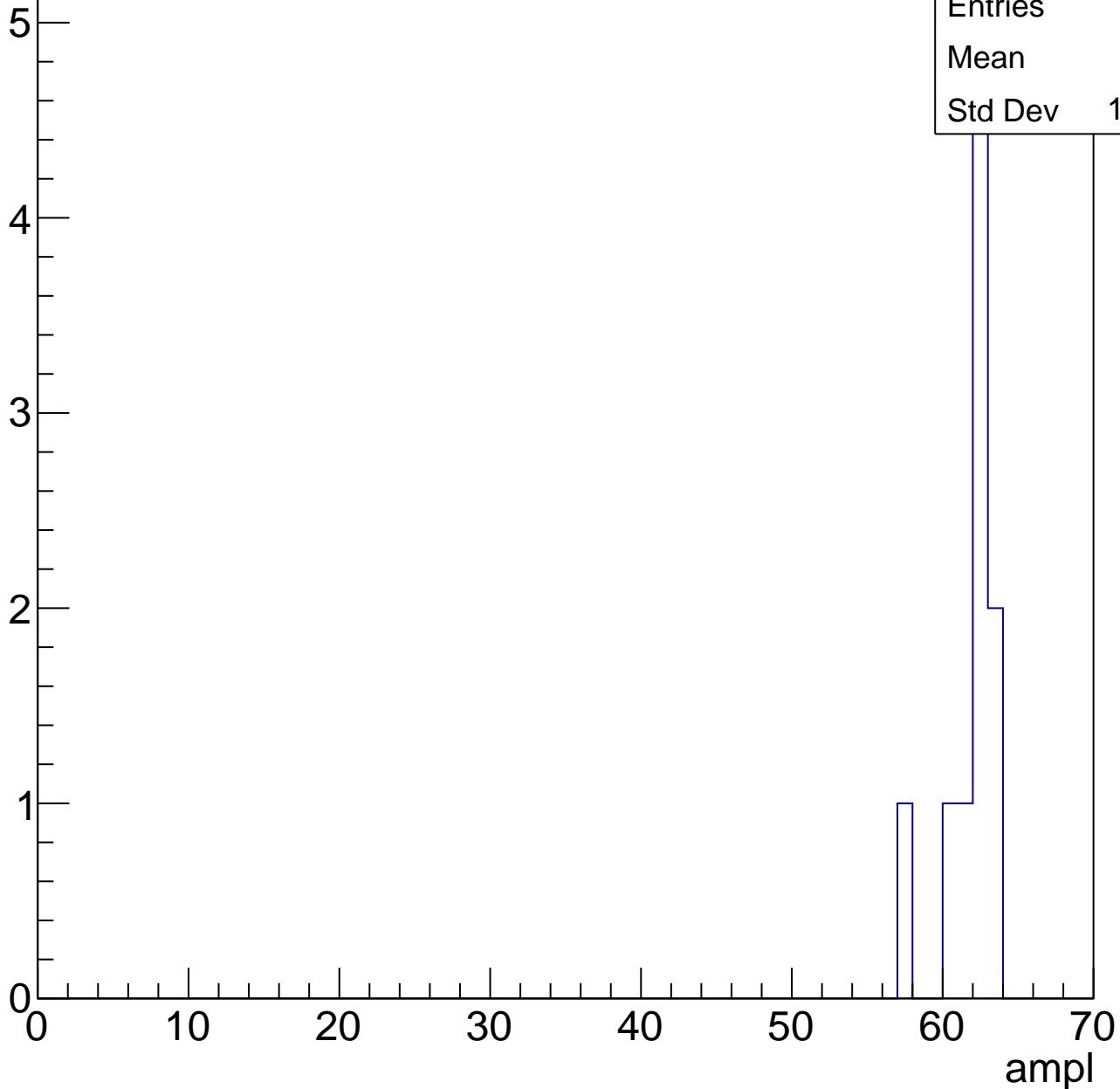


B0L101S, U20-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	61.4
Std Dev	1.685



B0L101S, U20-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

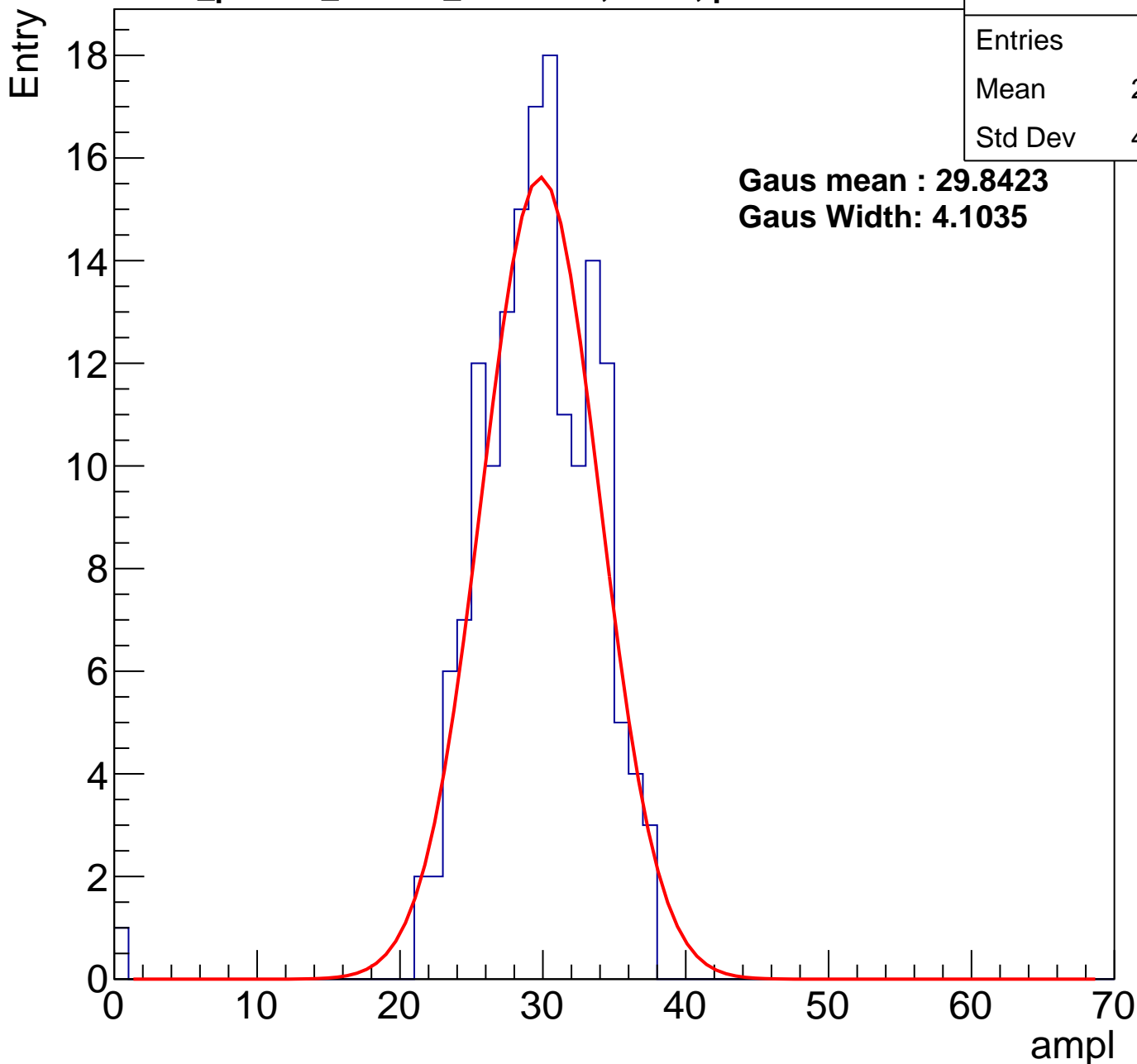
B0L101S, U20-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	29.12
Std Dev	4.338

Gaus mean : 29.8423

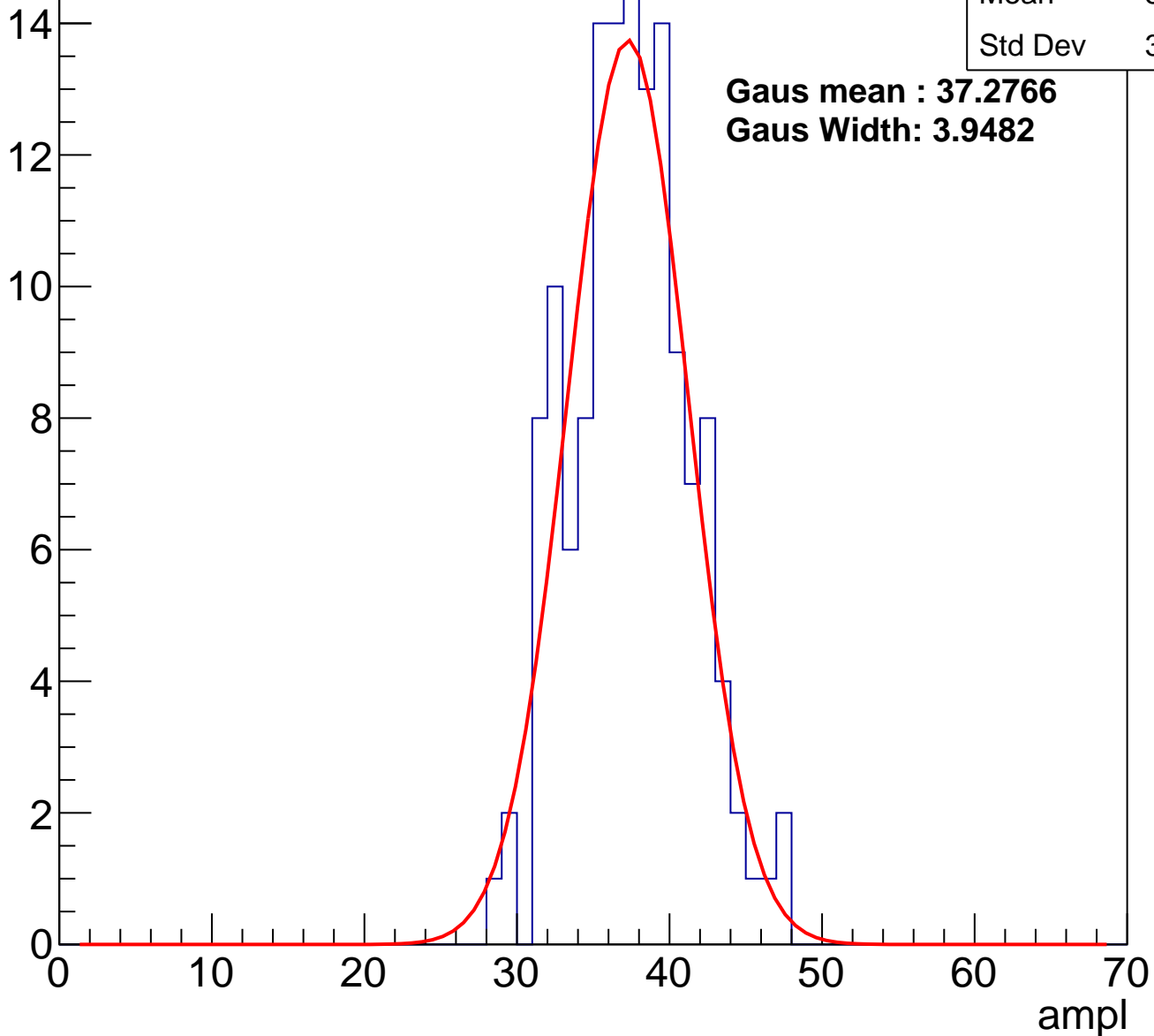
Gaus Width: 4.1035



B0L101S, U20-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

133

Mean

43.41

Std Dev

3.503

Gaus mean : 43.9246

Gaus Width: 3.6223

0

10

20

30

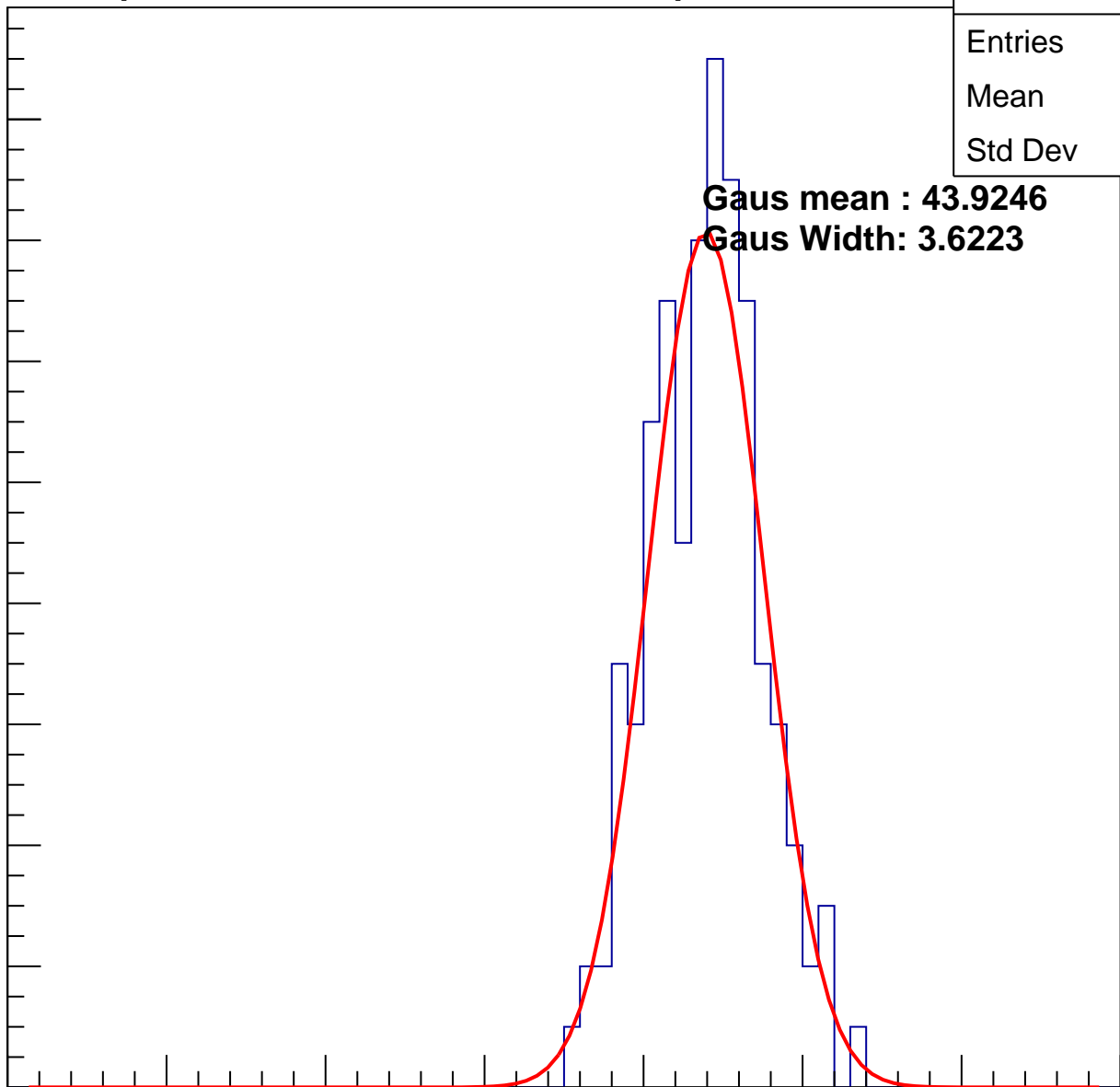
40

50

60

70

ampl



B0L101S, U20-ch8, adc3

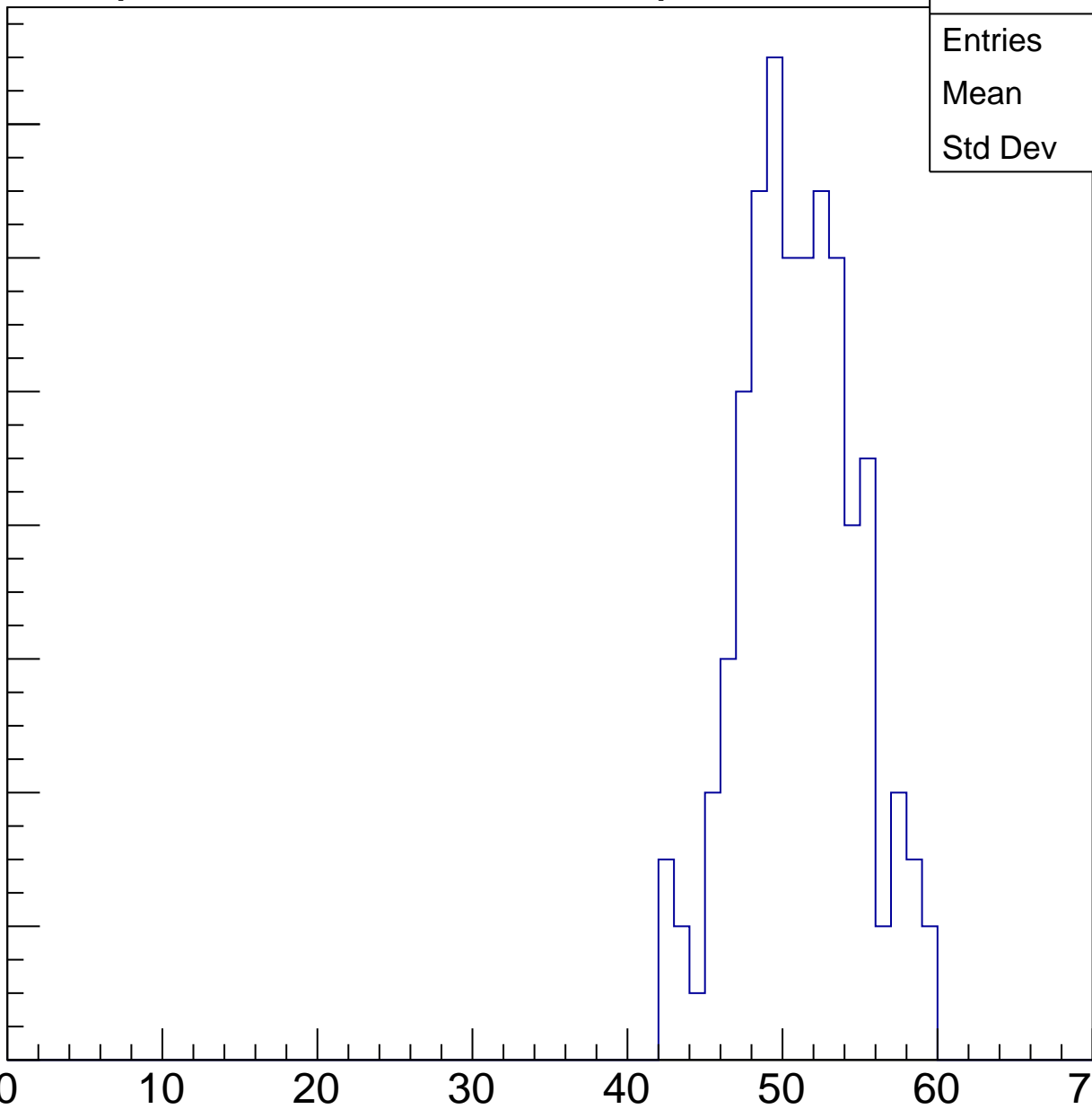
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	50.56
Std Dev	3.691

ampl



B0L101S, U20-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

6

4

2

0

0

10

20

30

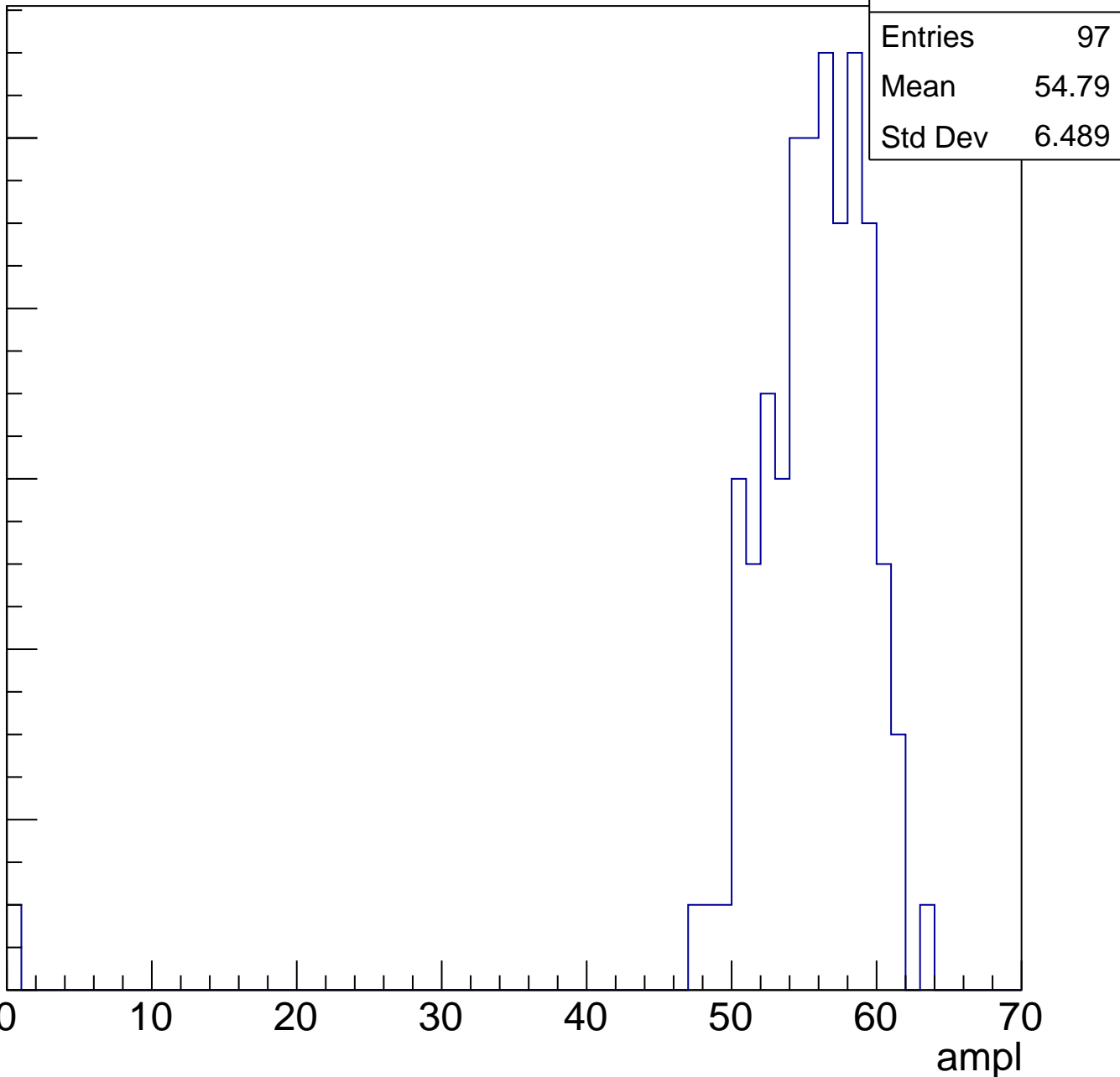
40

50

60

ampl

Entries	97
Mean	54.79
Std Dev	6.489



B0L101S, U20-ch8, adc5

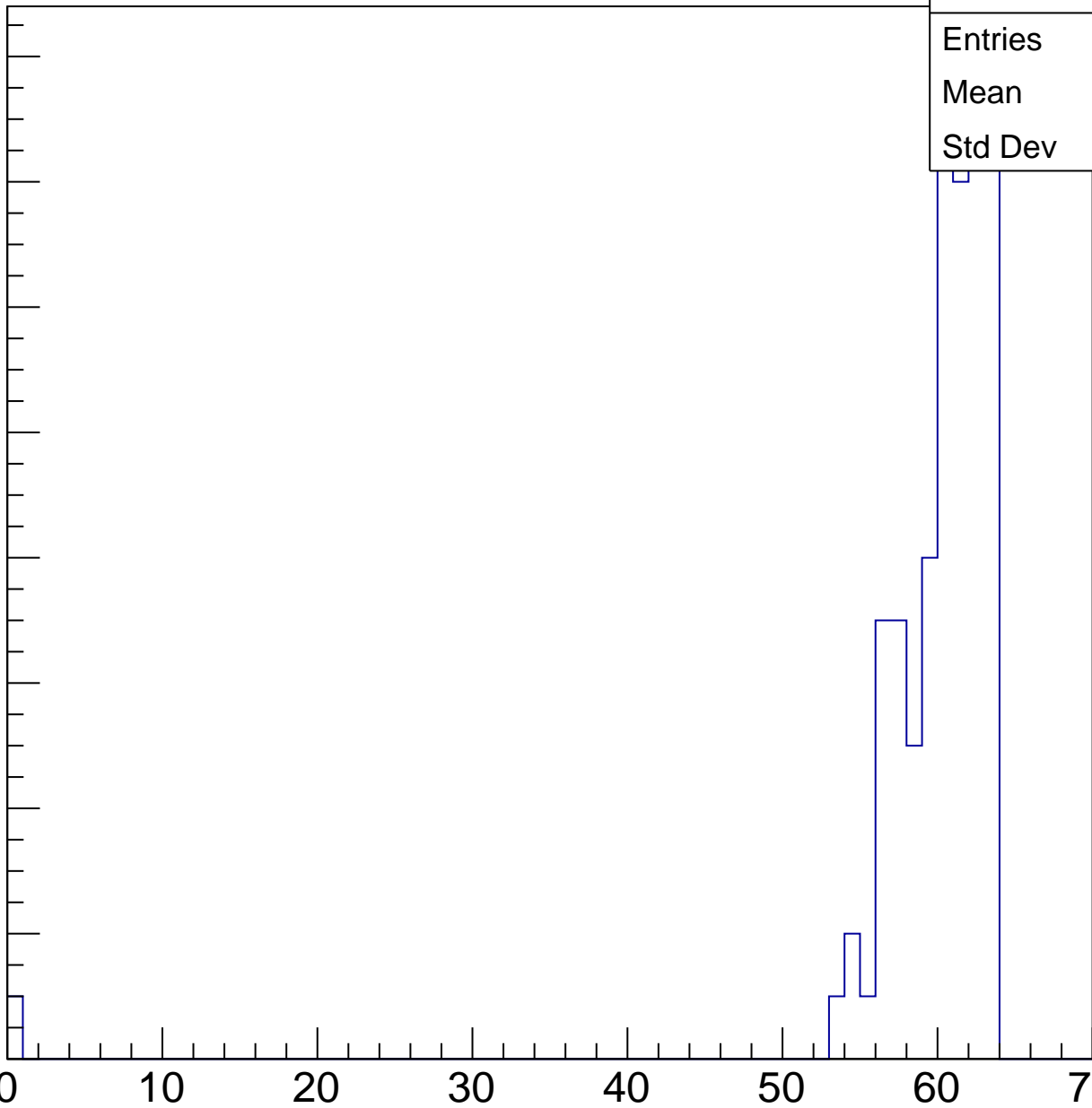
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.36
Std Dev	6.699

ampl

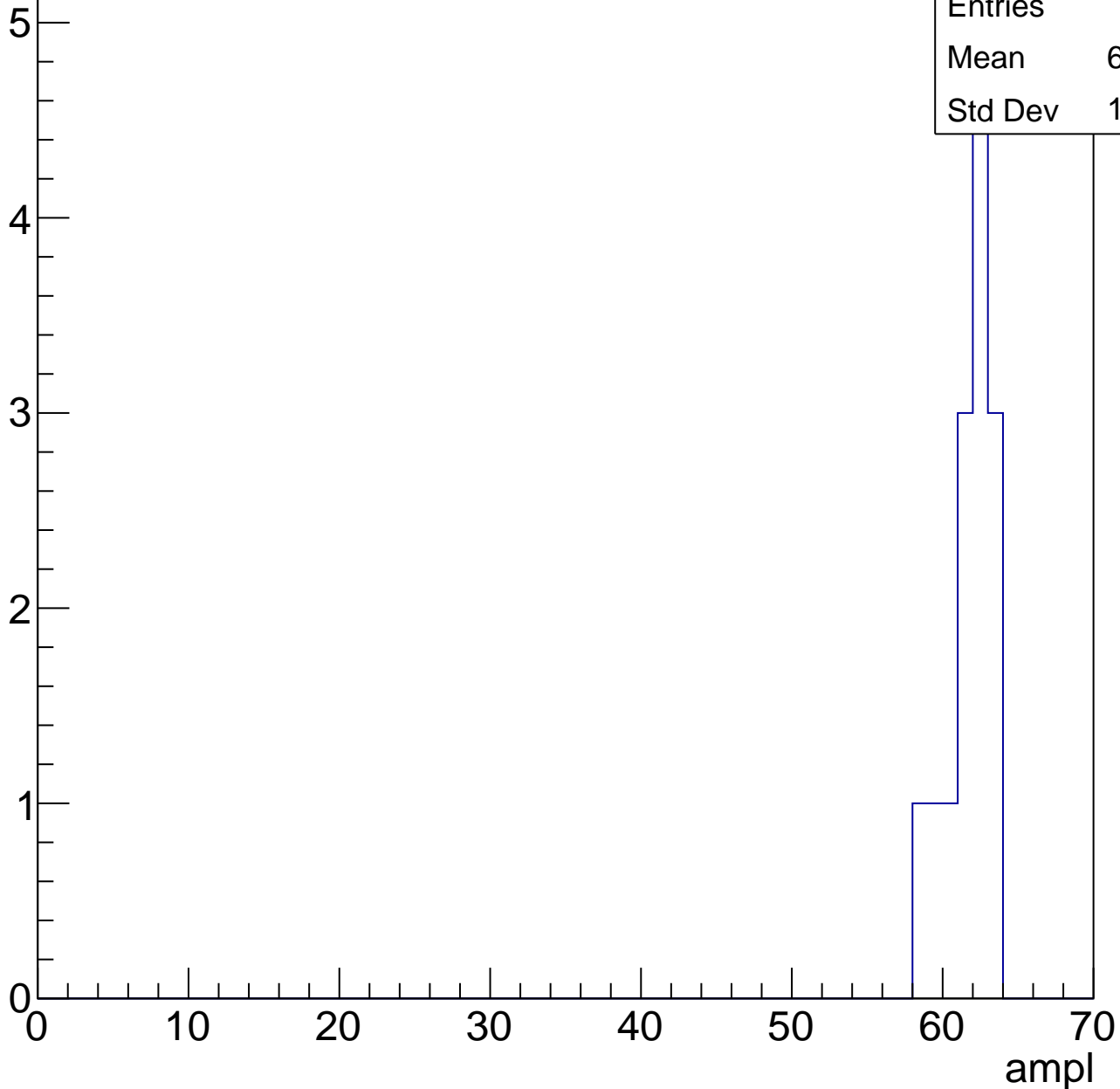


B0L101S, U20-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

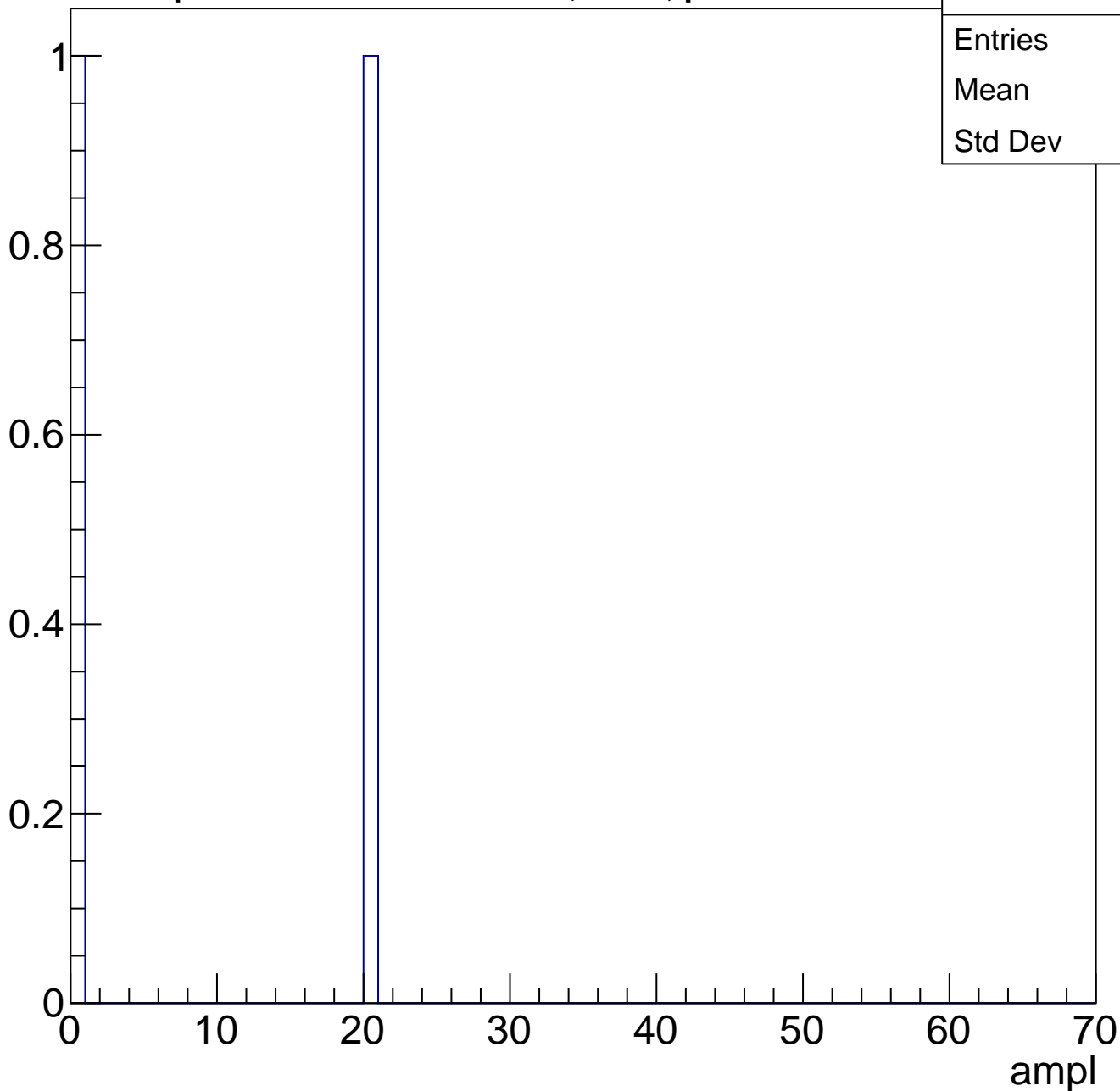
Entries	14
Mean	61.36
Std Dev	1.445



B0L101S, U20-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U20-ch9, adc0

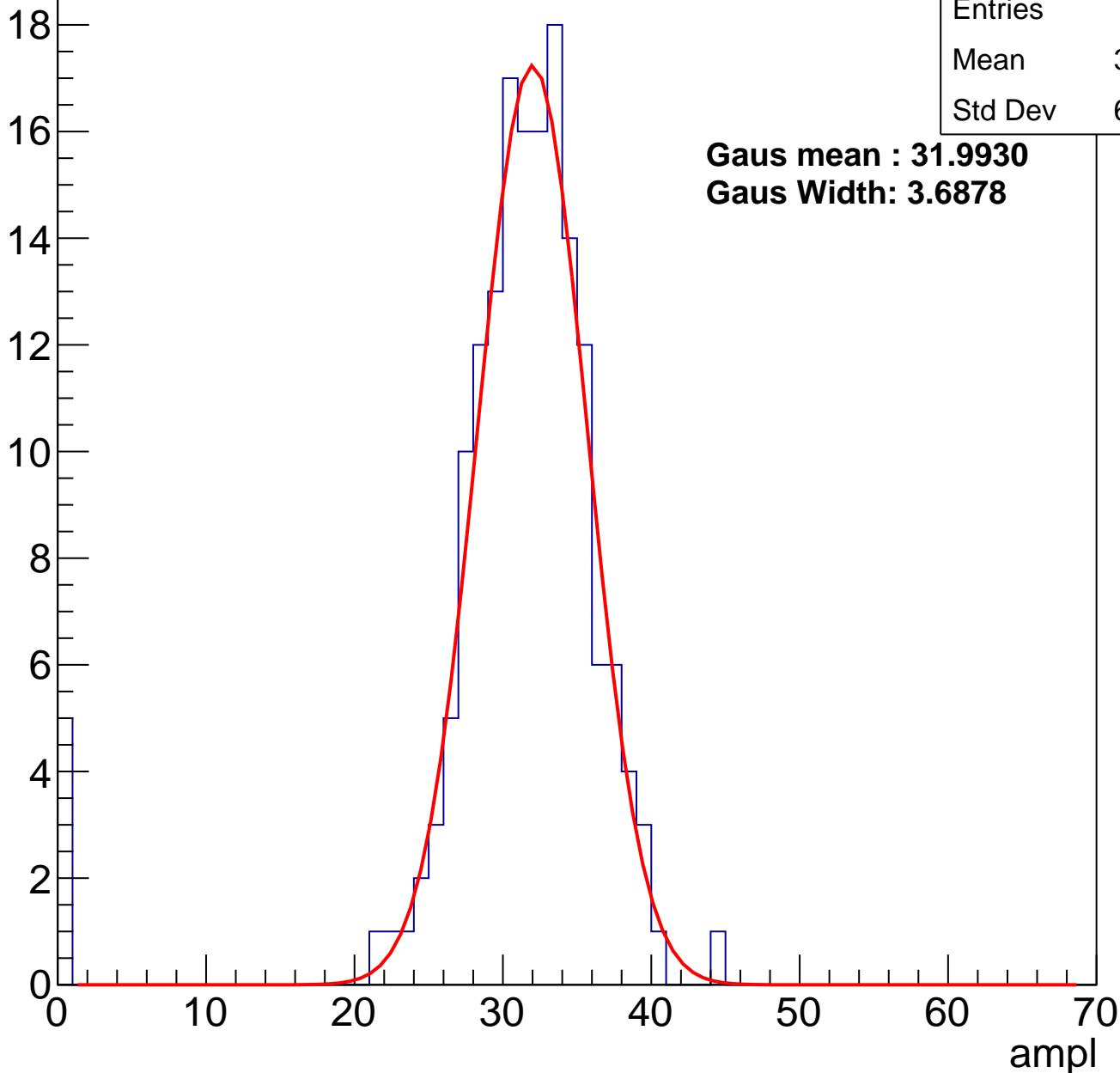
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	30.54
Std Dev	6.517

Gaus mean : 31.9930

Gaus Width: 3.6878

Entry



B0L101S, U20-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	38.04
Std Dev	3.231

Gaus mean : 38.5760

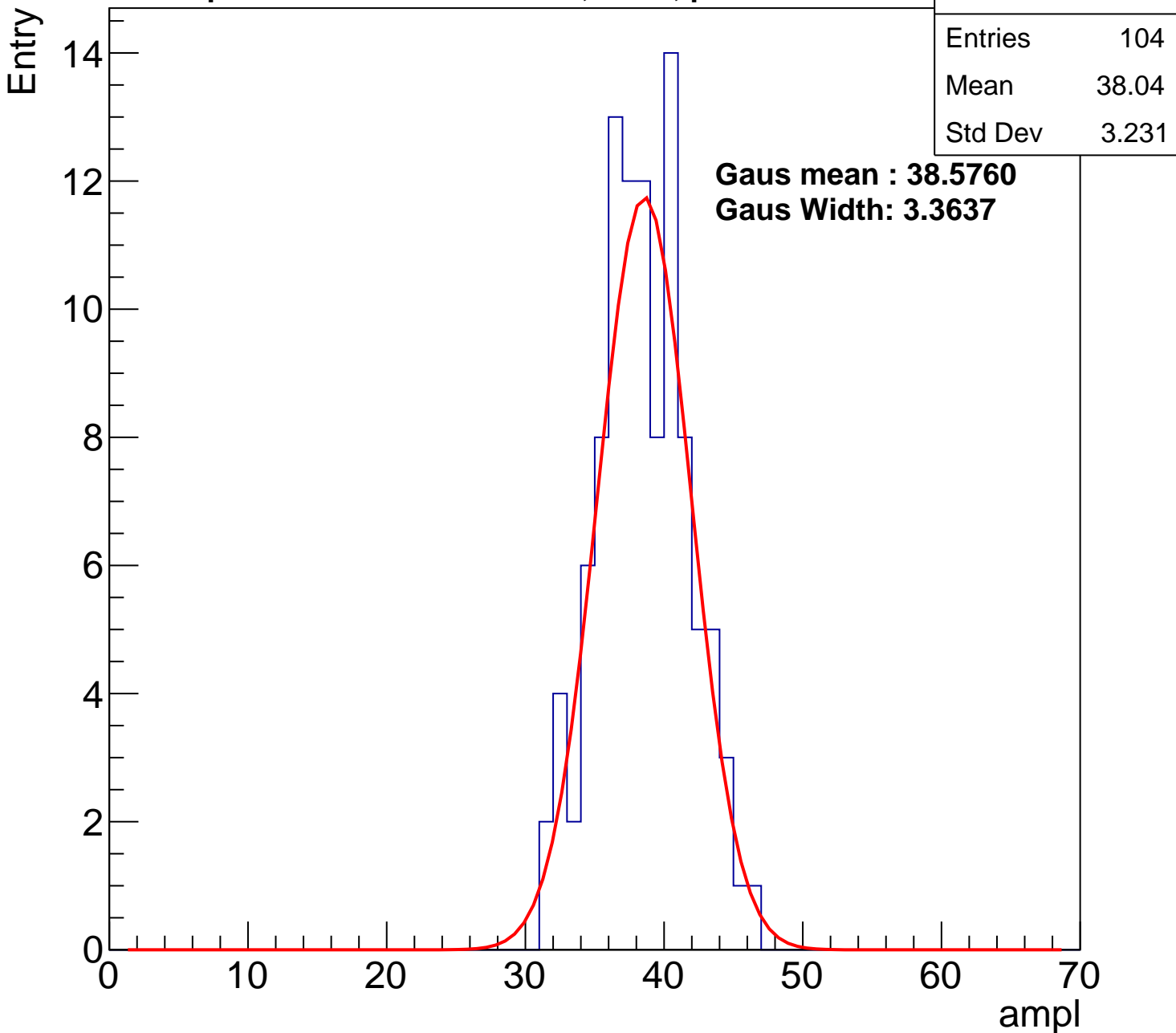
Gaus Width: 3.3637

Entry

14
12
10
8
6
4
2
0

ampl

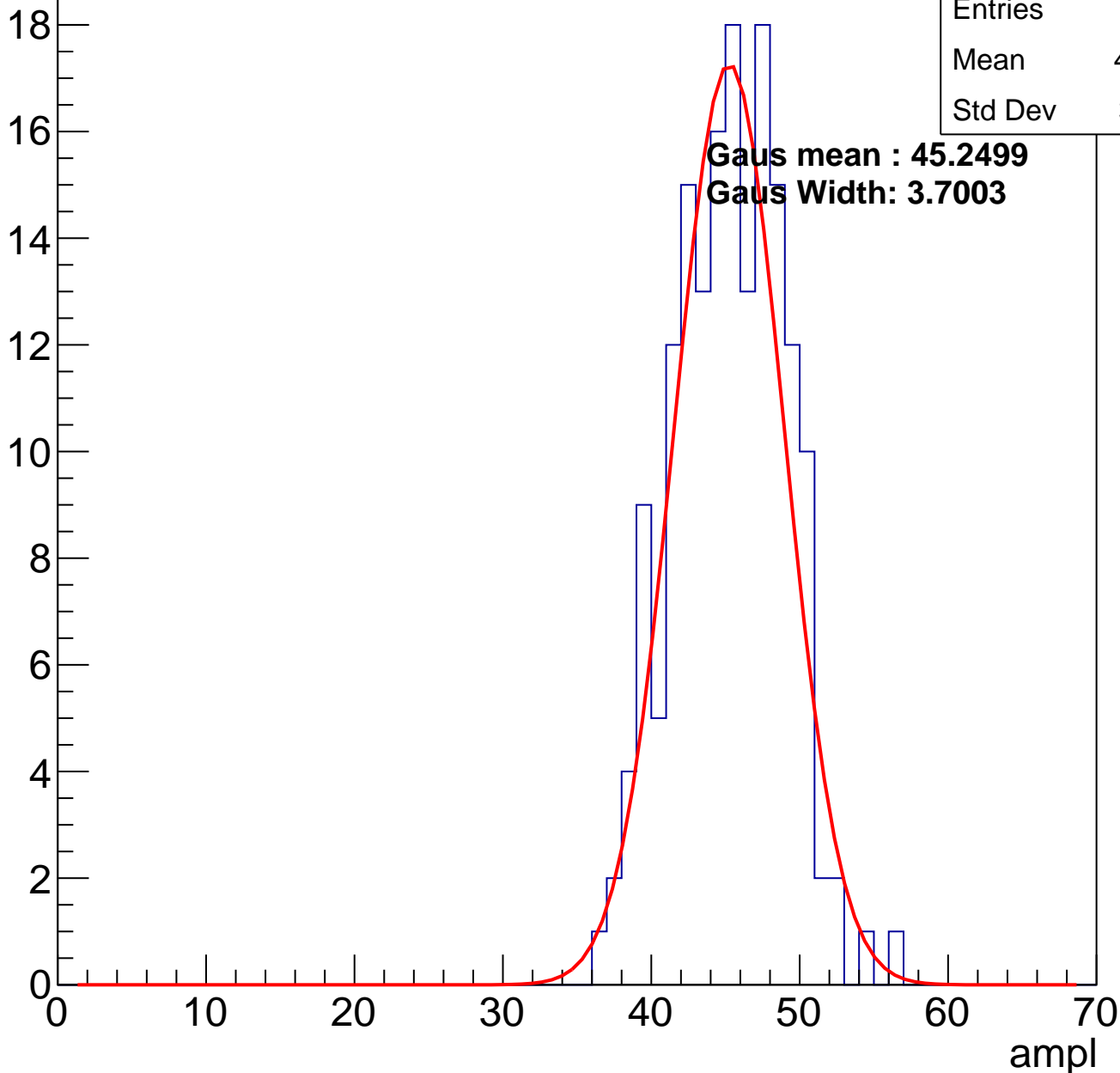
0 10 20 30 40 50 60 70



B0L101S, U20-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

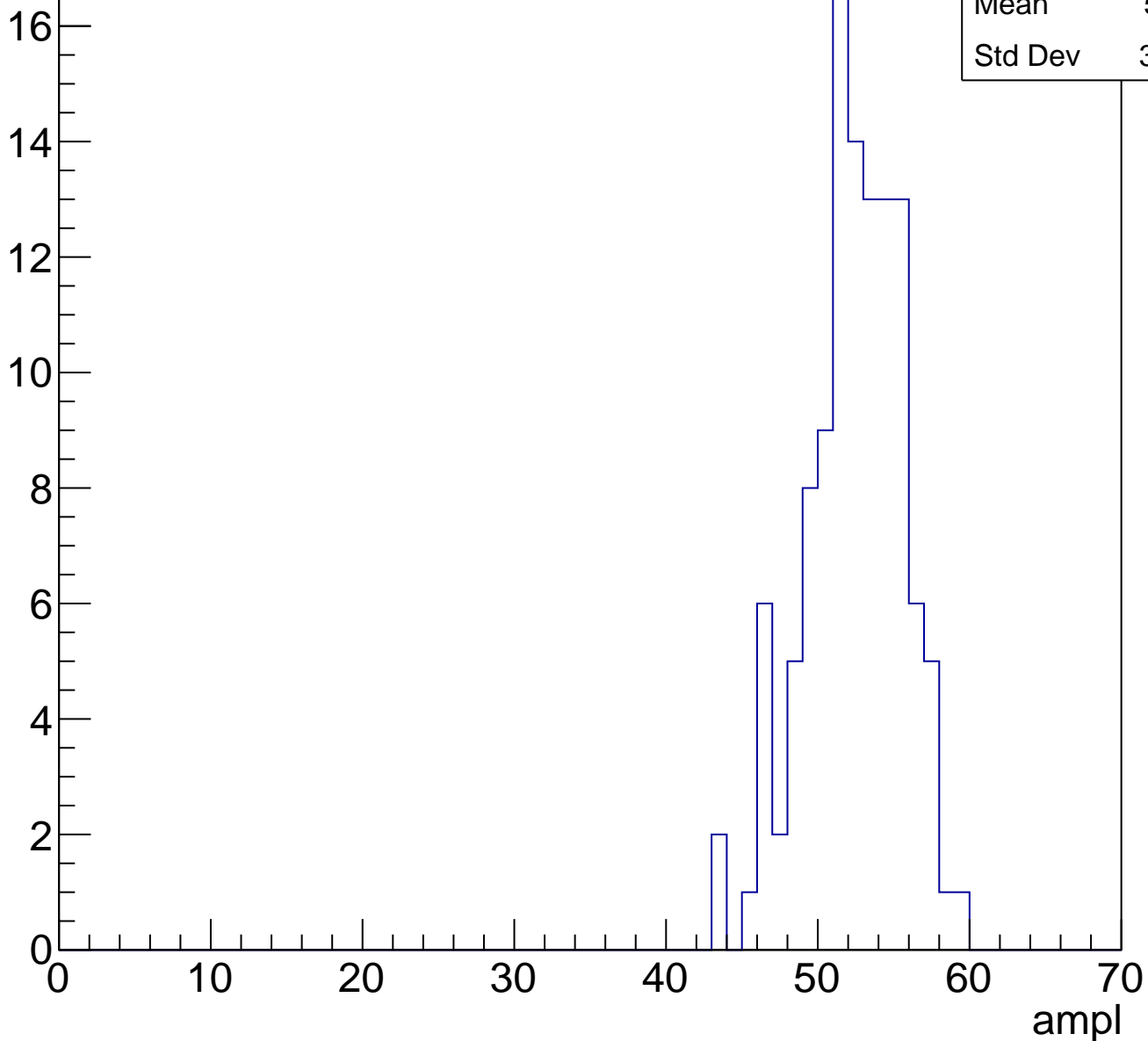


B0L101S, U20-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	116
Mean	51.91
Std Dev	3.175



B0L101S, U20-ch9, adc4

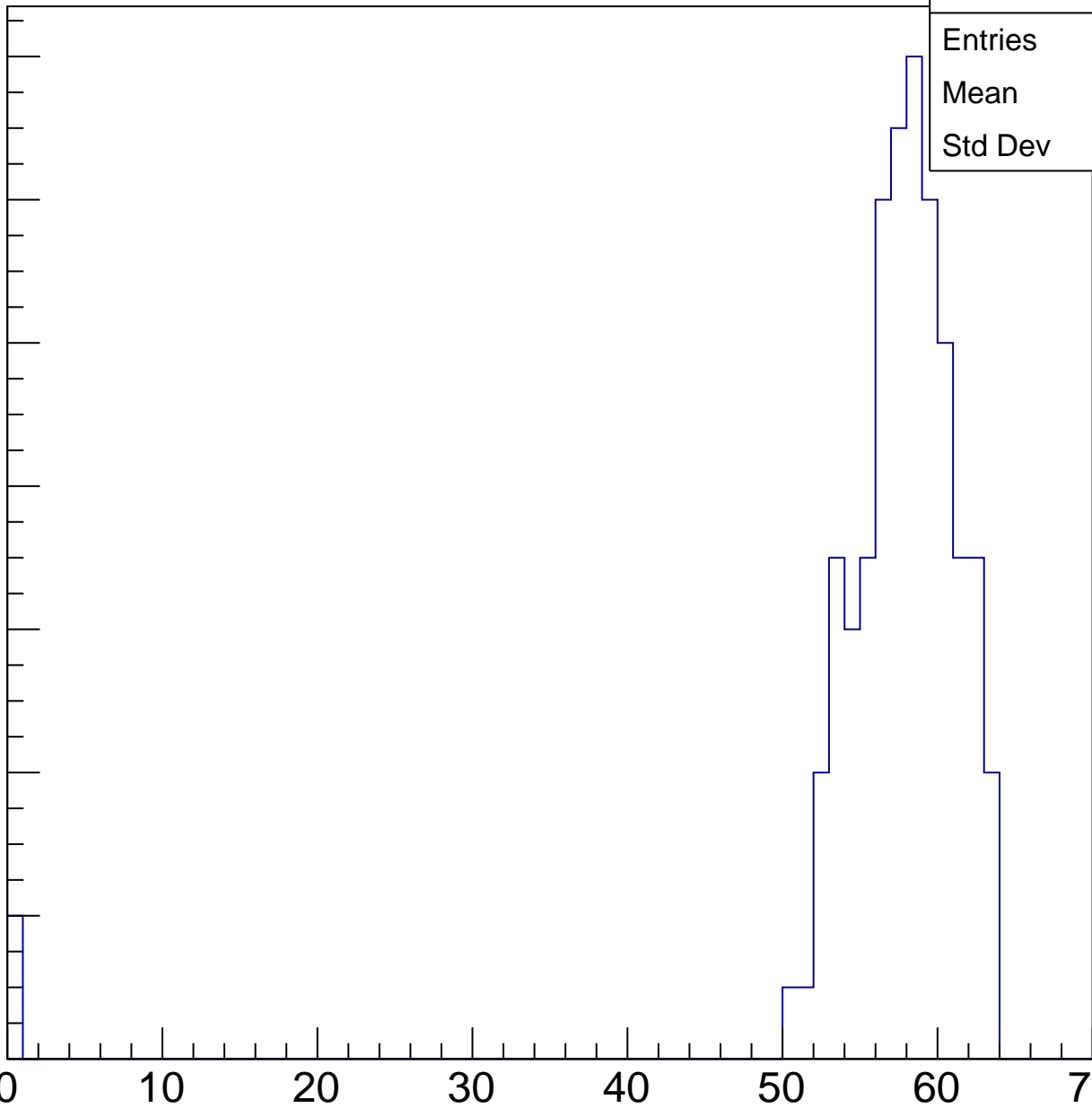
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	56.4
Std Dev	8.335

ampl



B0L101S, U20-ch9, adc5

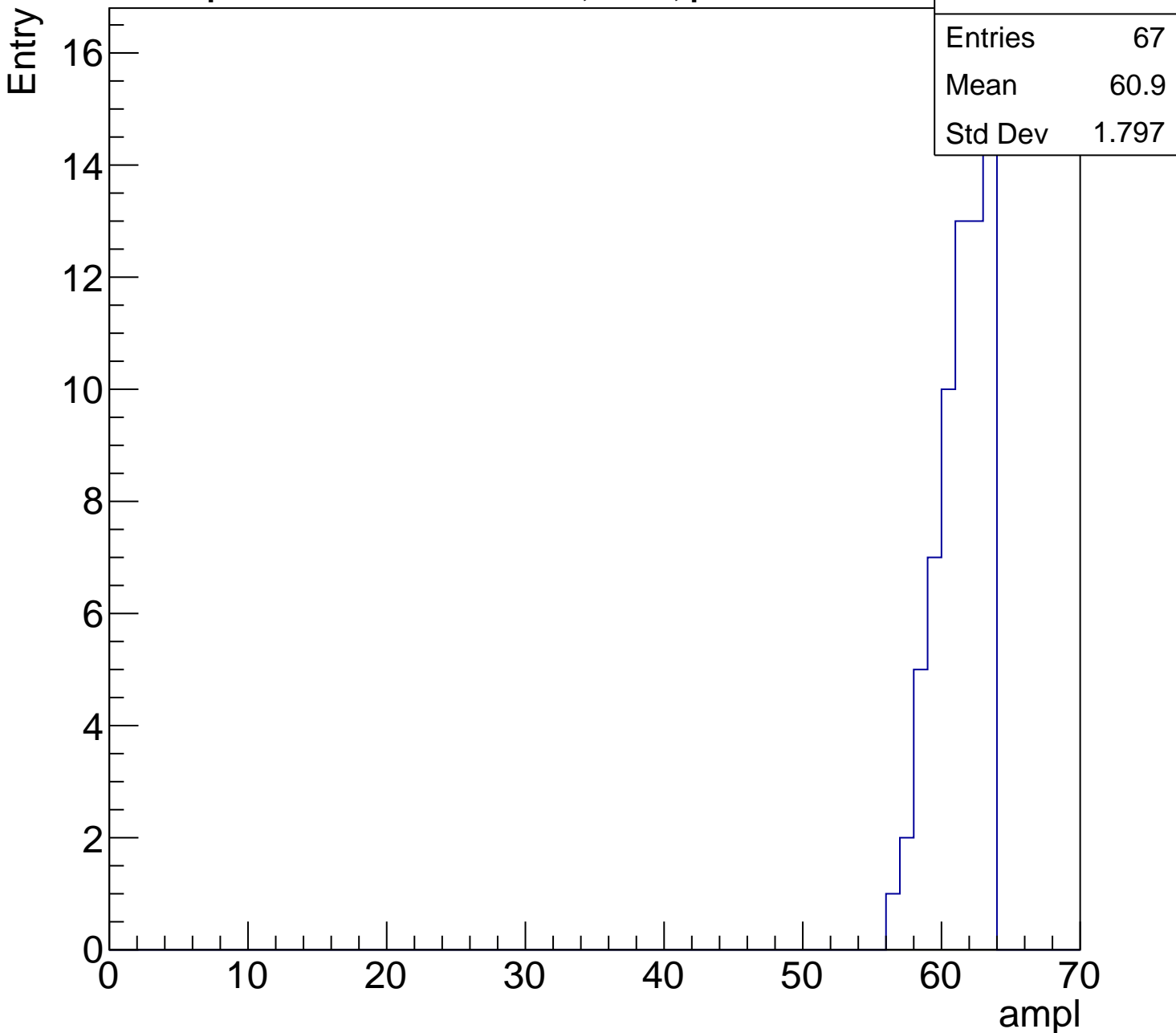
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	60.9
Std Dev	1.797

ampl



B0L101S, U20-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

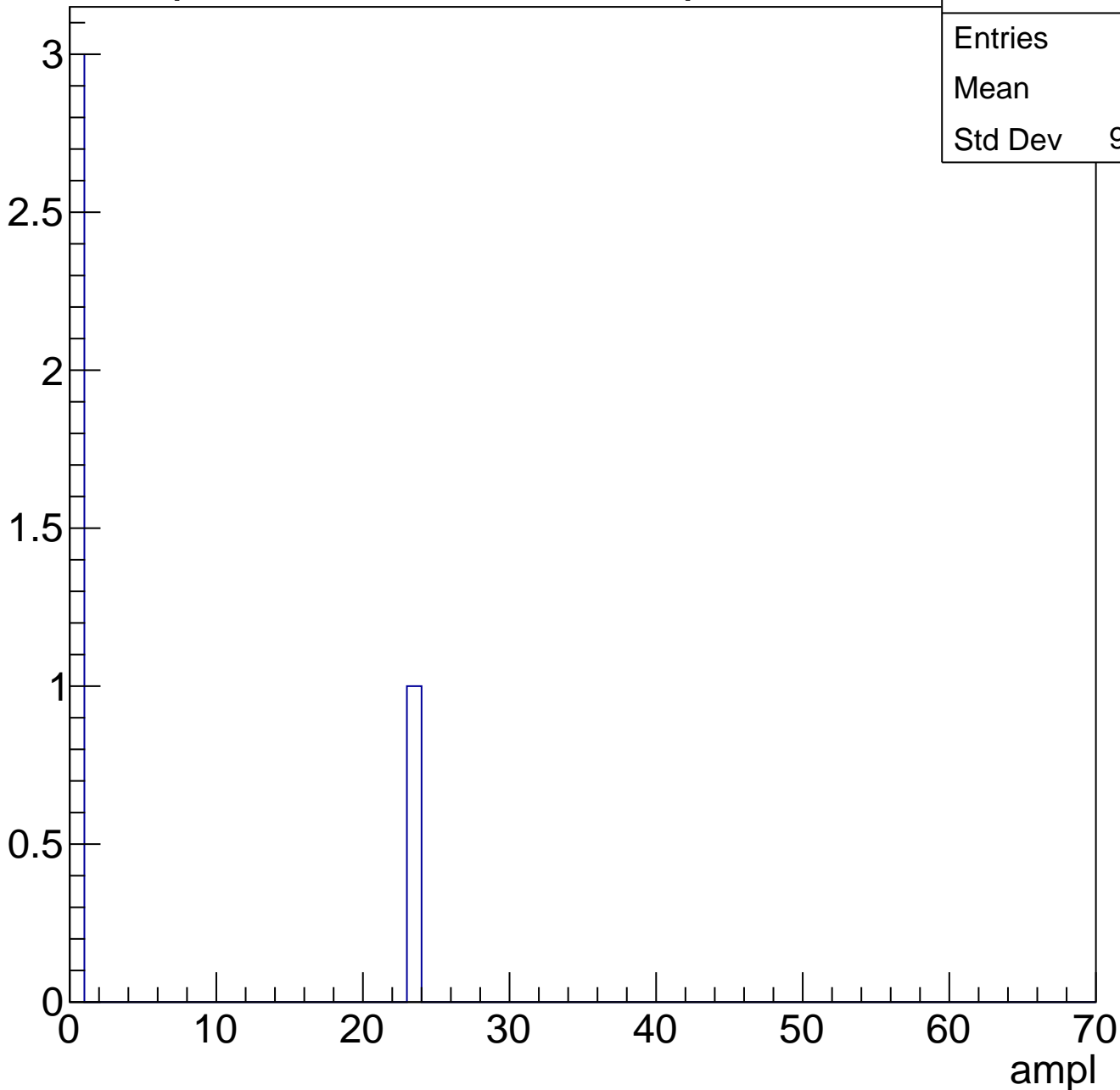


Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch10, adc0

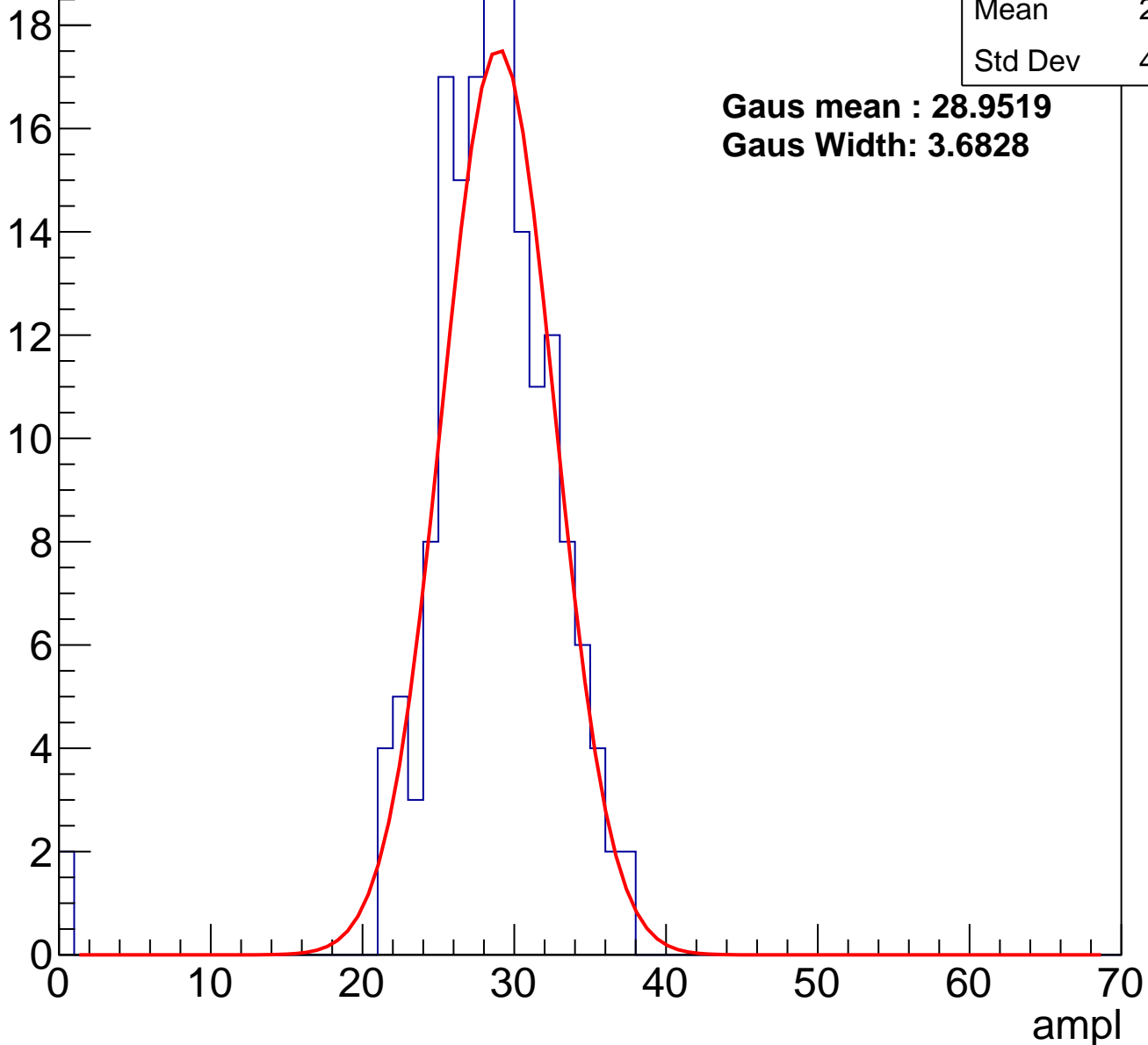
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.04
Std Dev	4.658

Gaus mean : 28.9519

Gaus Width: 3.6828

Entry



B0L101S, U20-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.01
Std Dev	3.622

Gaus mean : 36.2170

Gaus Width: 3.3445

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

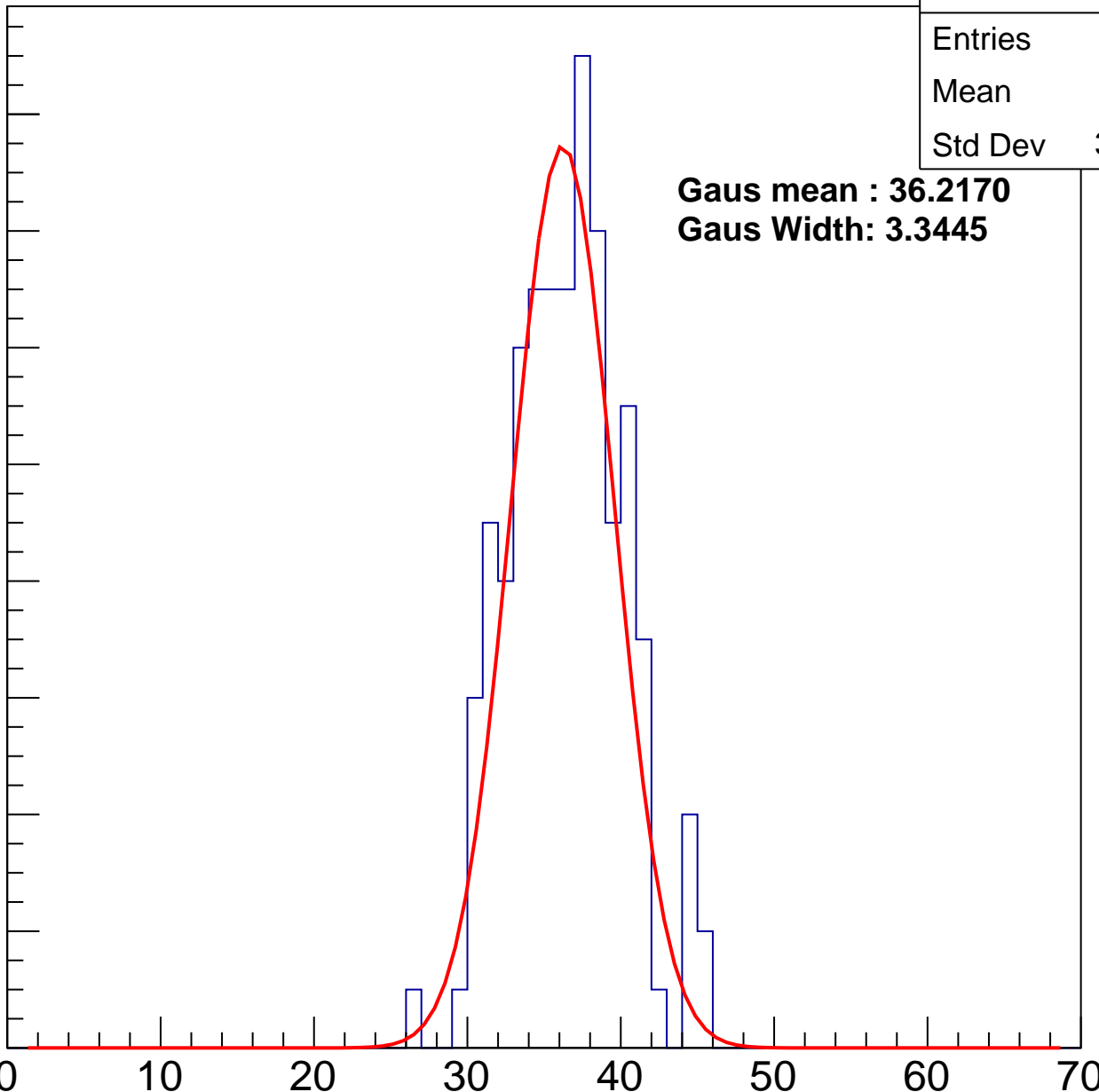
40

50

60

70

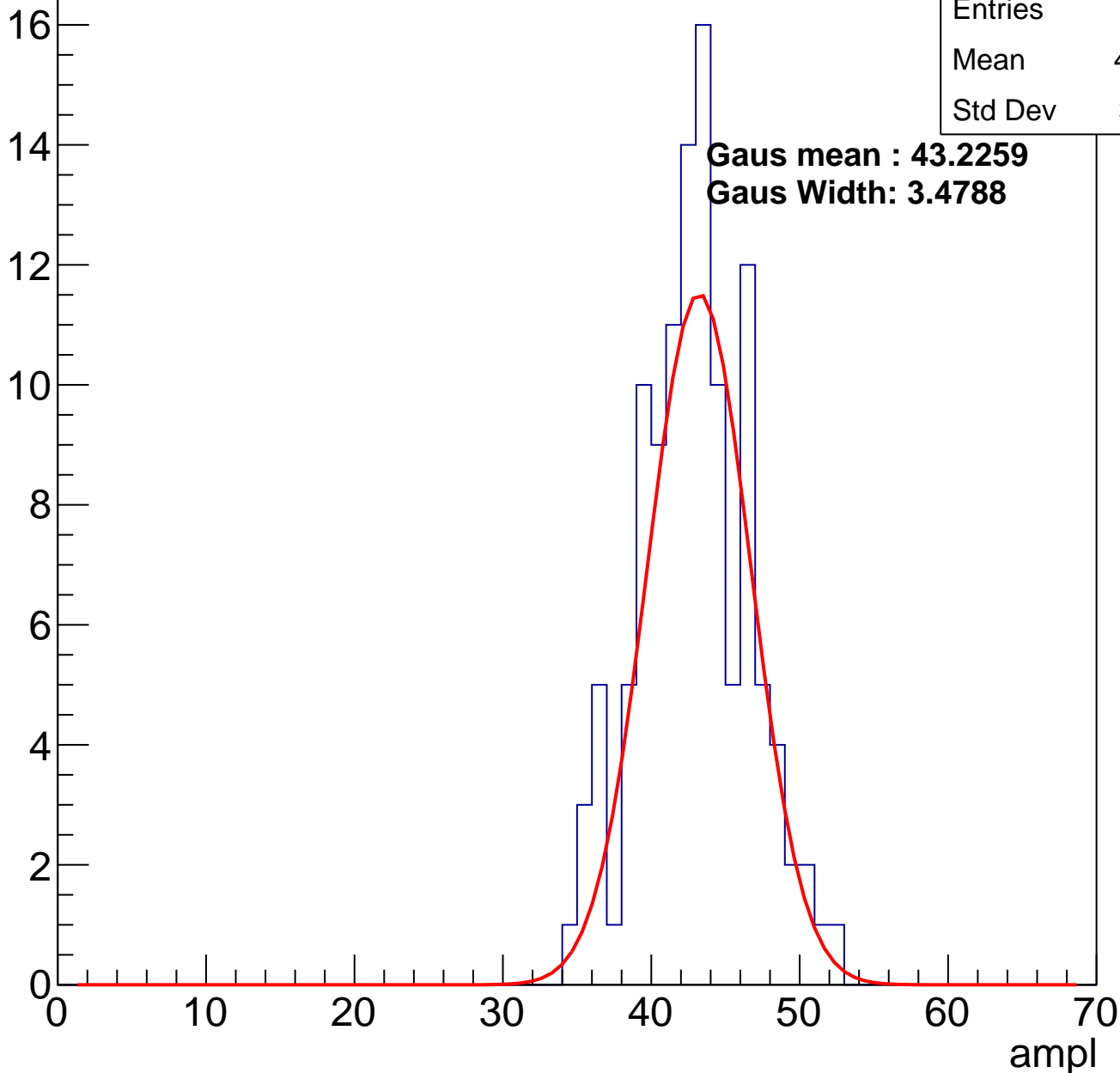
ampl



B0L101S, U20-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

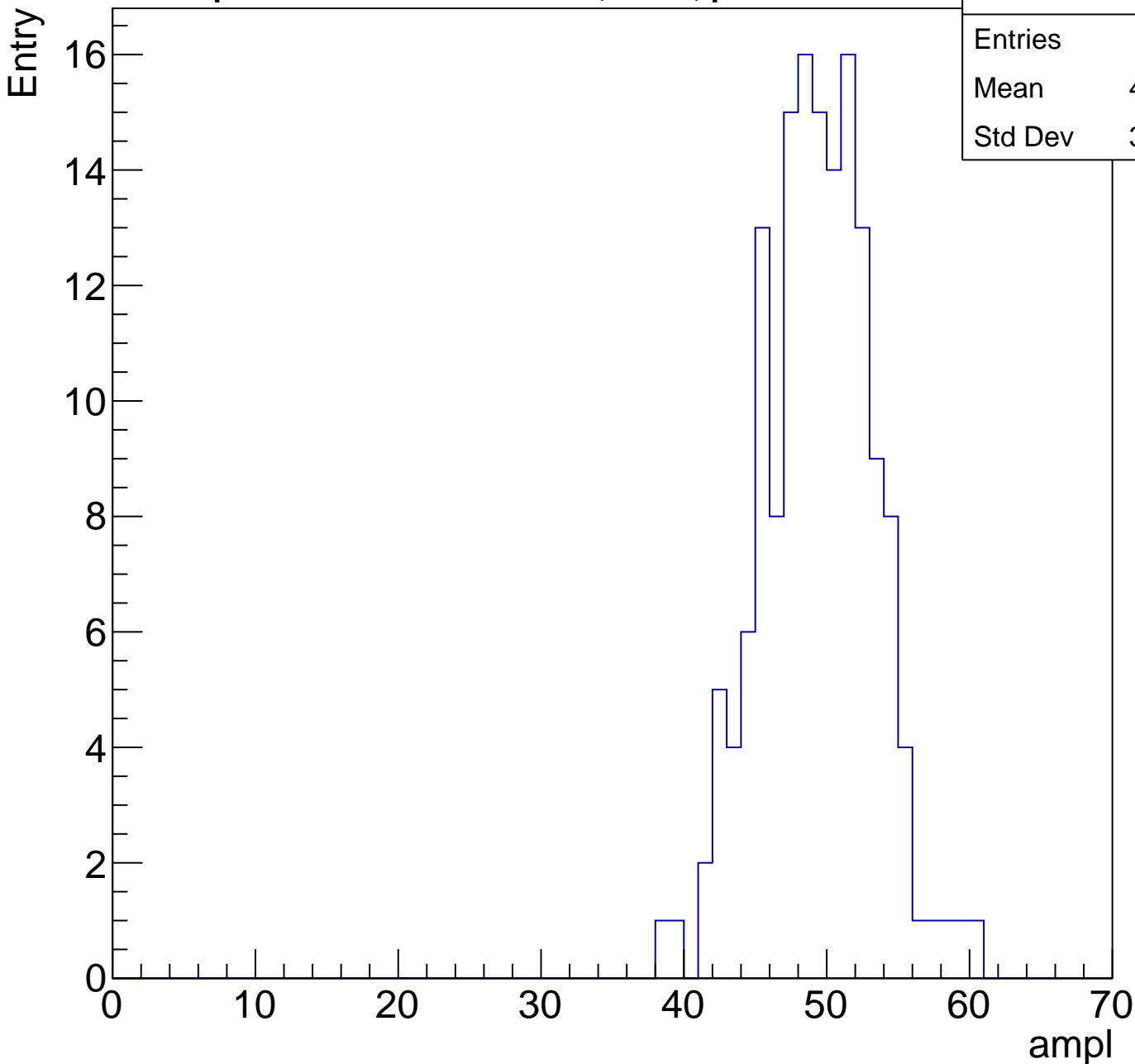
Entry



B0L101S, U20-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	48.88
Std Dev	3.885



B0L101S, U20-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	55.29
Std Dev	6.033

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

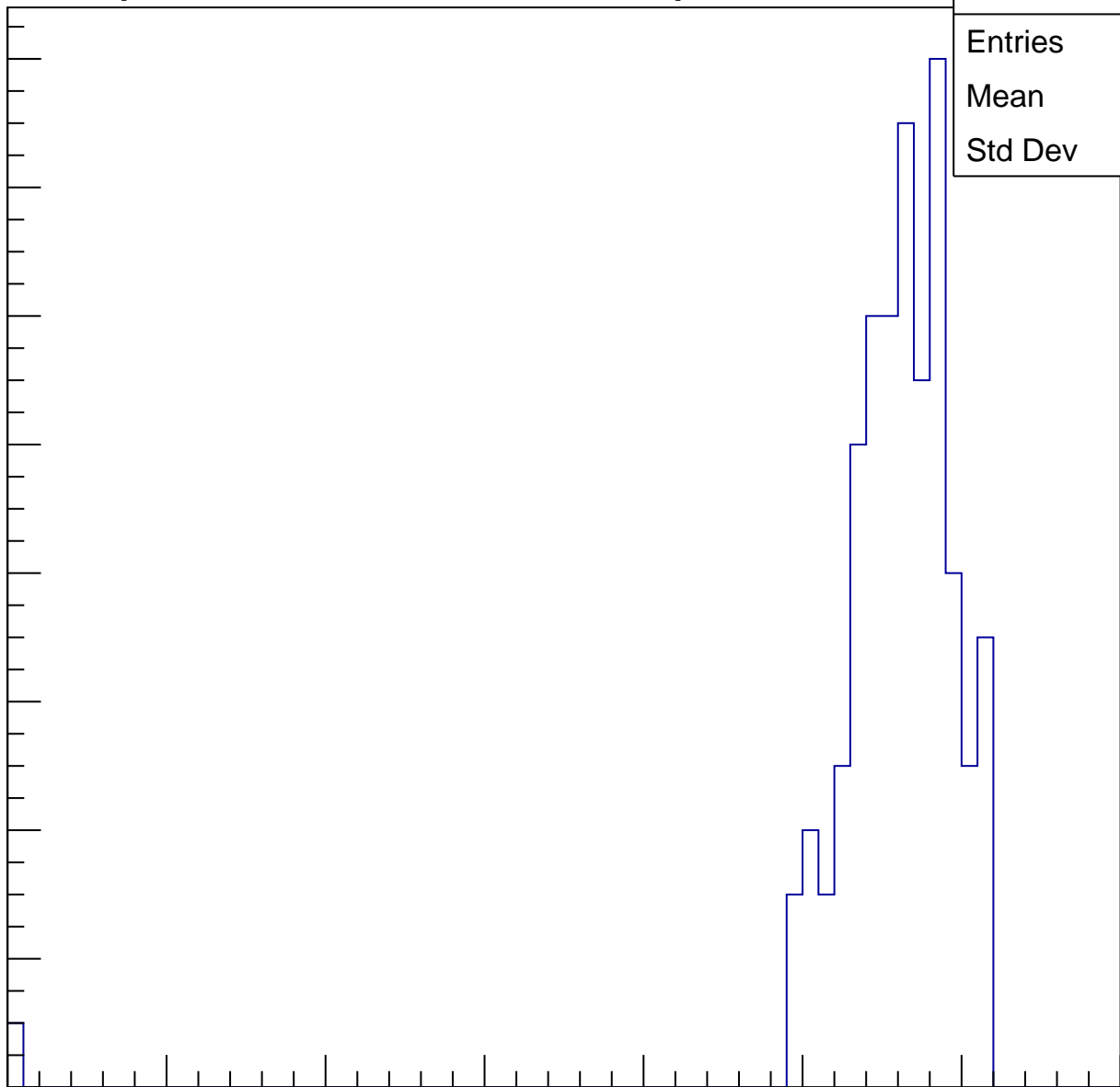
40

50

60

70

ampl



B0L101S, U20-ch10, adc5

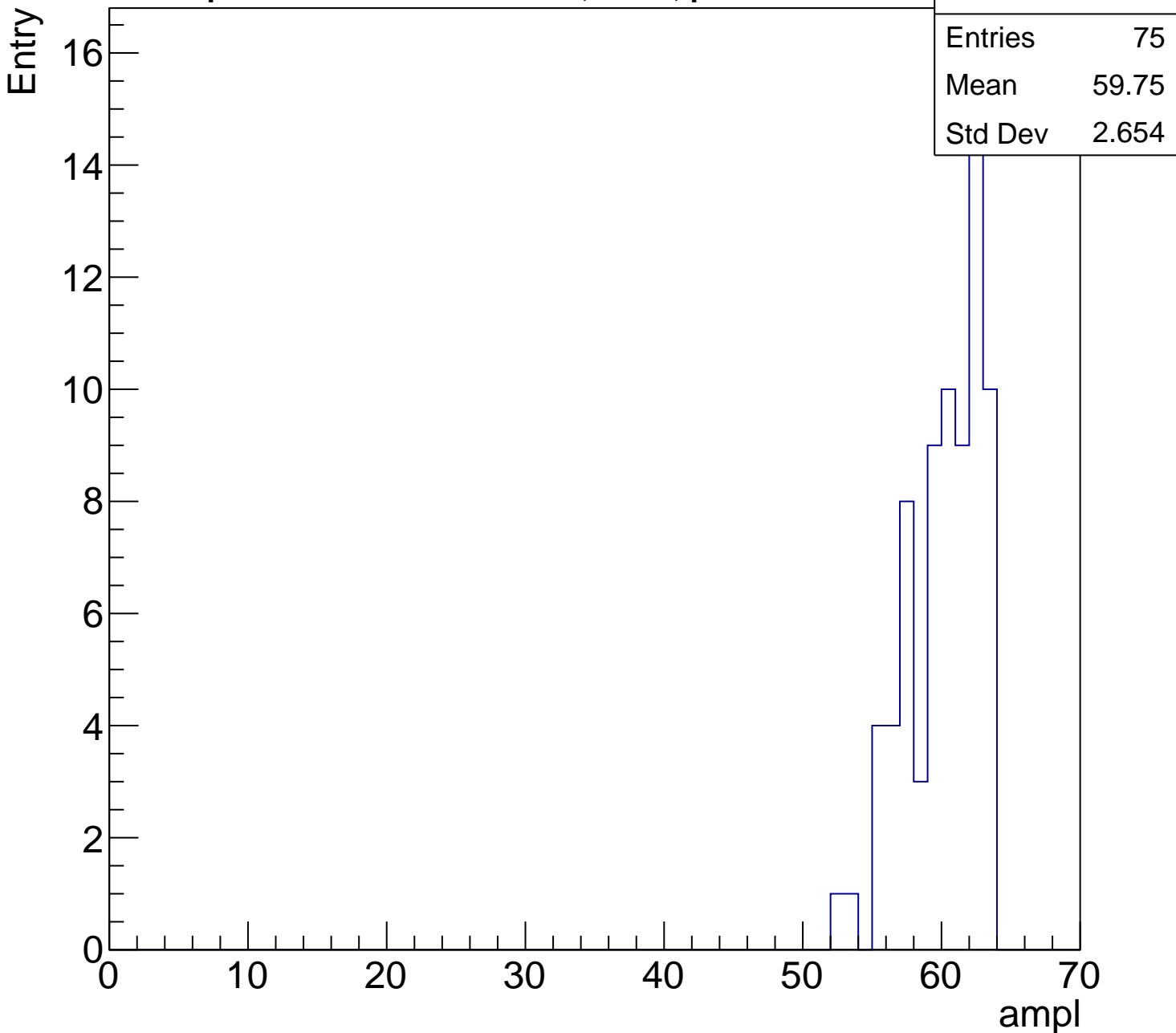
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.75
Std Dev	2.654

ampl

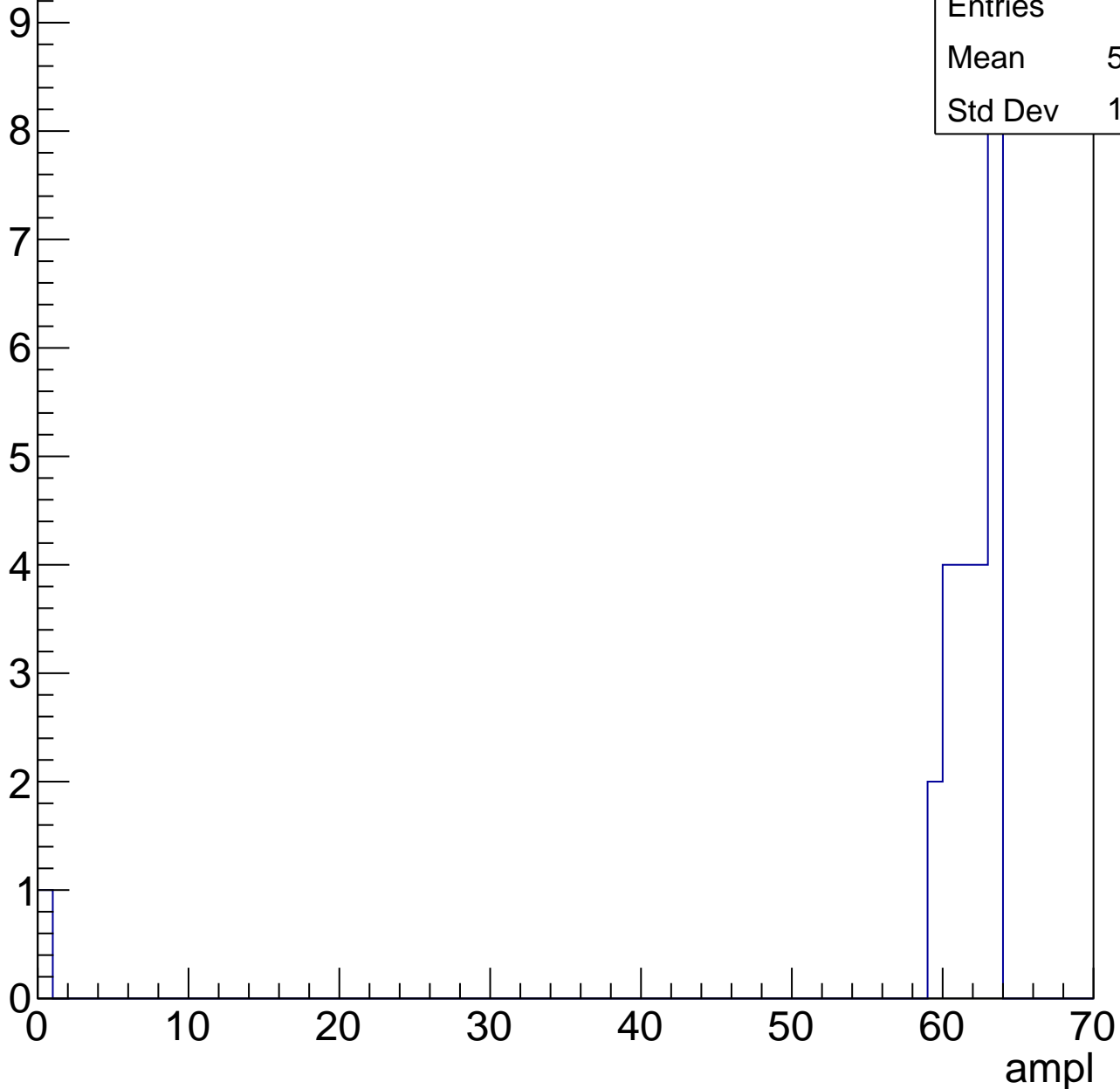


B0L101S, U20-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

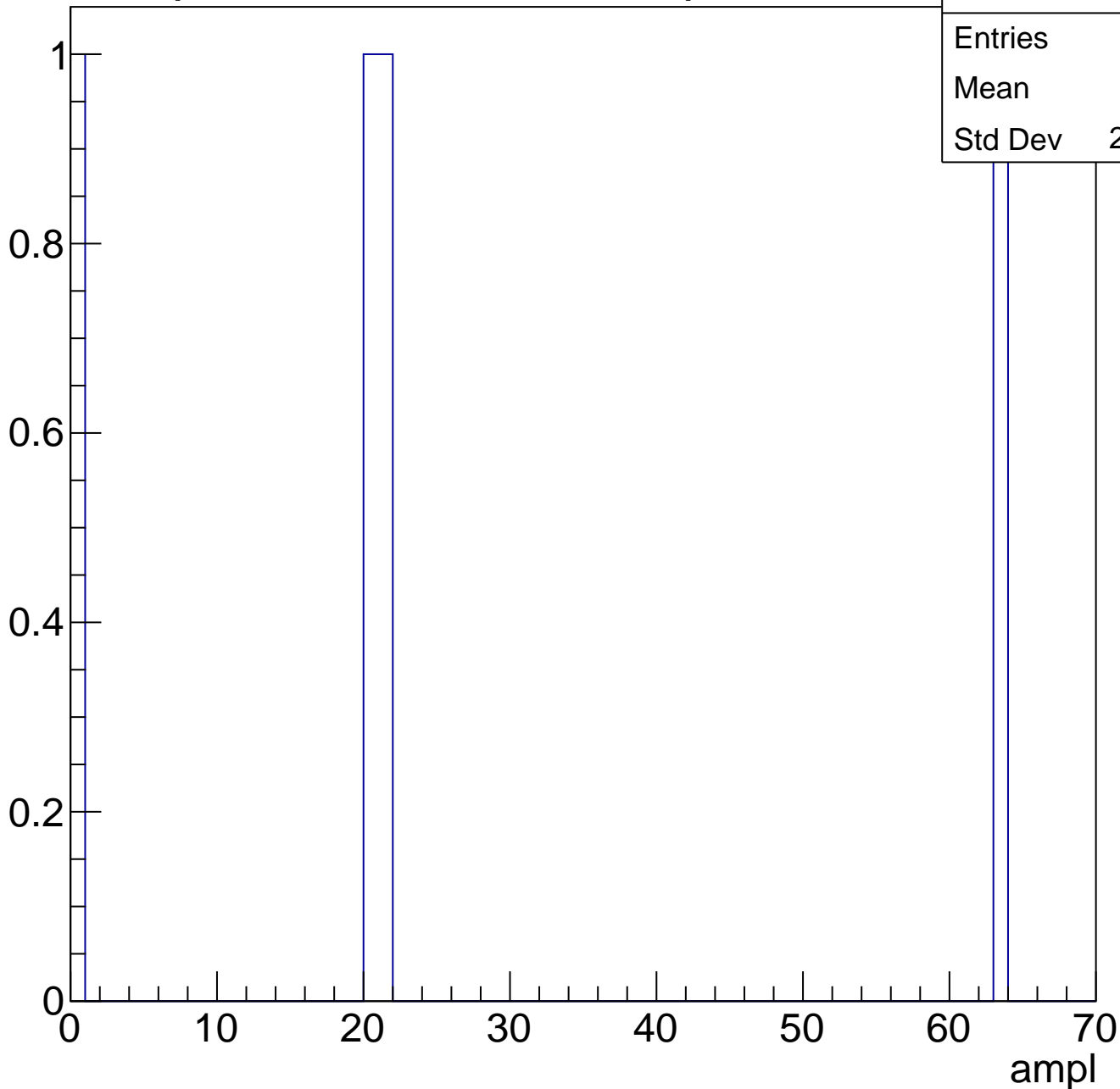
Entries	24
Mean	59.04
Std Dev	12.38



B0L101S, U20-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



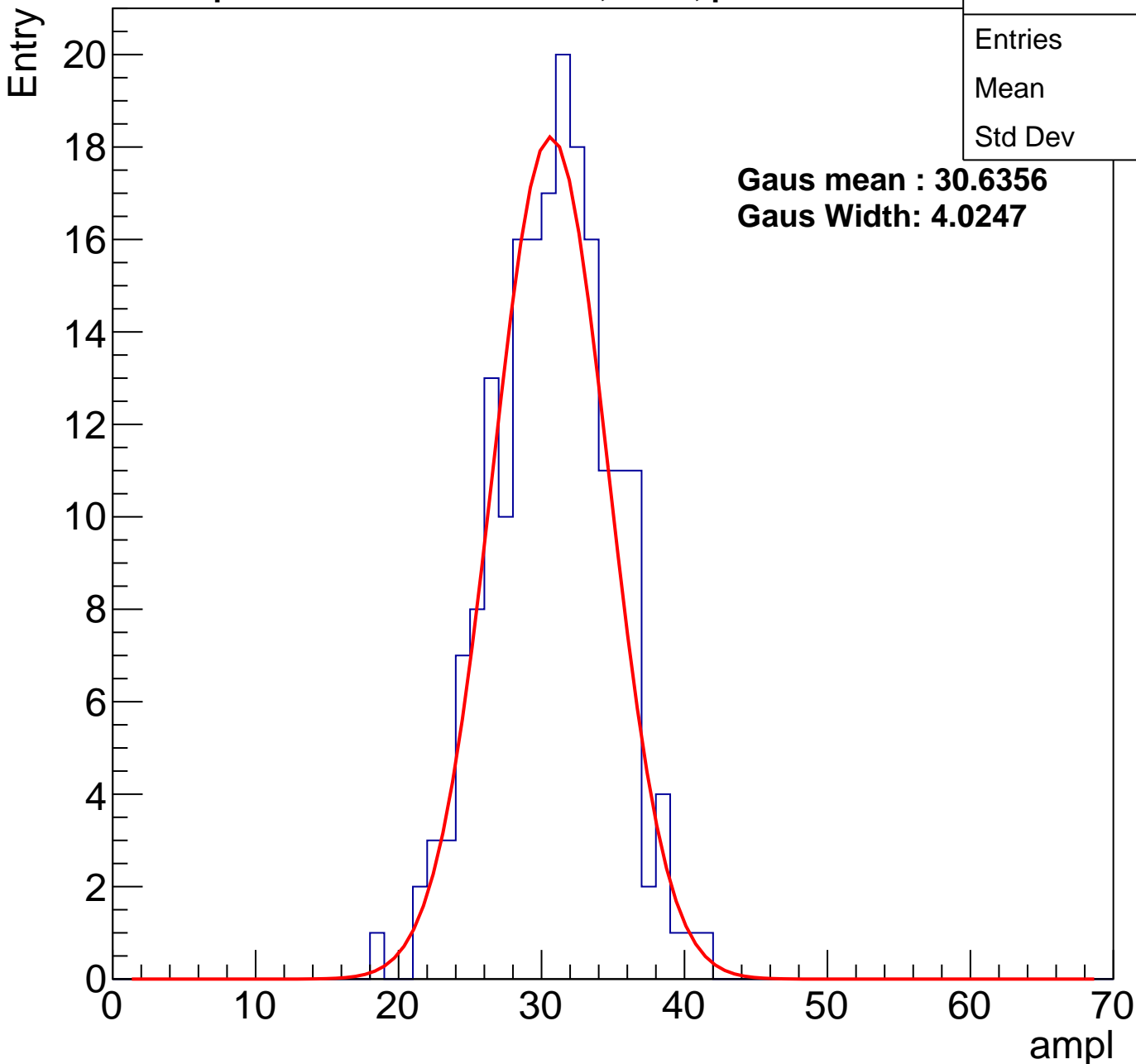
B0L101S, U20-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	192
Mean	30.3
Std Dev	4.08

Gaus mean : 30.6356

Gaus Width: 4.0247



B0L101S, U20-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	38.11
Std Dev	3.164

Gaus mean : 38.6402

Gaus Width: 3.3110

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

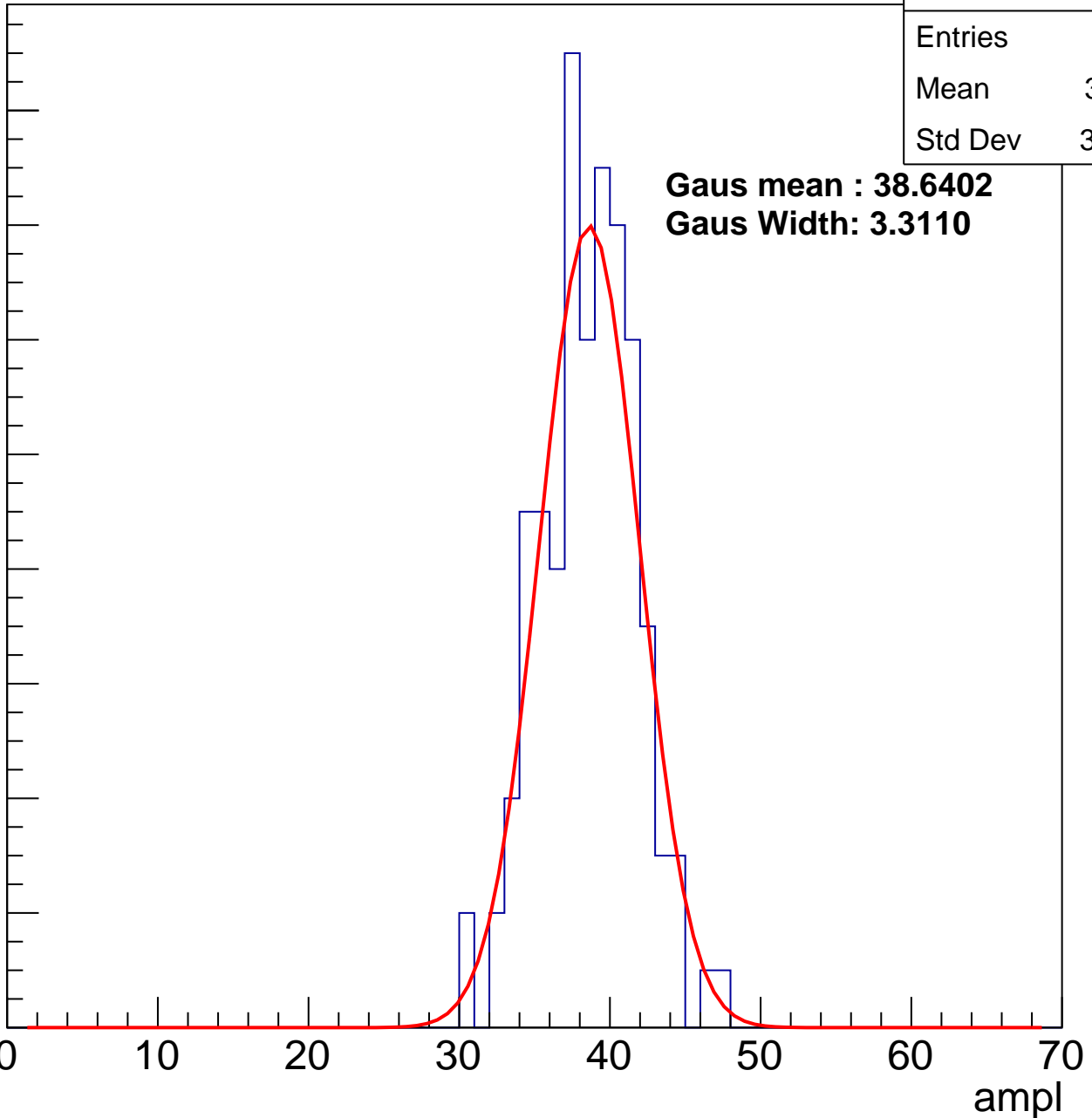
40

50

60

70

ampl



B0L101S, U20-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

126

Mean

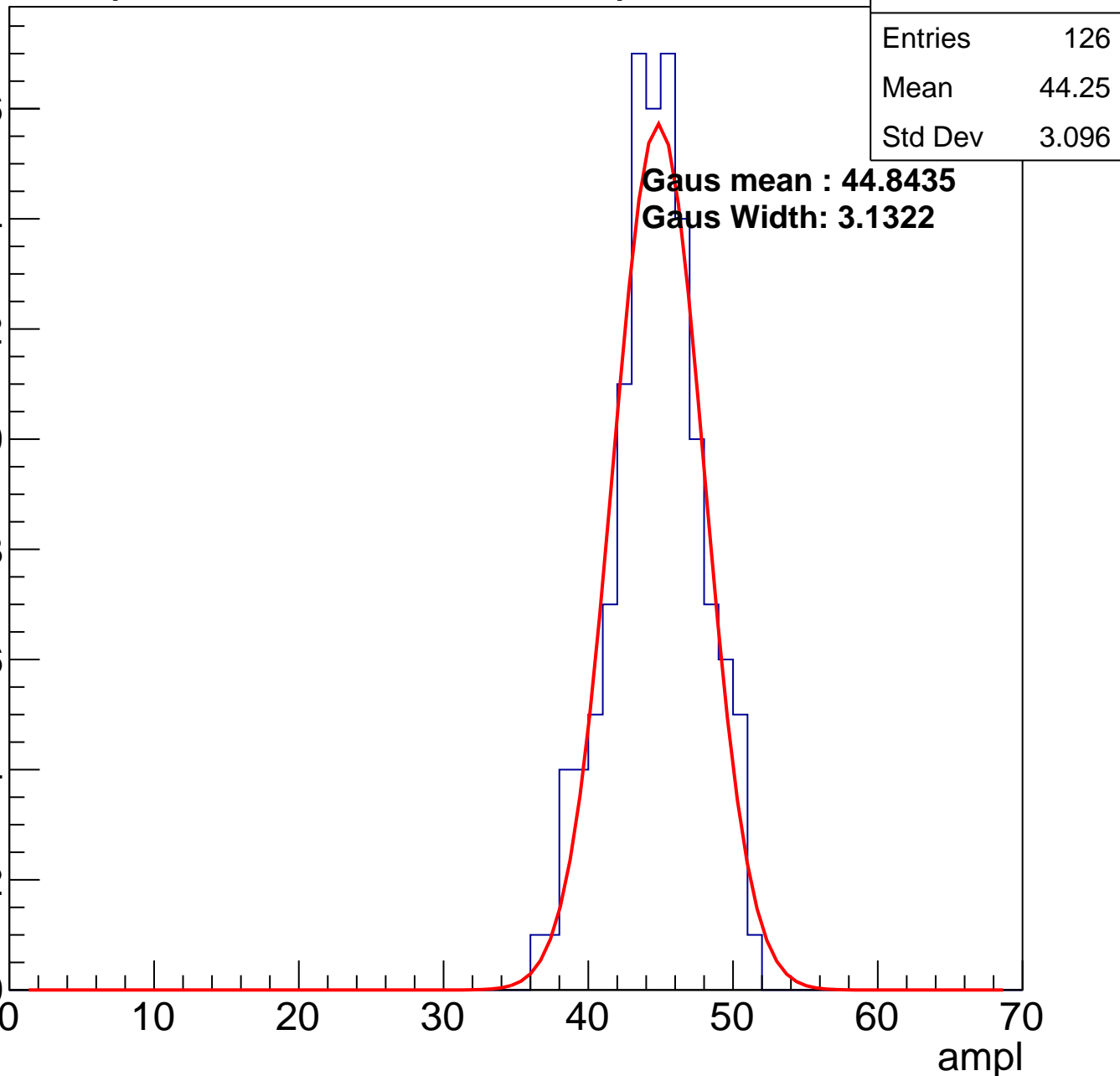
44.25

Std Dev

3.096

Gaus mean : 44.8435

Gaus Width: 3.1322

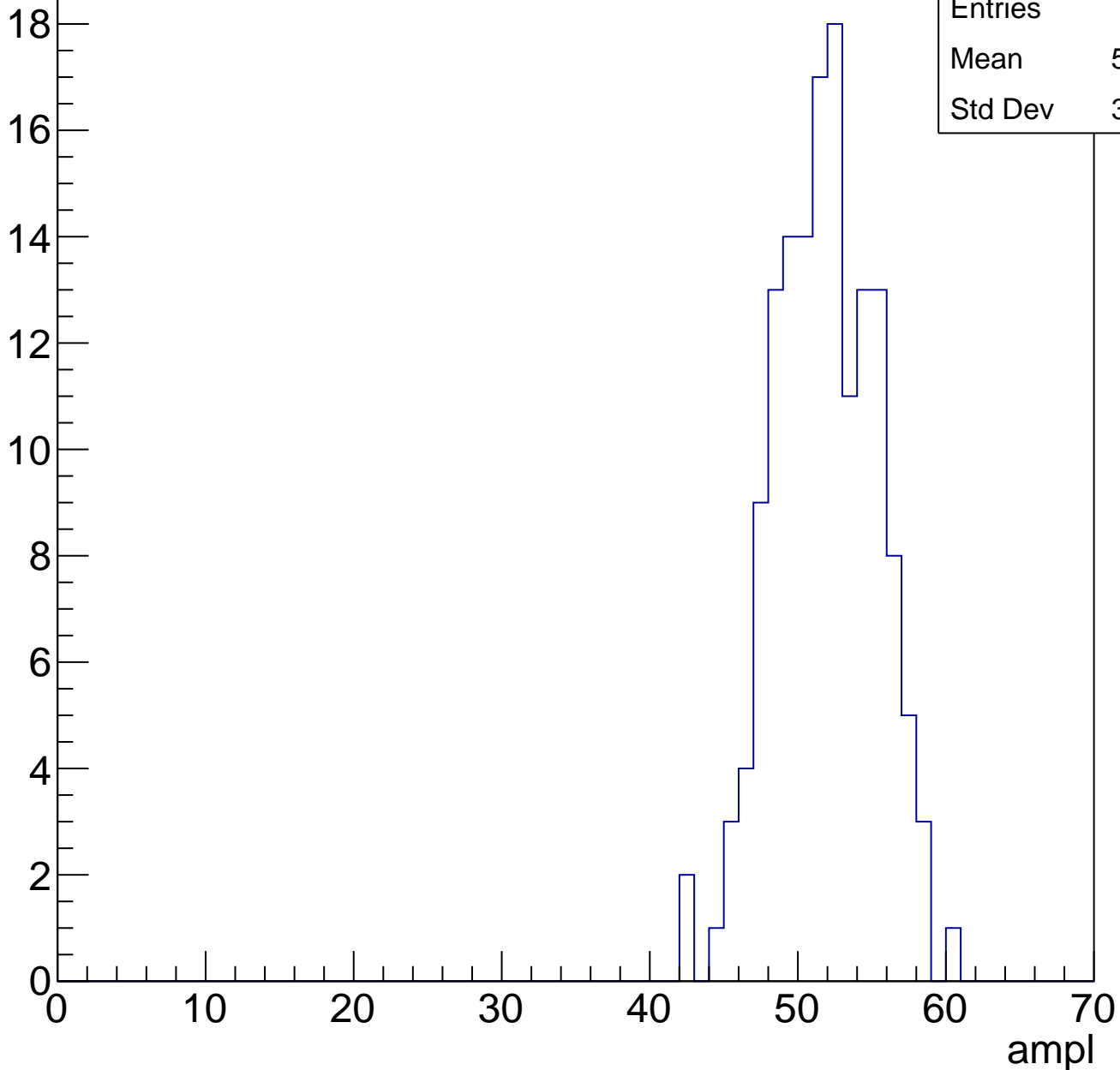


B0L101S, U20-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	51.34
Std Dev	3.405

Entry

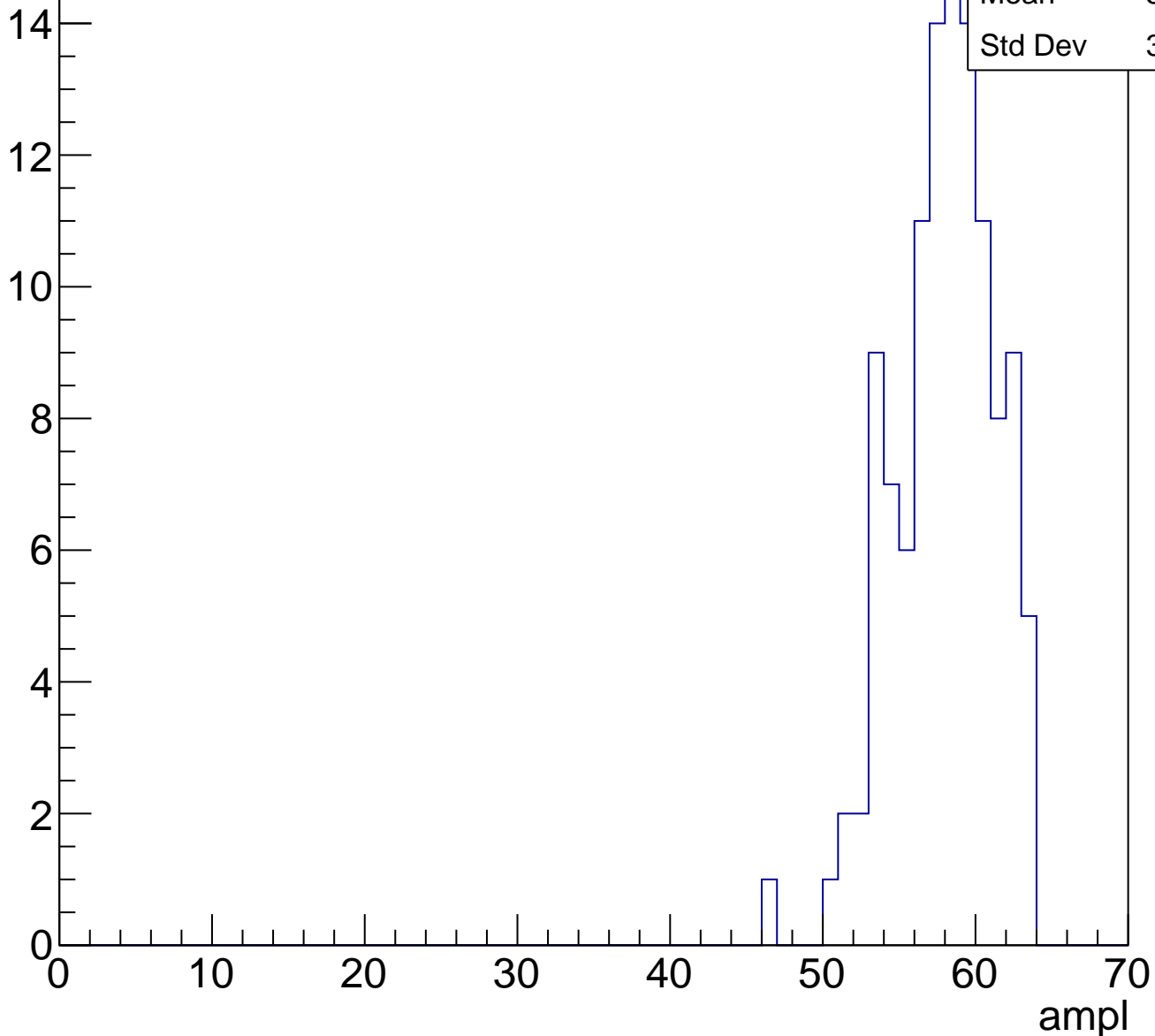


B0L101S, U20-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	57.55
Std Dev	3.234

Entry



B0L101S, U20-ch11, adc5

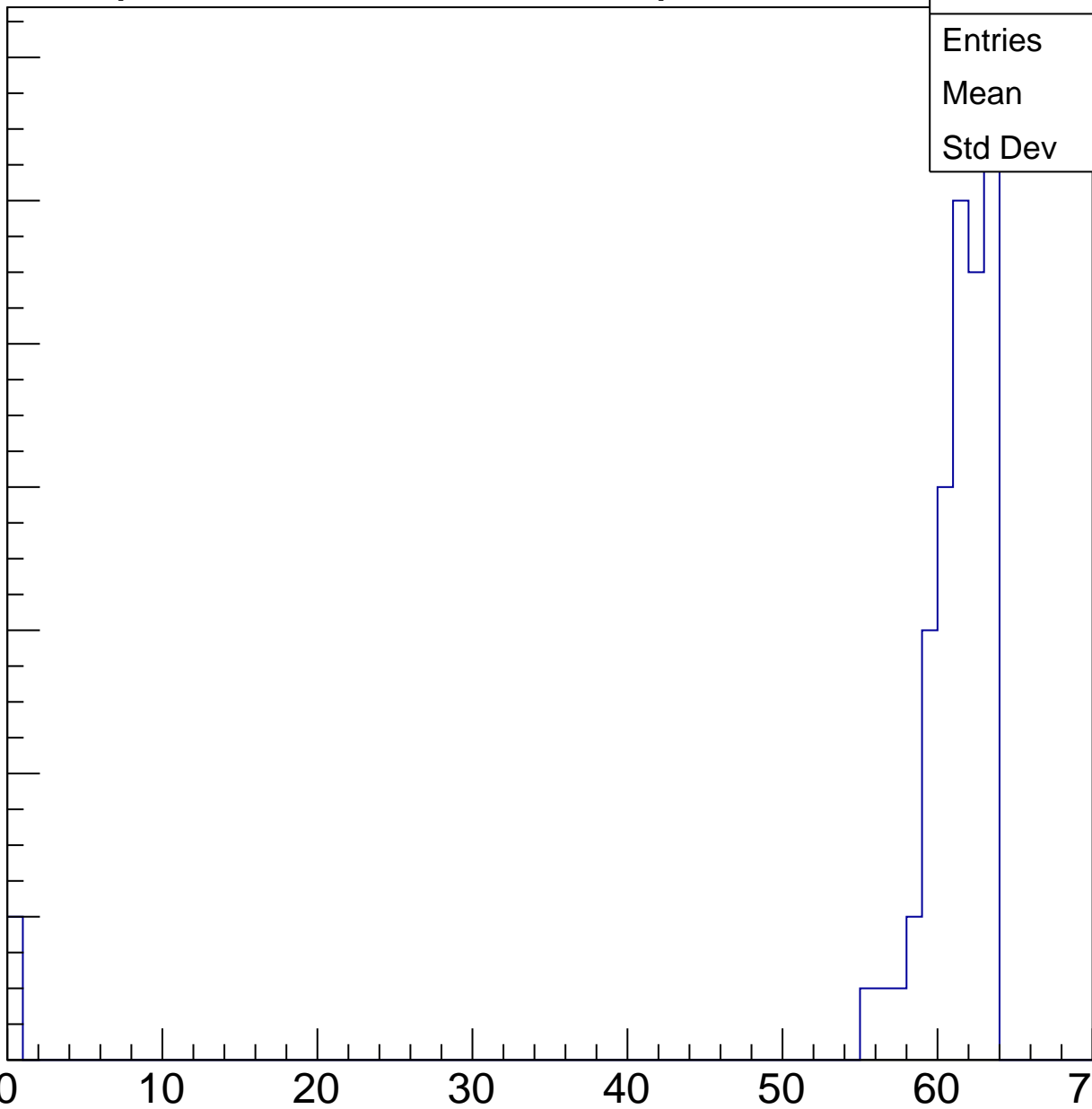
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	58.86
Std Dev	11.27

ampl



B0L101S, U20-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

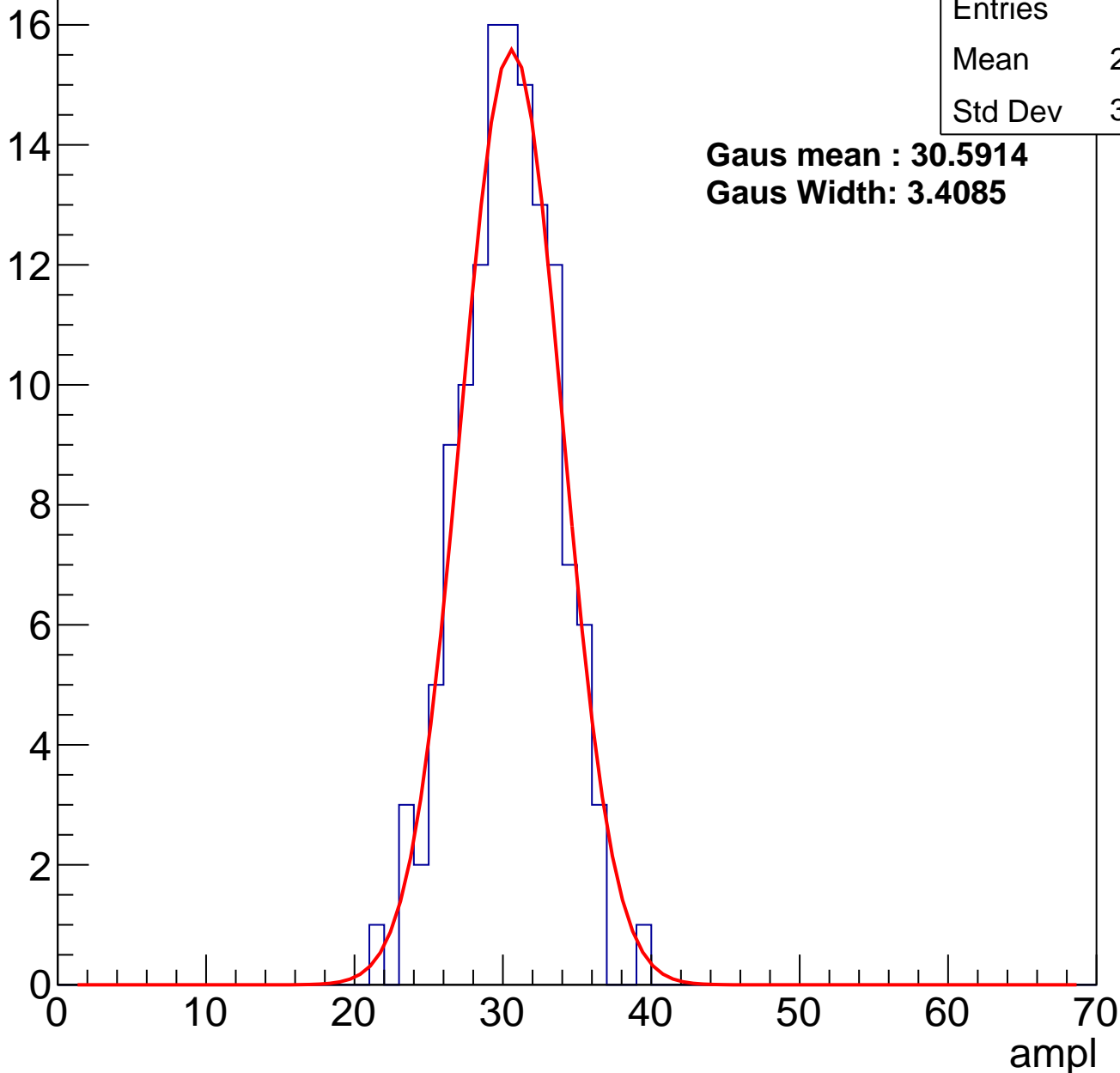
Entry



B0L101S, U20-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch12, adc1

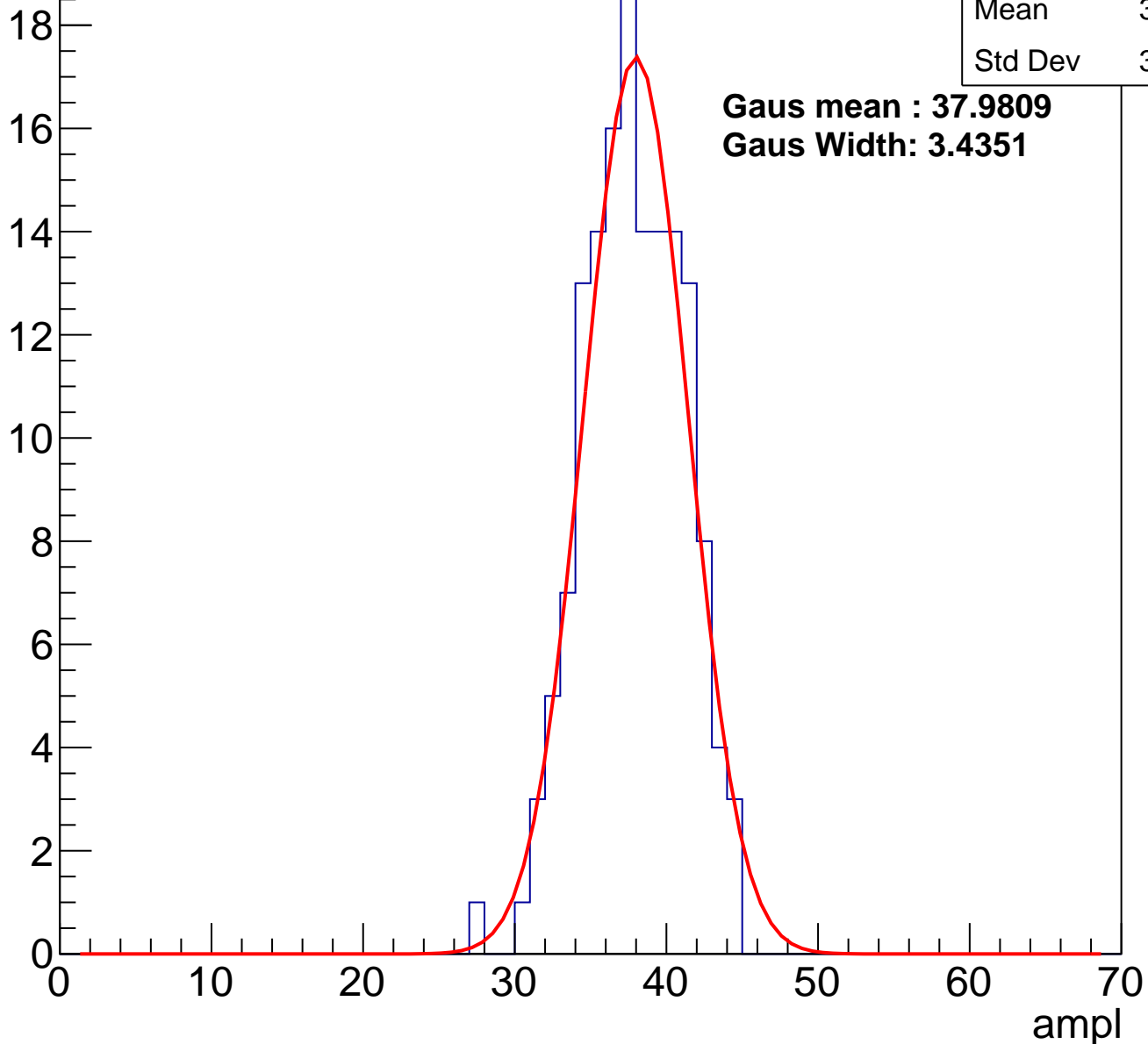
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	37.34
Std Dev	3.237

Gaus mean : 37.9809

Gaus Width: 3.4351

Entry



B0L101S, U20-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

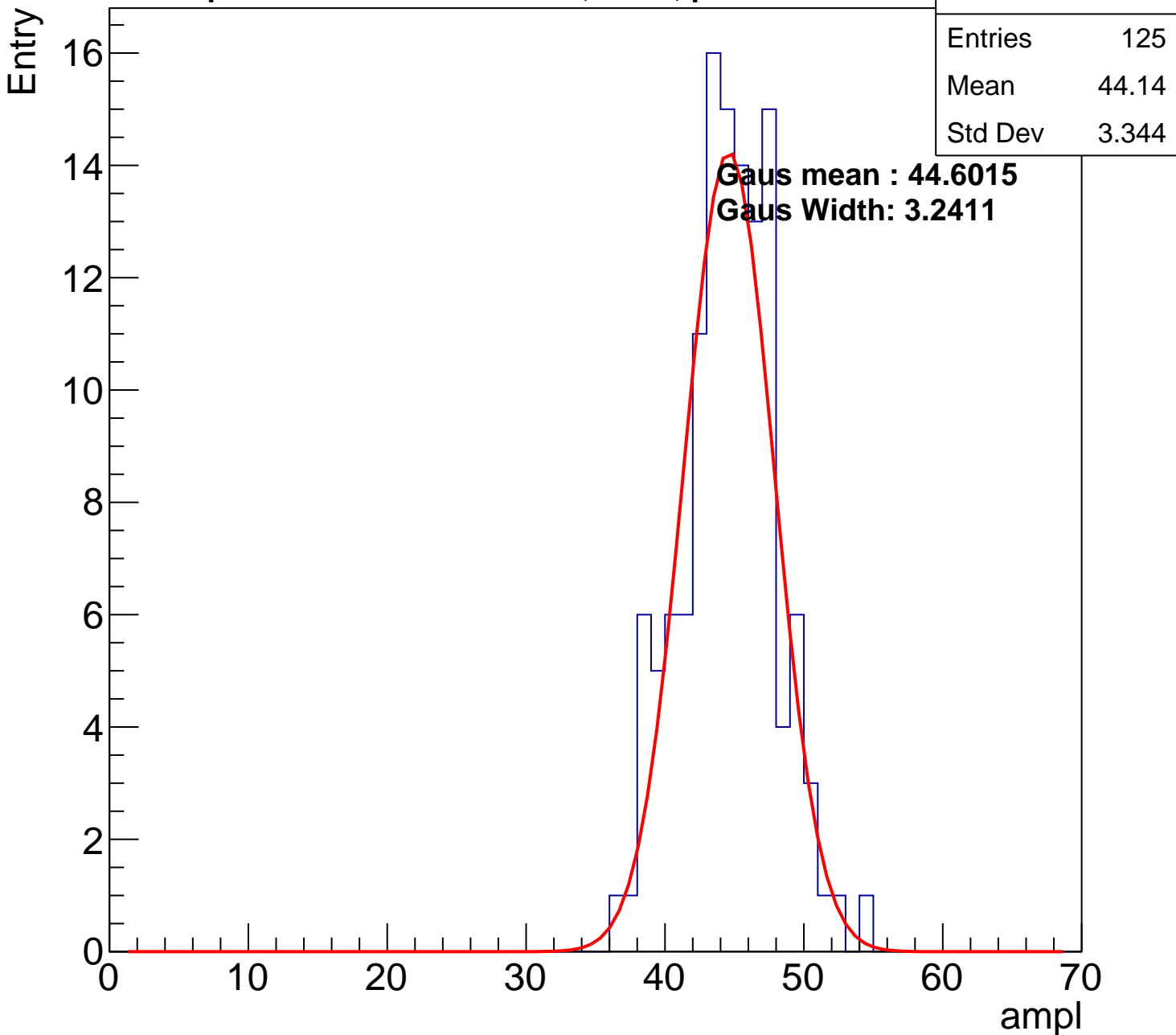
Entries	125
Mean	44.14
Std Dev	3.344

Gaus mean : 44.6015

Gaus Width: 3.2411

0 10 20 30 40 50 60 70

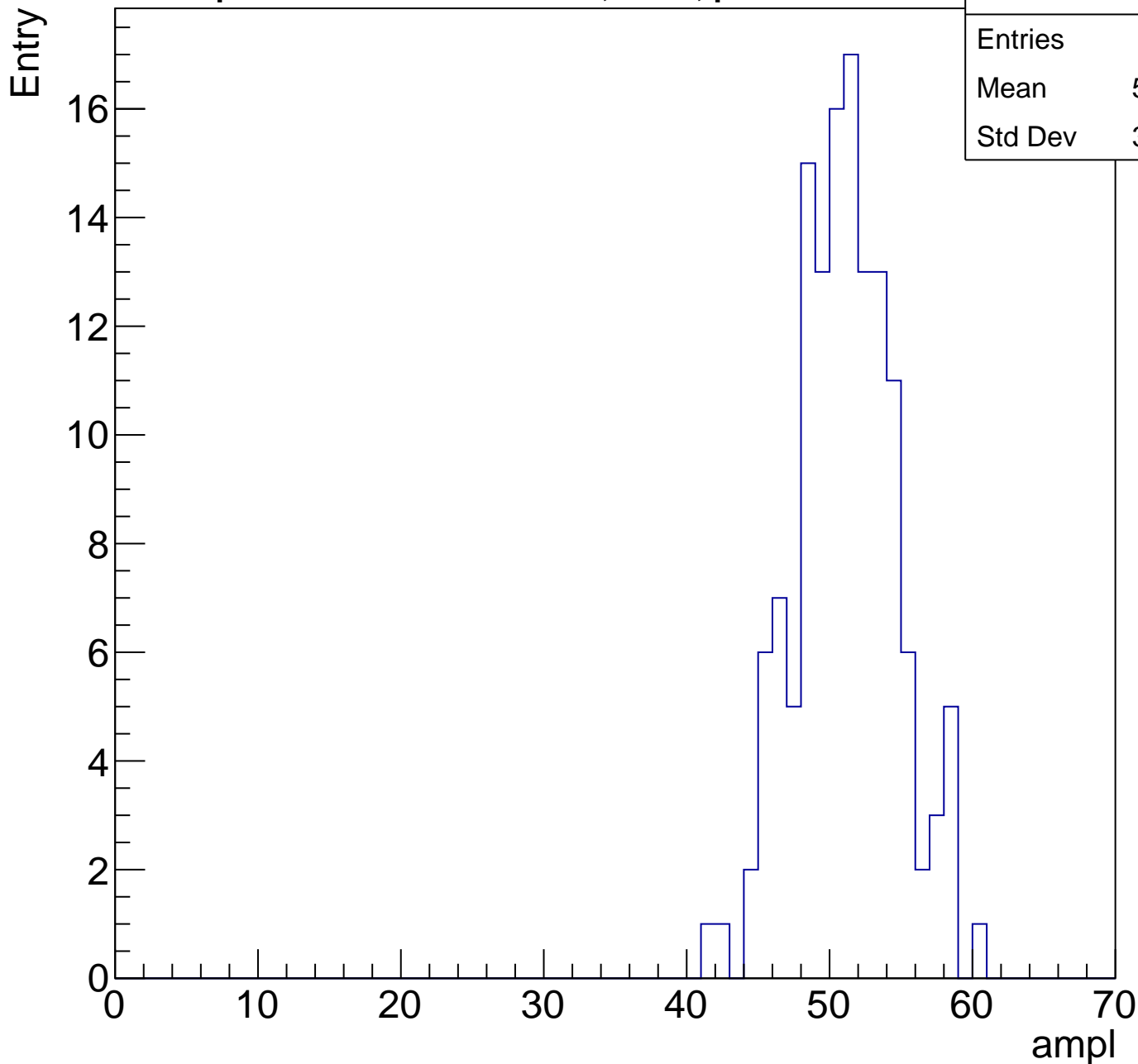
ampl



B0L101S, U20-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	50.69
Std Dev	3.505



B0L101S, U20-ch12, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

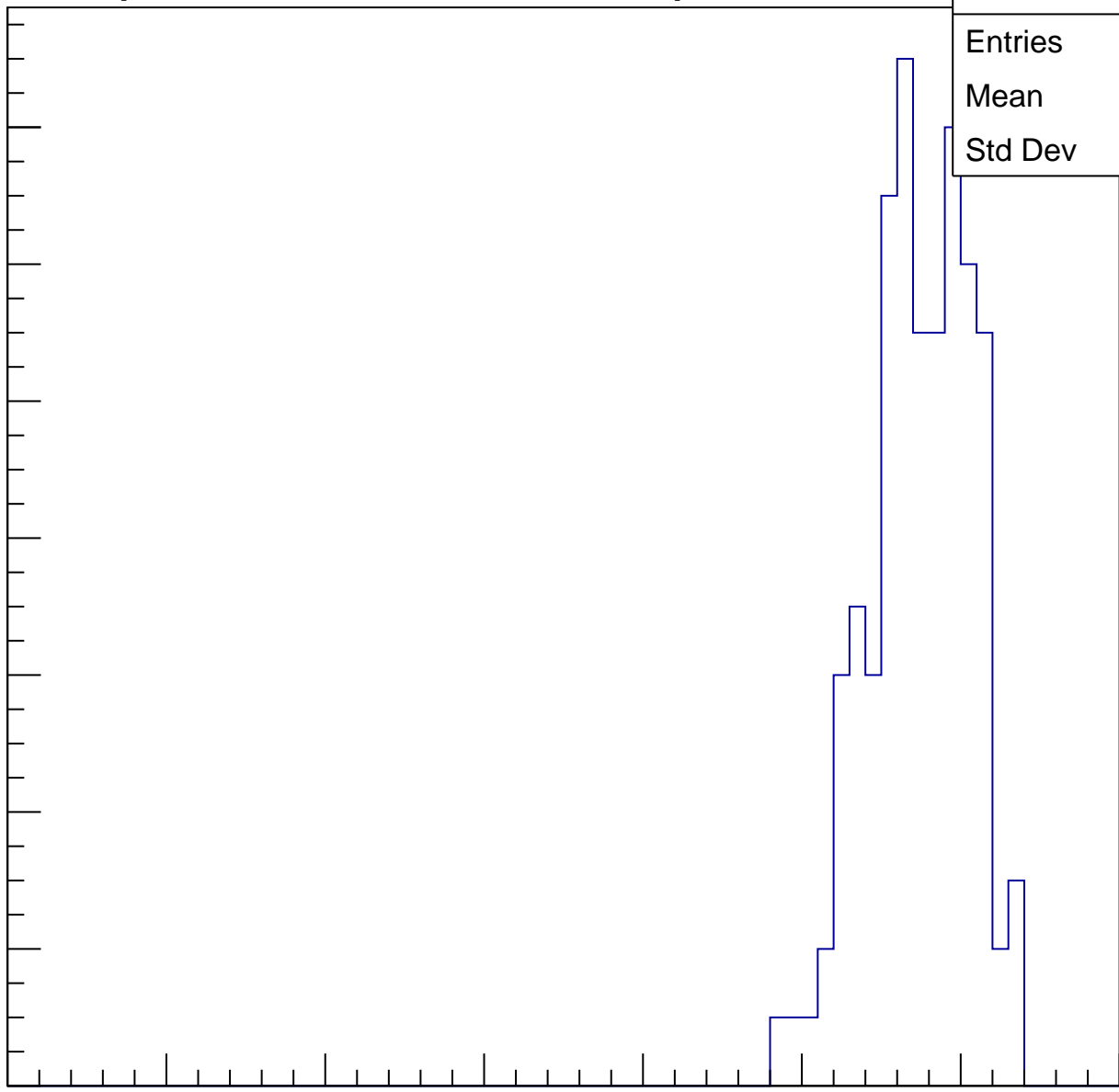
50

60

70

ampl

Entries	116
Mean	56.95
Std Dev	3.159



B0L101S, U20-ch12, adc5

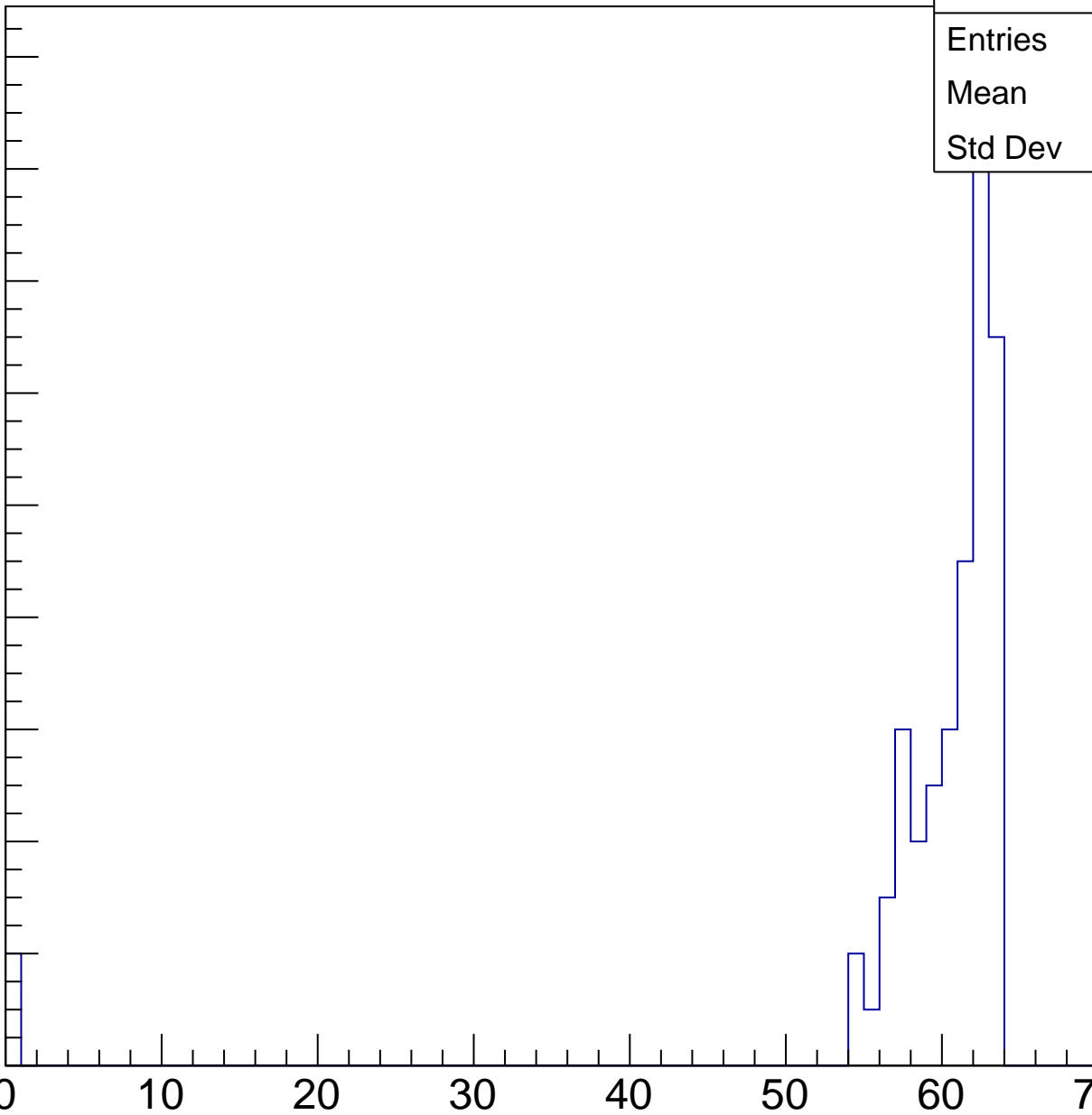
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.61
Std Dev	10.41

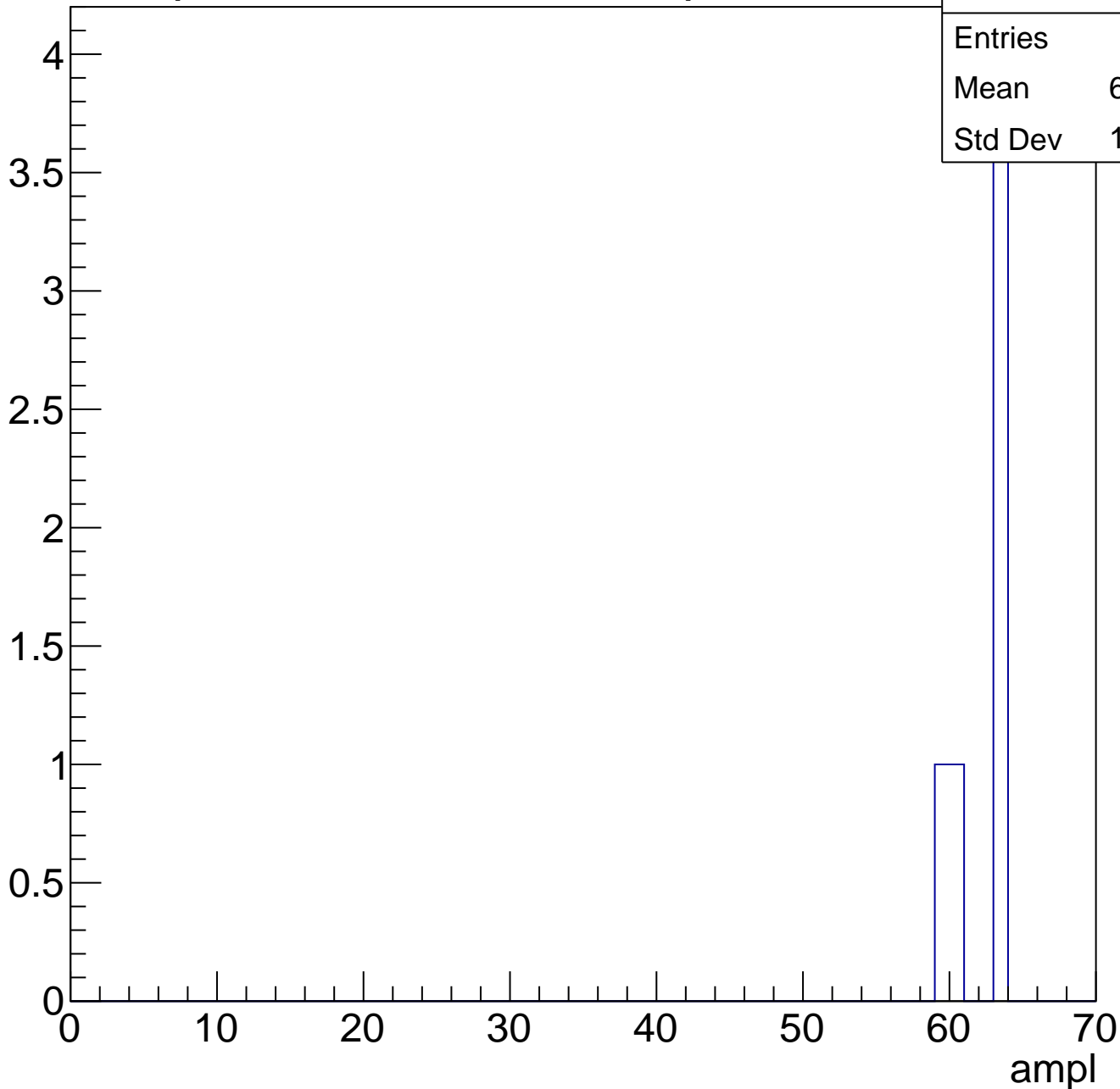
ampl



B0L101S, U20-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch13, adc0

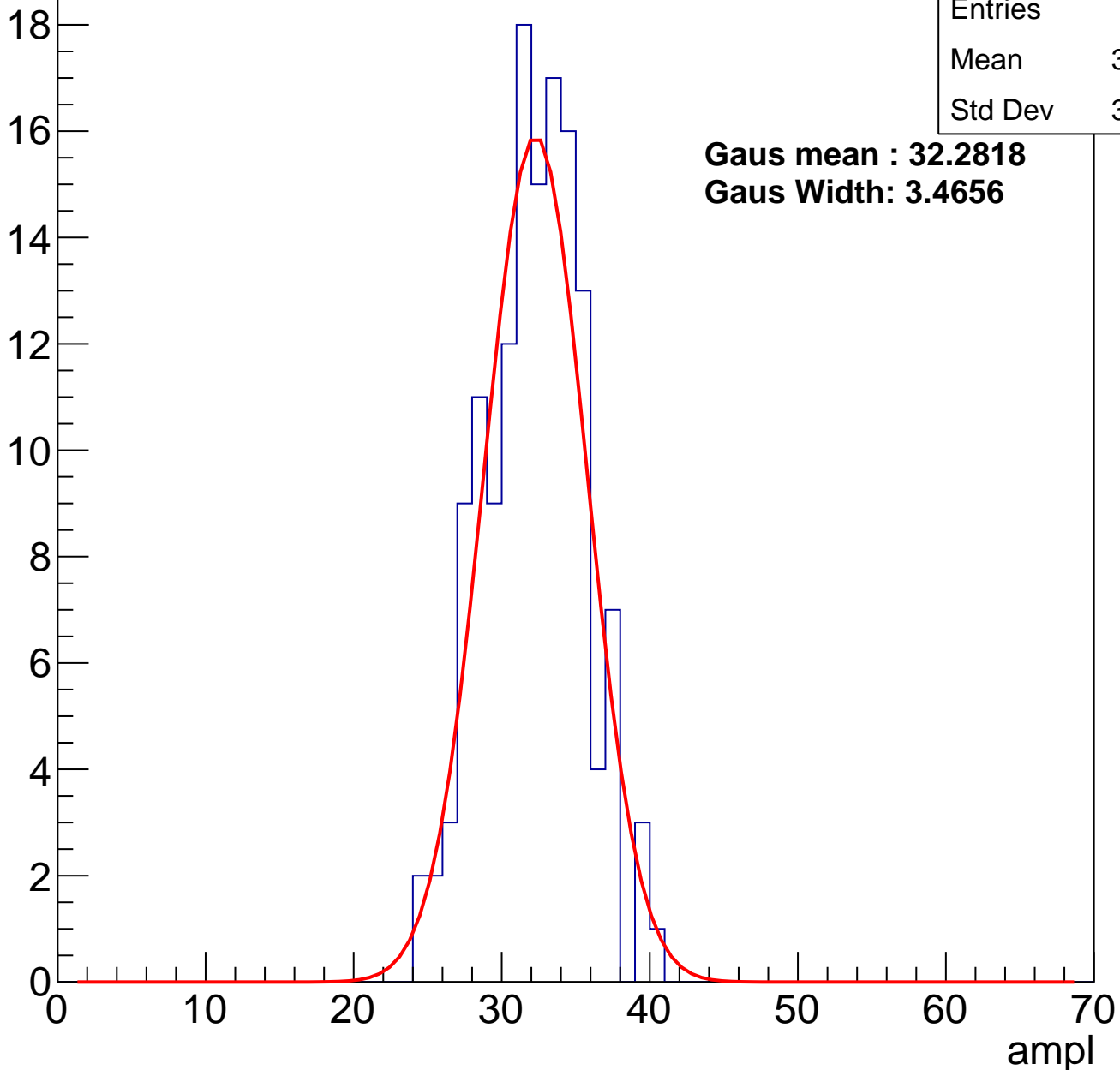
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	31.73
Std Dev	3.272

Gaus mean : 32.2818

Gaus Width: 3.4656

Entry



B0L101S, U20-ch13, adc1

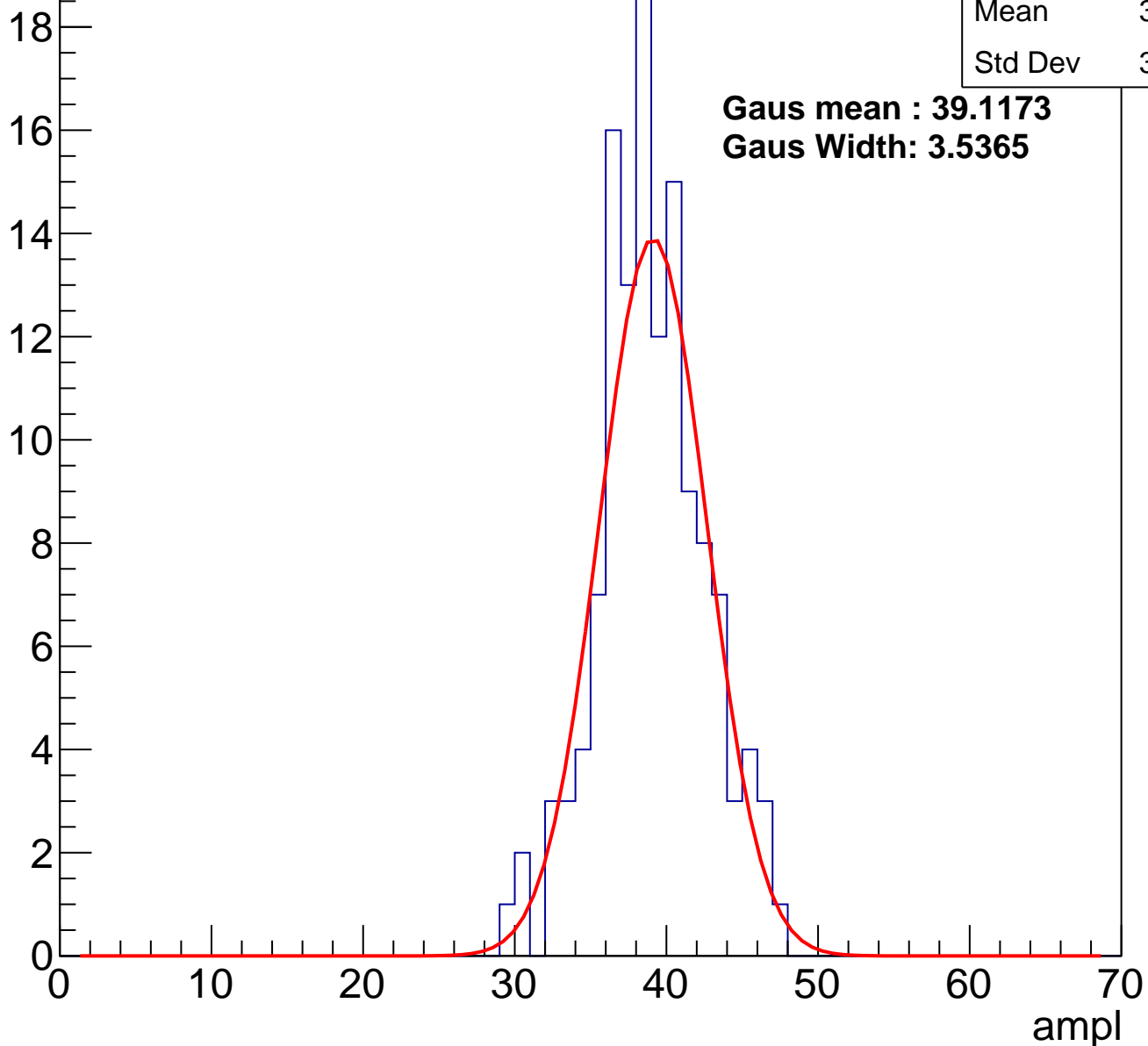
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	38.58
Std Dev	3.493

Gaus mean : 39.1173

Gaus Width: 3.5365

Entry



B0L101S, U20-ch13, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	45.13
Std Dev	3.334

Gaus mean : 45.5807

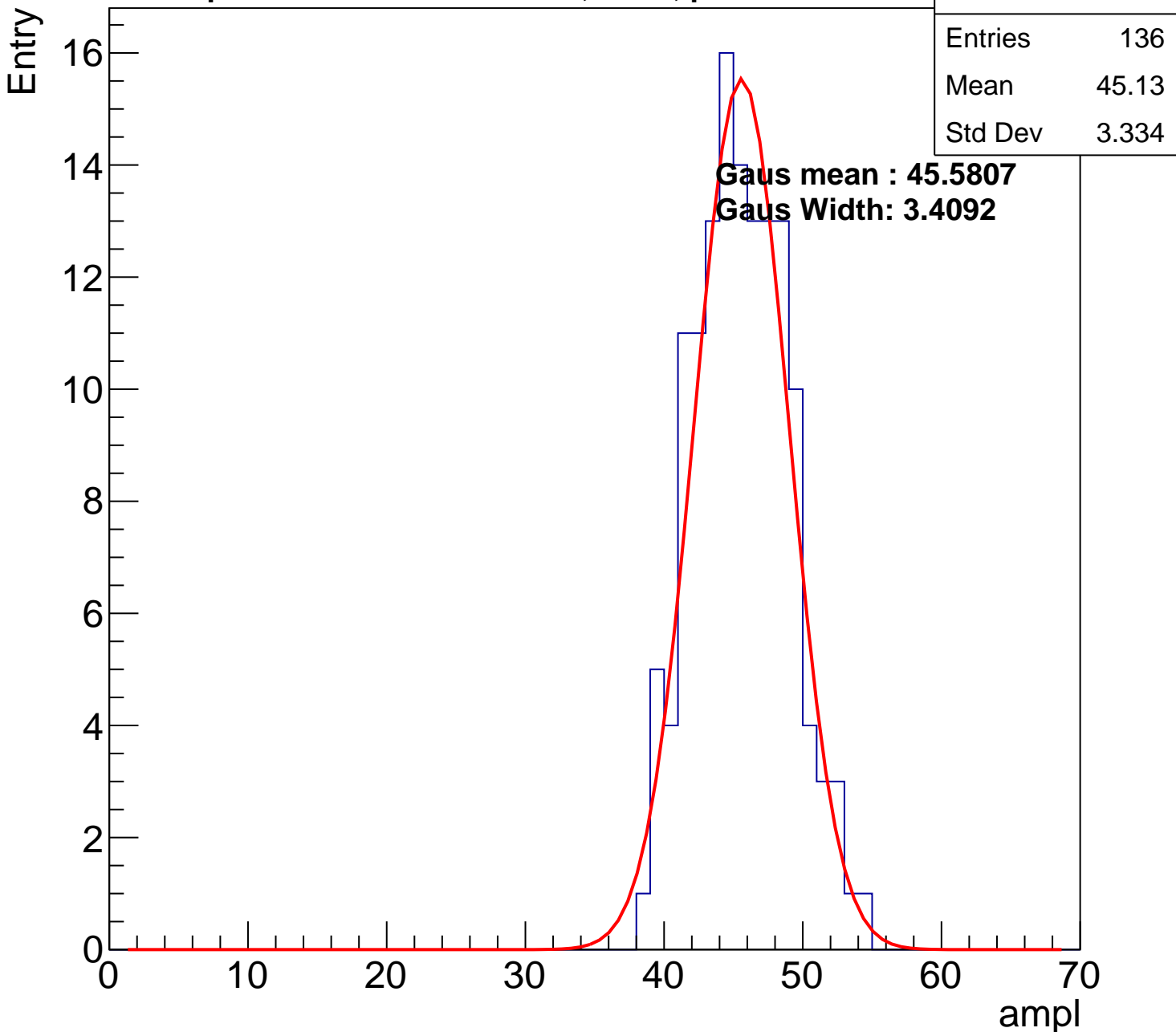
Gaus Width: 3.4092

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

124

Mean

51.83

Std Dev

3.498

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch13, adc4

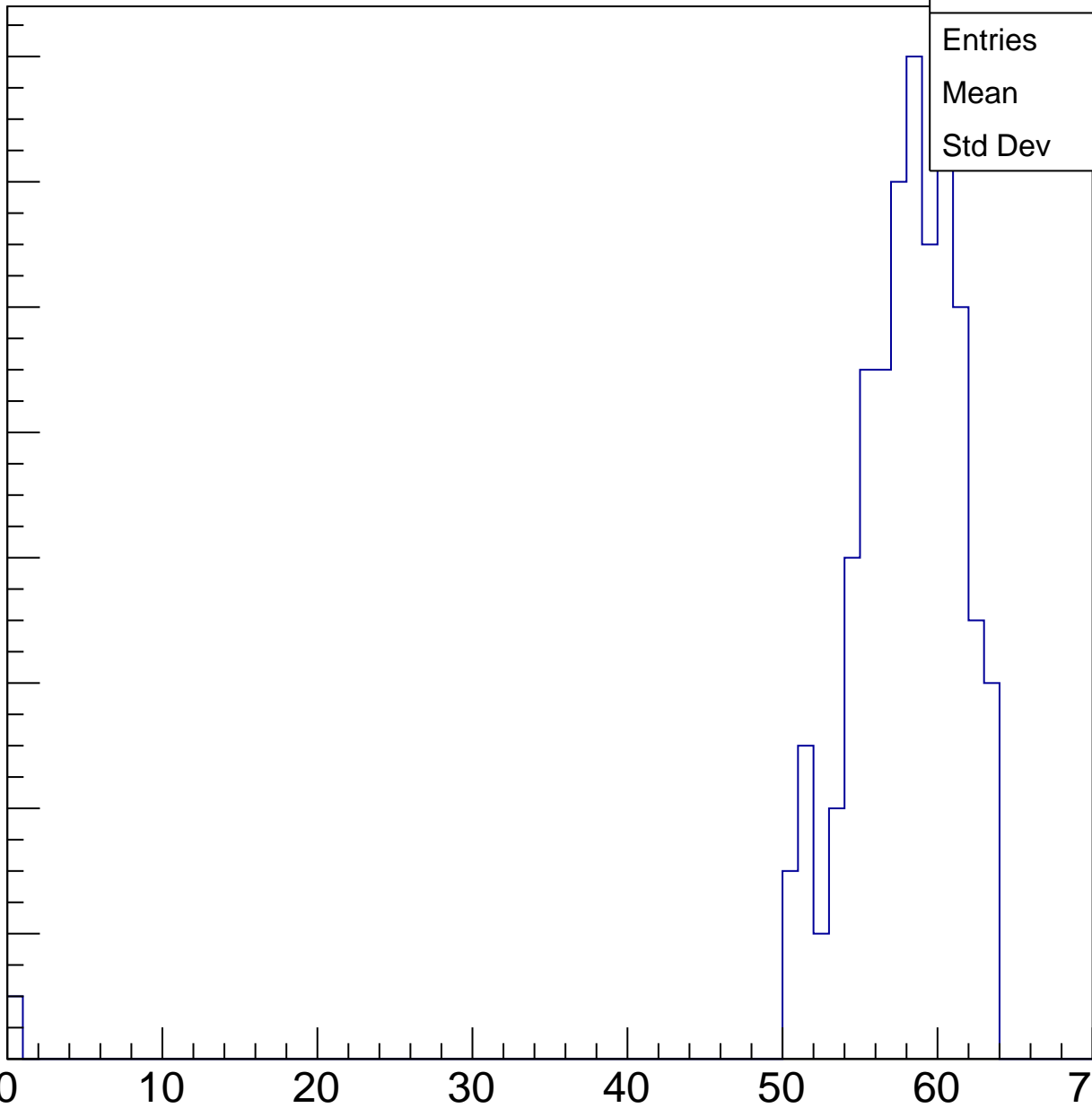
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	57.12
Std Dev	5.996

ampl



B0L101S, U20-ch13, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	59.49
Std Dev	9.392

0

2

4

6

8

10

B0L101S, U20-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

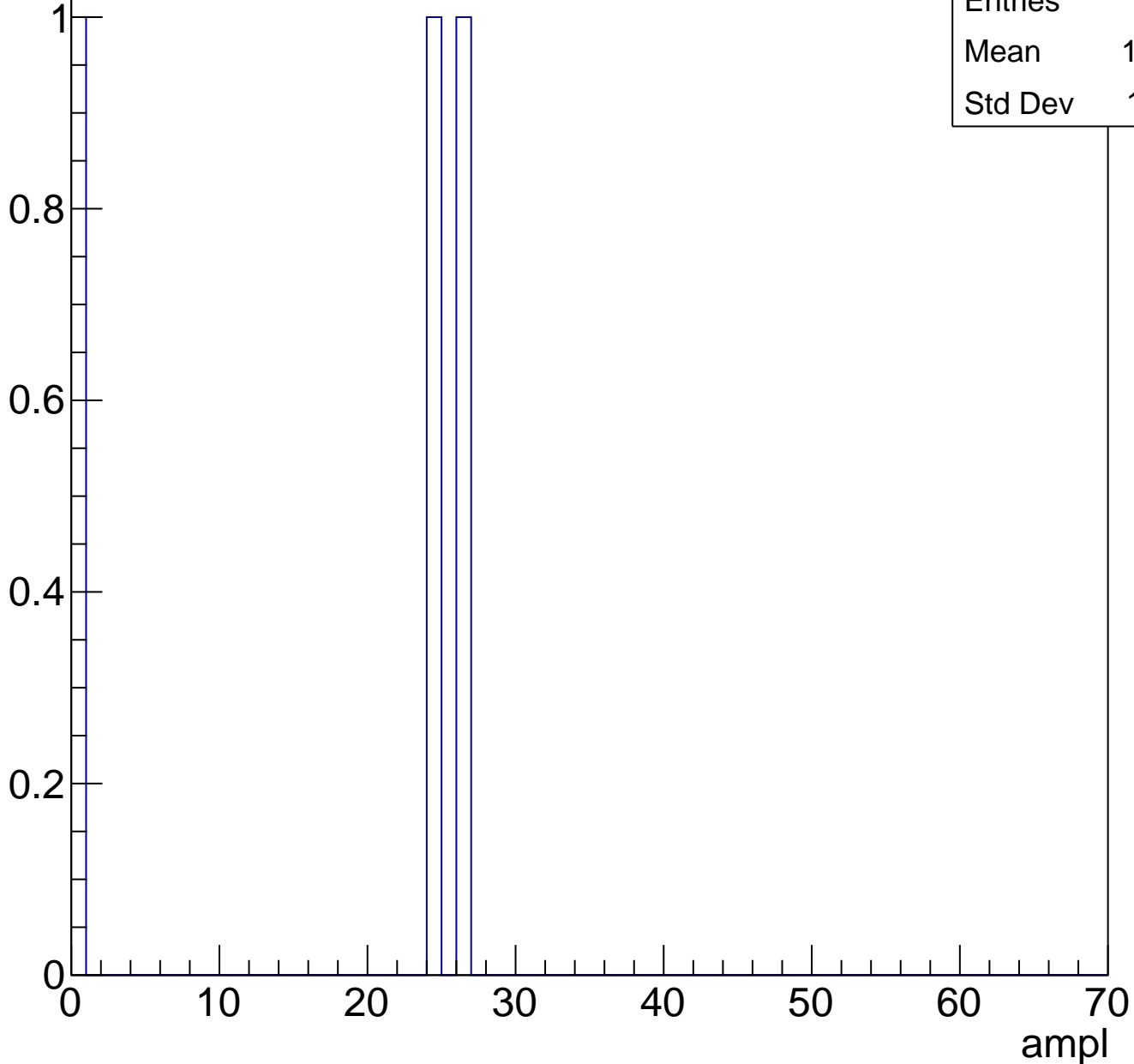
Entry



B0L101S, U20-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch14, adc0

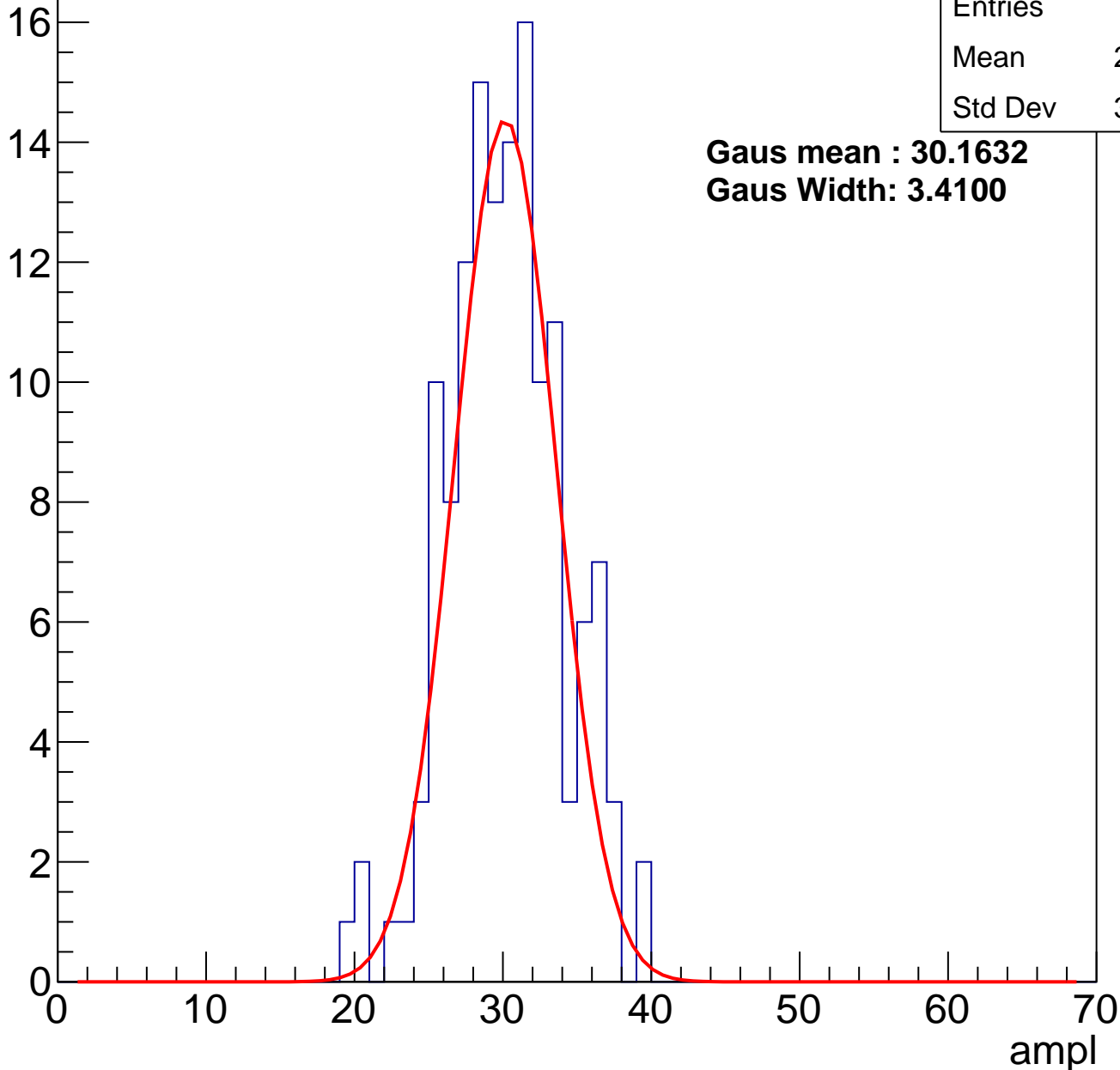
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	29.76
Std Dev	3.814

Gaus mean : 30.1632

Gaus Width: 3.4100

Entry



B0L101S, U20-ch14, adc1

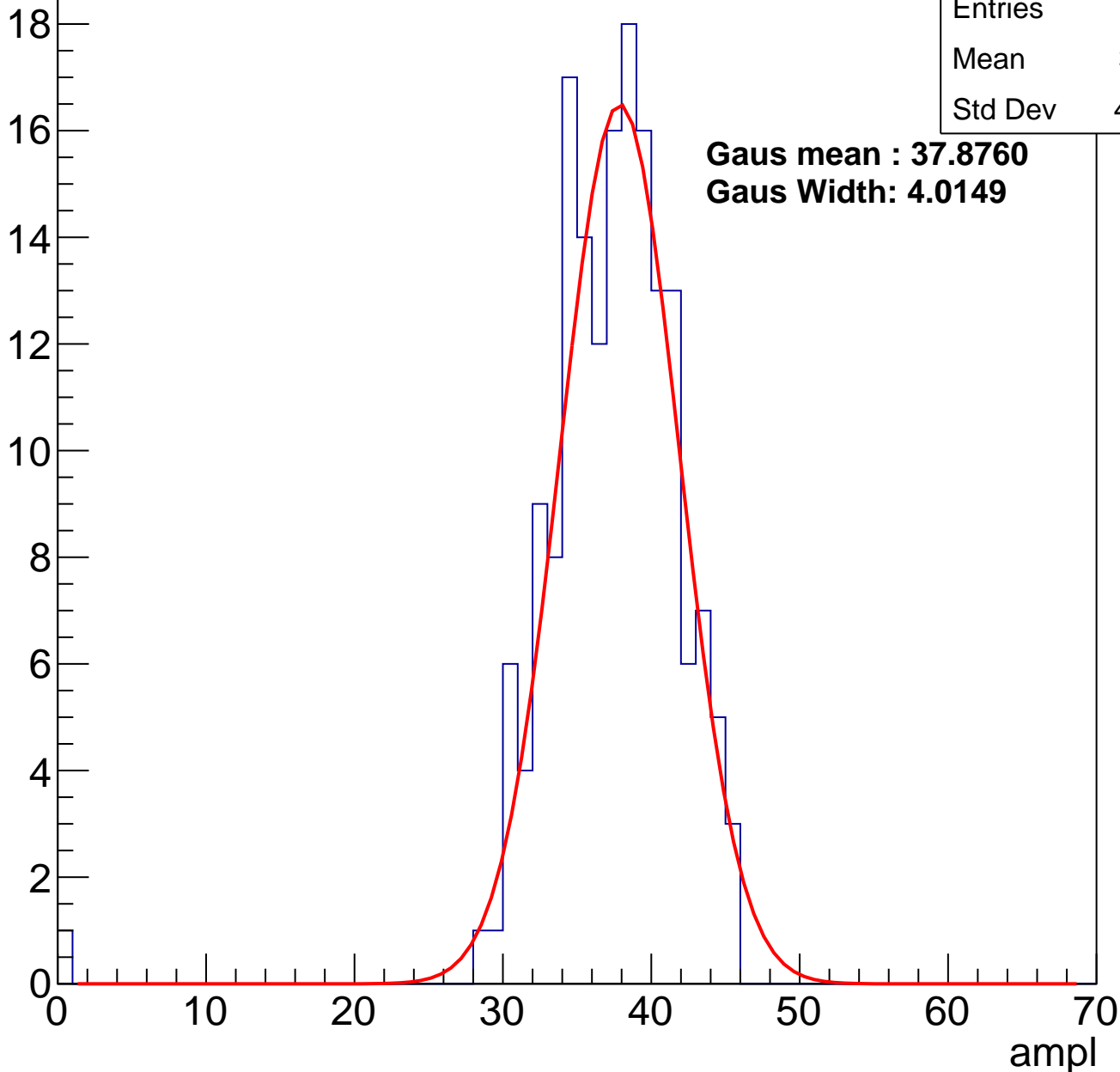
calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	36.91
Std Dev	4.709

Gaus mean : 37.8760

Gaus Width: 4.0149

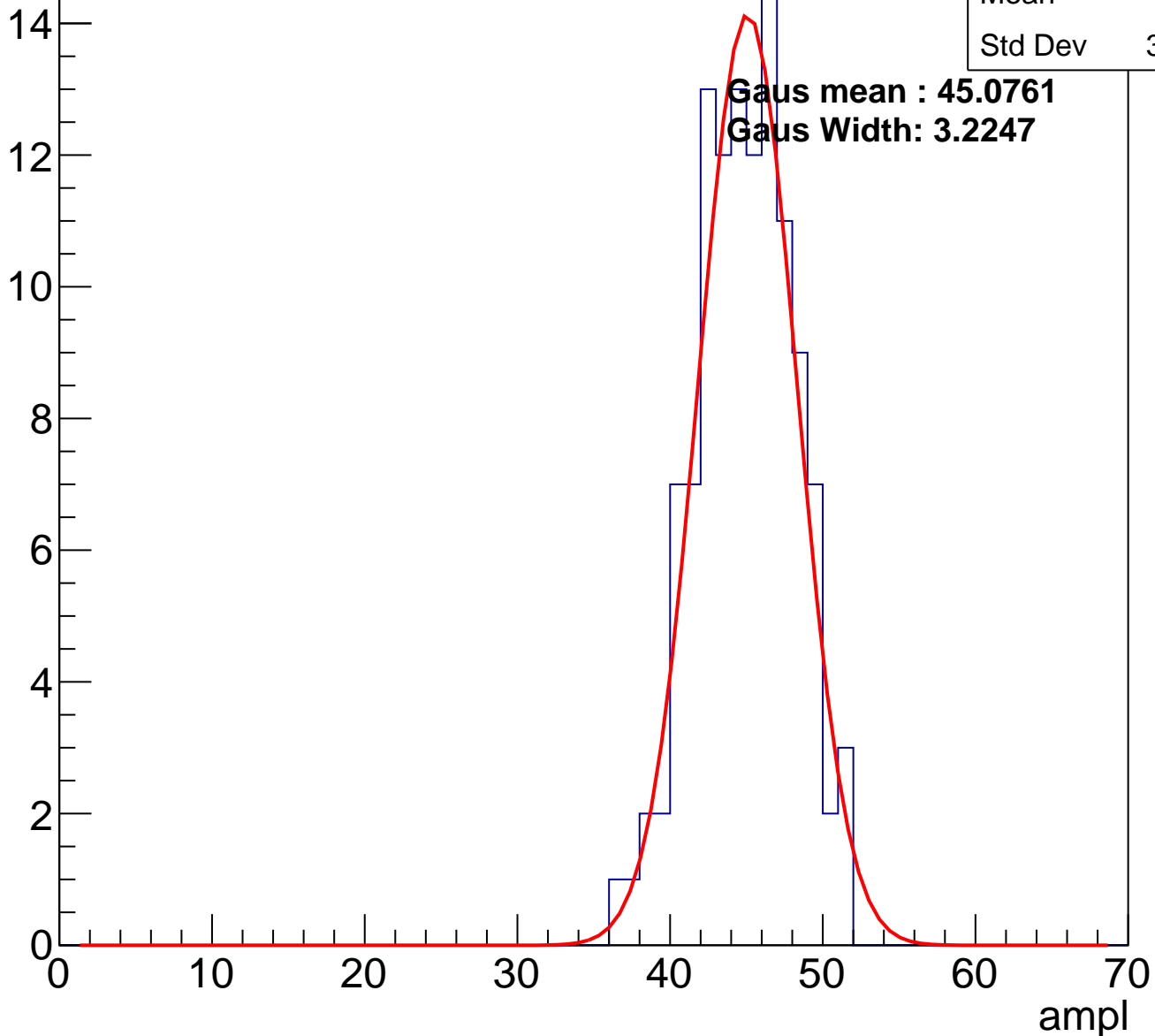
Entry



B0L101S, U20-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

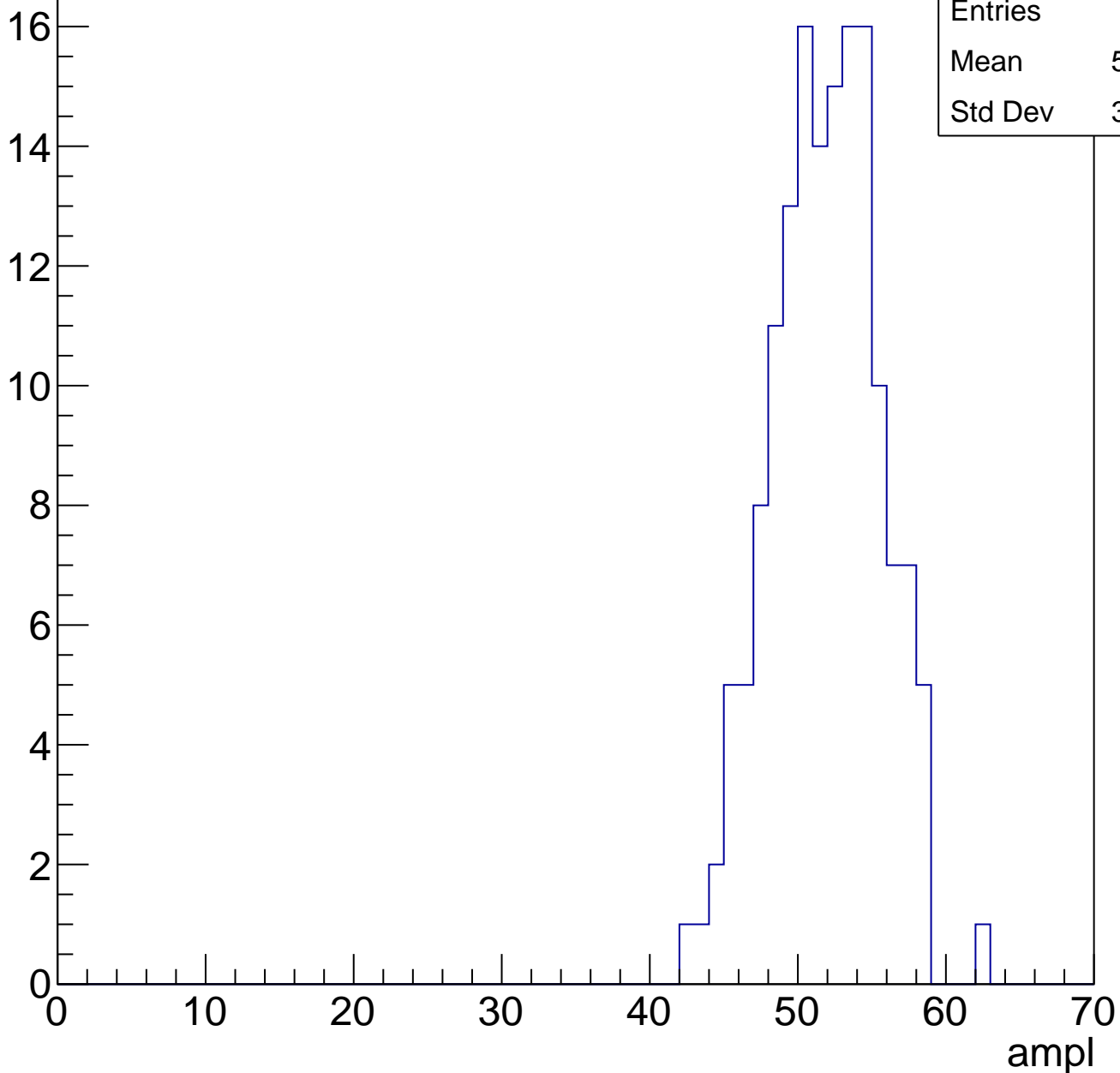
Entry



B0L101S, U20-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	153
Mean	51.42
Std Dev	3.608

B0L101S, U20-ch14, adc4

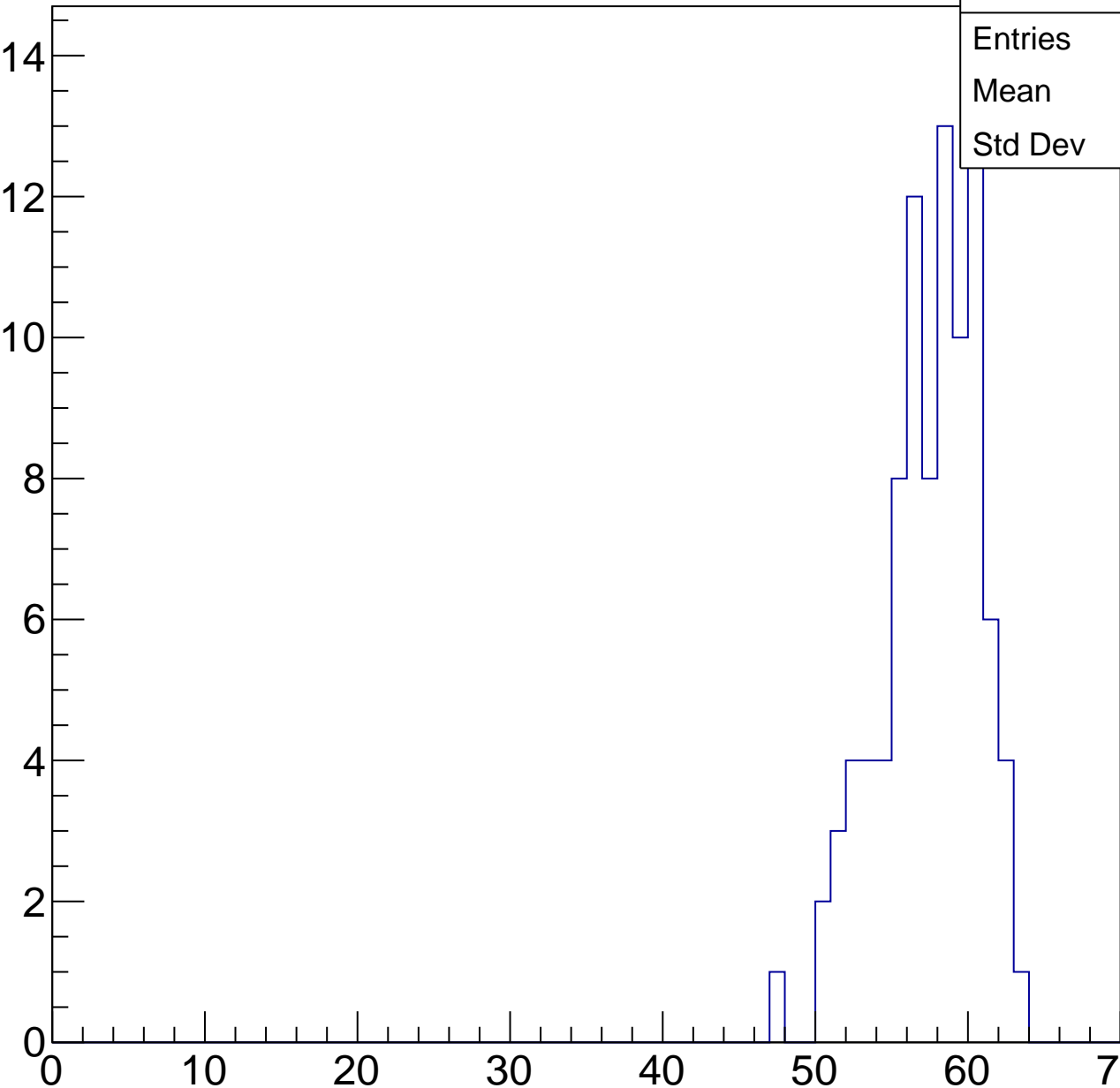
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	57.07
Std Dev	3.197

ampl



B0L101S, U20-ch14, adc5

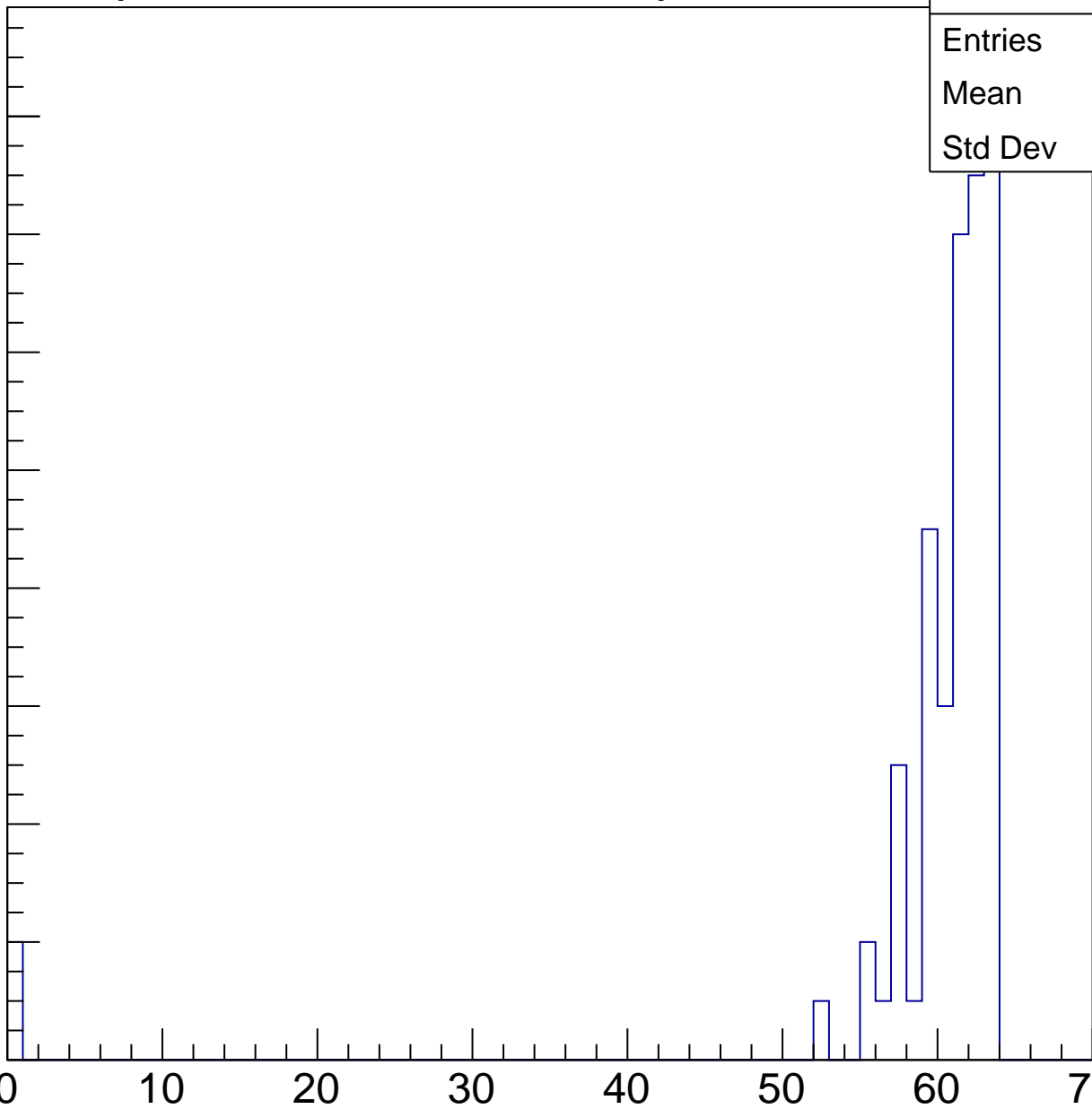
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59
Std Dev	10.16

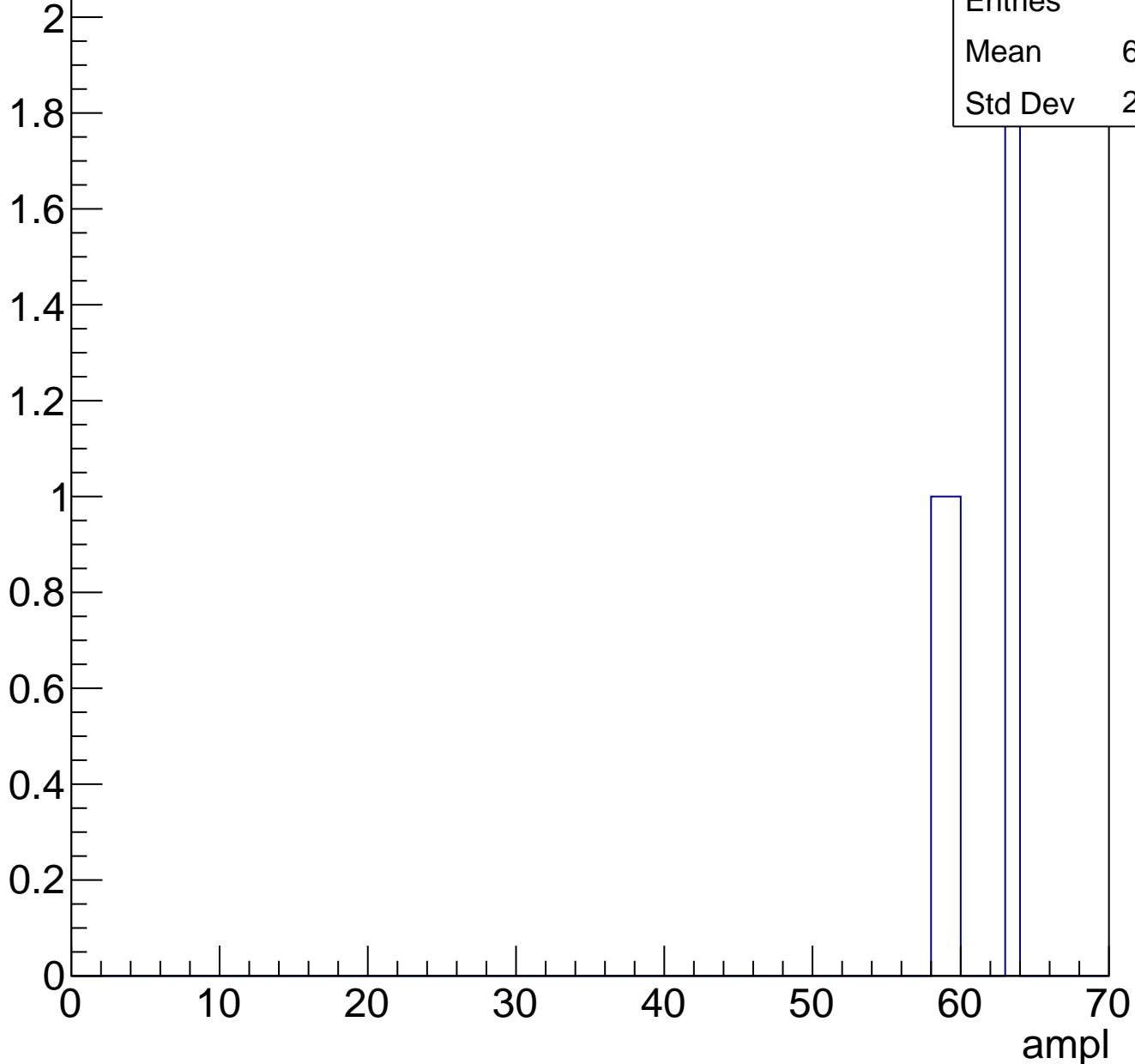
ampl



B0L101S, U20-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	60.75
Std Dev	2.278

B0L101S, U20-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

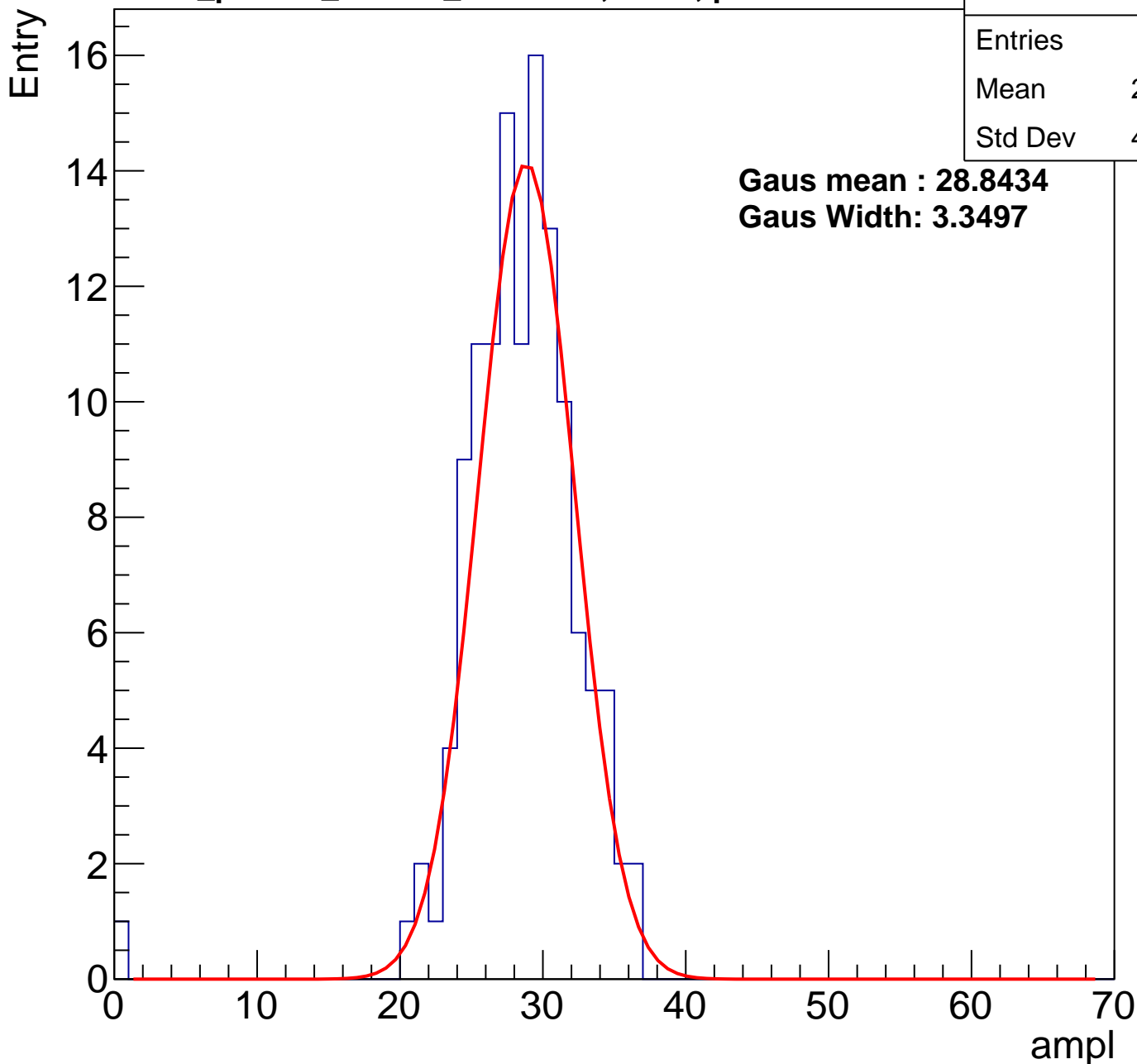
B0L101S, U20-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	27.99
Std Dev	4.166

Gaus mean : 28.8434

Gaus Width: 3.3497

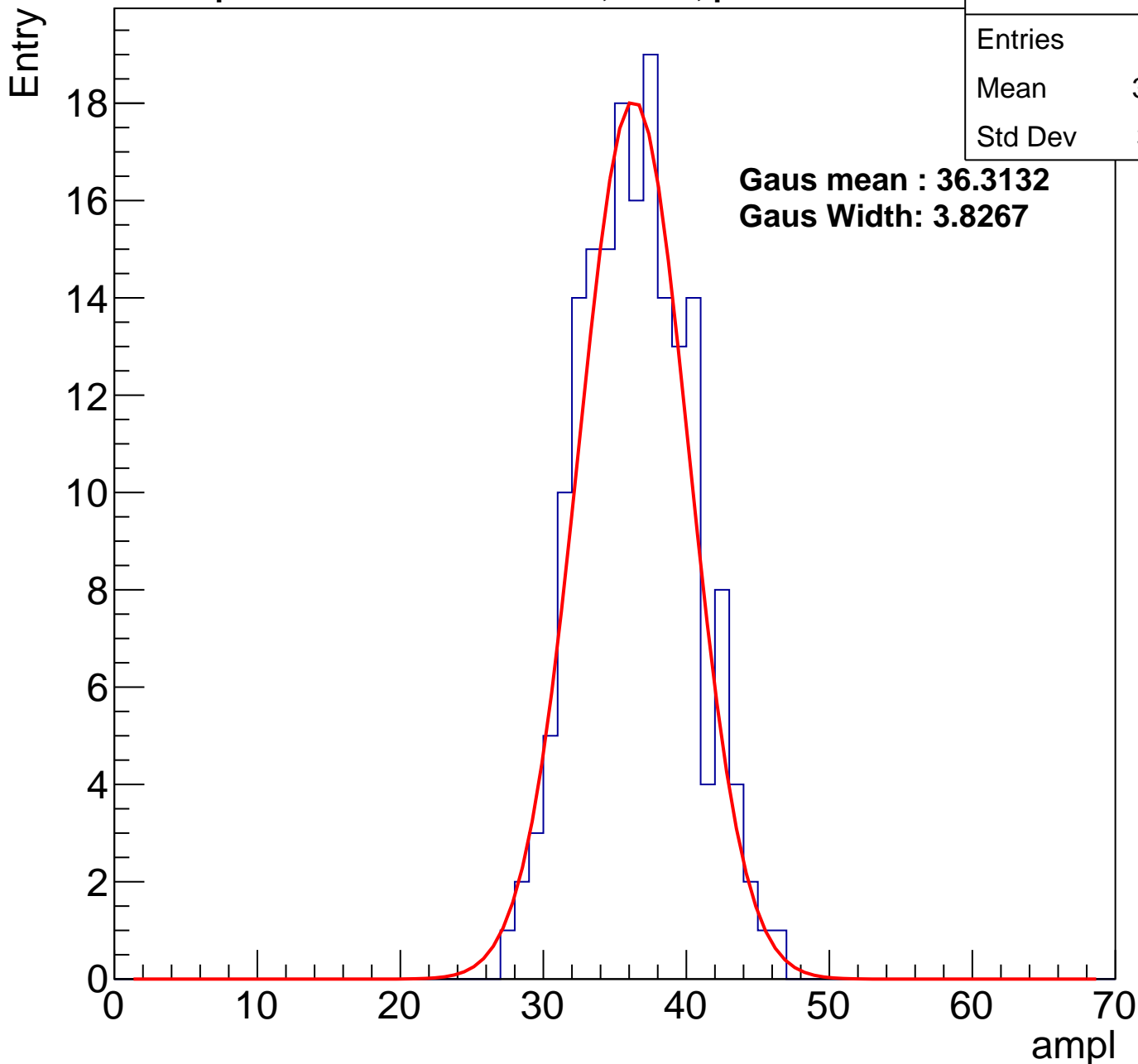


B0L101S, U20-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	35.99
Std Dev	3.761

Gaus mean : 36.3132
Gaus Width: 3.8267



B0L101S, U20-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

122

Mean

43.61

Std Dev

3.367

Gaus mean : 44.0240

Gaus Width: 3.2695

0

10

20

30

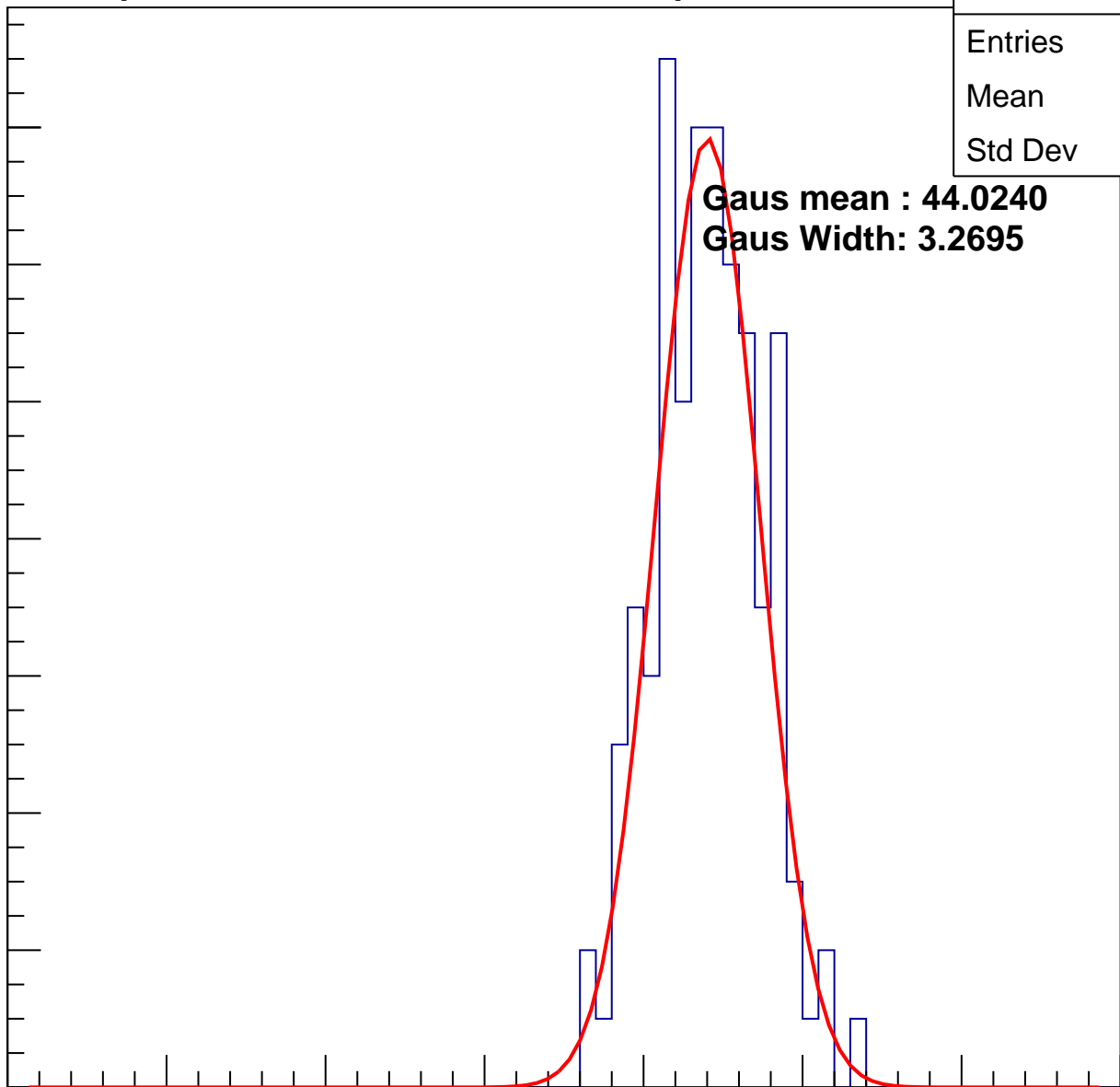
40

50

60

70

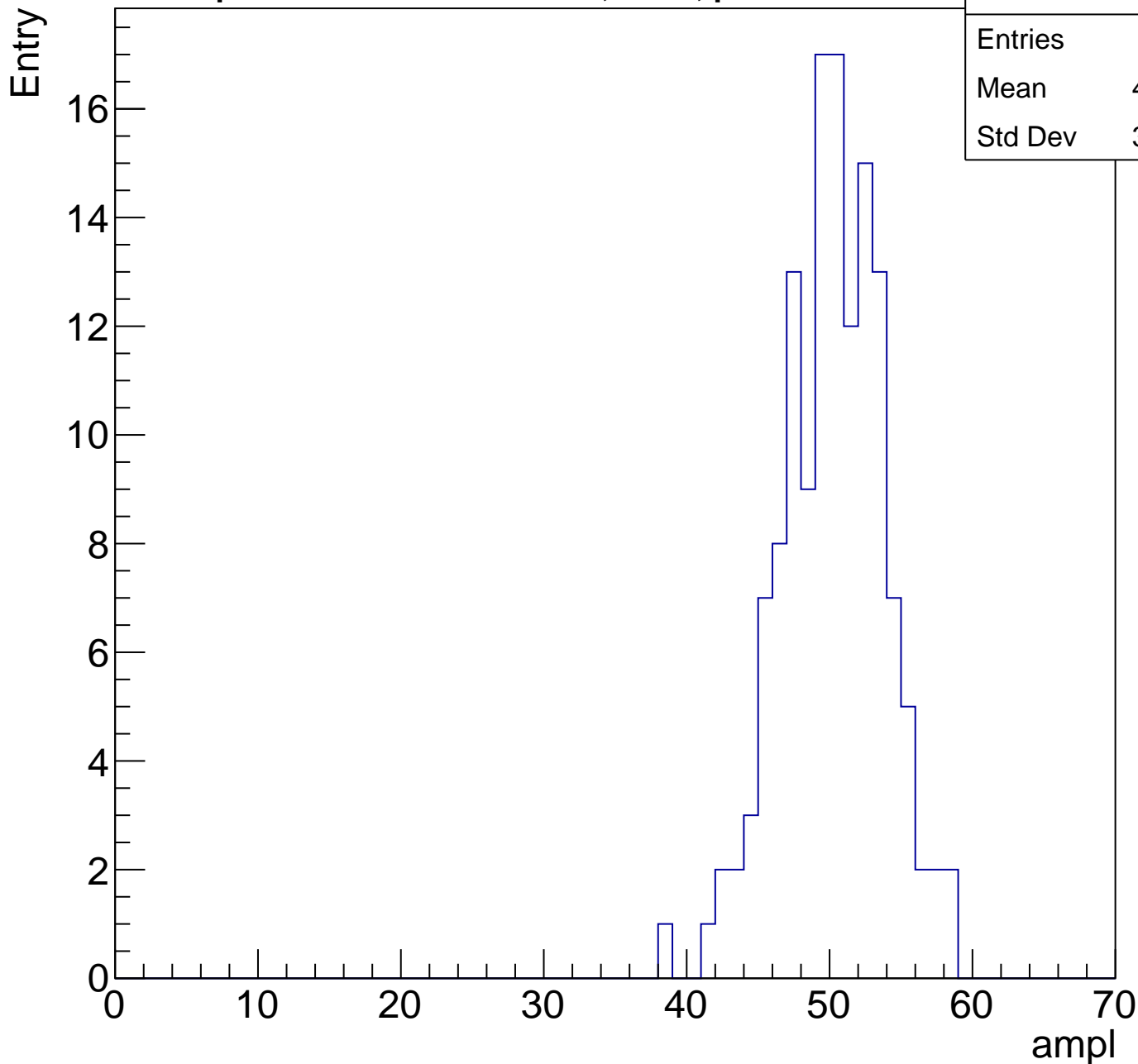
ampl



B0L101S, U20-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	49.75
Std Dev	3.559



B0L101S, U20-ch15, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.95
Std Dev	3.218

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch15, adc5

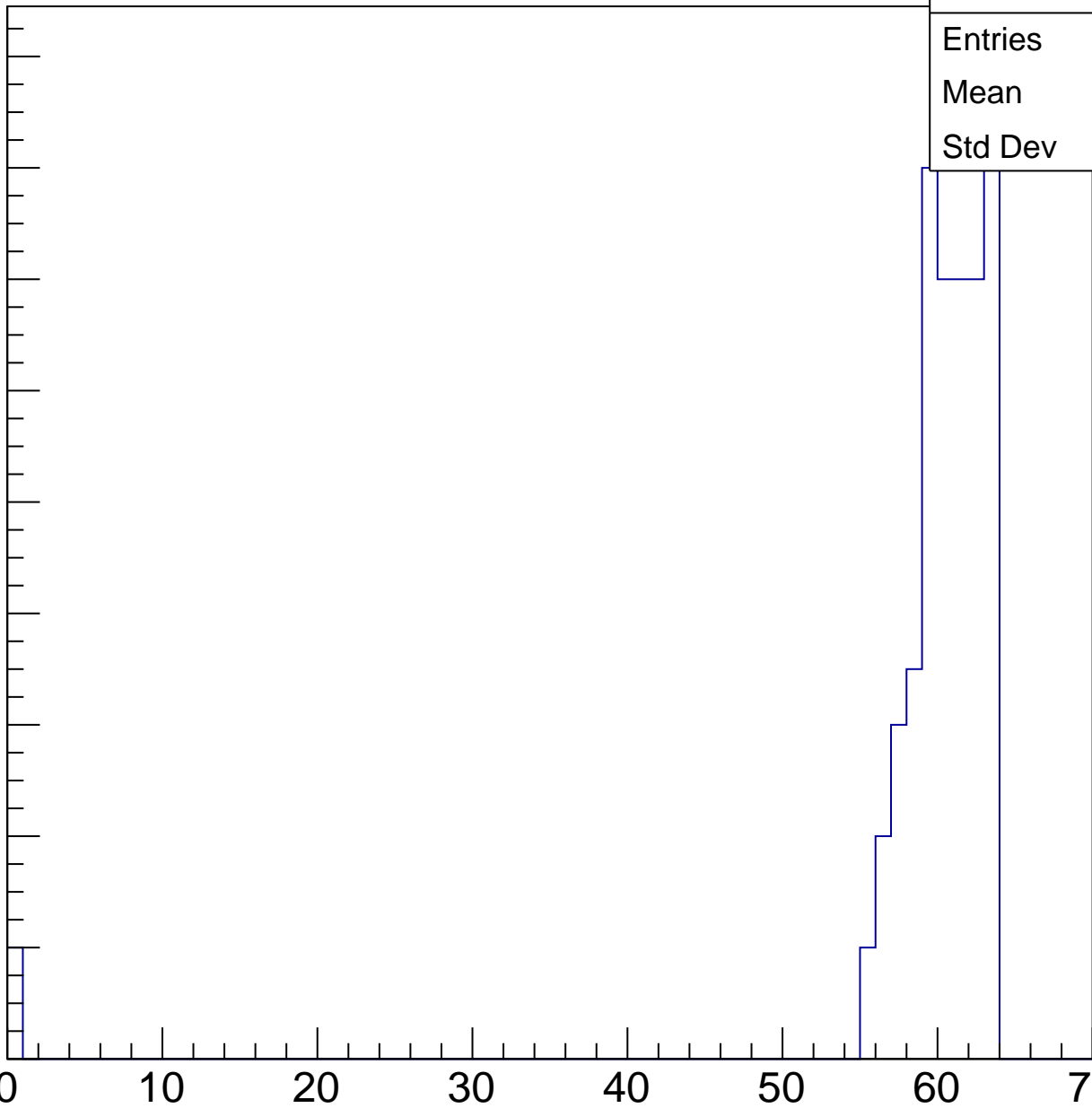
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.99
Std Dev	8.819

ampl



B0L101S, U20-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch16, adc0

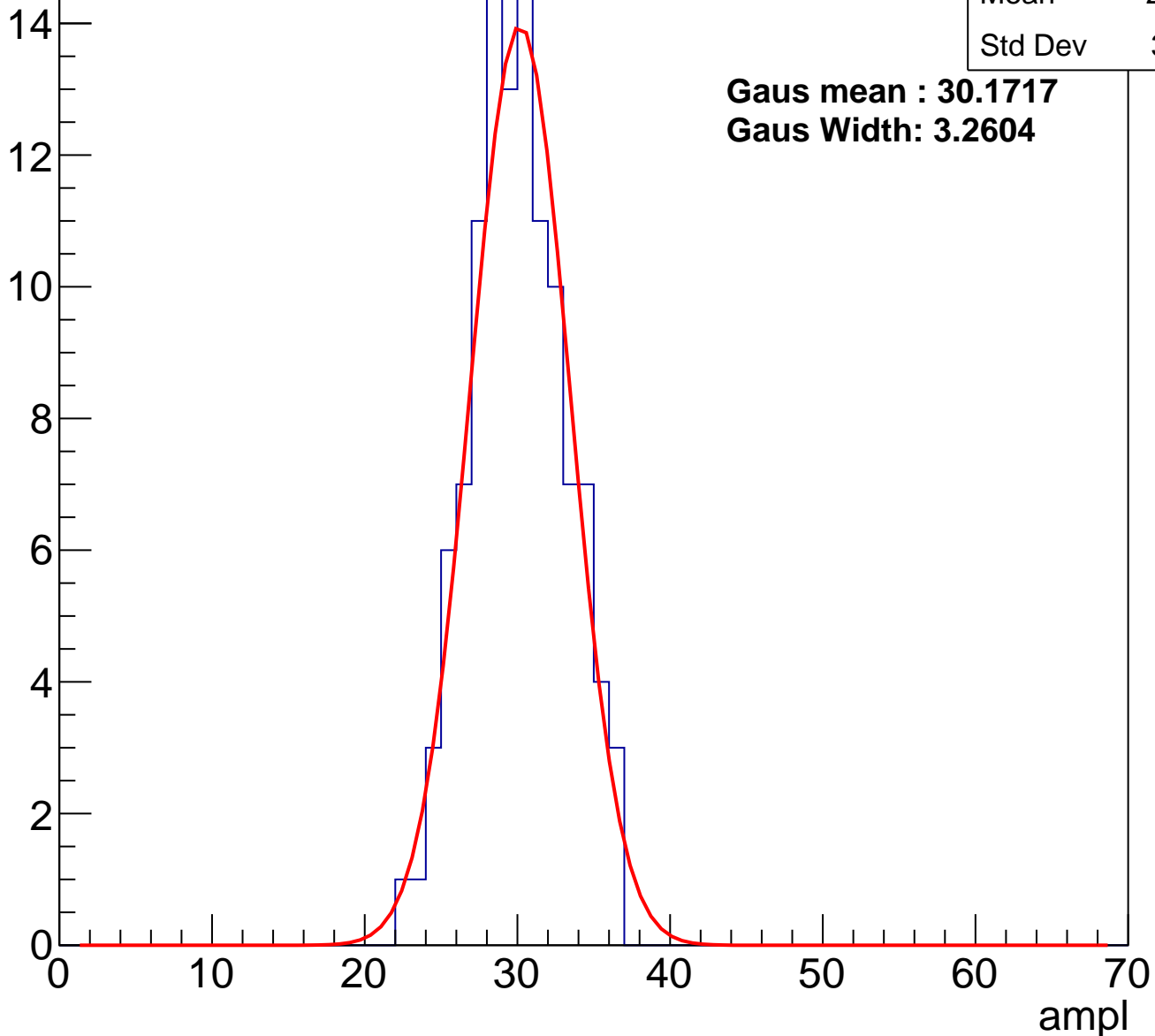
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	29.57
Std Dev	3.061

Gaus mean : 30.1717

Gaus Width: 3.2604

Entry



B0L101S, U20-ch16, adc1

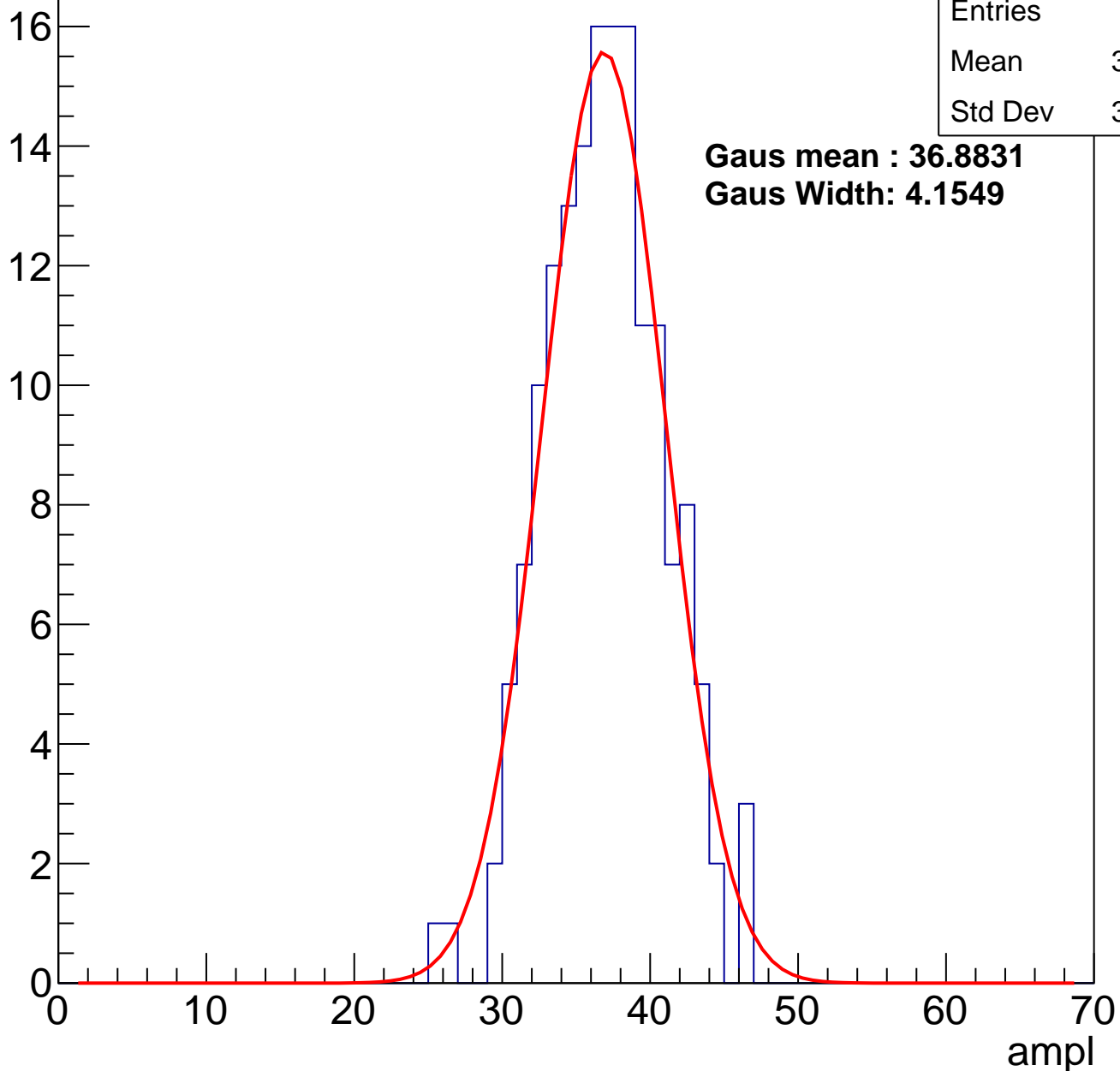
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	36.46
Std Dev	3.926

Gaus mean : 36.8831

Gaus Width: 4.1549

Entry



B0L101S, U20-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	115
Mean	43.2
Std Dev	3.347

Gaus mean : 43.7290

Gaus Width: 3.8288

0

2

4

6

8

10

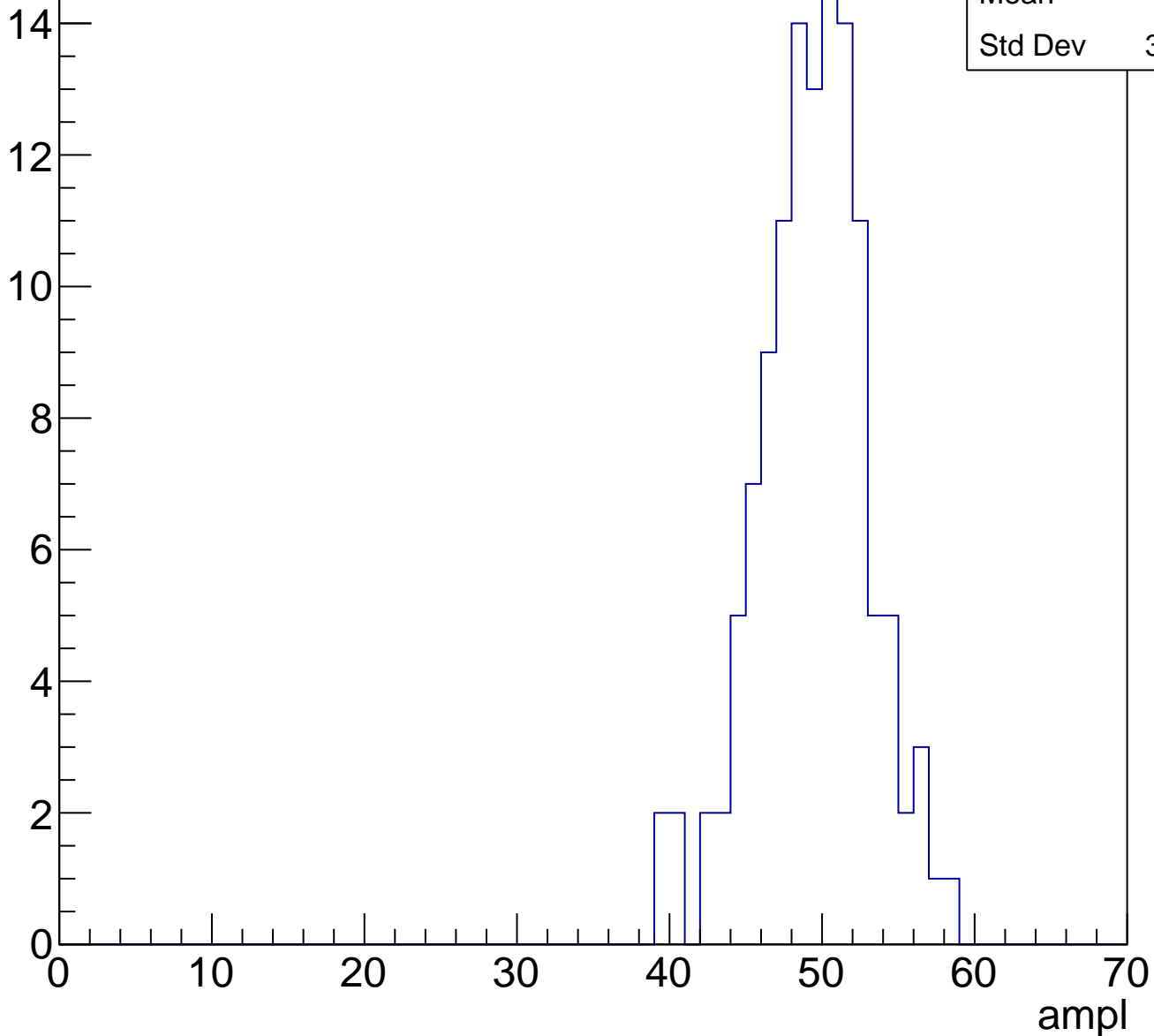
12

B0L101S, U20-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	48.93
Std Dev	3.644

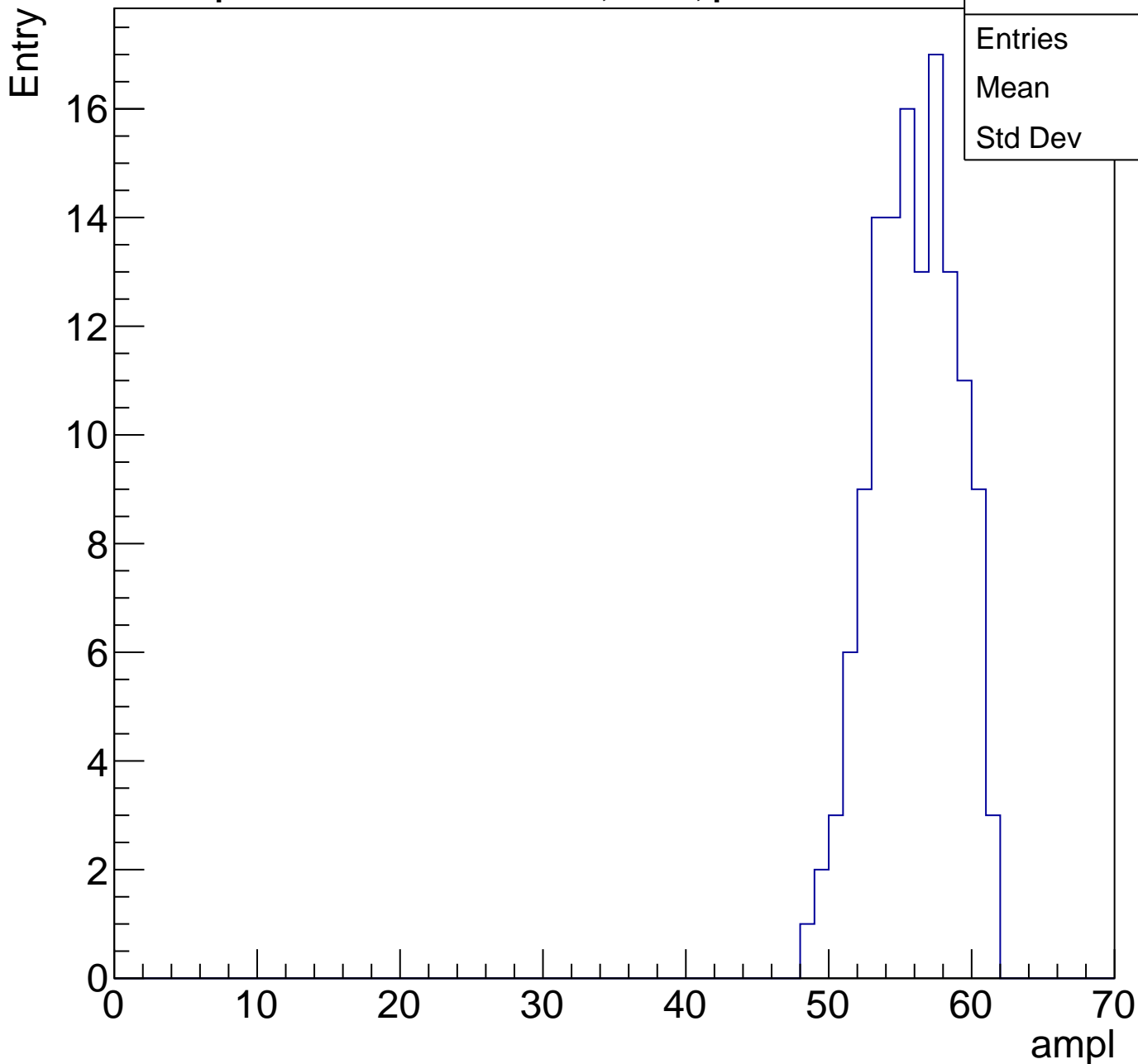
Entry



B0L101S, U20-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	55.5
Std Dev	2.92



B0L101S, U20-ch16, adc5

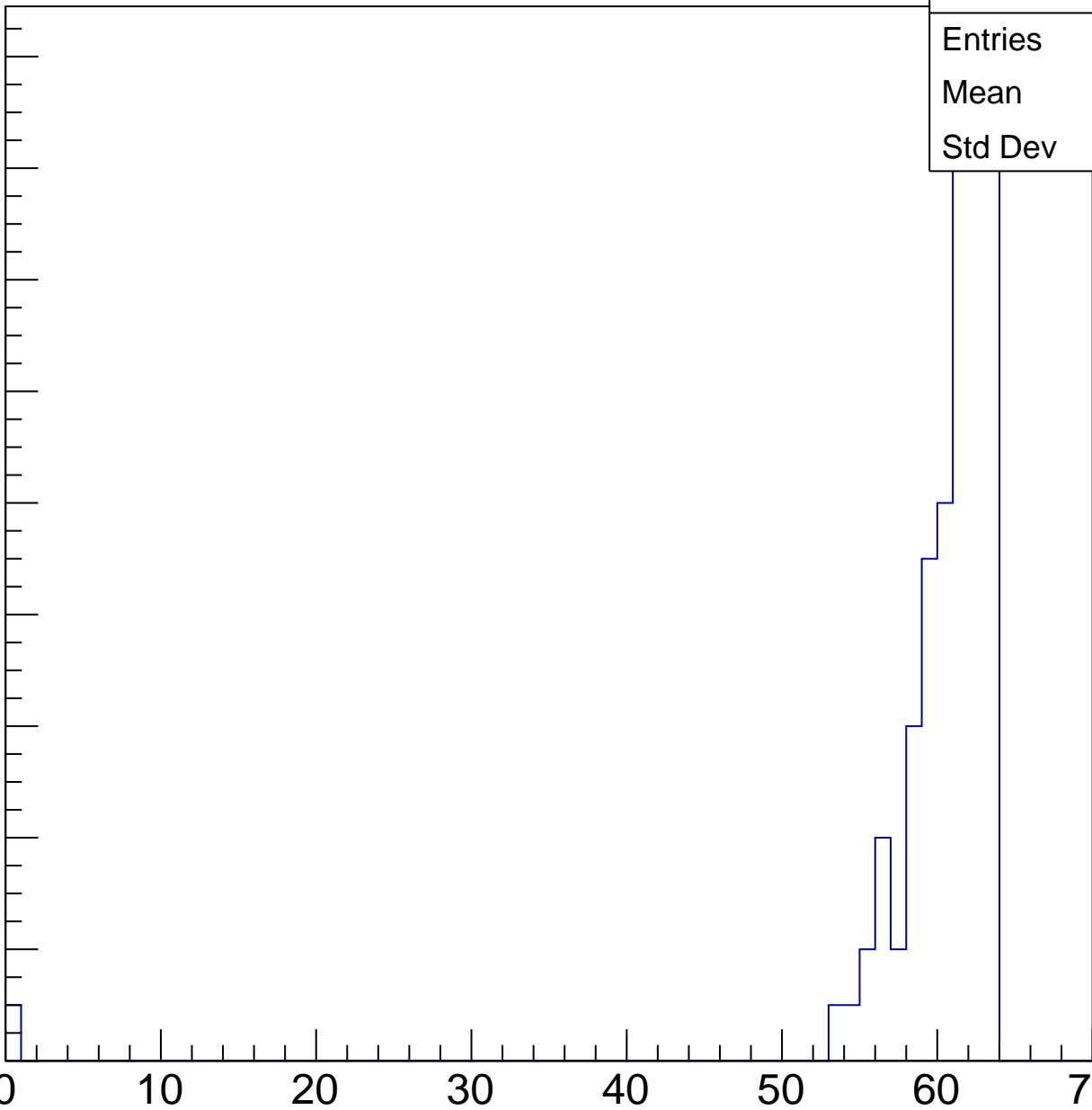
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.74
Std Dev	6.852

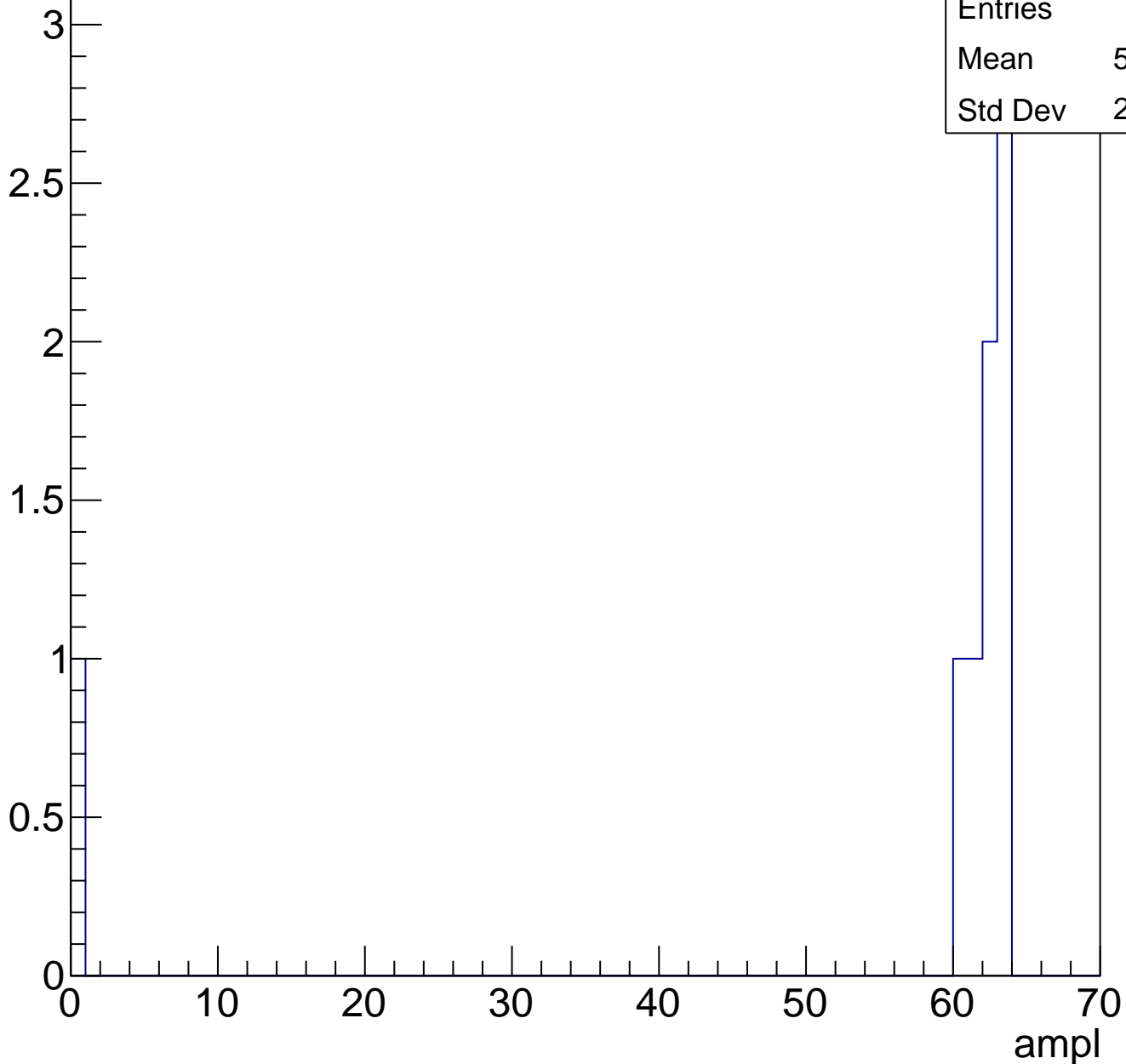
ampl



B0L101S, U20-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch17, adc0

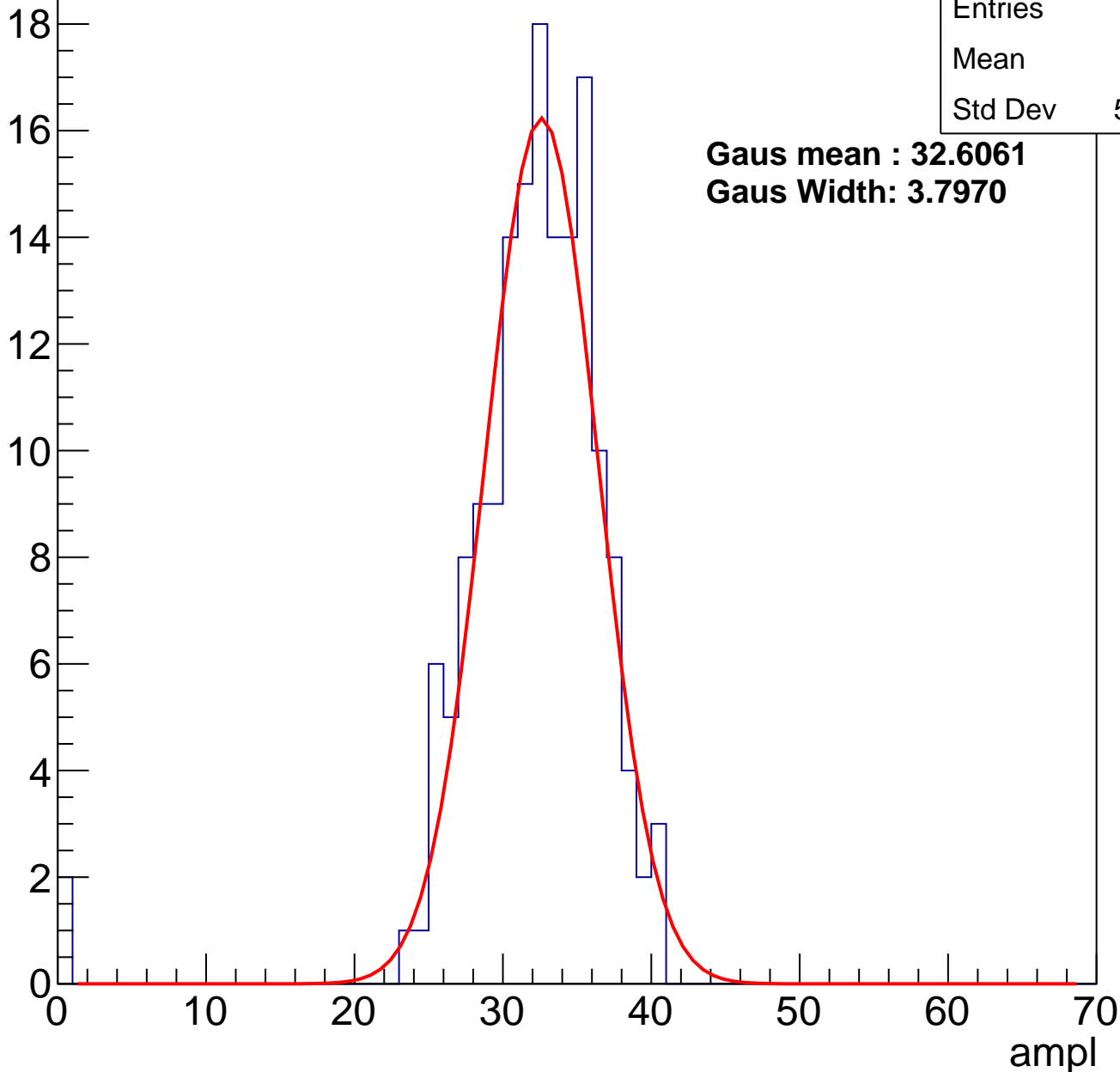
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	31.6
Std Dev	5.096

Gaus mean : 32.6061

Gaus Width: 3.7970

Entry



B0L101S, U20-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	39.28
Std Dev	3.535

Gaus mean : 40.1276

Gaus Width: 3.4305

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

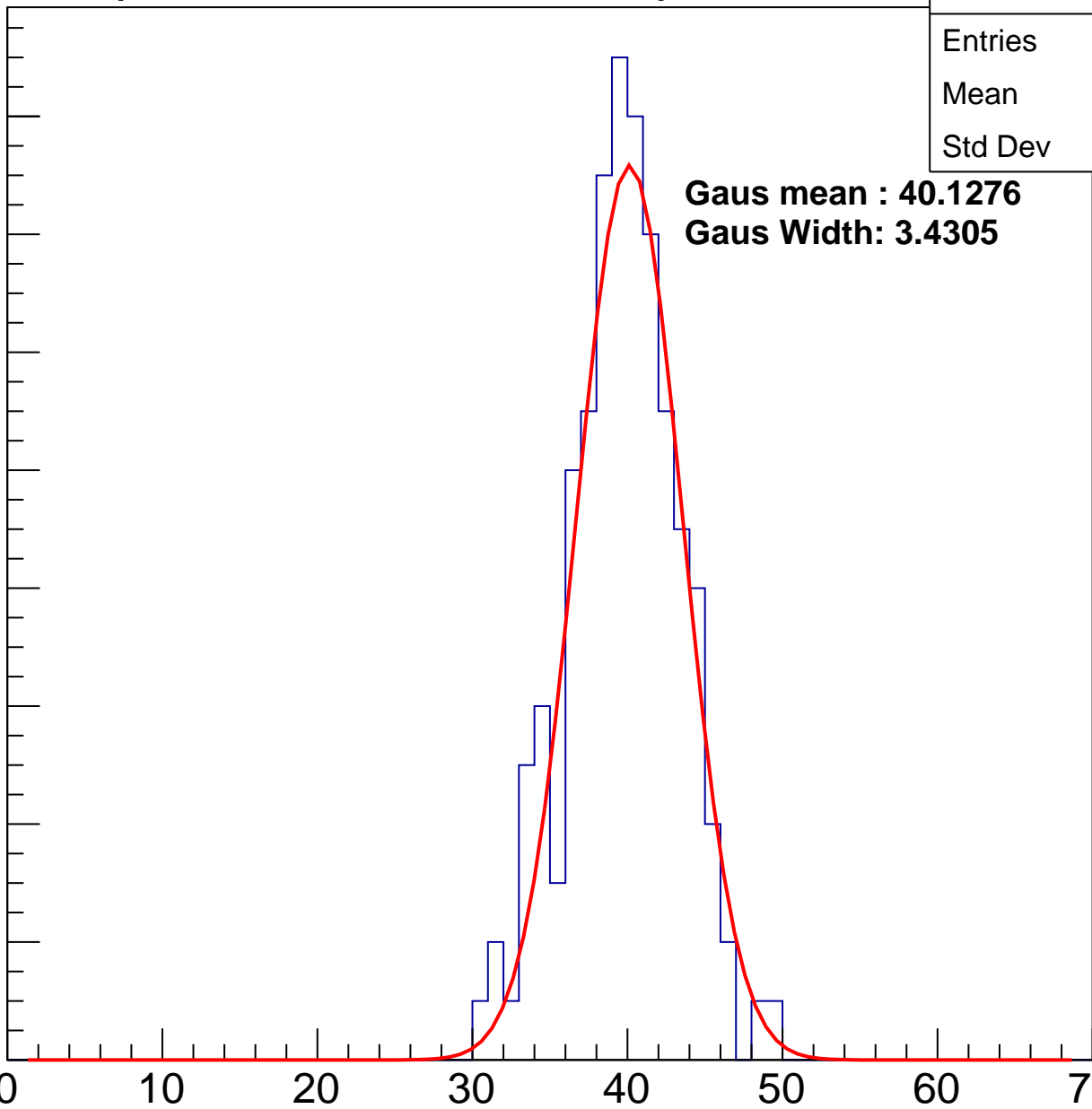
40

50

60

70

ampl



B0L101S, U20-ch17, adc2

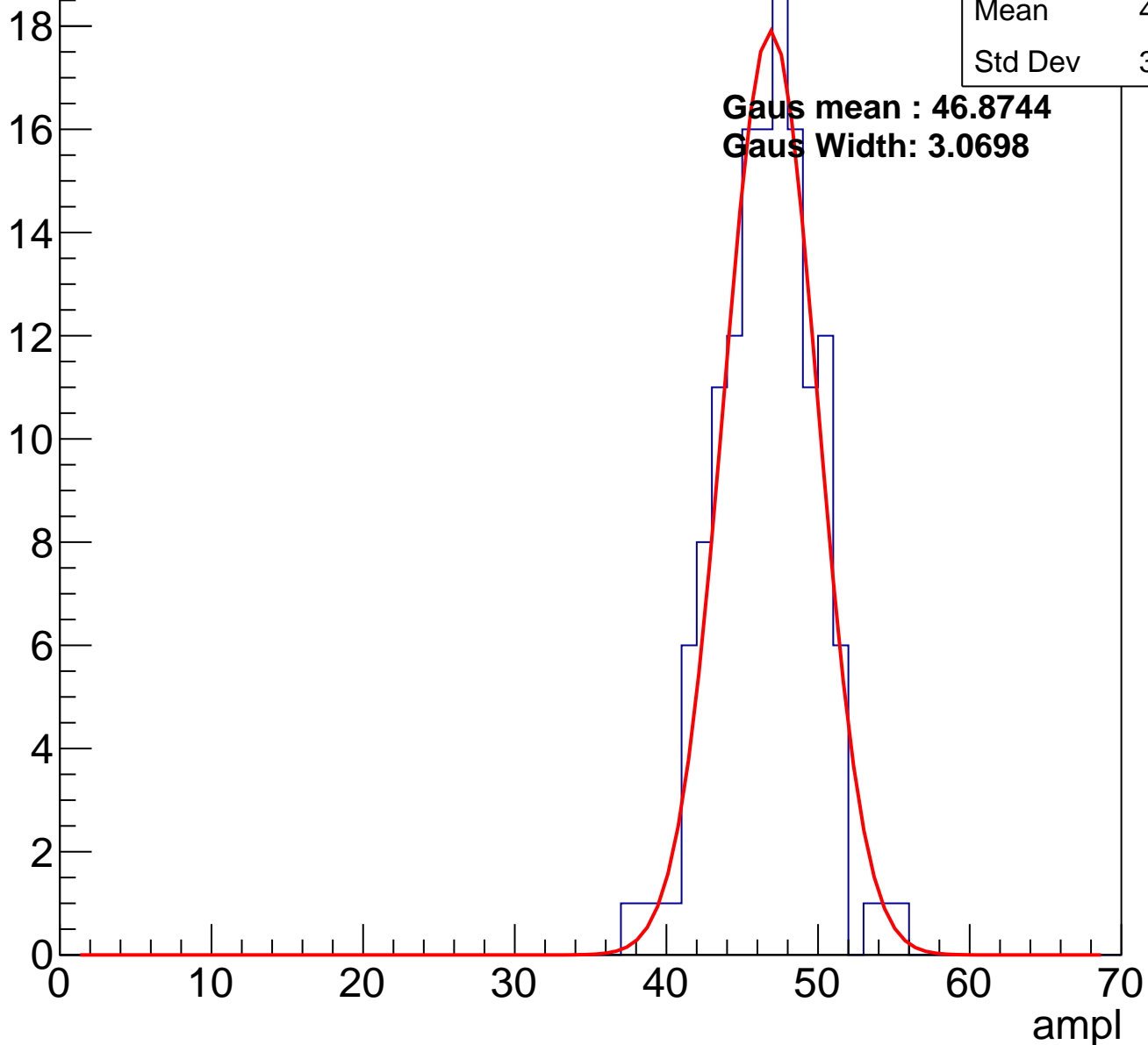
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	46.15
Std Dev	3.146

Gaus mean : 46.8744

Gaus Width: 3.0698

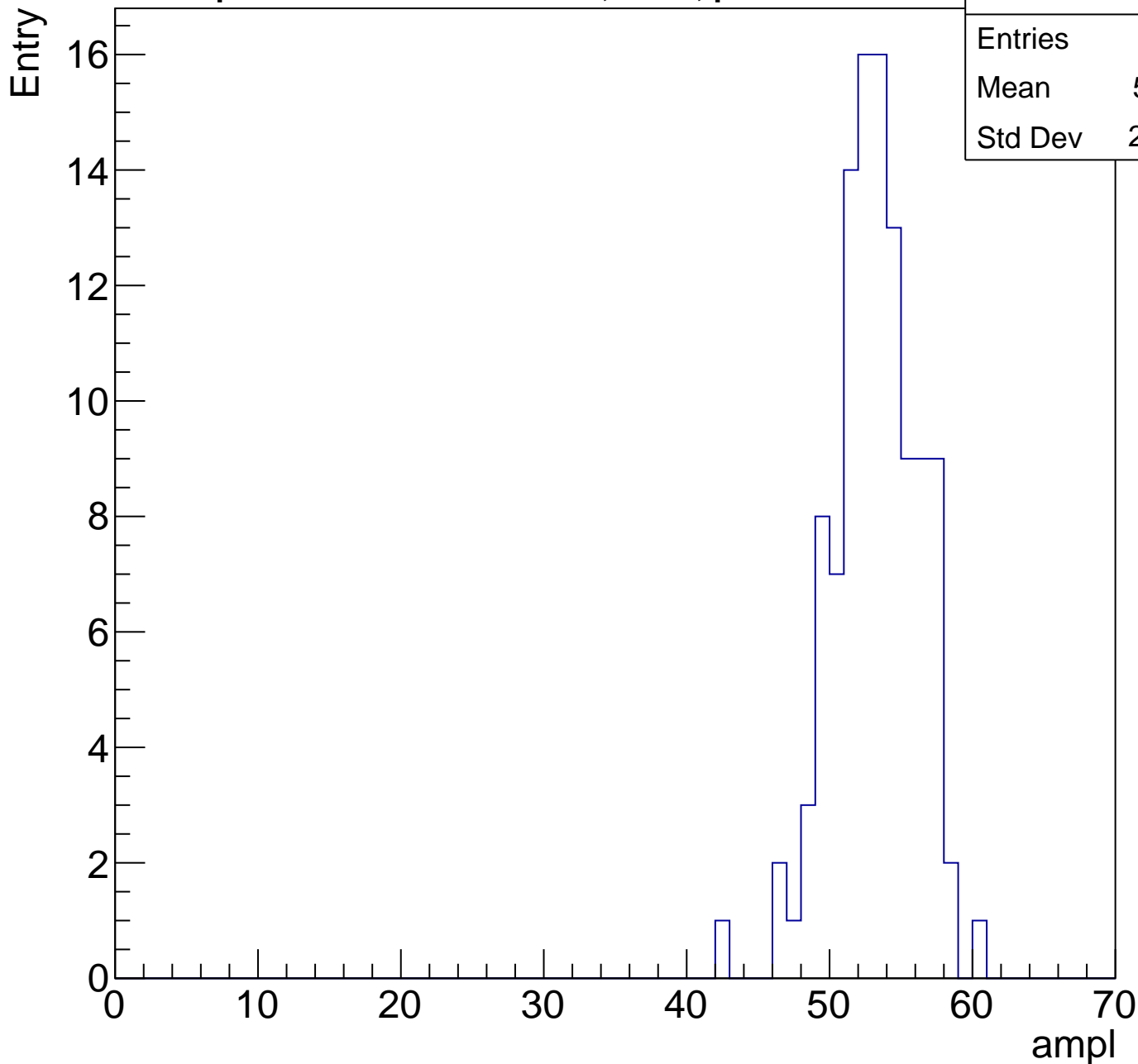
Entry



B0L101S, U20-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	52.71
Std Dev	2.936



B0L101S, U20-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

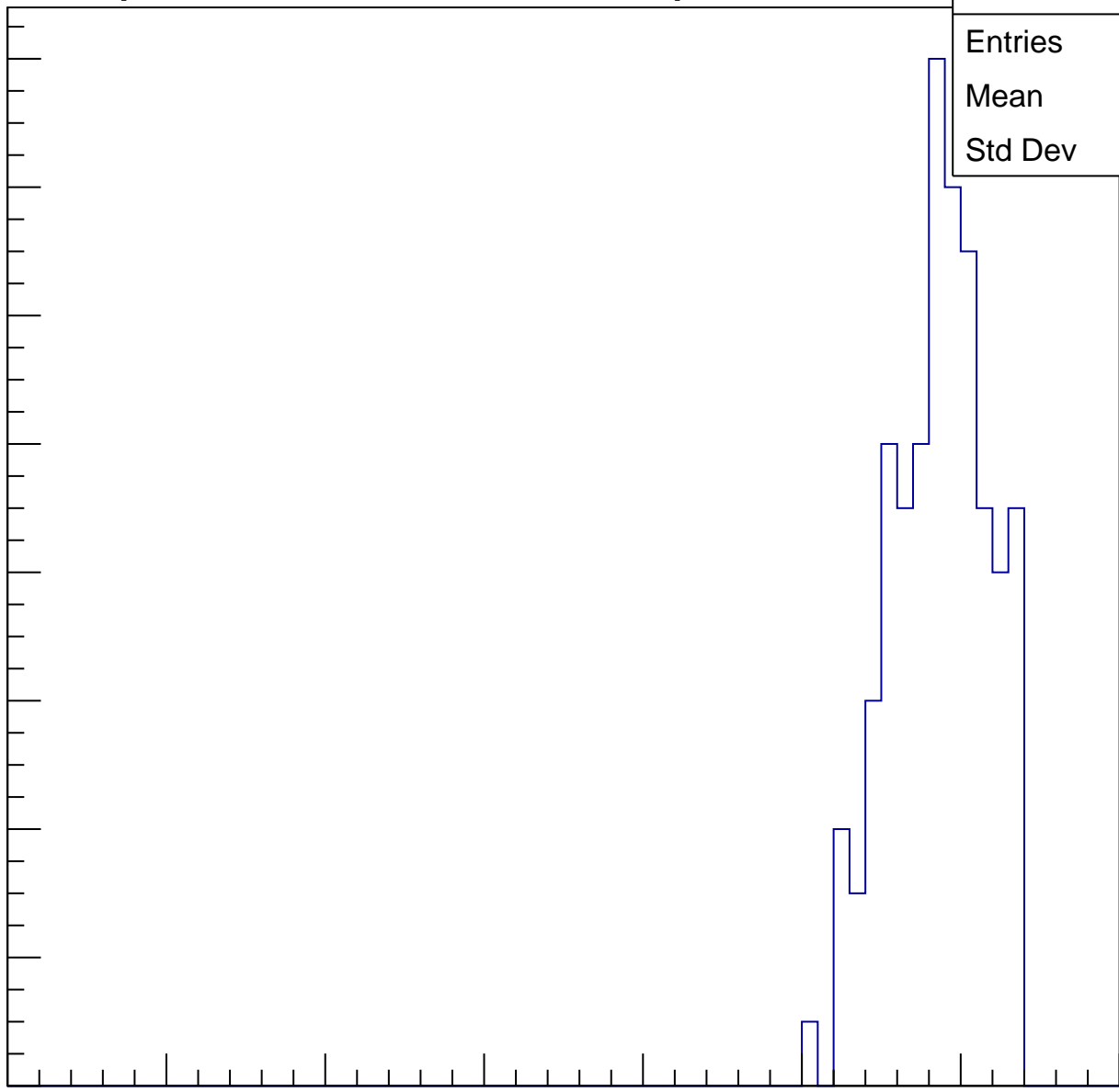
Entries	112
Mean	58.13
Std Dev	3.007

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch17, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	52
Mean	58.56
Std Dev	11.85

0

B0L101S, U20-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.91
Std Dev	3.771

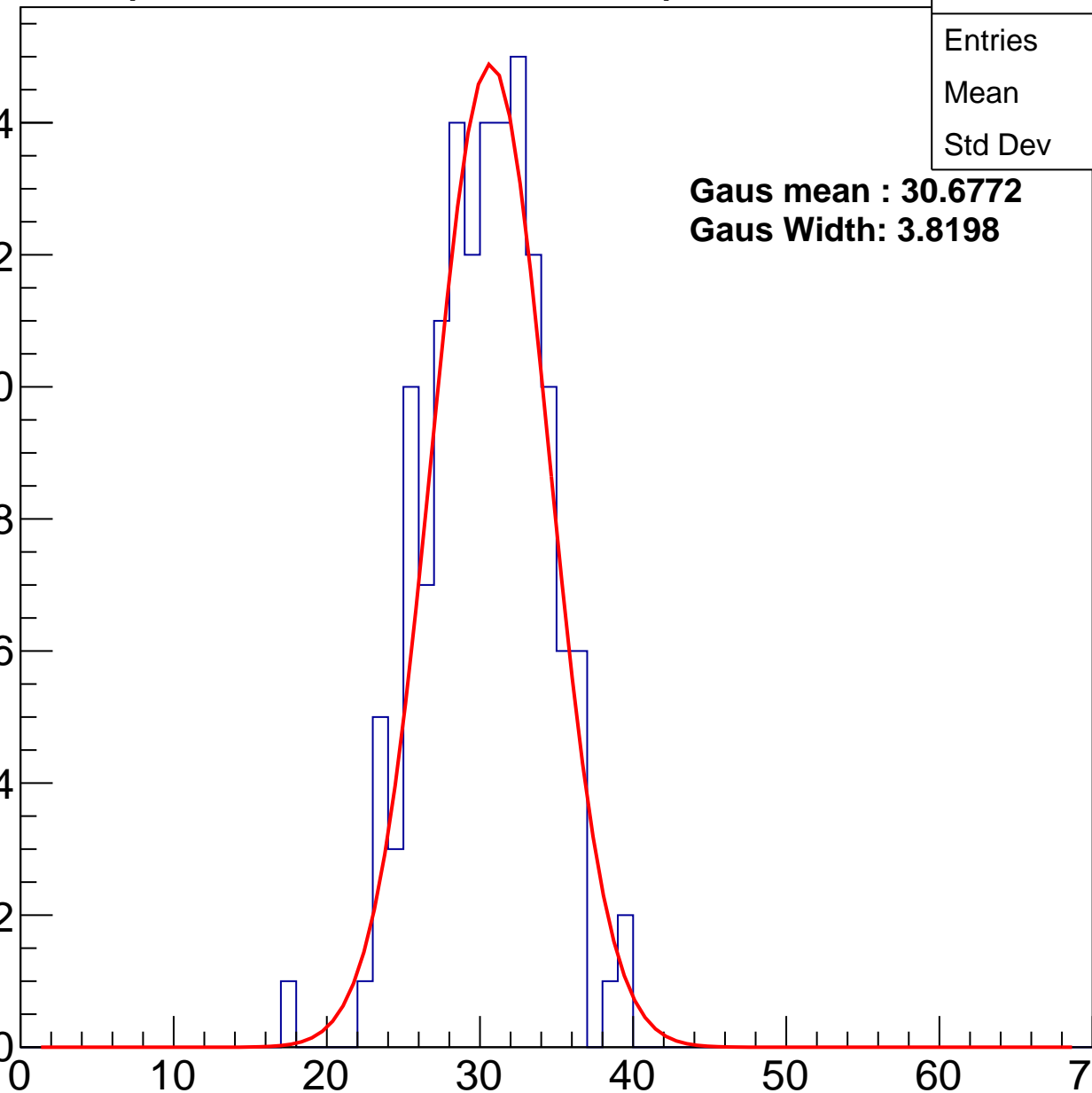
Gaus mean : 30.6772

Gaus Width: 3.8198

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch18, adc1

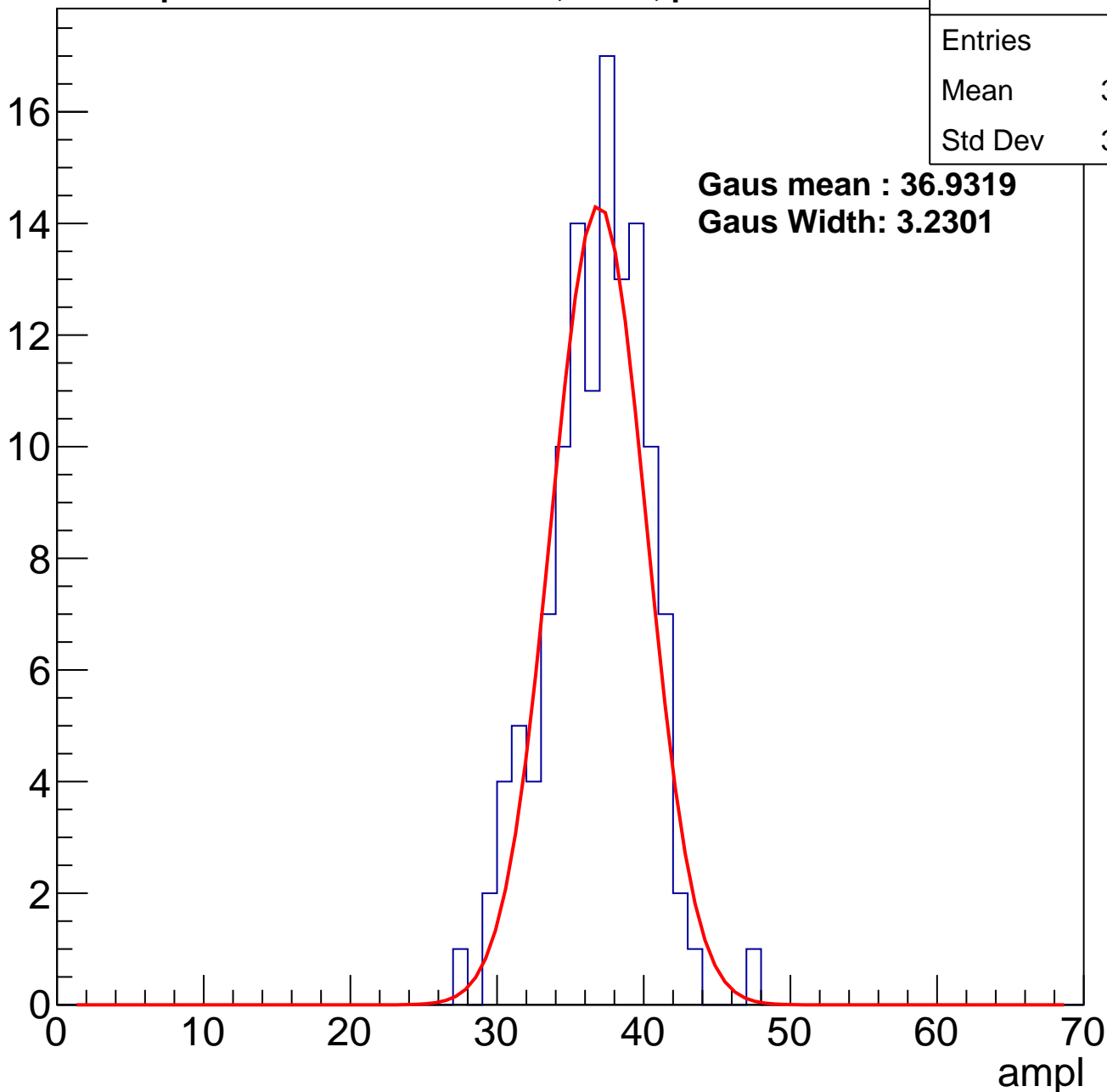
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.38
Std Dev	3.353

Gaus mean : 36.9319

Gaus Width: 3.2301

Entry



B0L101S, U20-ch18, adc2

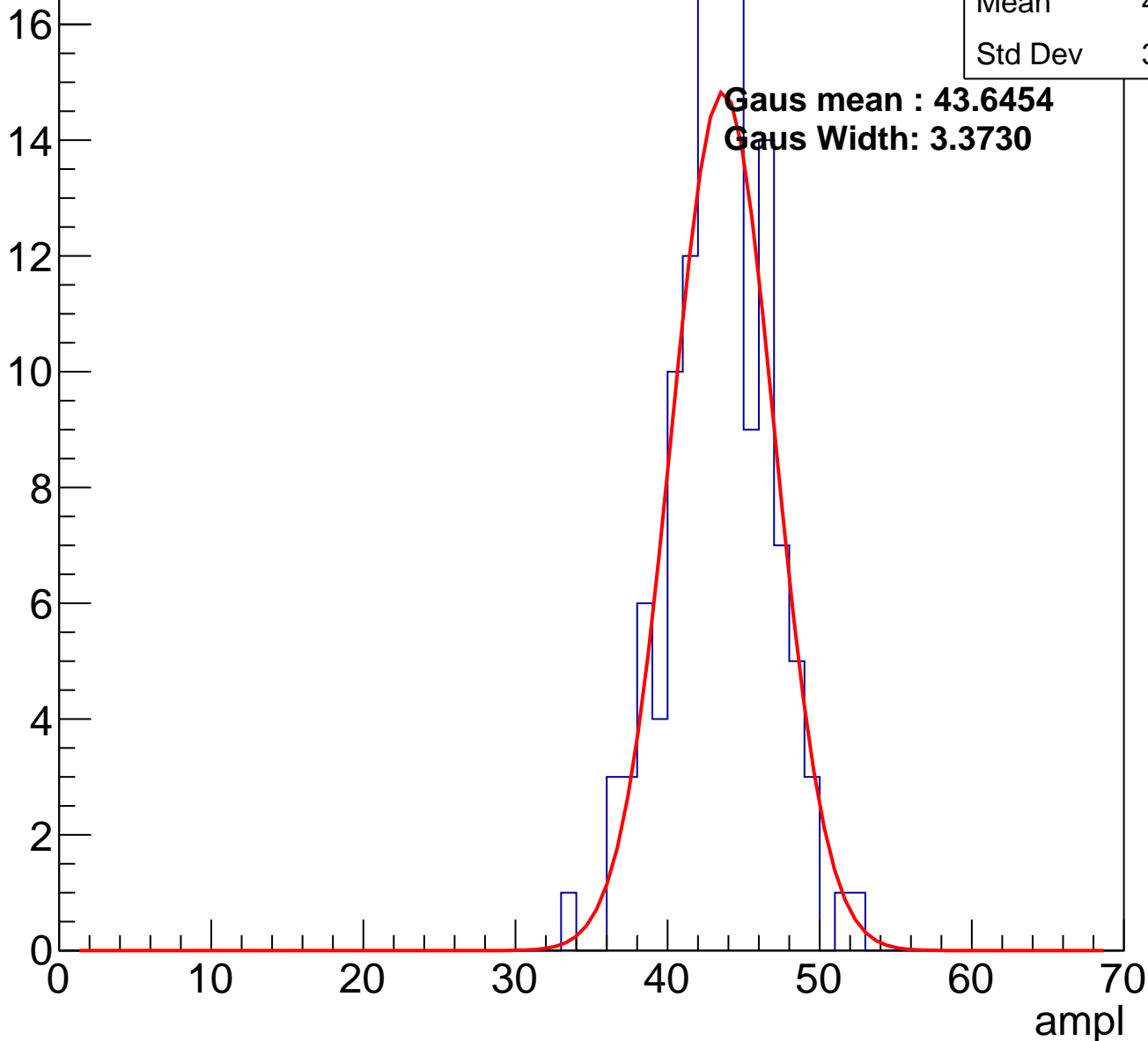
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	130
Mean	42.99
Std Dev	3.273

Gaus mean : 43.6454

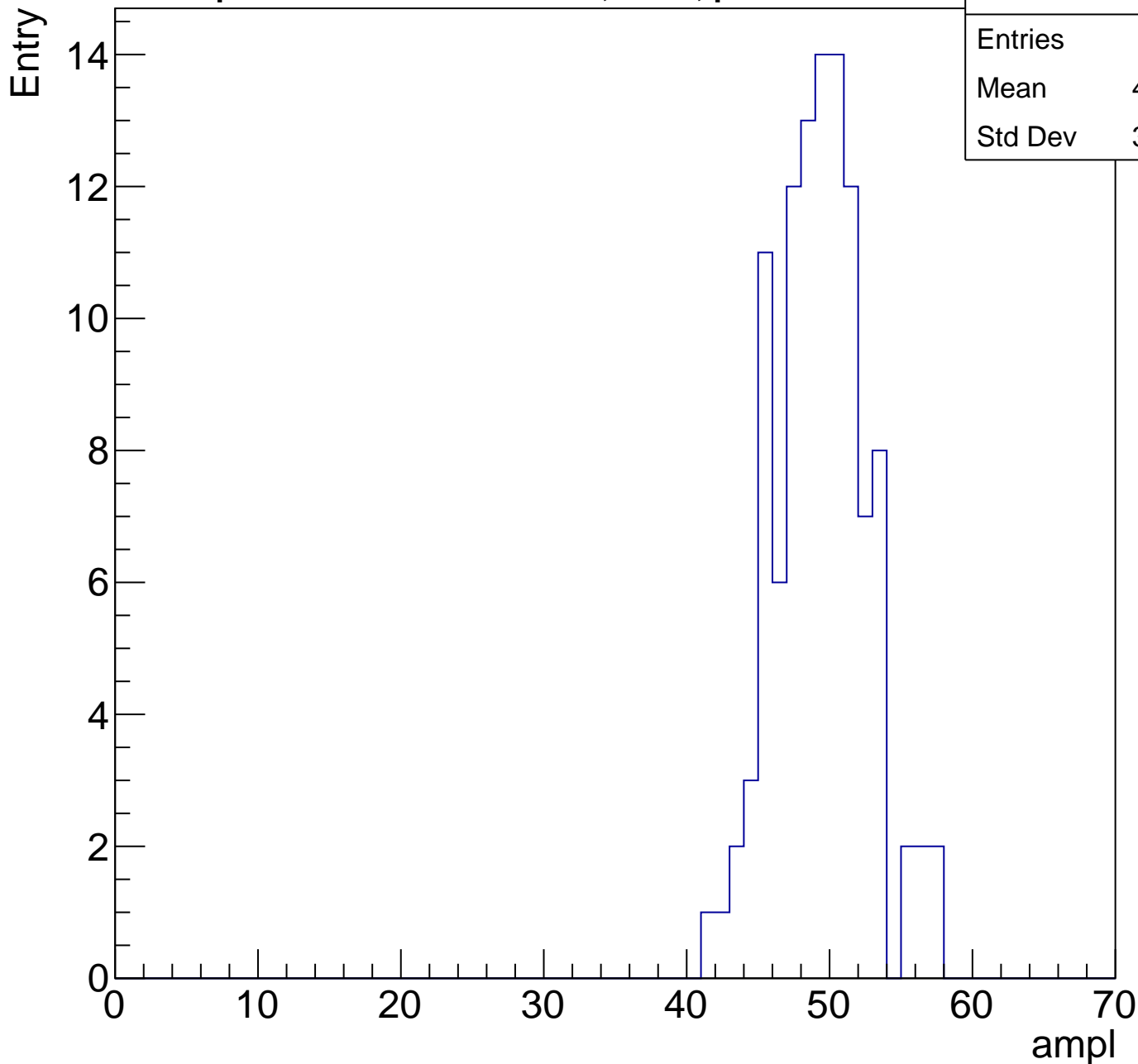
Gaus Width: 3.3730



B0L101S, U20-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

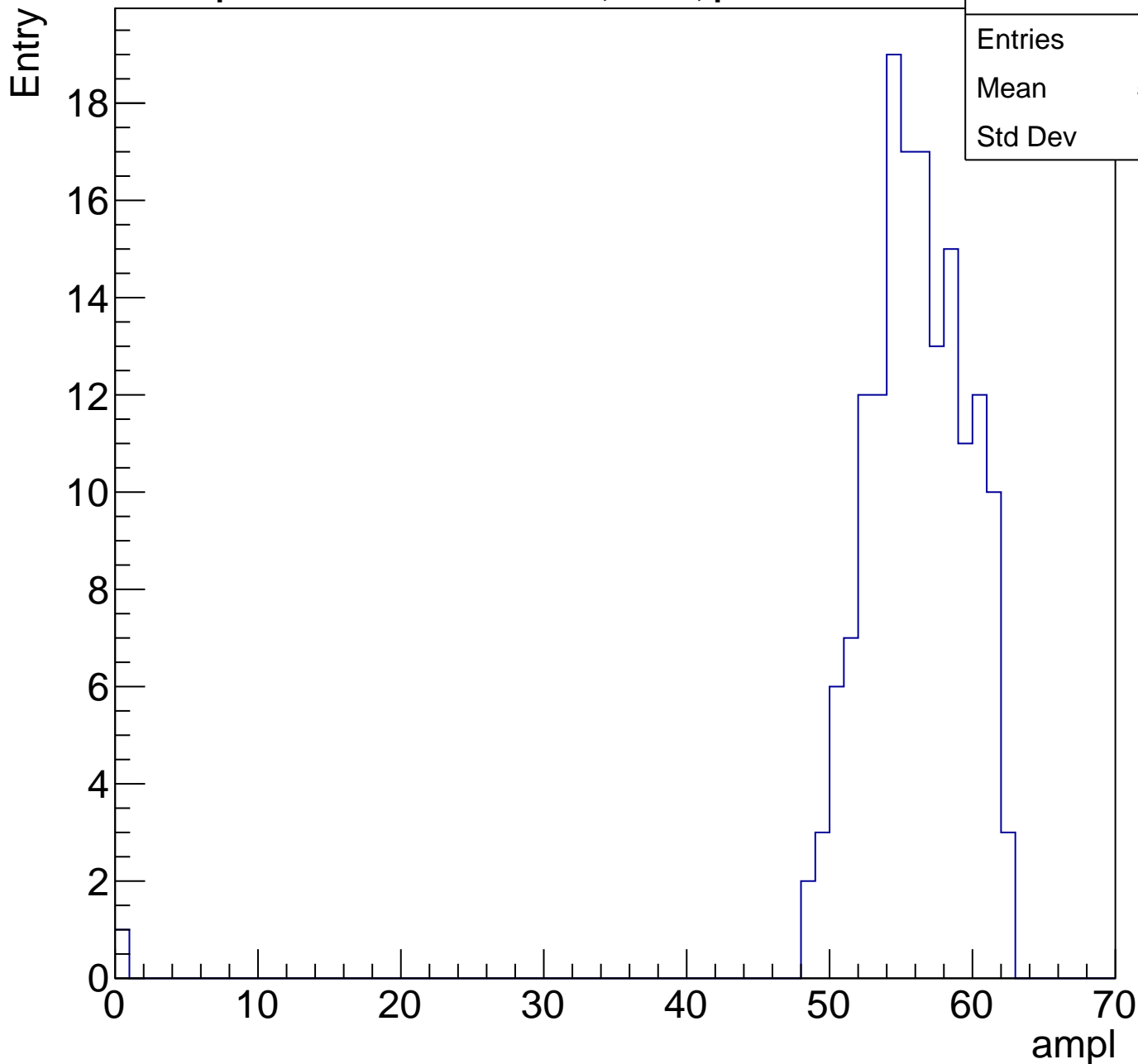
Entries	110
Mean	48.93
Std Dev	3.164



B0L101S, U20-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	55.31
Std Dev	5.5



B0L101S, U20-ch18, adc5

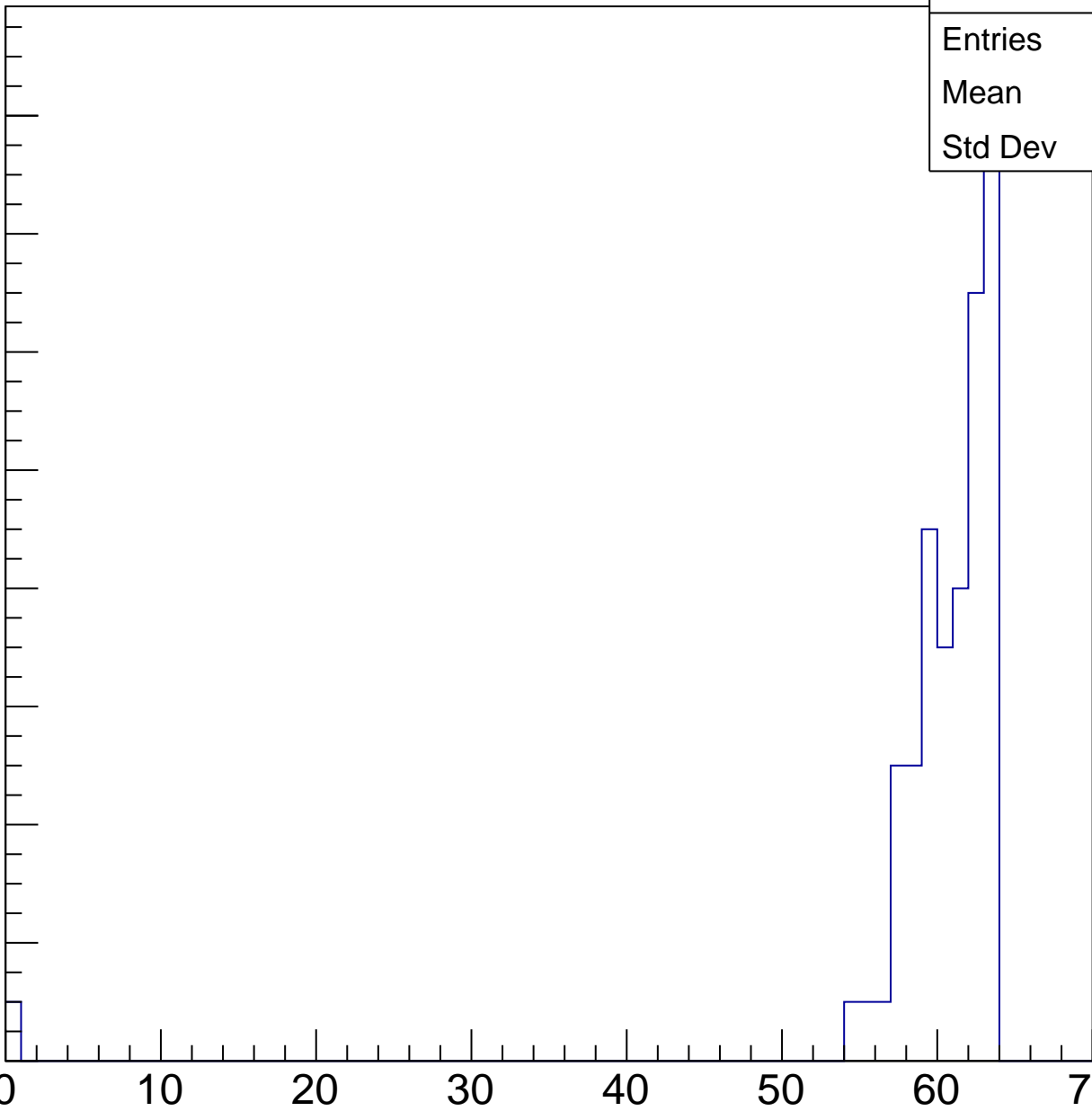
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	59.65
Std Dev	7.627

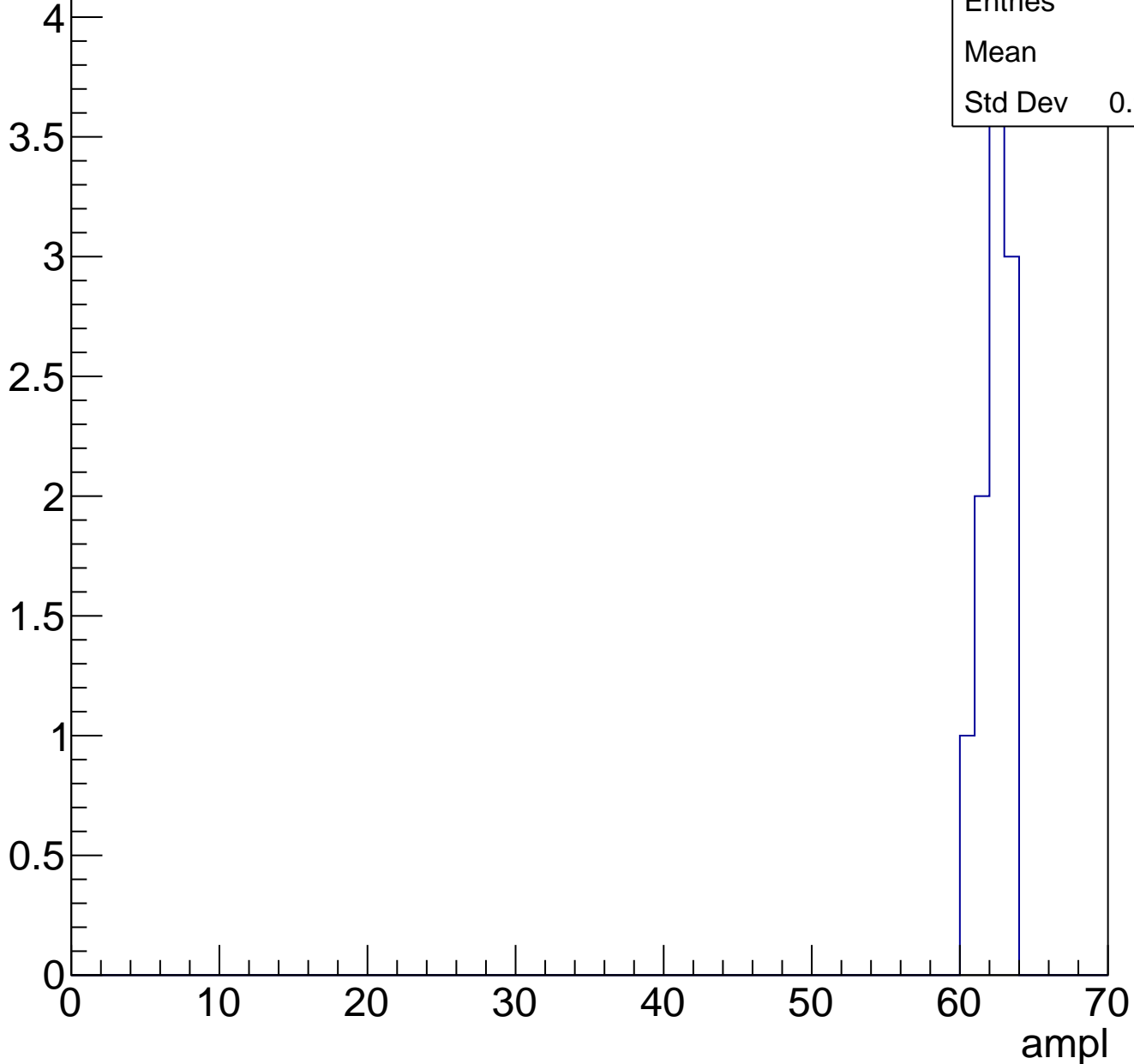
ampl



B0L101S, U20-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch19, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	30.44
Std Dev	3.852

Gaus mean : 30.7491

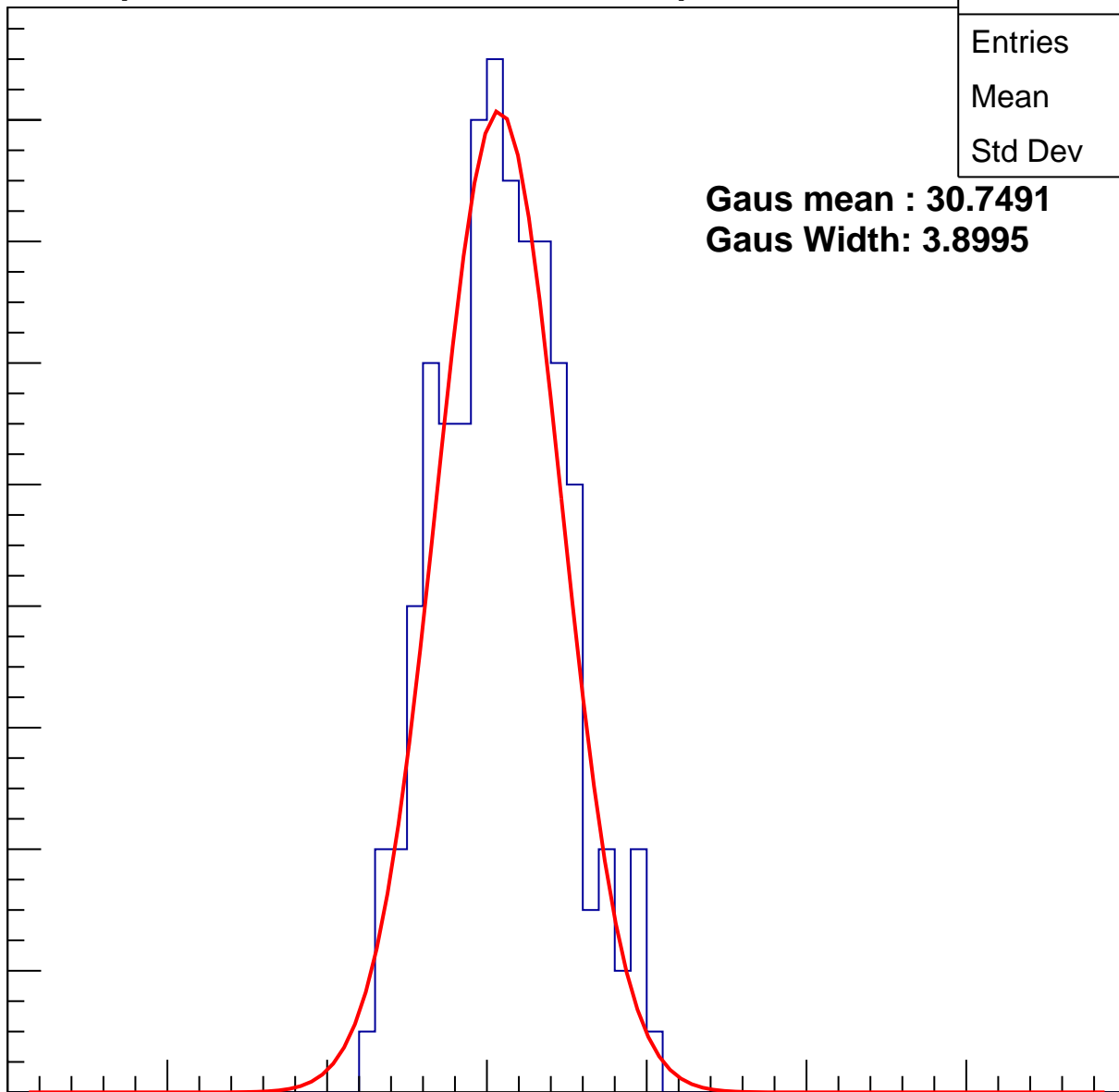
Gaus Width: 3.8995

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch19, adc1

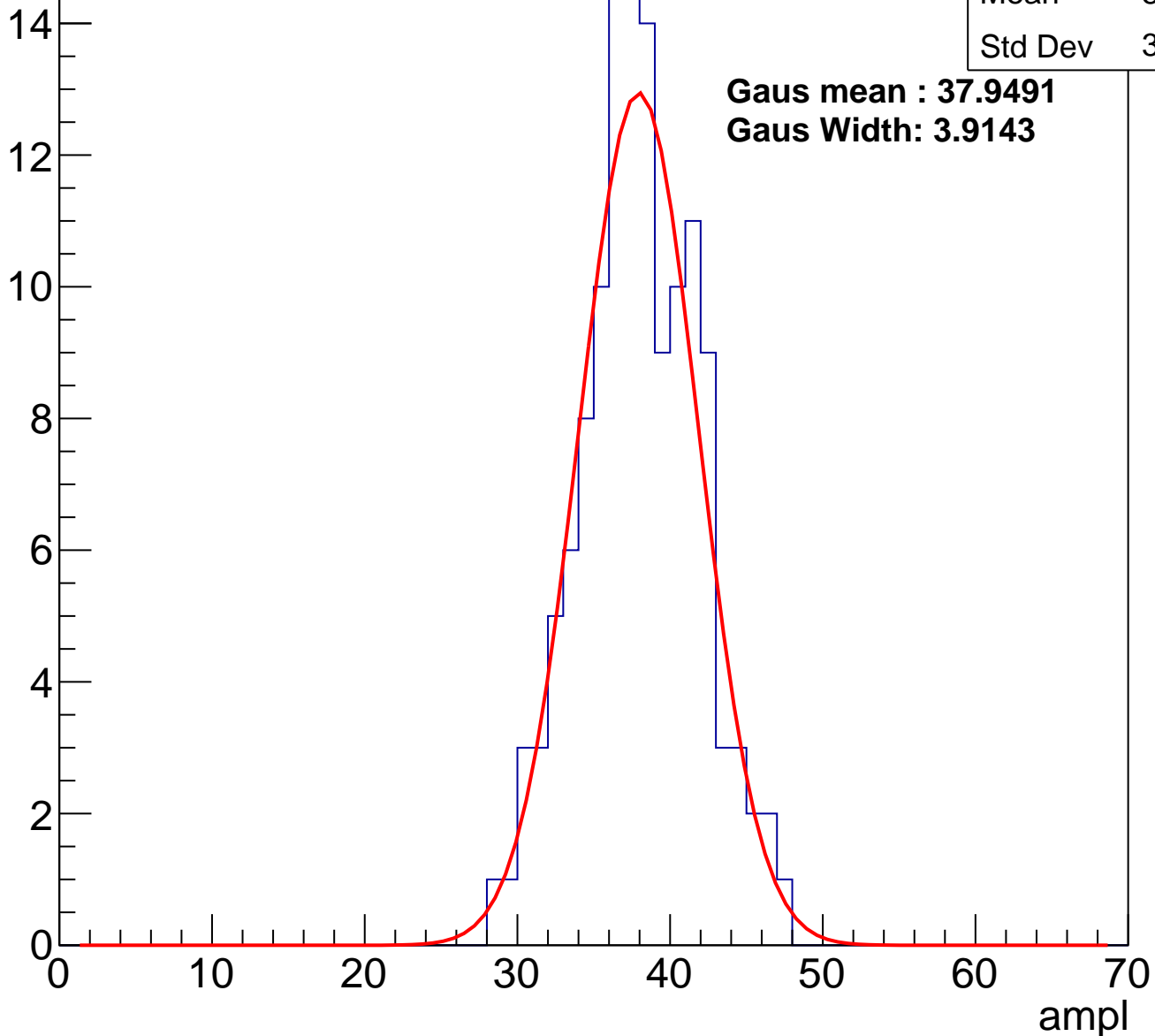
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	37.53
Std Dev	3.805

Gaus mean : 37.9491

Gaus Width: 3.9143

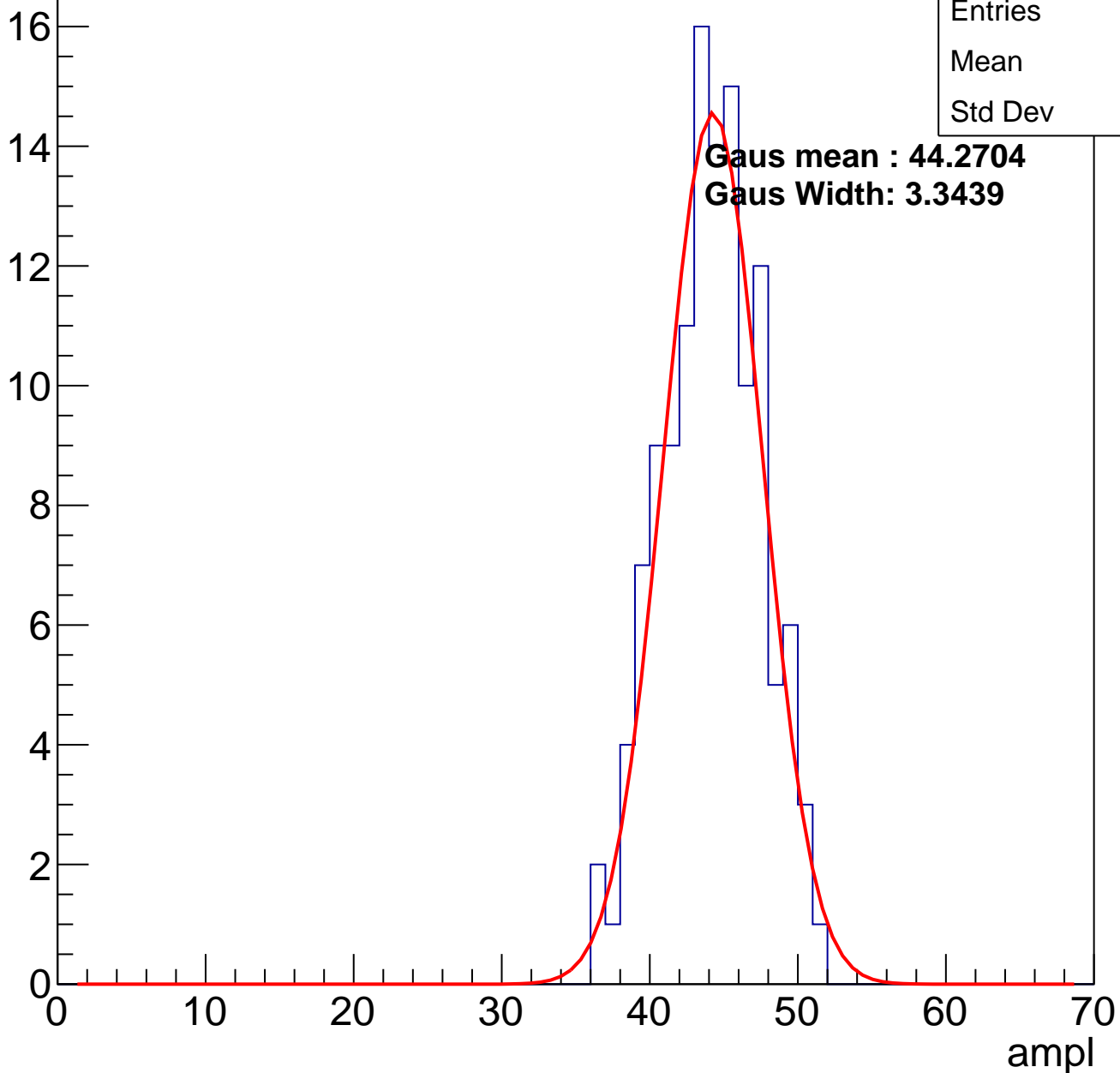
Entry



B0L101S, U20-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

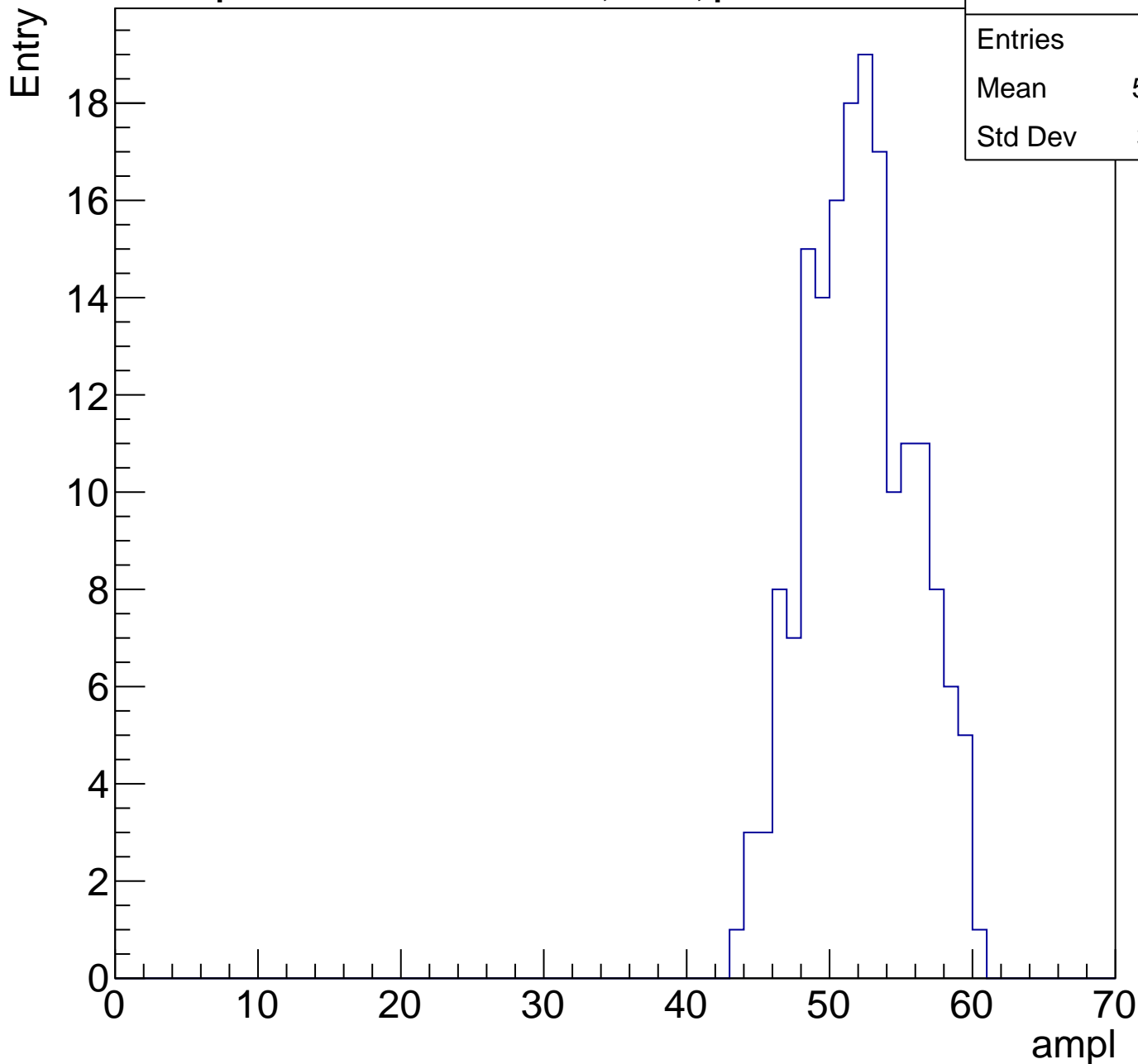
Entry



B0L101S, U20-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	51.68
Std Dev	3.701



B0L101S, U20-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	87
Mean	57.7
Std Dev	3.025

0

2

4

6

8

10

B0L101S, U20-ch19, adc5

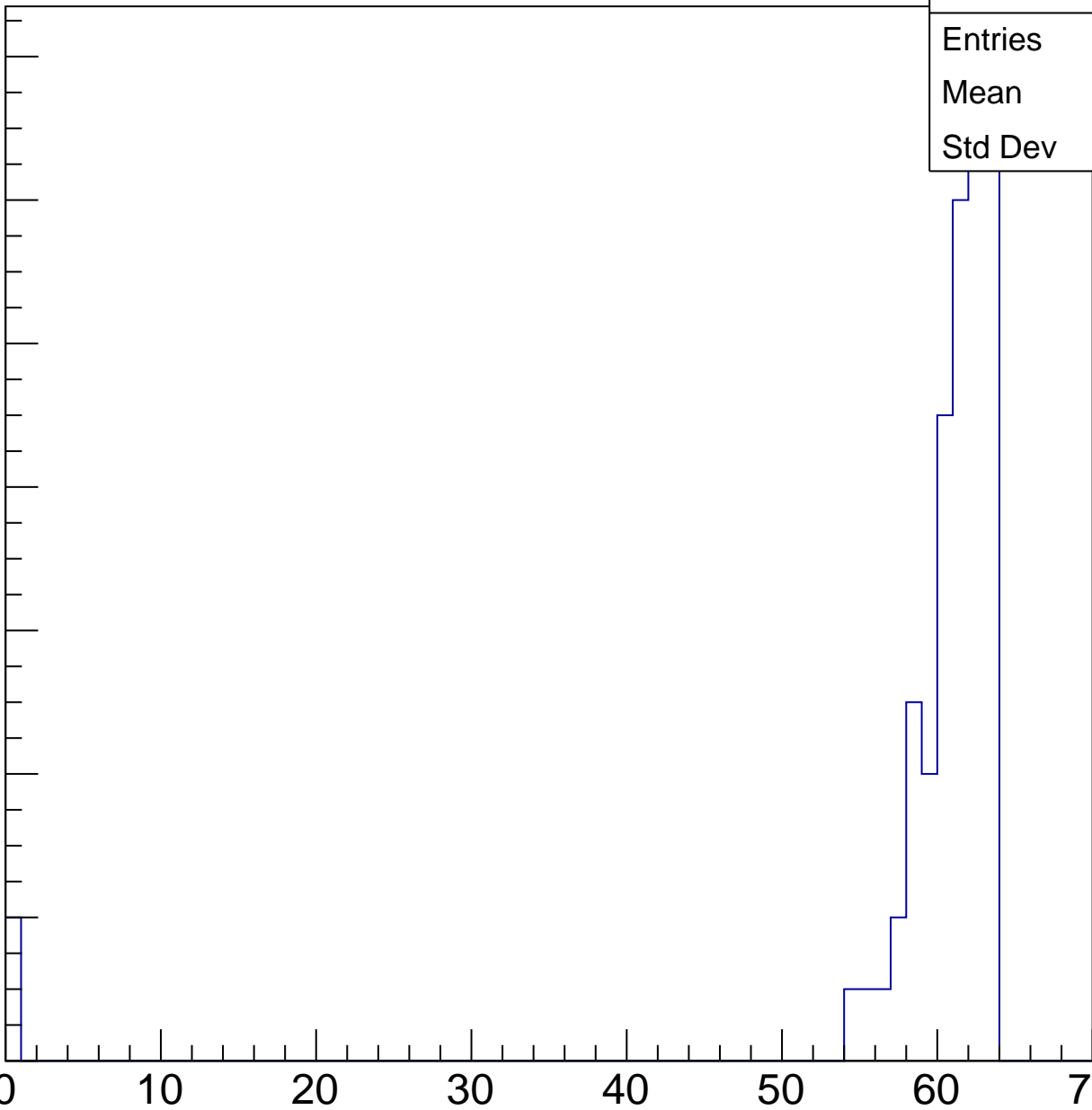
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.88
Std Dev	10.69

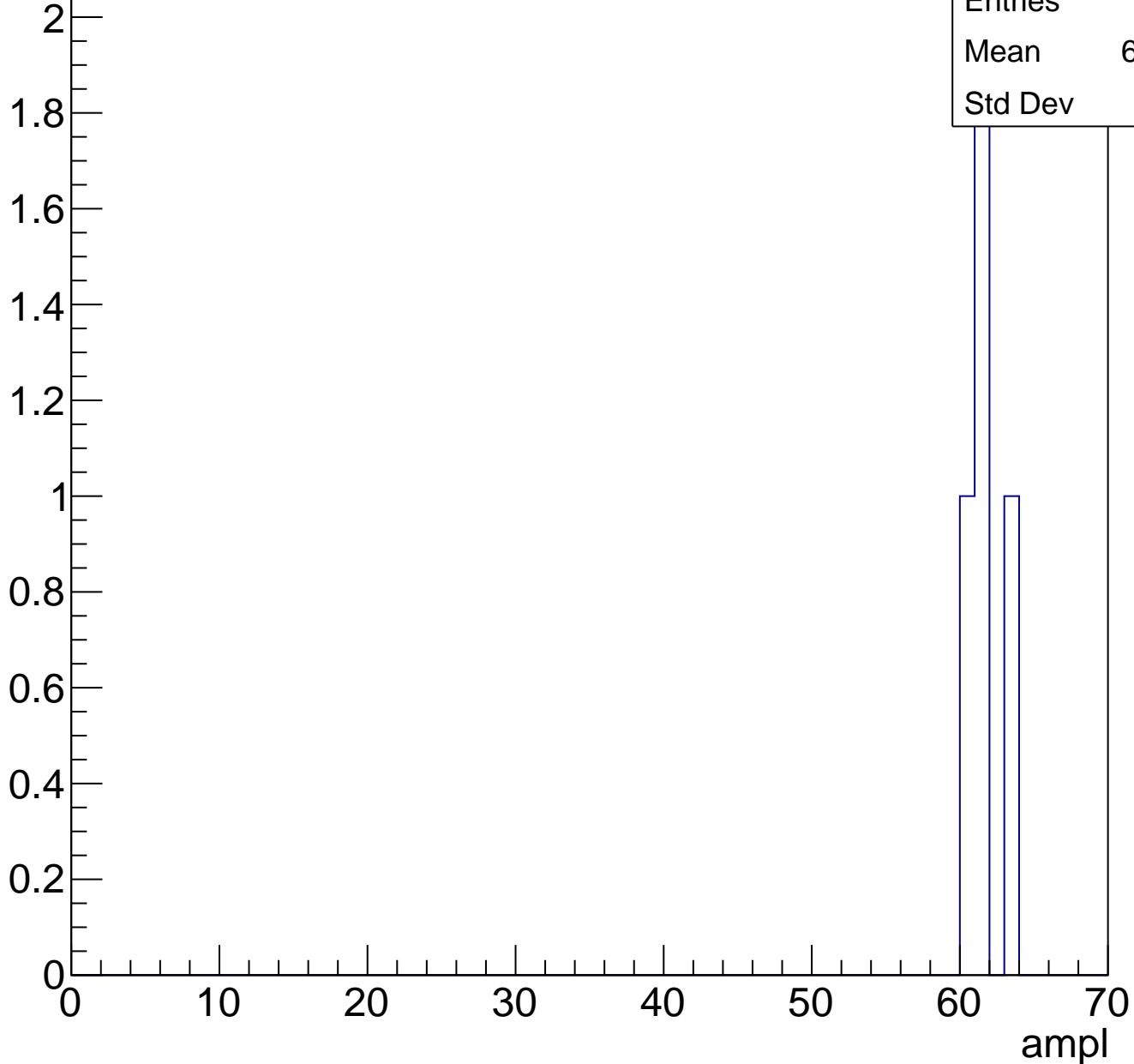
ampl



B0L101S, U20-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

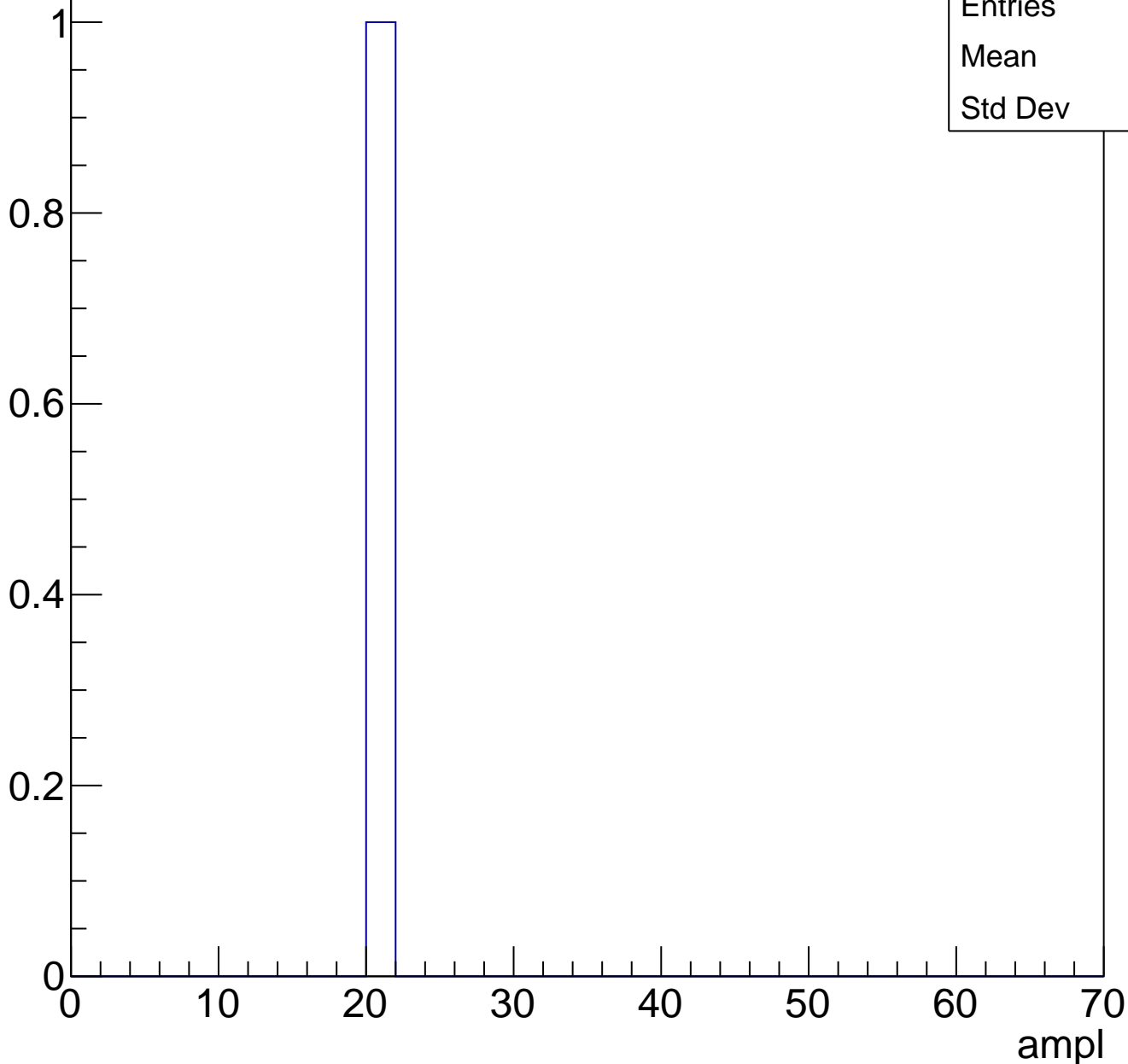
Entry



B0L101S, U20-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



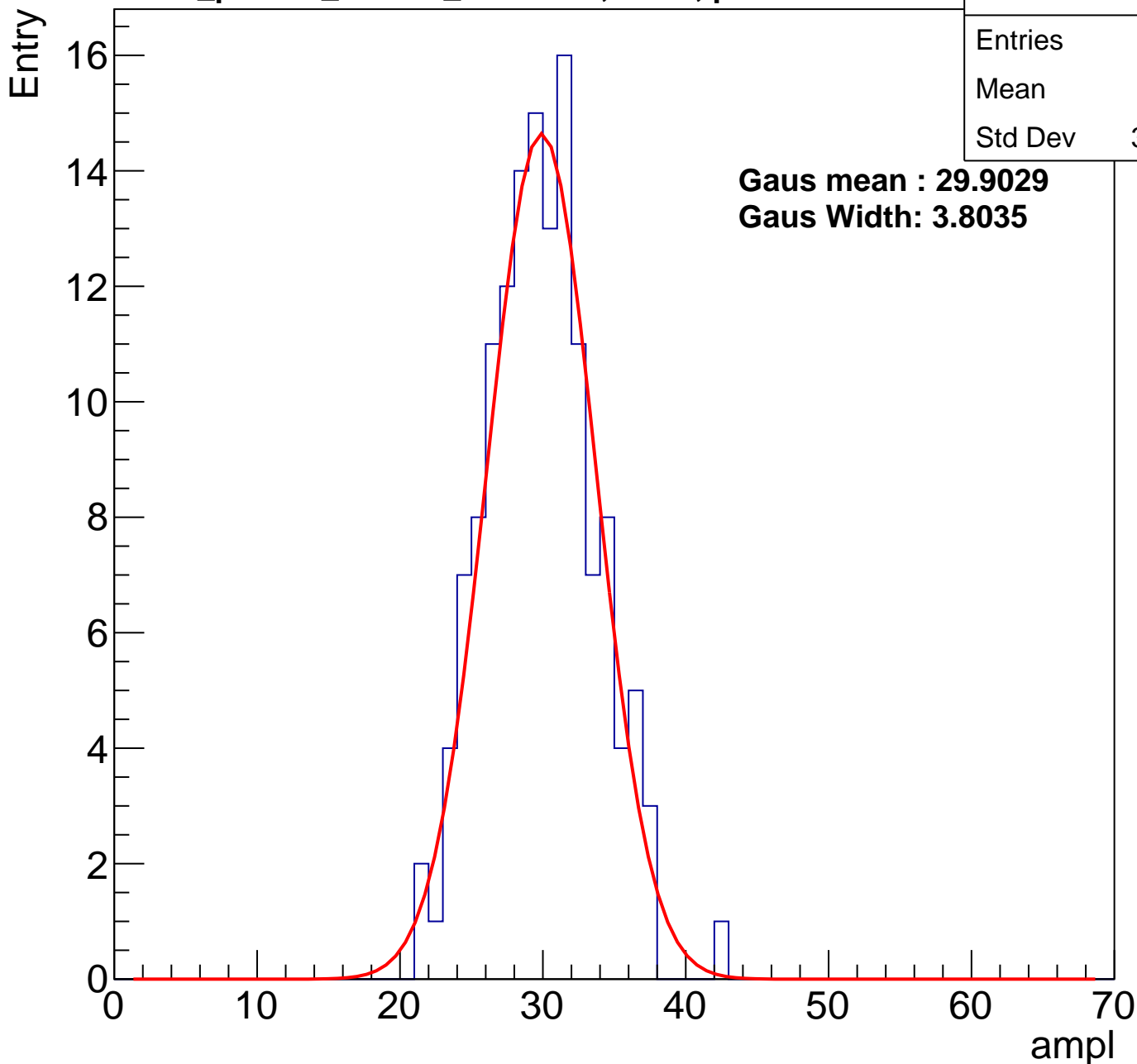
B0L101S, U20-ch20, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	29.4
Std Dev	3.753

Gaus mean : 29.9029

Gaus Width: 3.8035



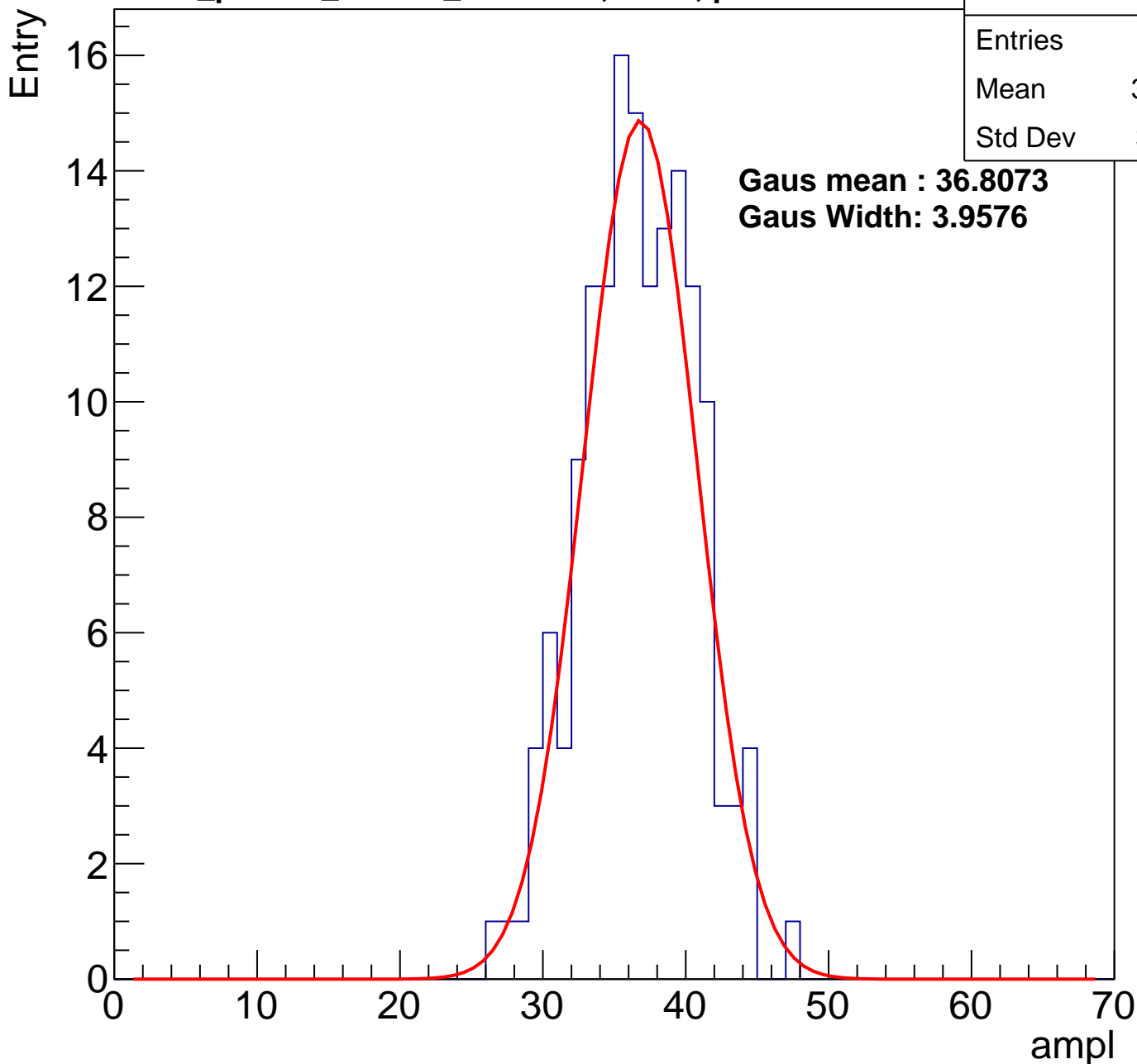
B0L101S, U20-ch20, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	36.24
Std Dev	3.901

Gaus mean : 36.8073

Gaus Width: 3.9576



B0L101S, U20-ch20, adc2

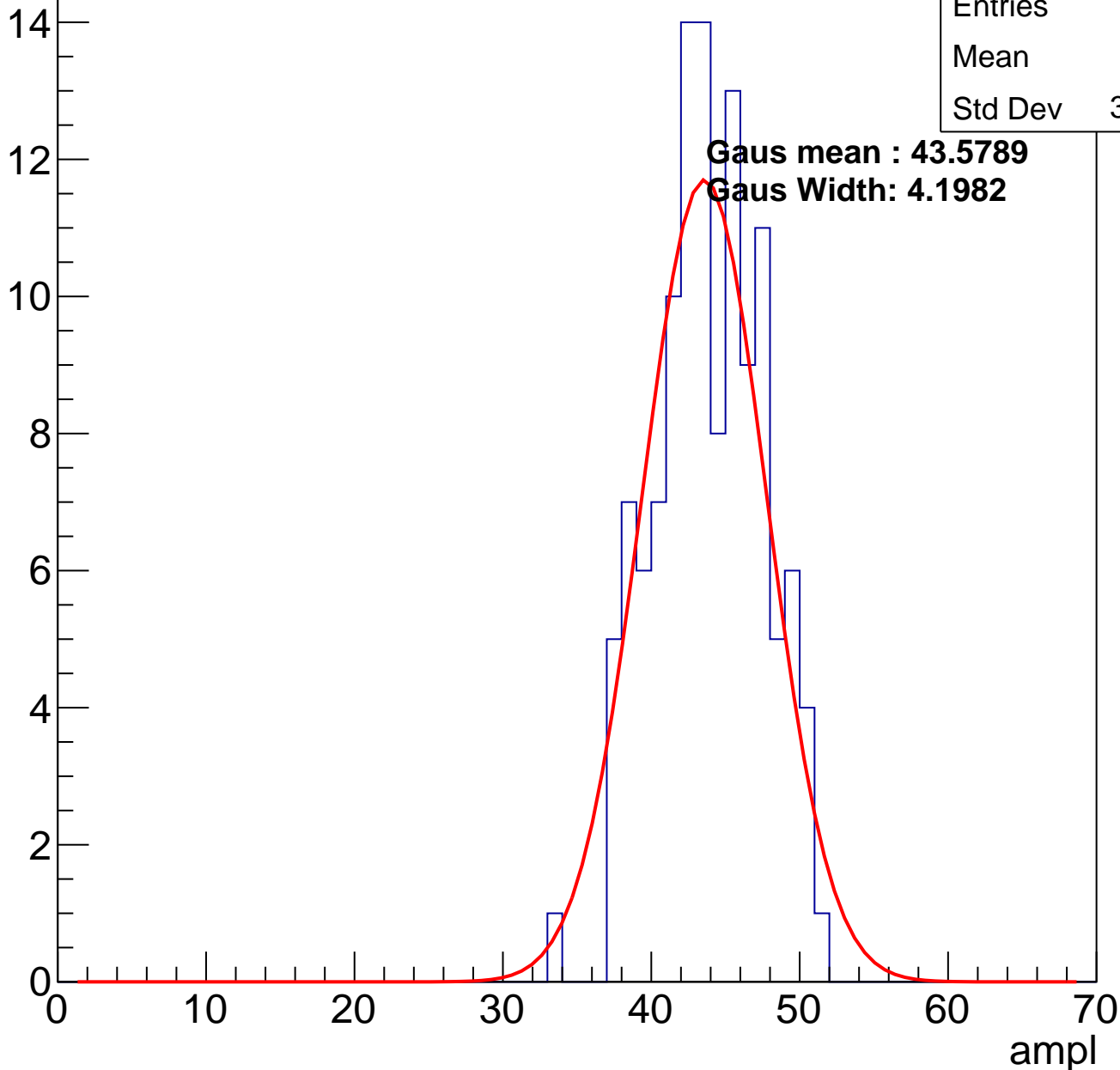
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	121
Mean	43.4
Std Dev	3.599

Gaus mean : 43.5789

Gaus Width: 4.1982



B0L101S, U20-ch20, adc3

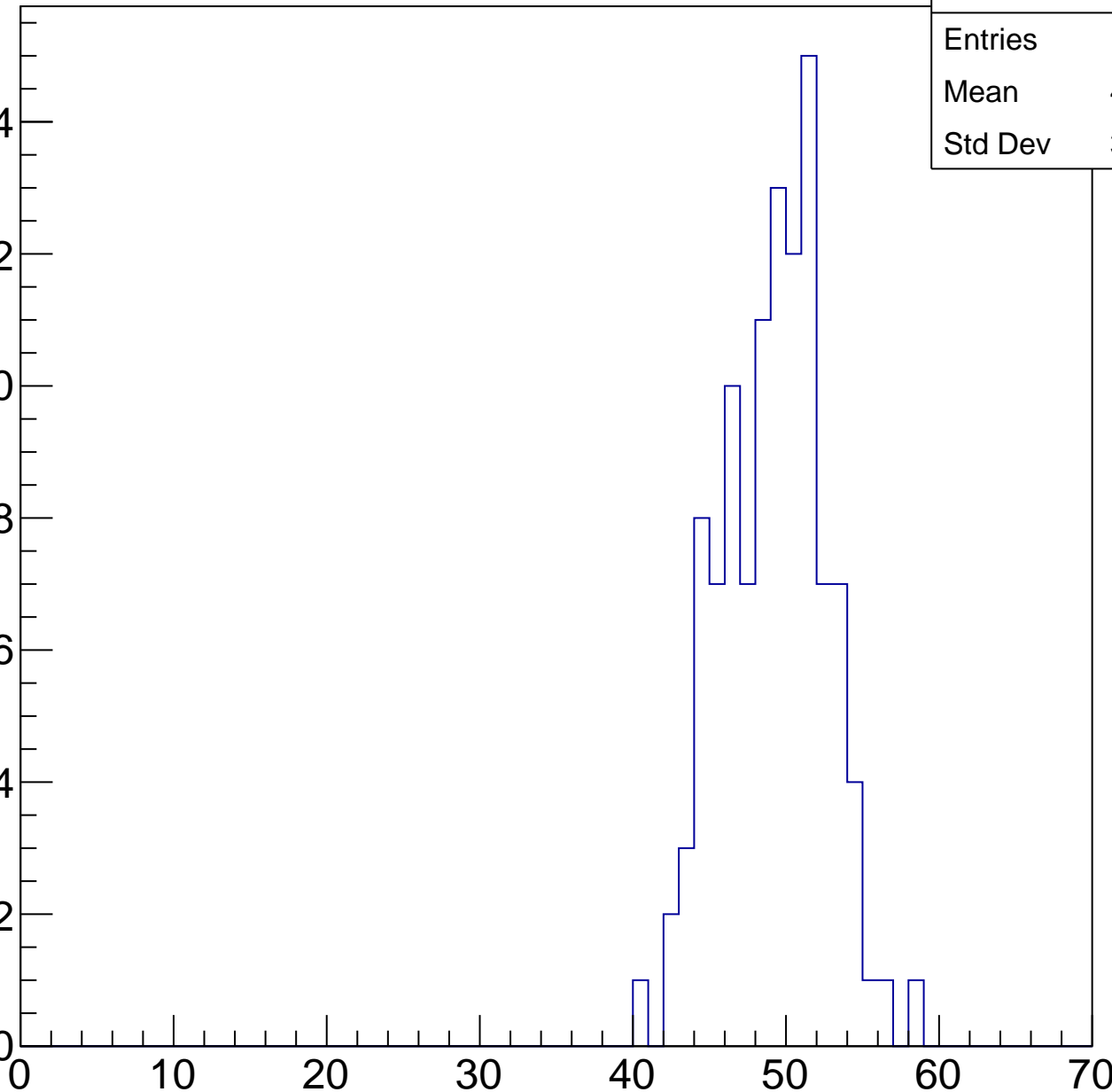
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	48.72
Std Dev	3.347

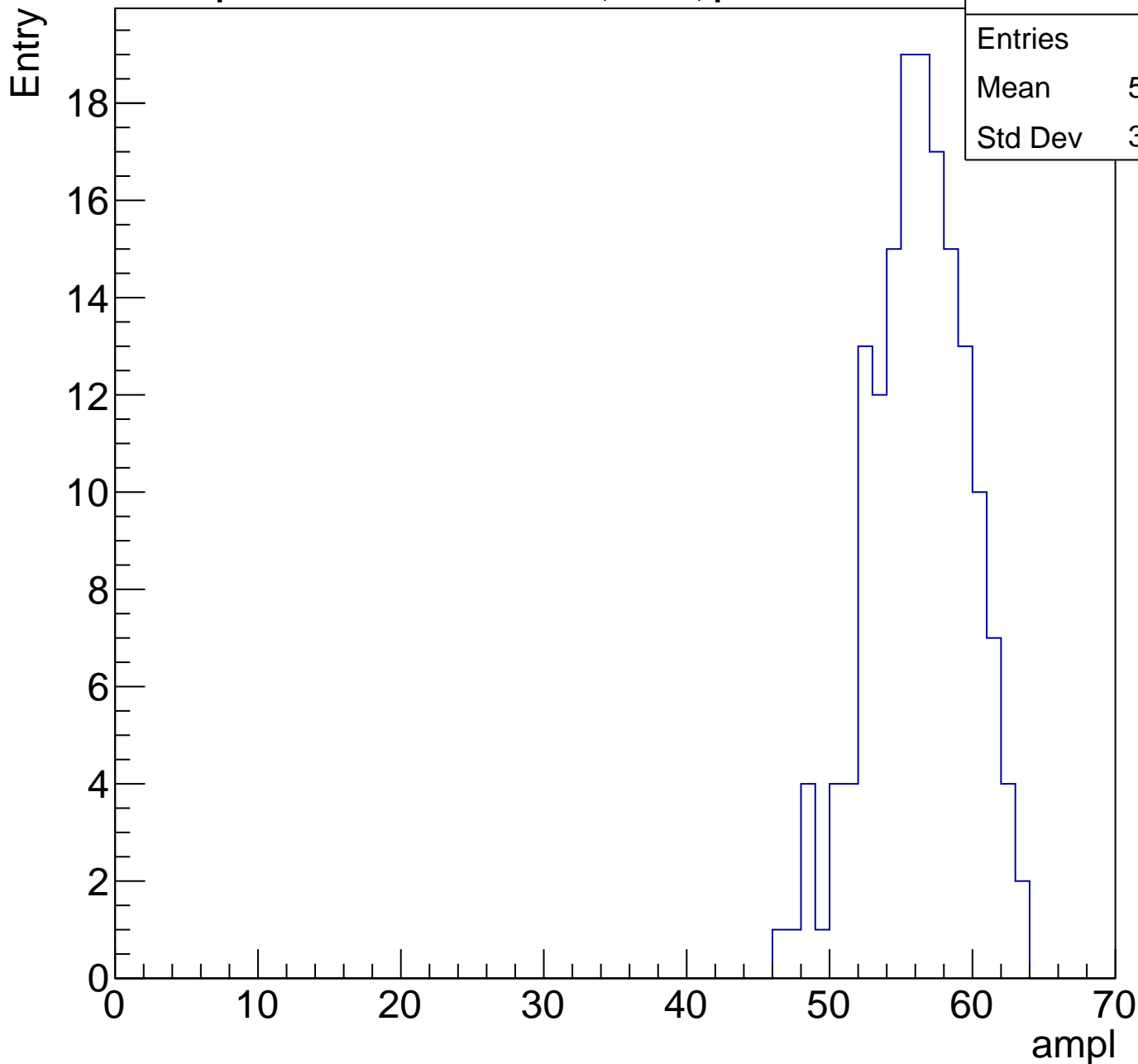
ampl



B0L101S, U20-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	55.75
Std Dev	3.423



B0L101S, U20-ch20, adc5

calib_packv5_042523_0143.root, FC#1, port C1

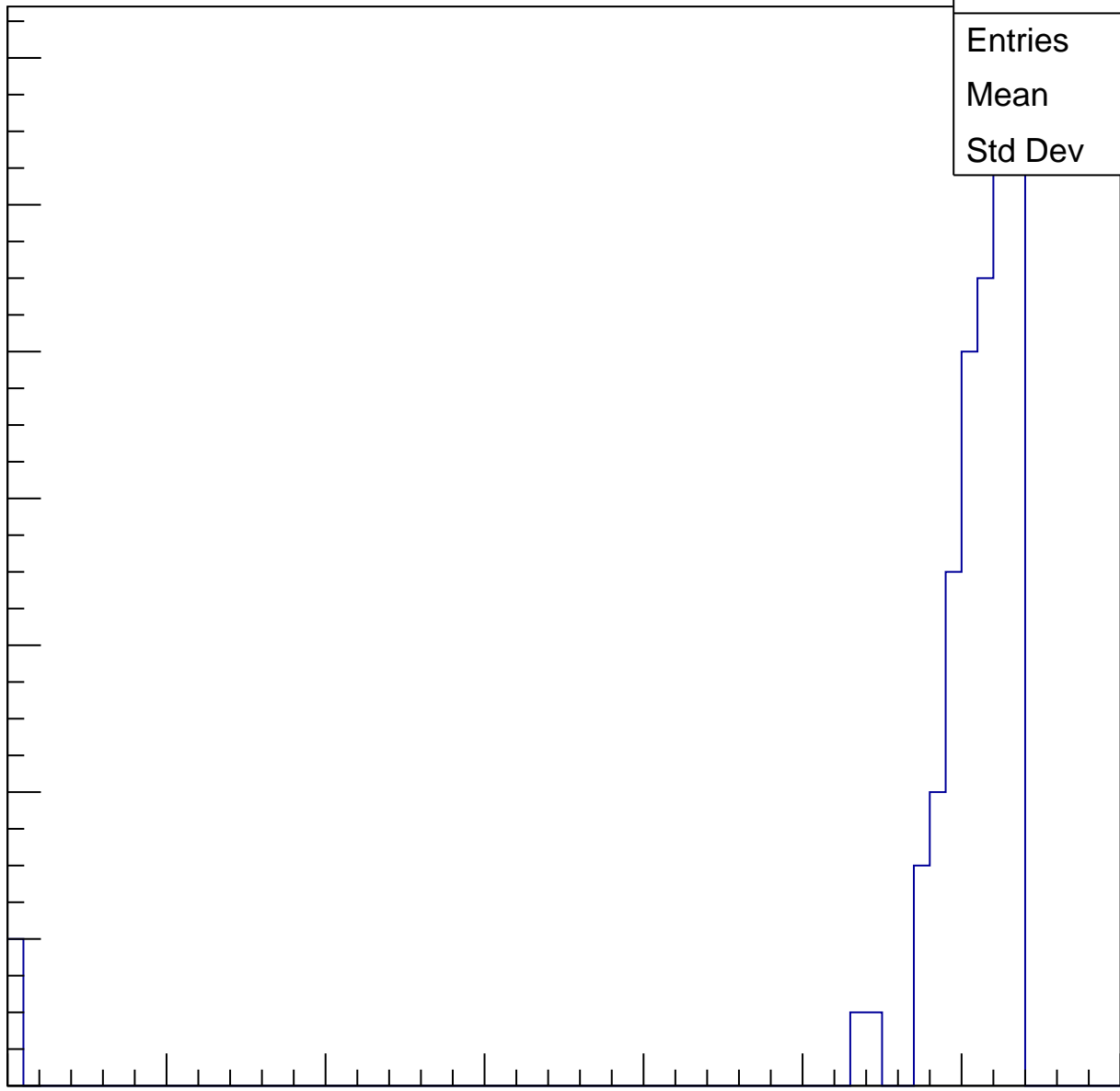
Entries	66
Mean	58.8
Std Dev	10.61

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

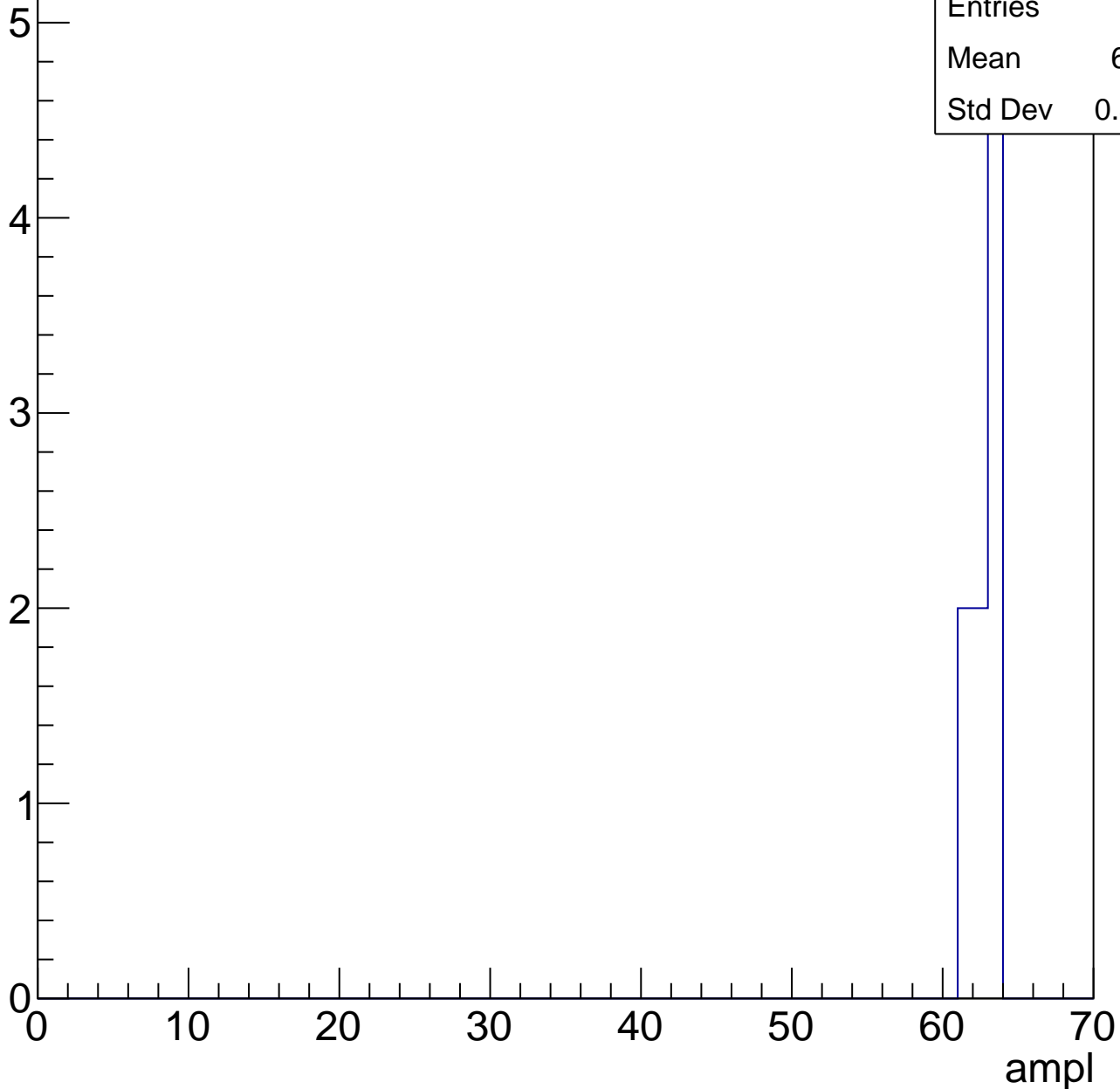


B0L101S, U20-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.33
Std Dev	0.8165



B0L101S, U20-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch21, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	29.24
Std Dev	3.845

Gaus mean : 29.6540

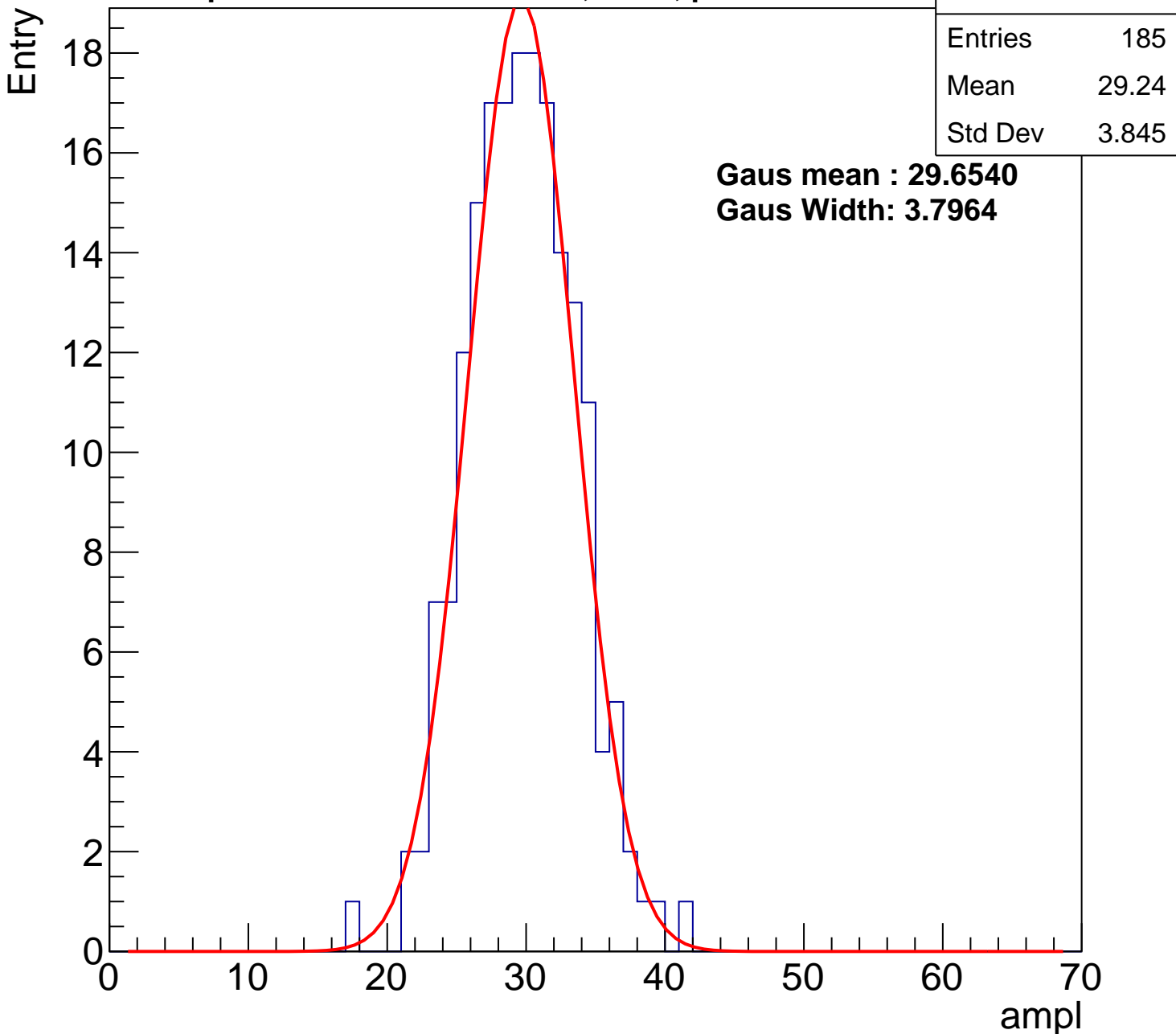
Gaus Width: 3.7964

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	37.1
Std Dev	3.455

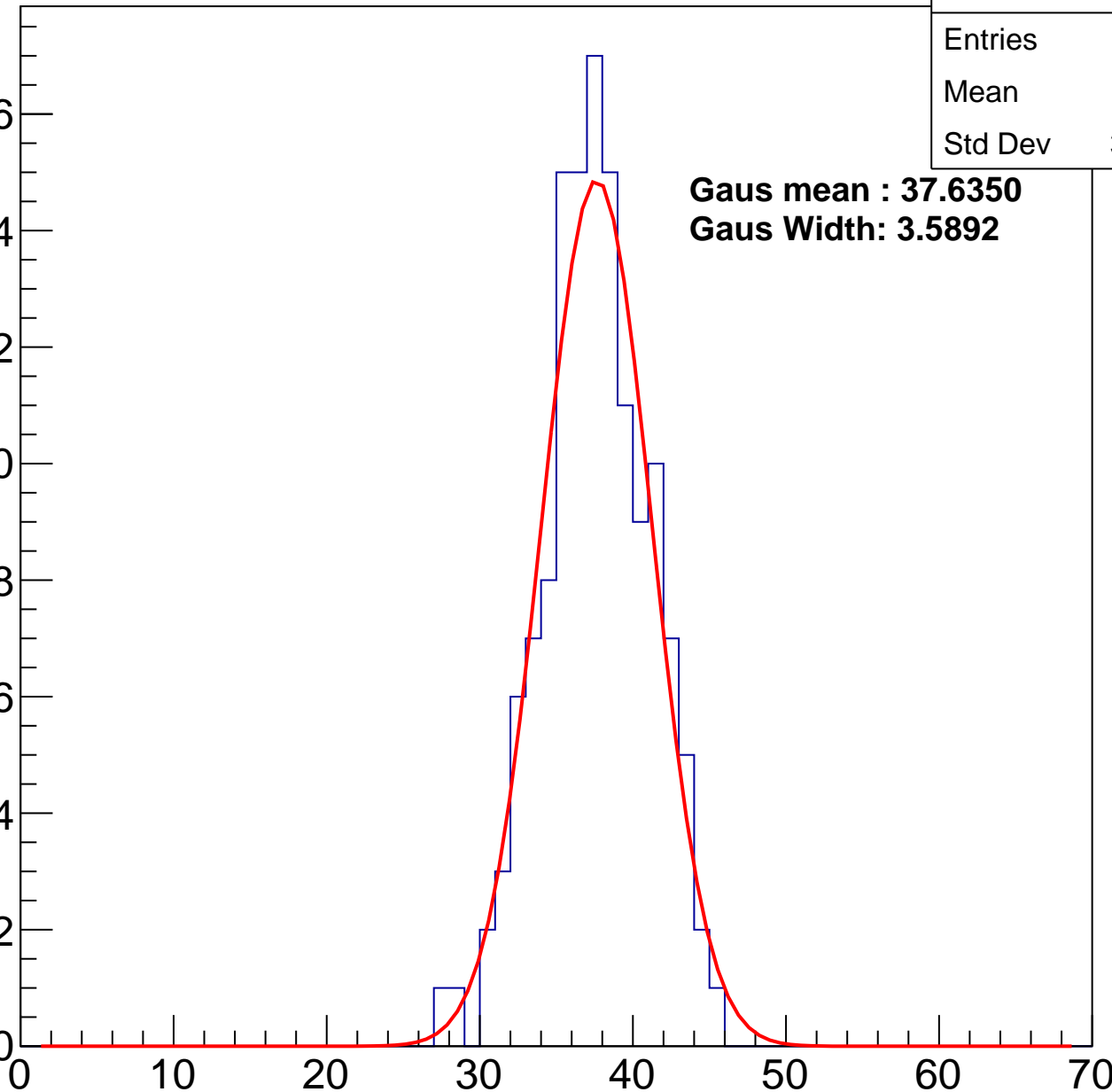
Gaus mean : 37.6350

Gaus Width: 3.5892

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

125

Mean

43.32

Std Dev

3.234

Gaus mean : 43.9686

Gaus Width: 3.2139

0

10

20

30

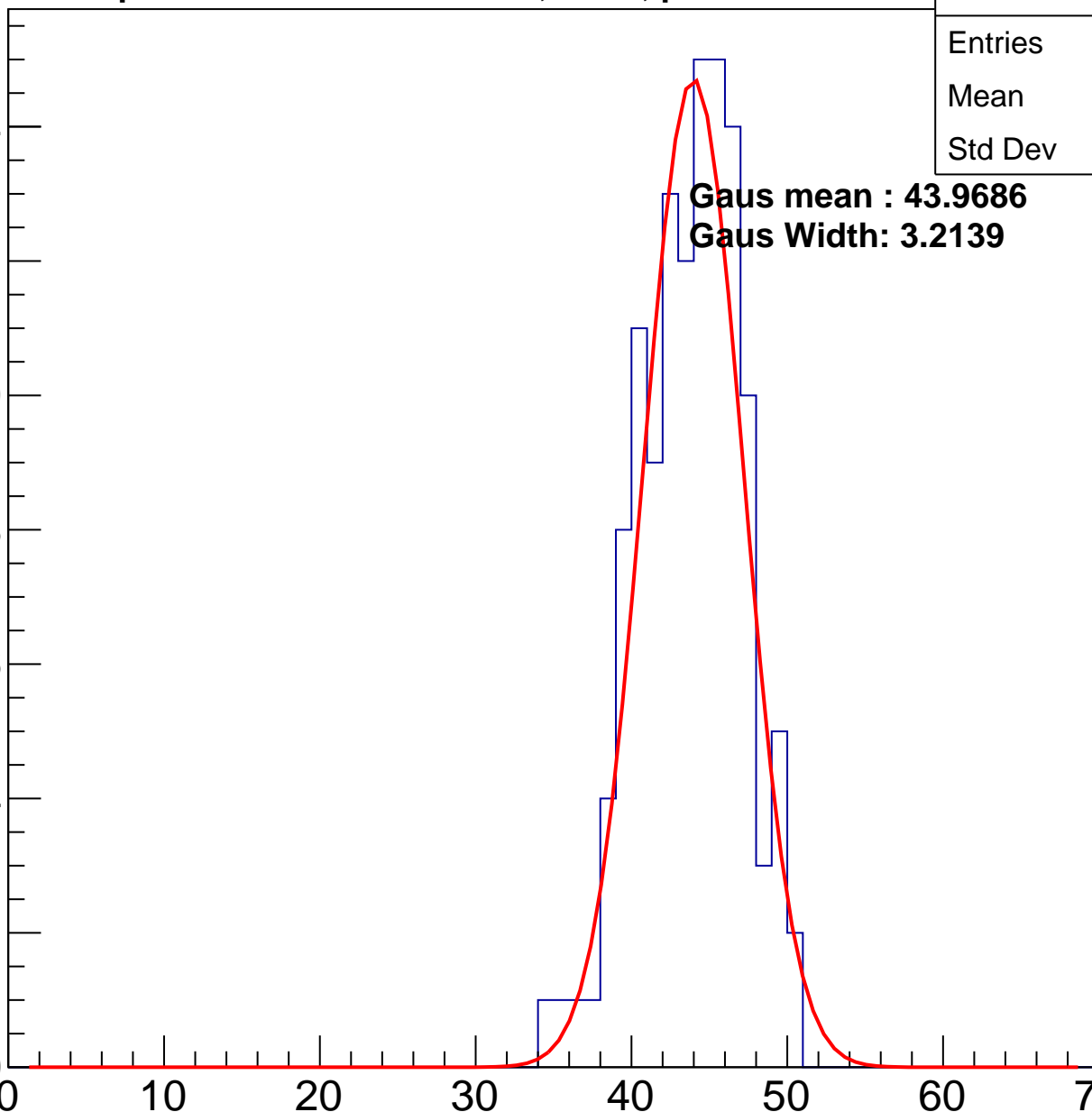
40

50

60

70

ampl

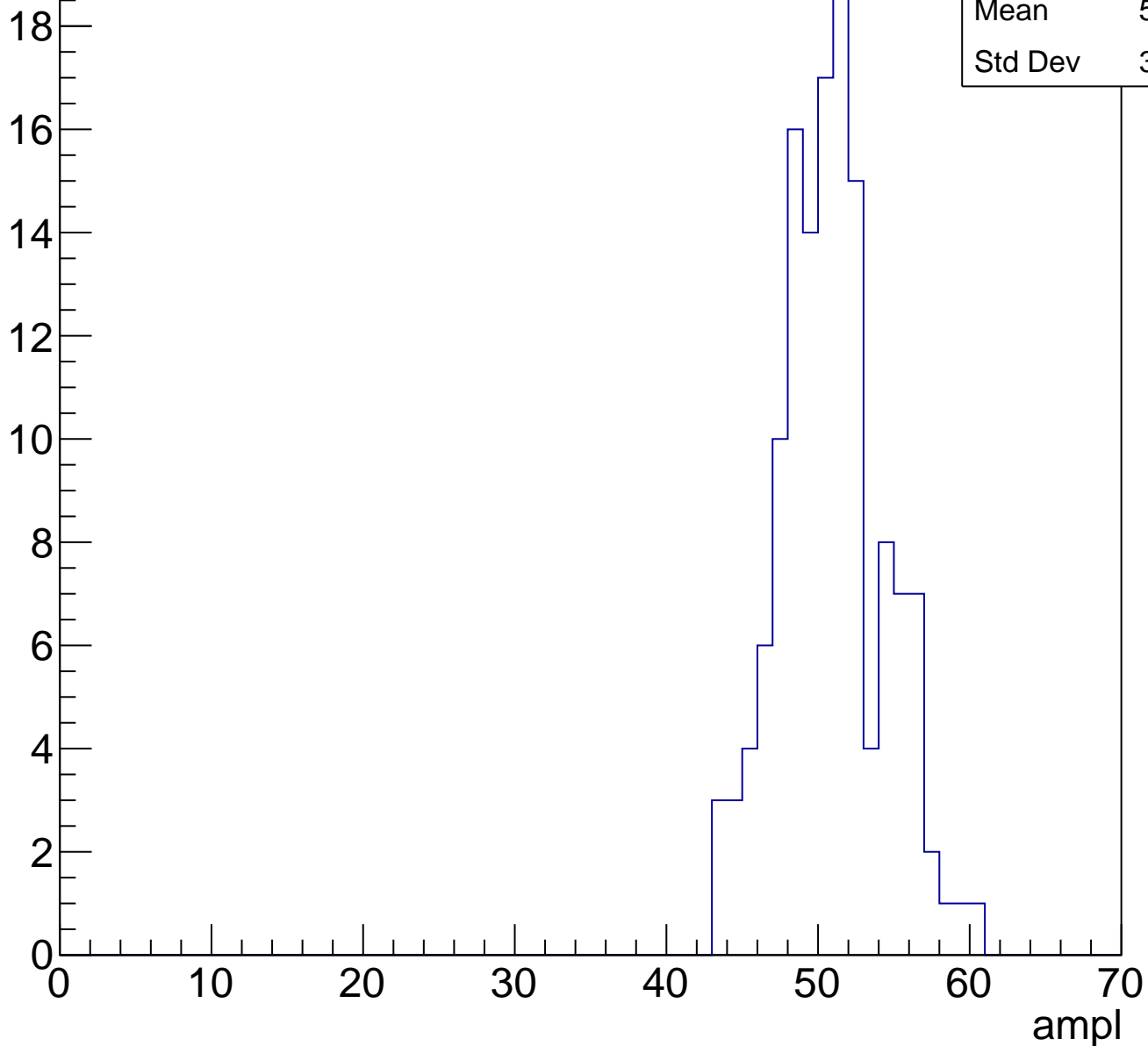


B0L101S, U20-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	50.38
Std Dev	3.418

Entry



B0L101S, U20-ch21, adc4

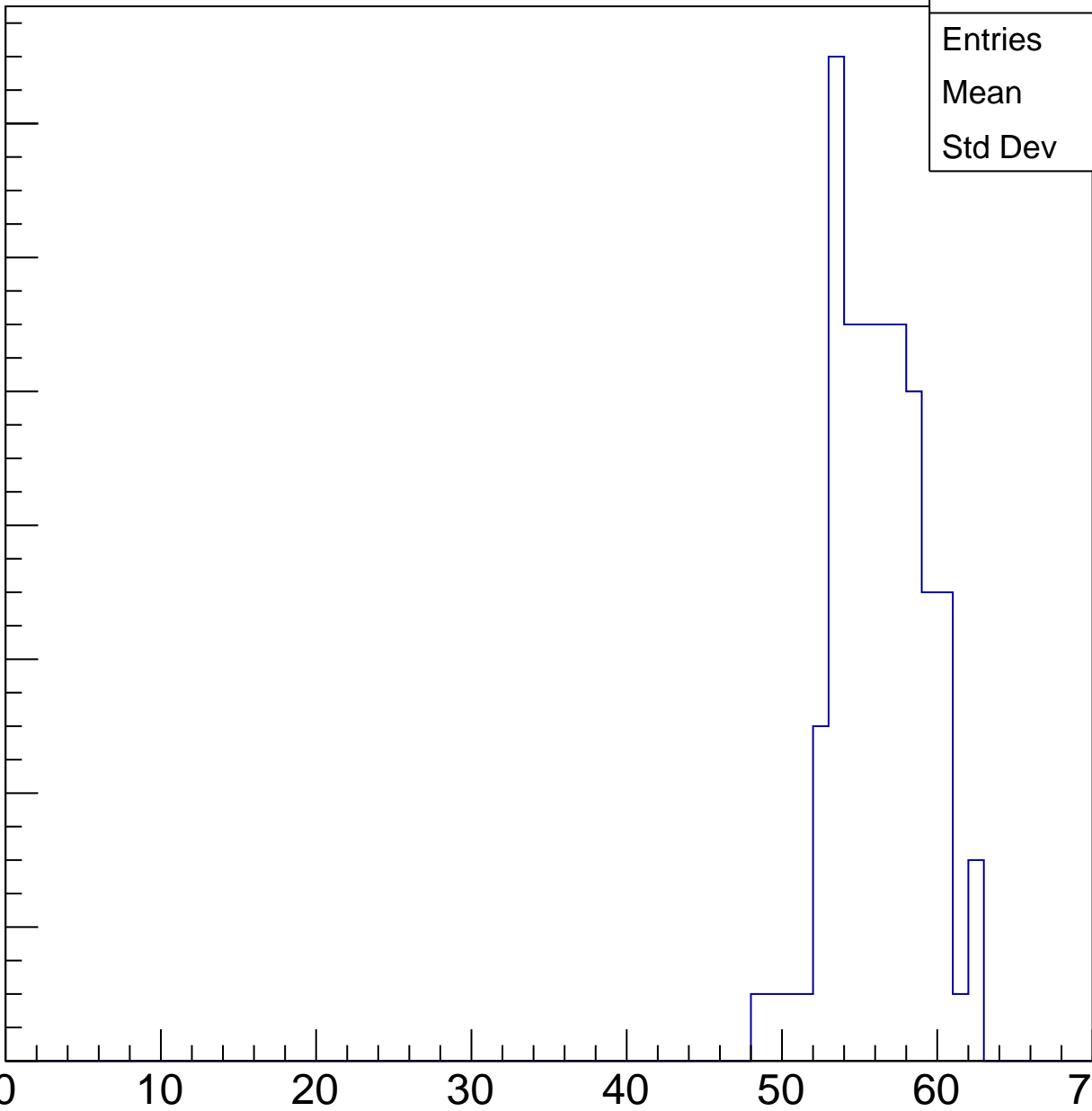
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	55.78
Std Dev	2.884

ampl



B0L101S, U20-ch21, adc5

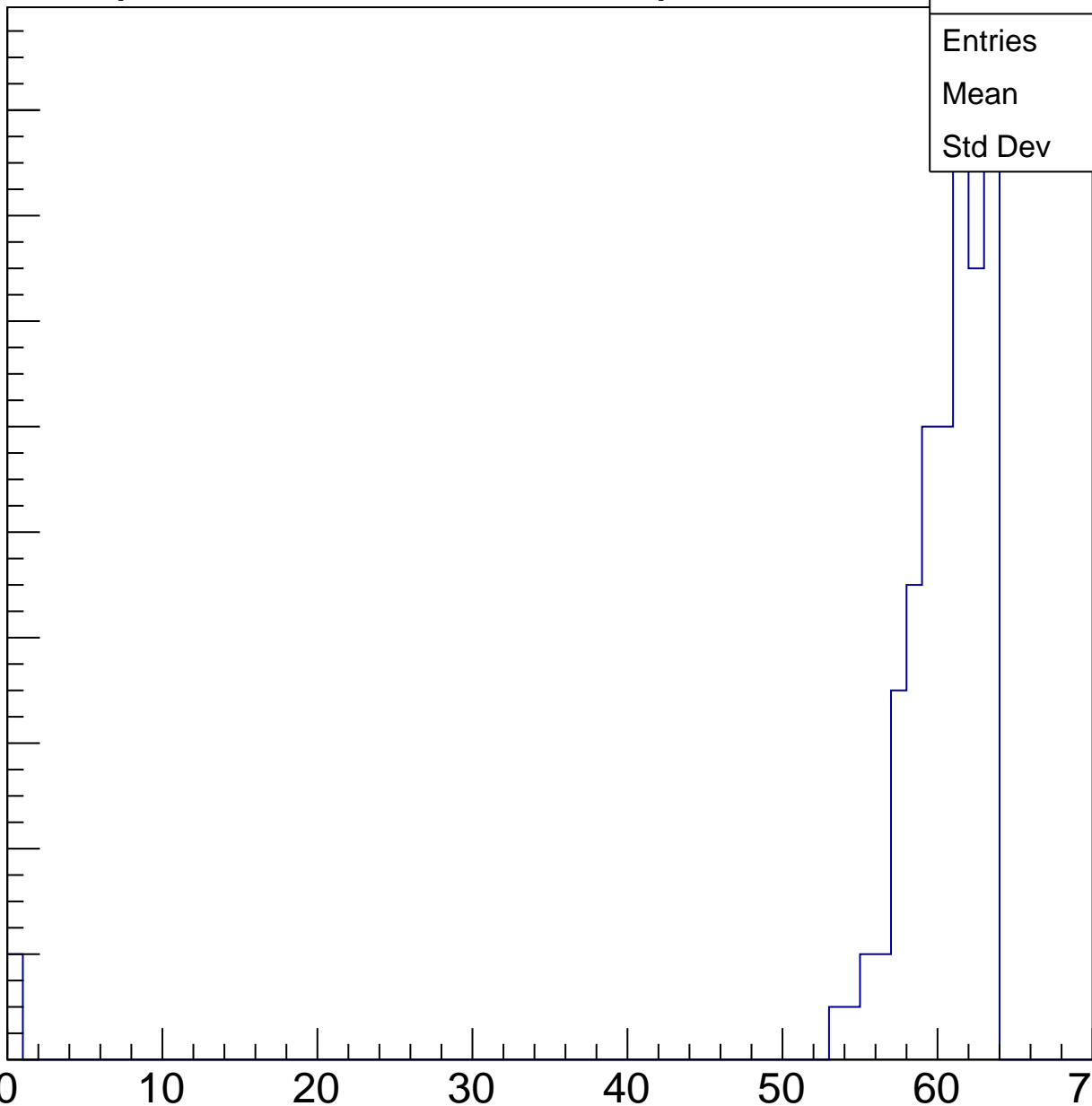
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.01
Std Dev	8.73

ampl



B0L101S, U20-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

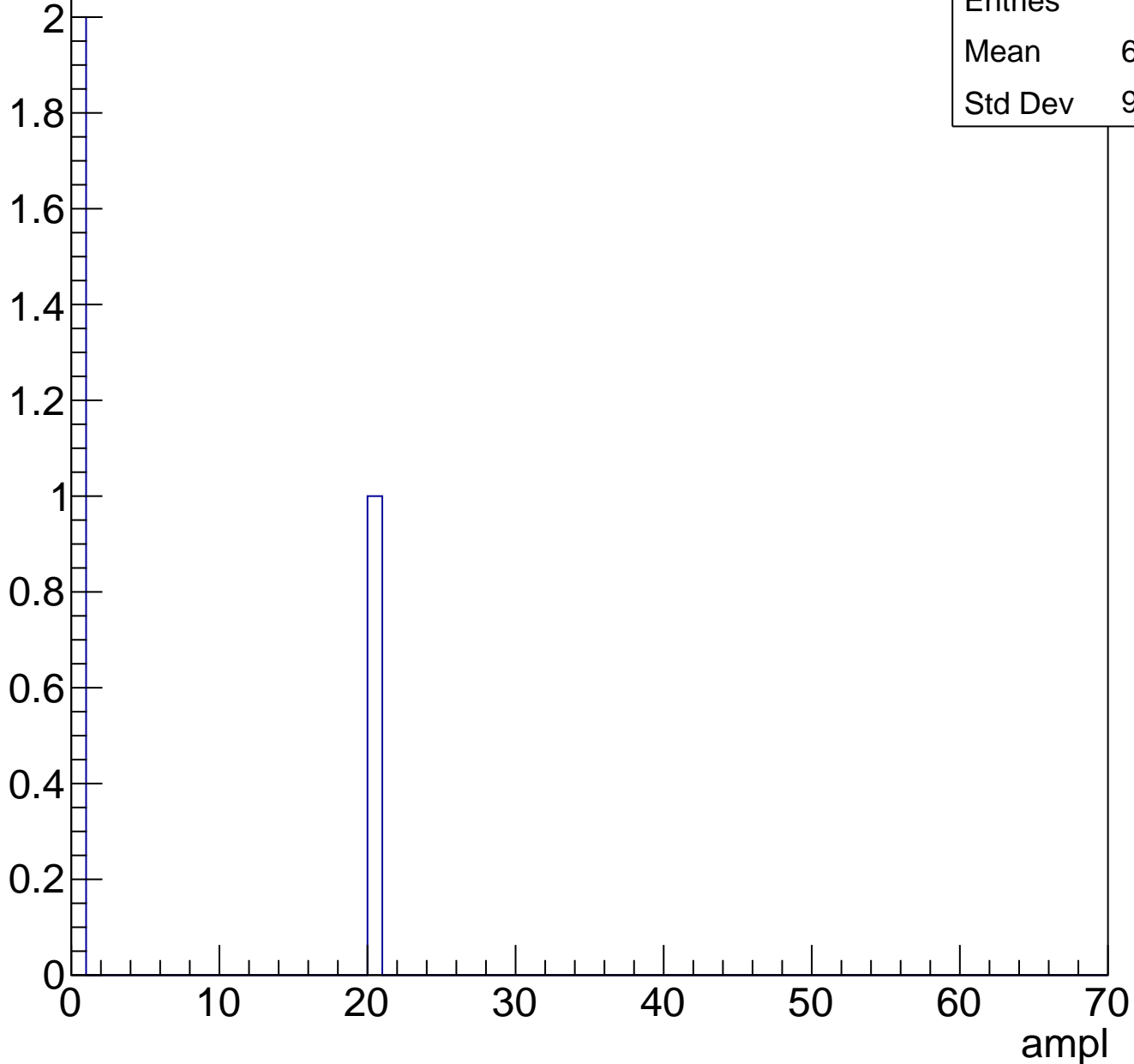
Entry



B0L101S, U20-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

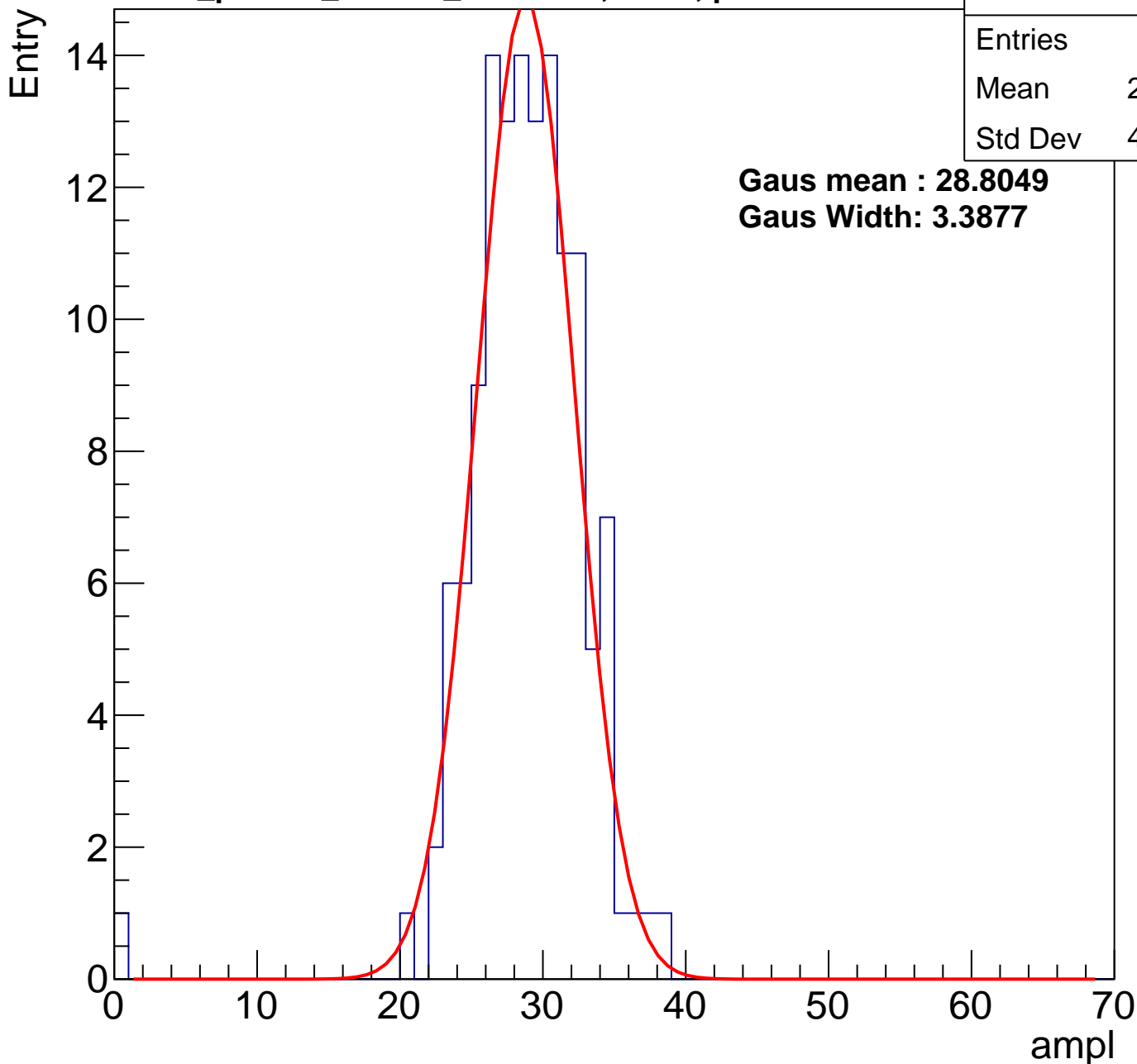
B0L101S, U20-ch22, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	28.37
Std Dev	4.202

Gaus mean : 28.8049

Gaus Width: 3.3877



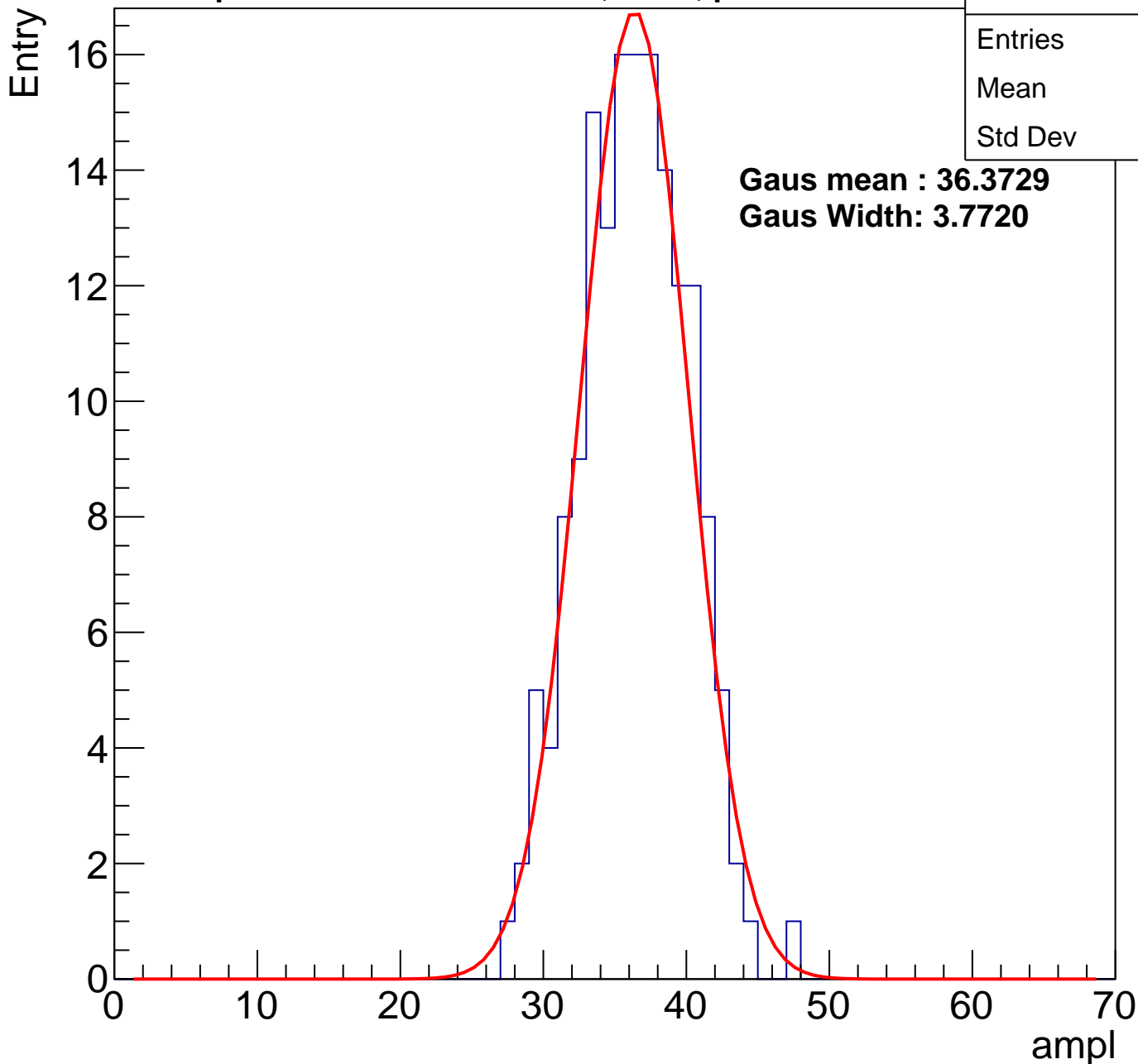
B0L101S, U20-ch22, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	35.9
Std Dev	3.69

Gaus mean : 36.3729

Gaus Width: 3.7720



B0L101S, U20-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

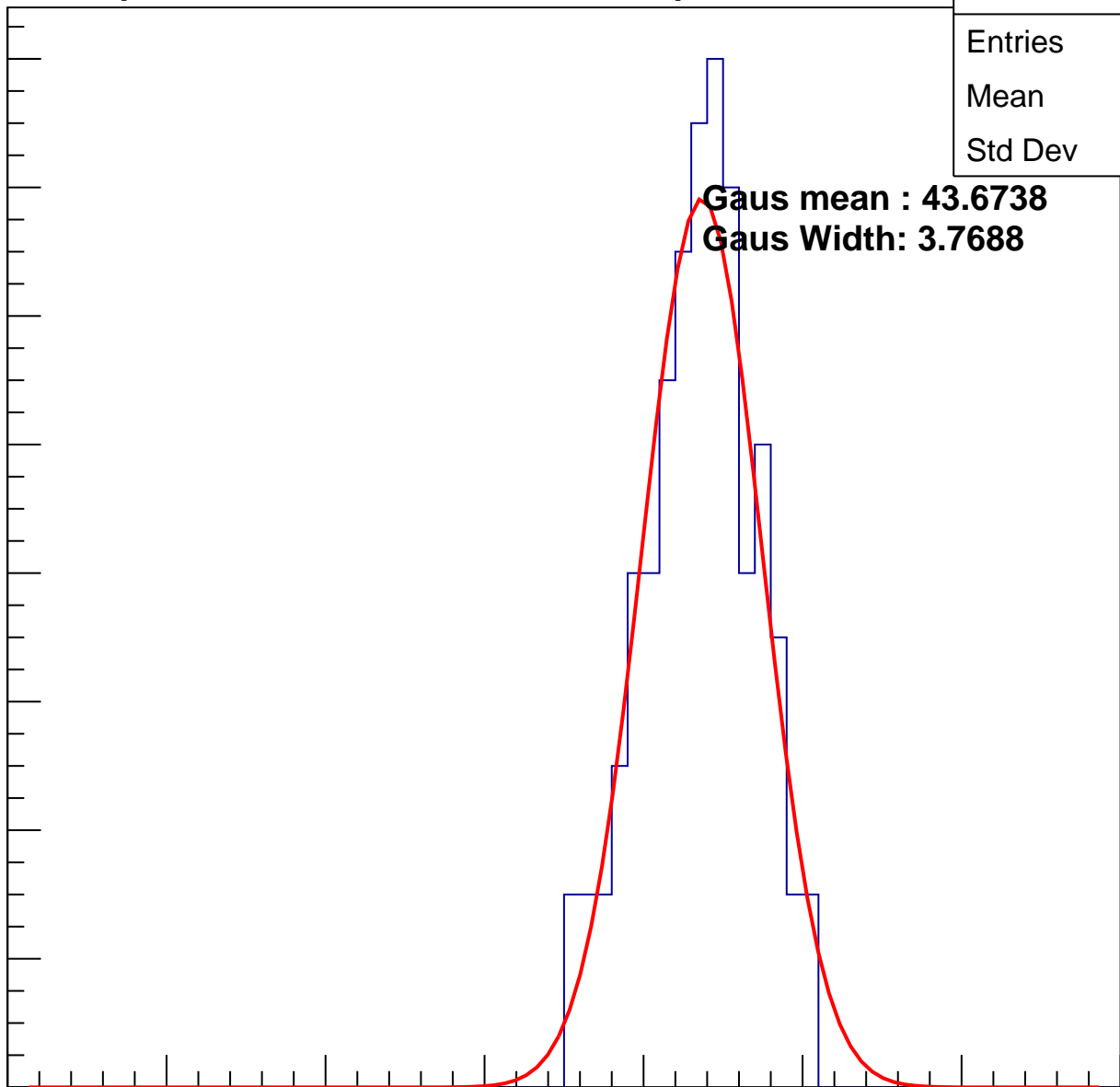
Entries	130
Mean	43.02
Std Dev	3.467

Gaus mean : 43.6738

Gaus Width: 3.7688

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	50.24
Std Dev	3.615

Entry

16

14

12

10

8

6

4

2

0

0

10

20

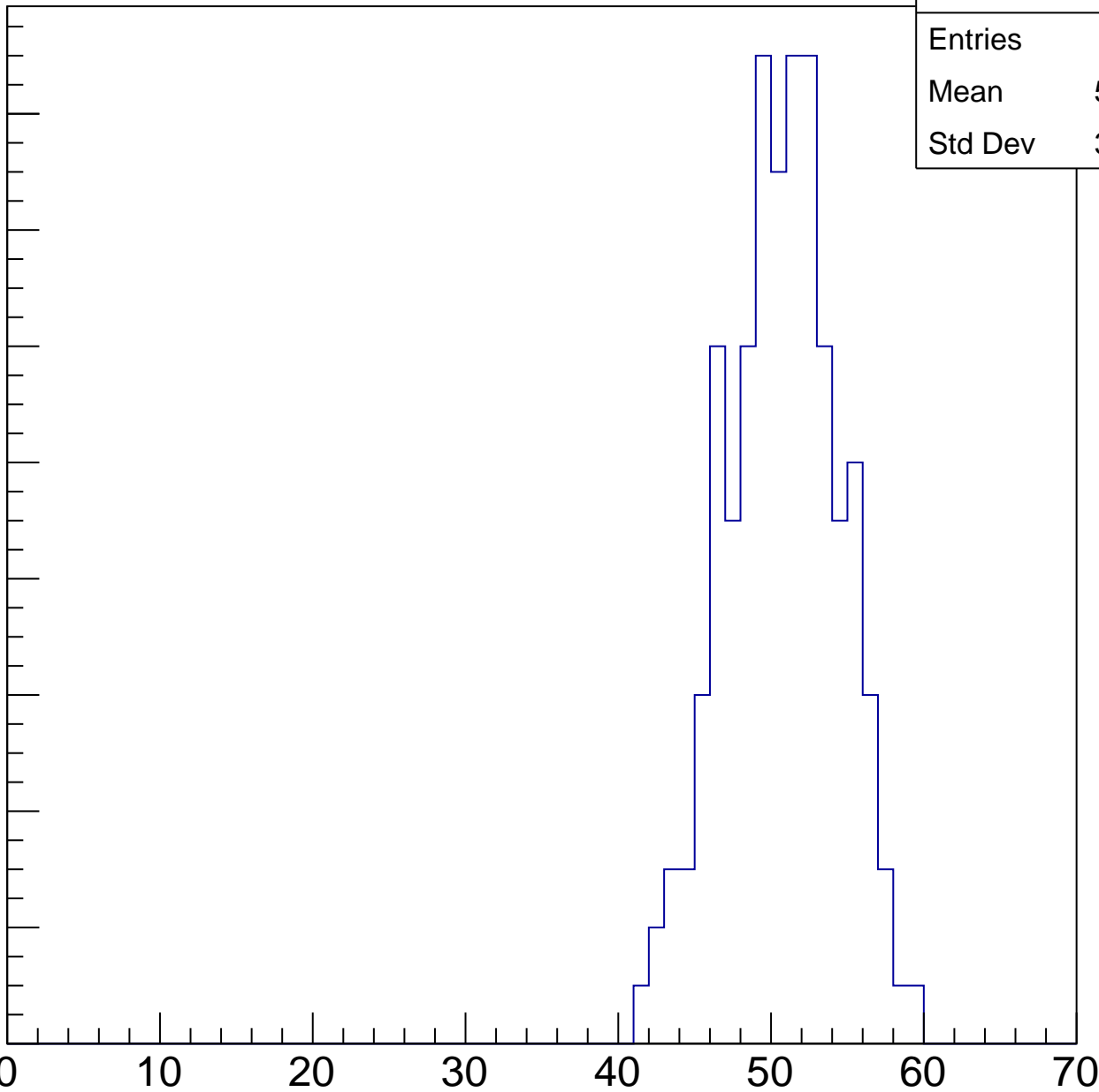
30

40

50

60

ampl



B0L101S, U20-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	56.67
Std Dev	3.269

Entry

12

10

8

6

4

2

0

0

10

20

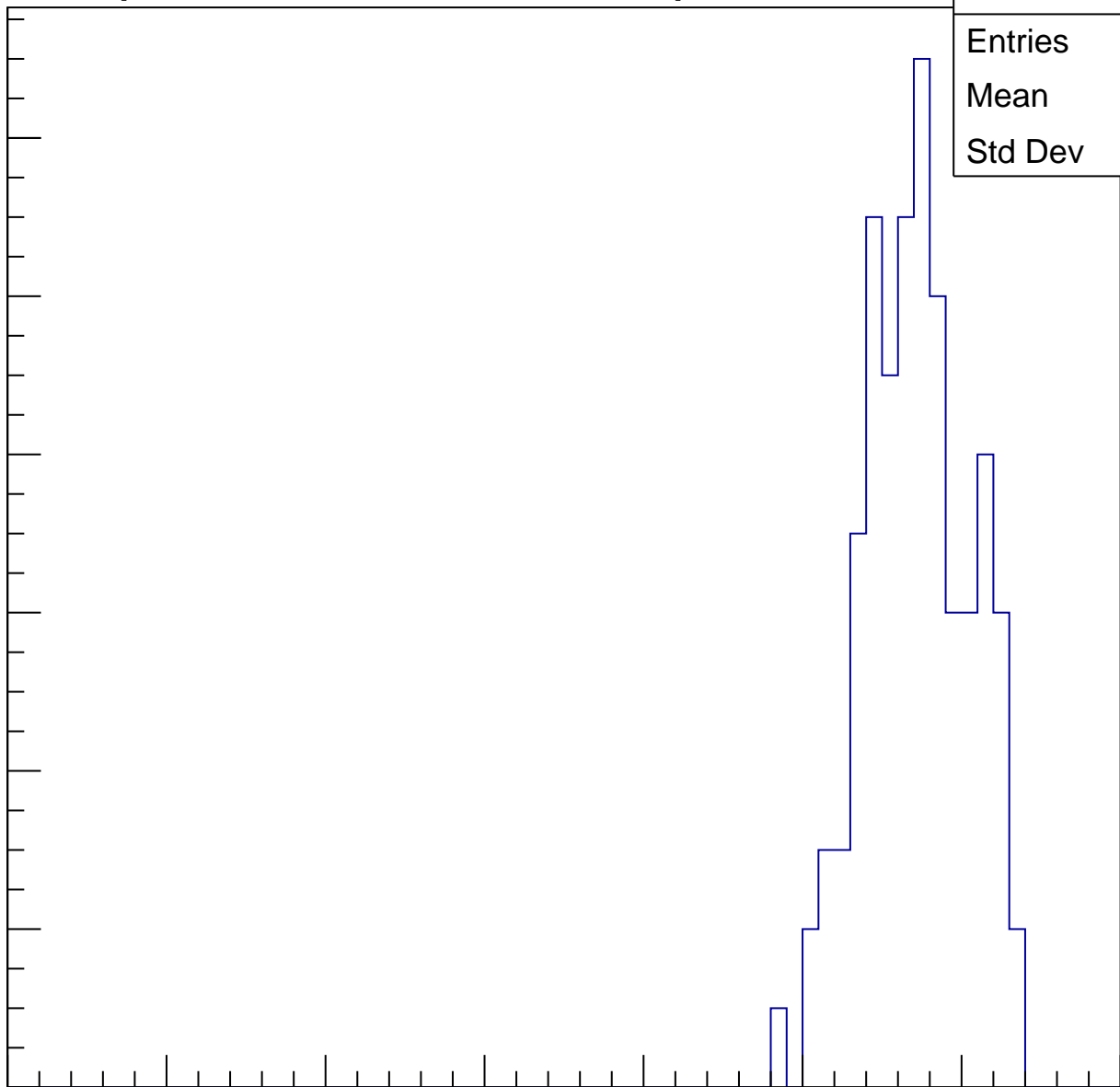
30

40

50

60

ampl



B0L101S, U20-ch22, adc5

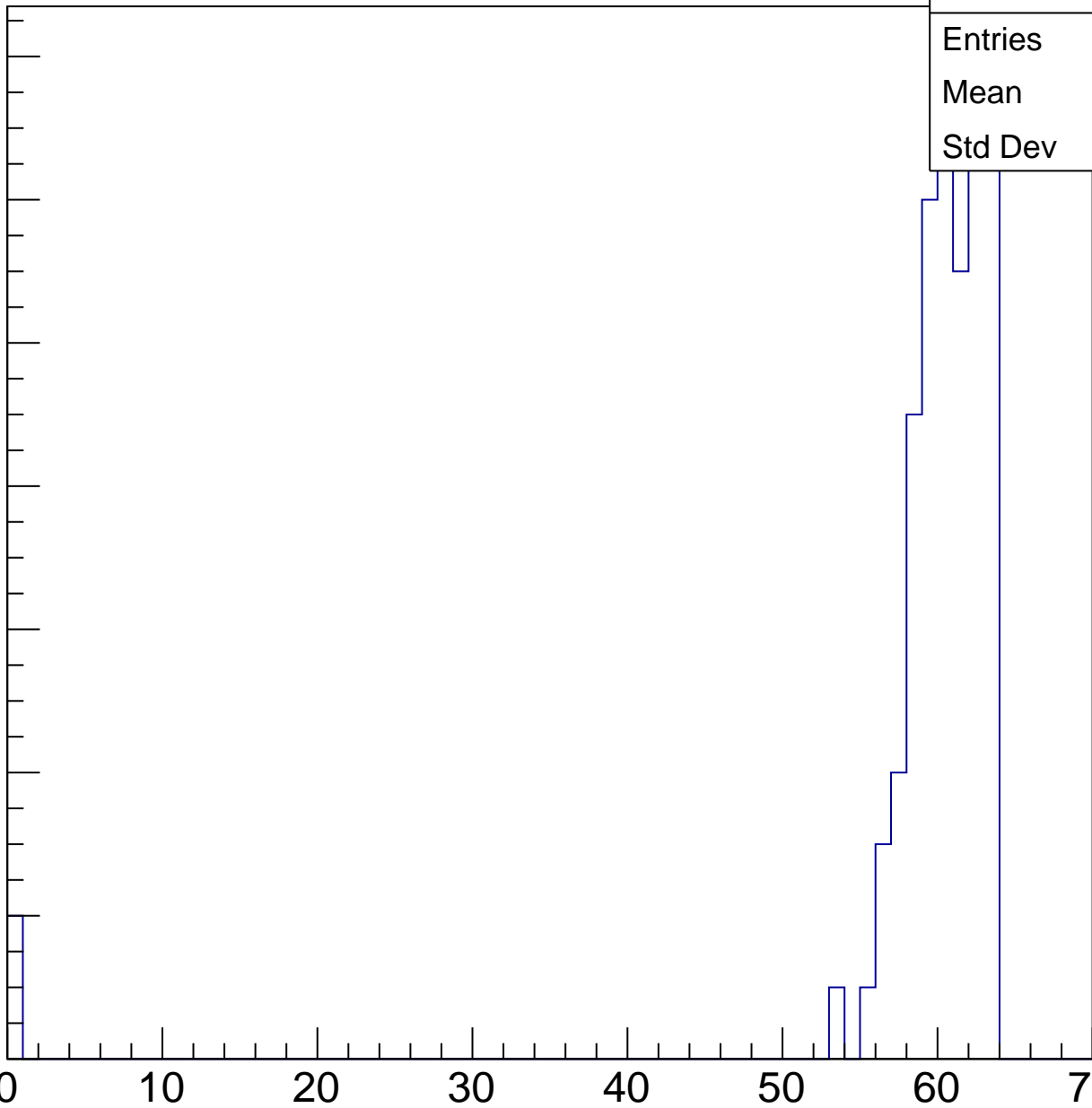
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	58.73
Std Dev	9.423

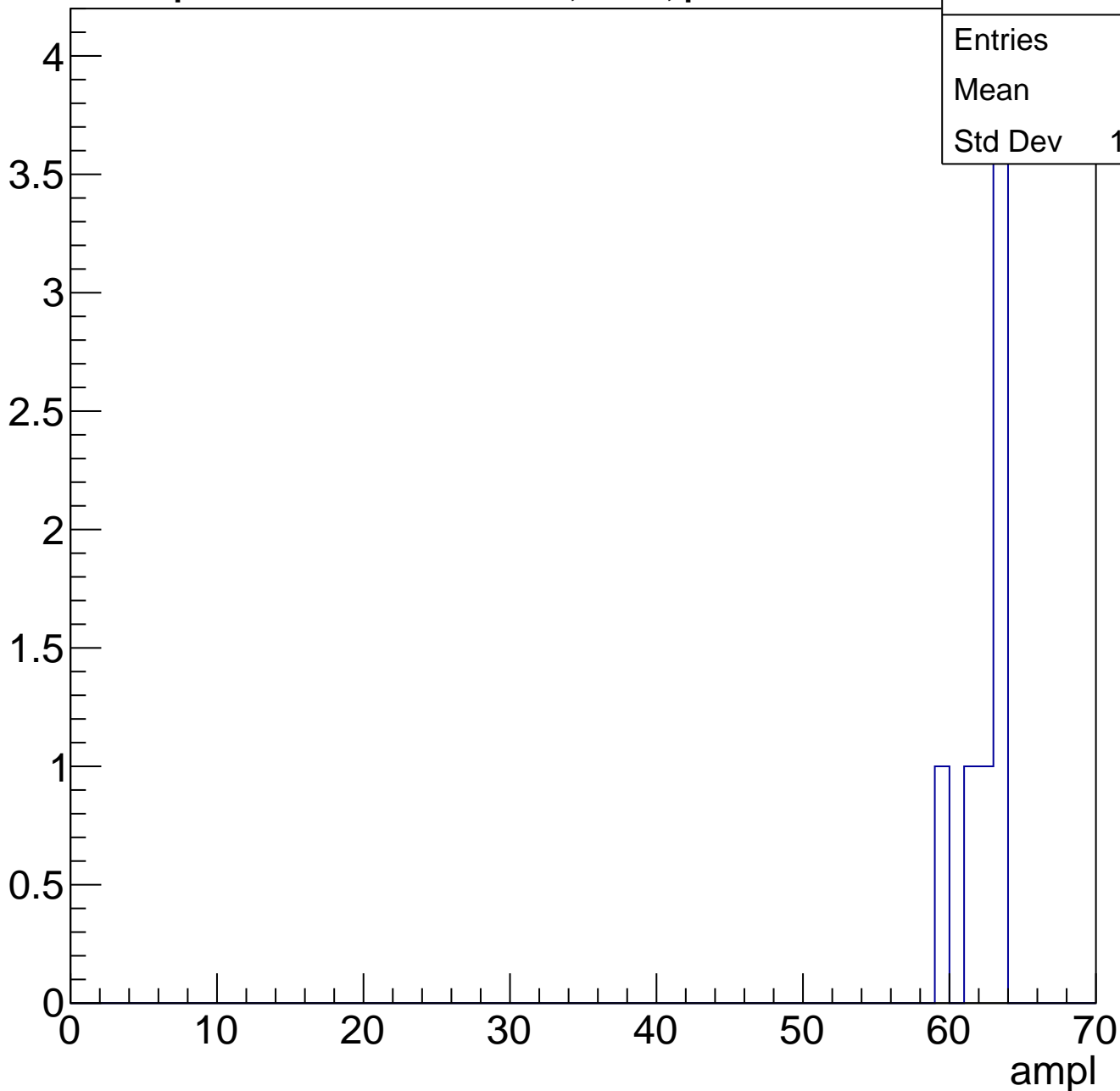
ampl



B0L101S, U20-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch23, adc0

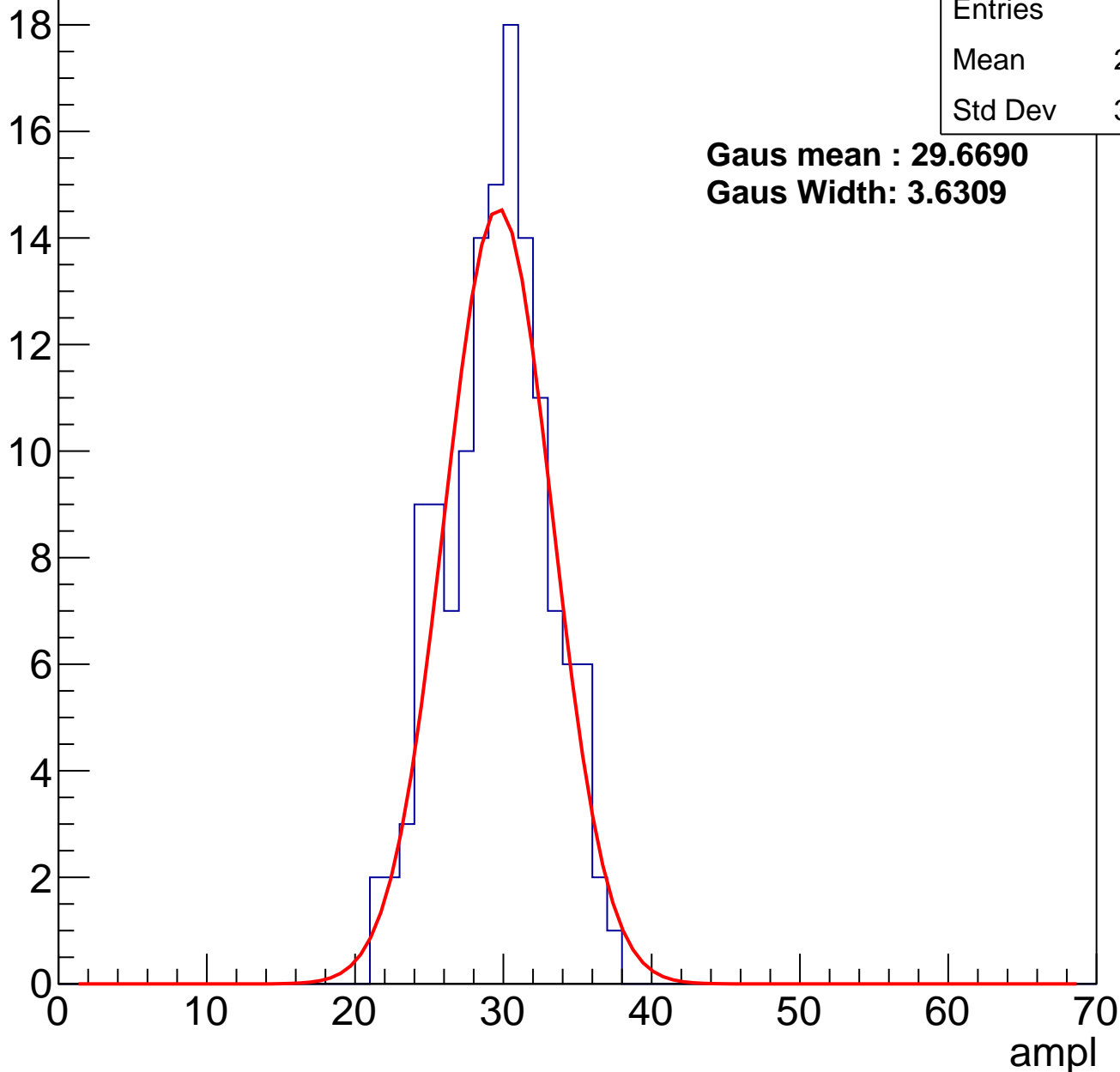
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	29.08
Std Dev	3.464

Gaus mean : 29.6690

Gaus Width: 3.6309

Entry



B0L101S, U20-ch23, adc1

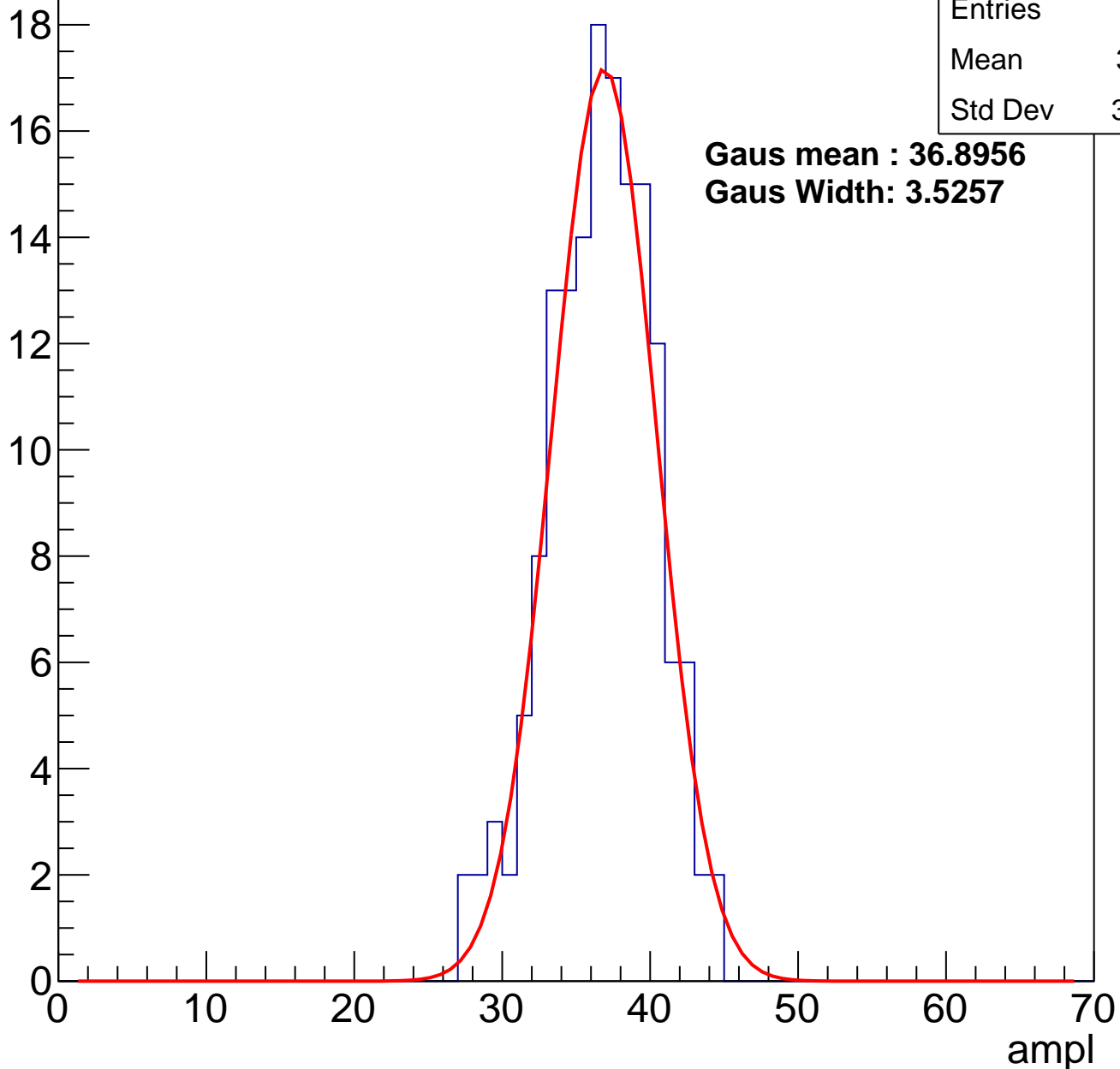
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	36.21
Std Dev	3.532

Gaus mean : 36.8956

Gaus Width: 3.5257

Entry



B0L101S, U20-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

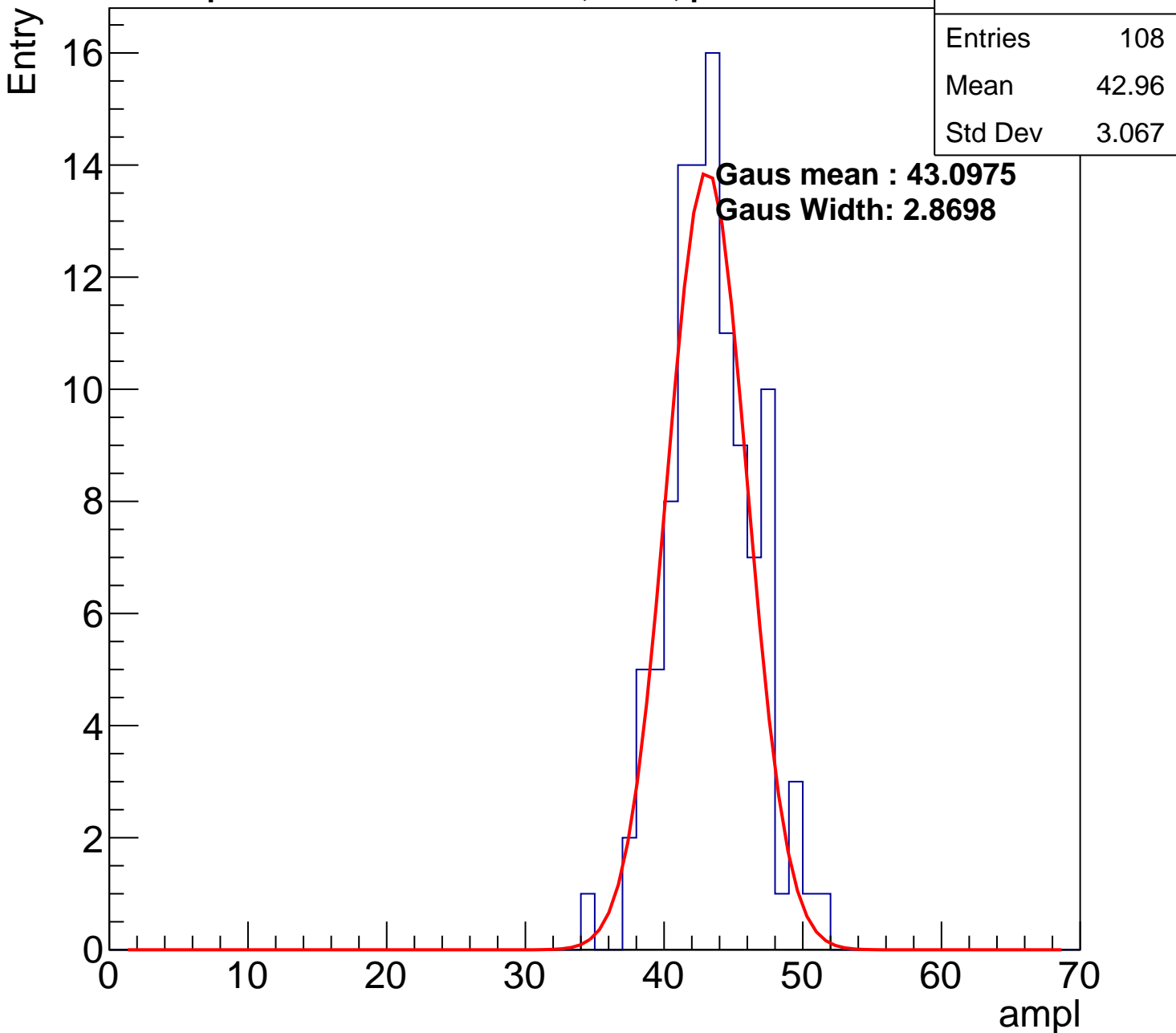
Entries	108
Mean	42.96
Std Dev	3.067

Gaus mean : 43.0975

Gaus Width: 2.8698

ampl

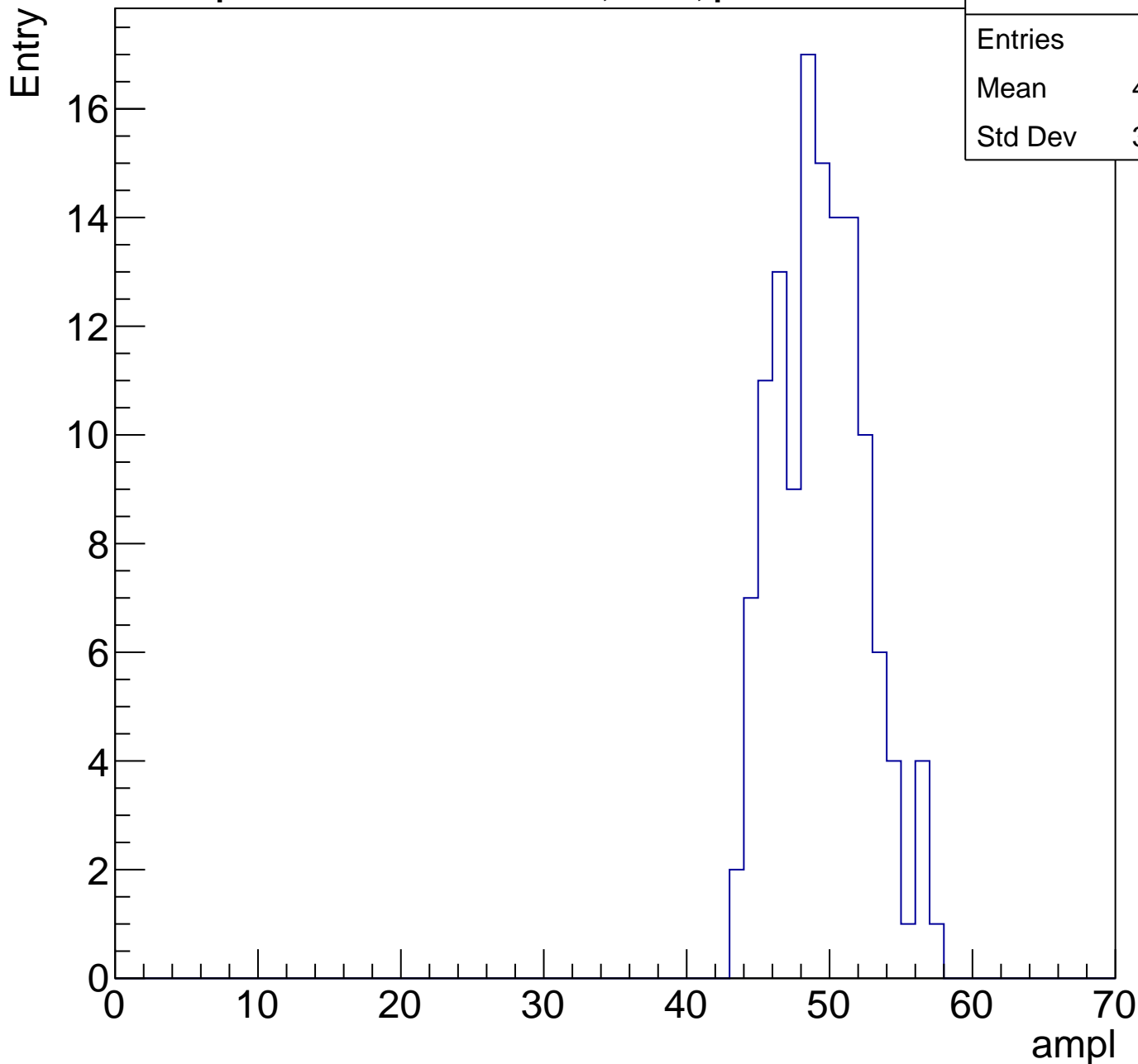
0 10 20 30 40 50 60 70



B0L101S, U20-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	48.95
Std Dev	3.118



B0L101S, U20-ch23, adc4

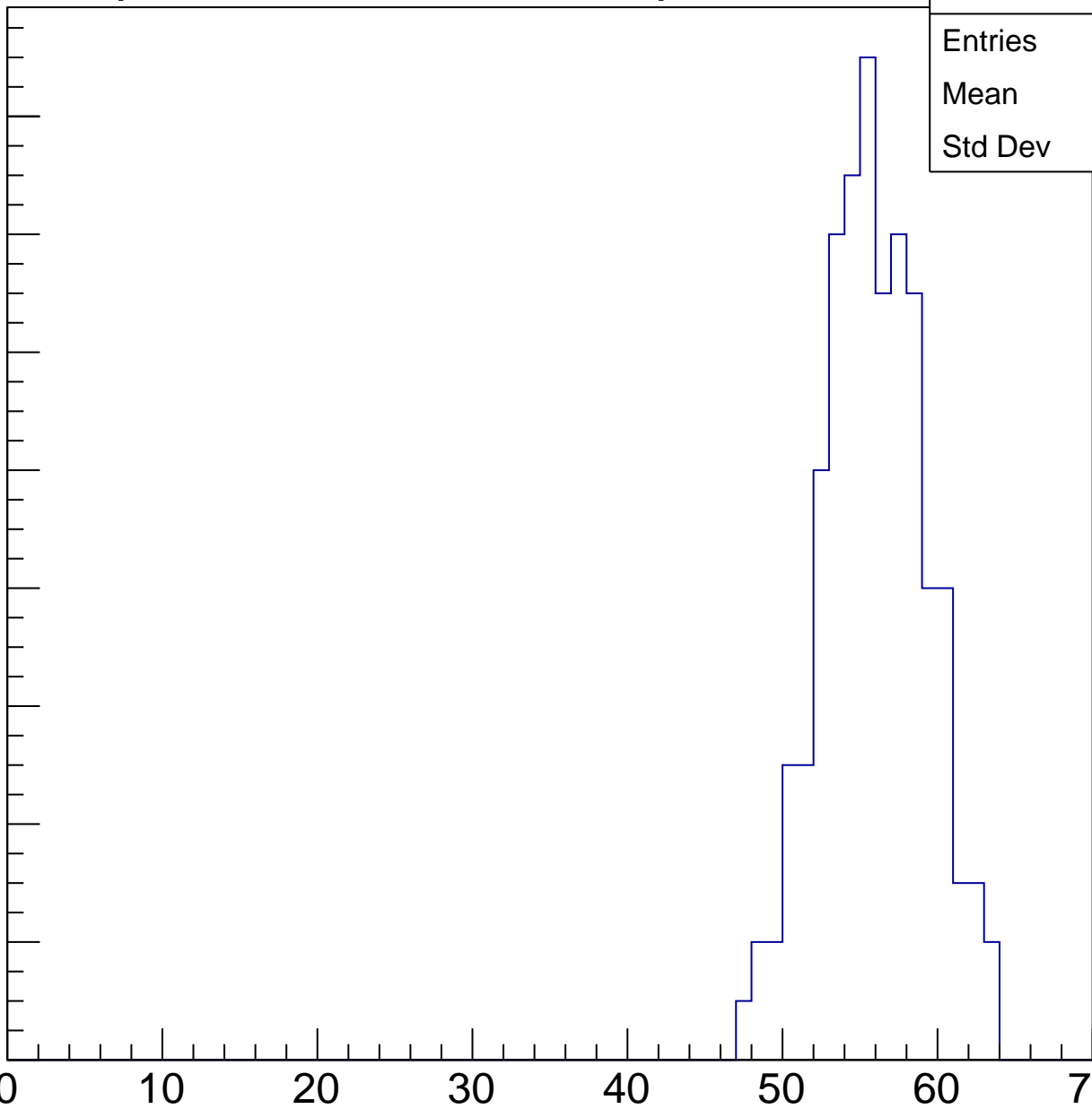
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	55.41
Std Dev	3.305

ampl



B0L101S, U20-ch23, adc5

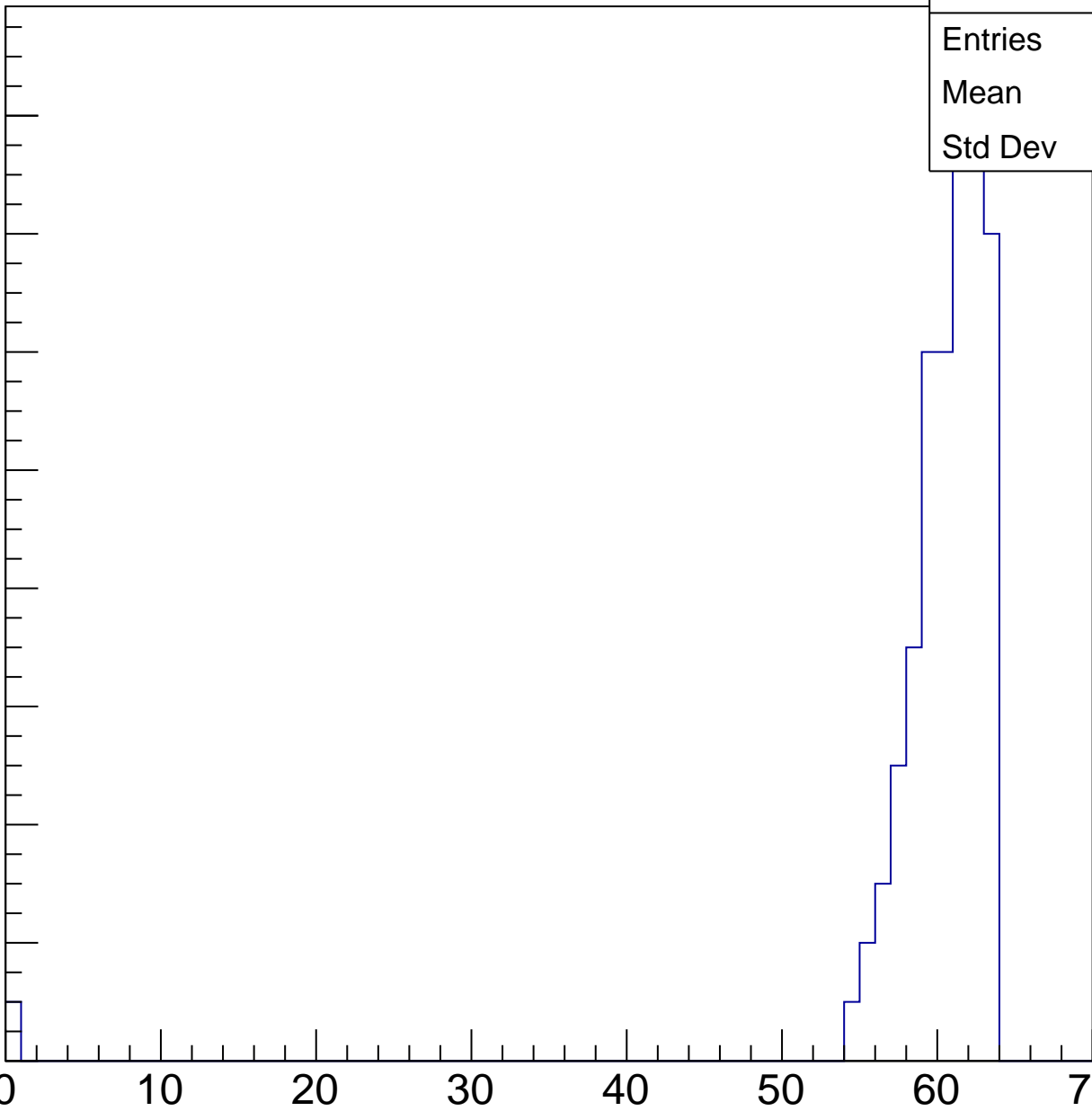
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	59.58
Std Dev	6.675

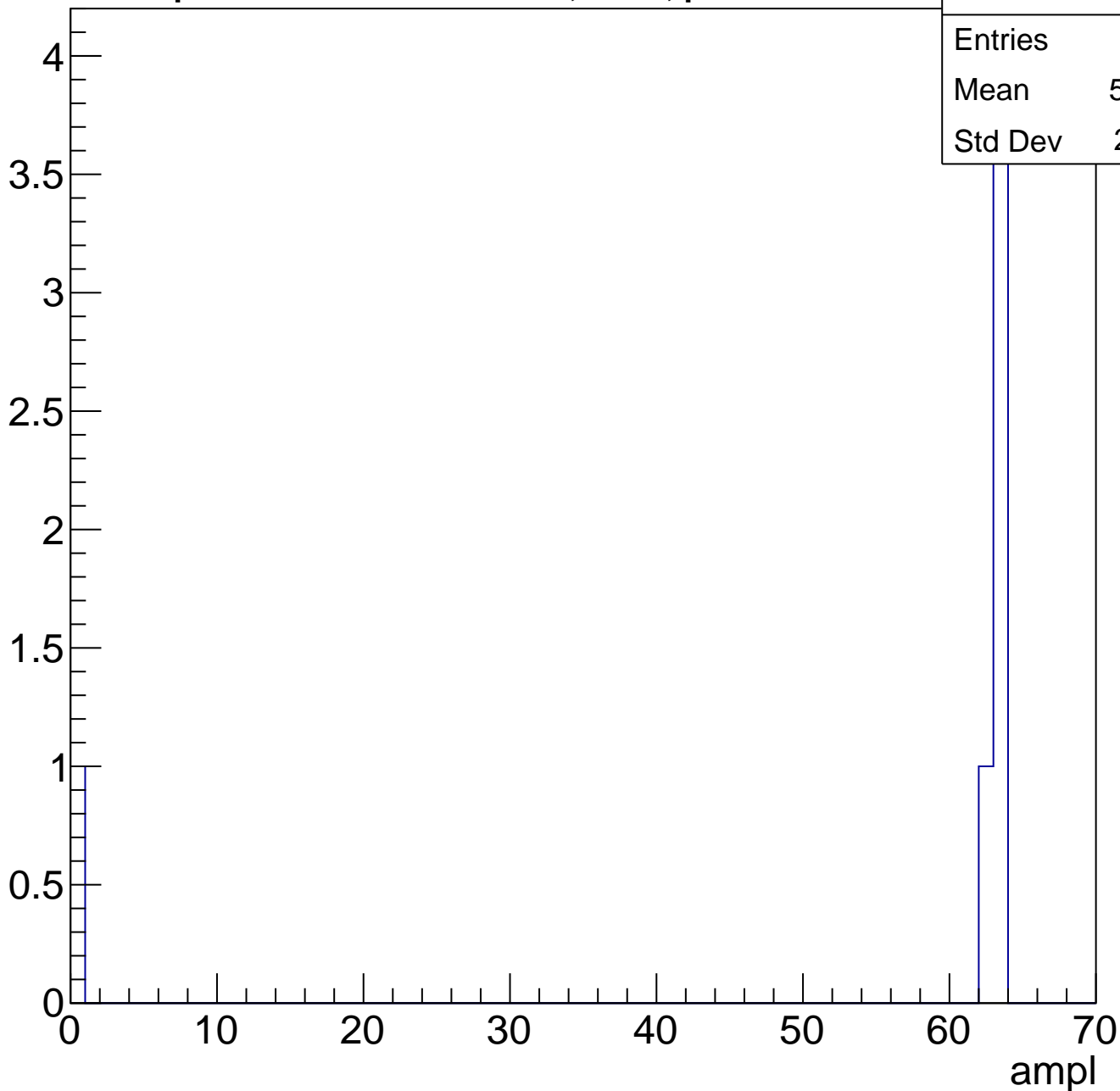
ampl



B0L101S, U20-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	6
Mean	52.33
Std Dev	23.41

B0L101S, U20-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	27.86
Std Dev	4.317

Gaus mean : 28.5282

Gaus Width: 3.5237

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

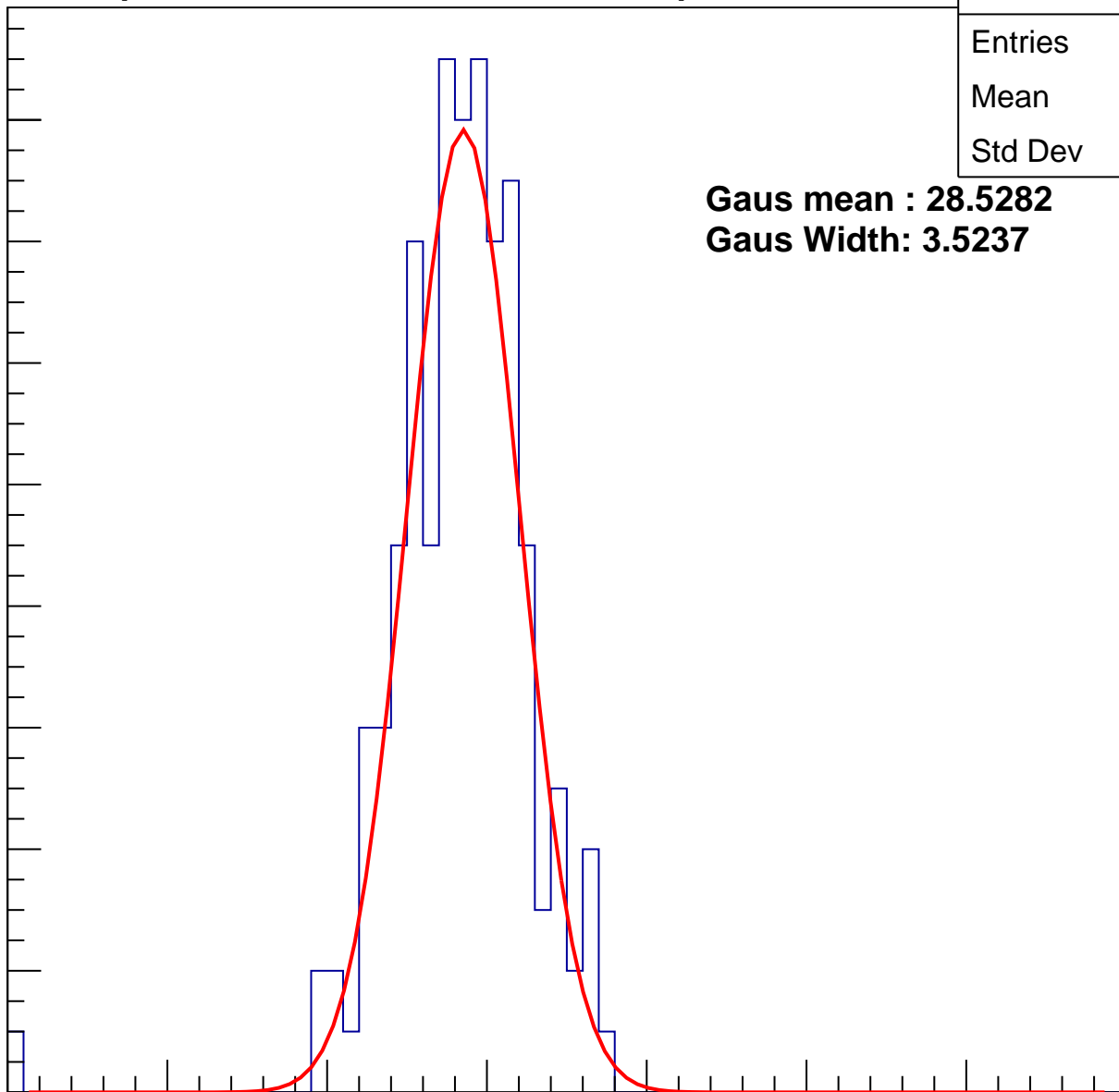
40

50

60

70

ampl



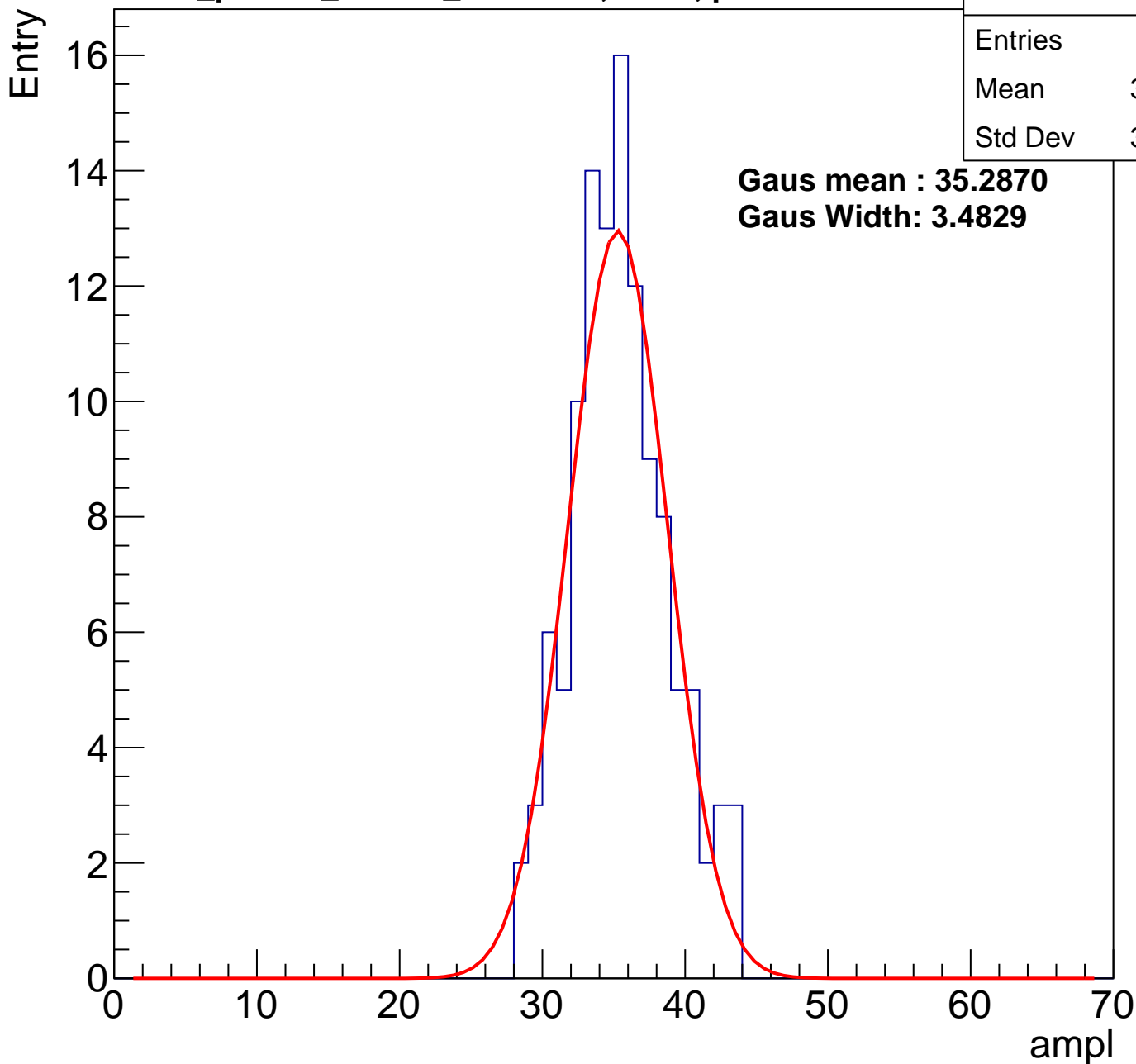
B0L101S, U20-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	35.03
Std Dev	3.387

Gaus mean : 35.2870

Gaus Width: 3.4829



B0L101S, U20-ch24, adc2

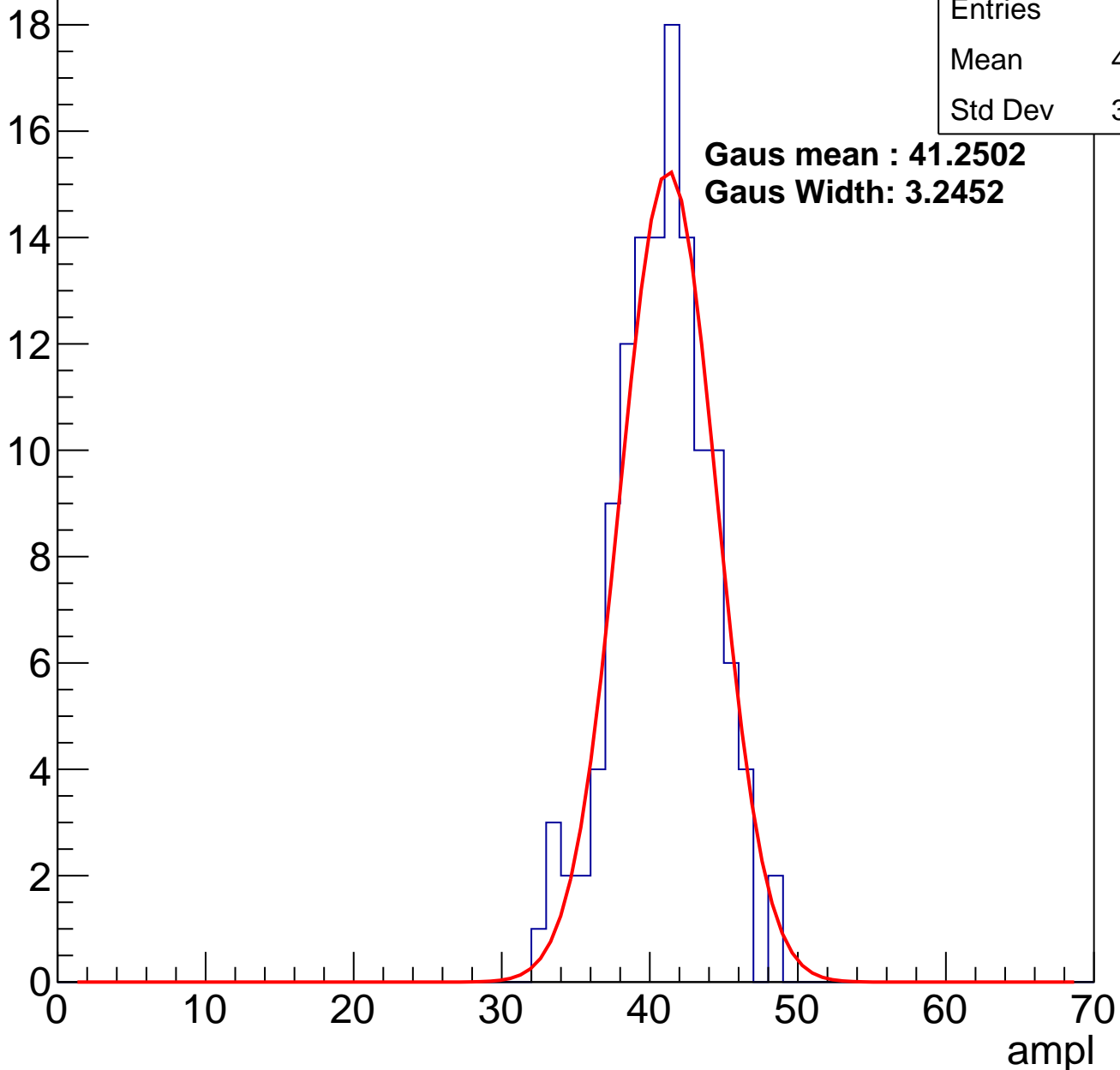
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	40.43
Std Dev	3.178

Gaus mean : 41.2502

Gaus Width: 3.2452

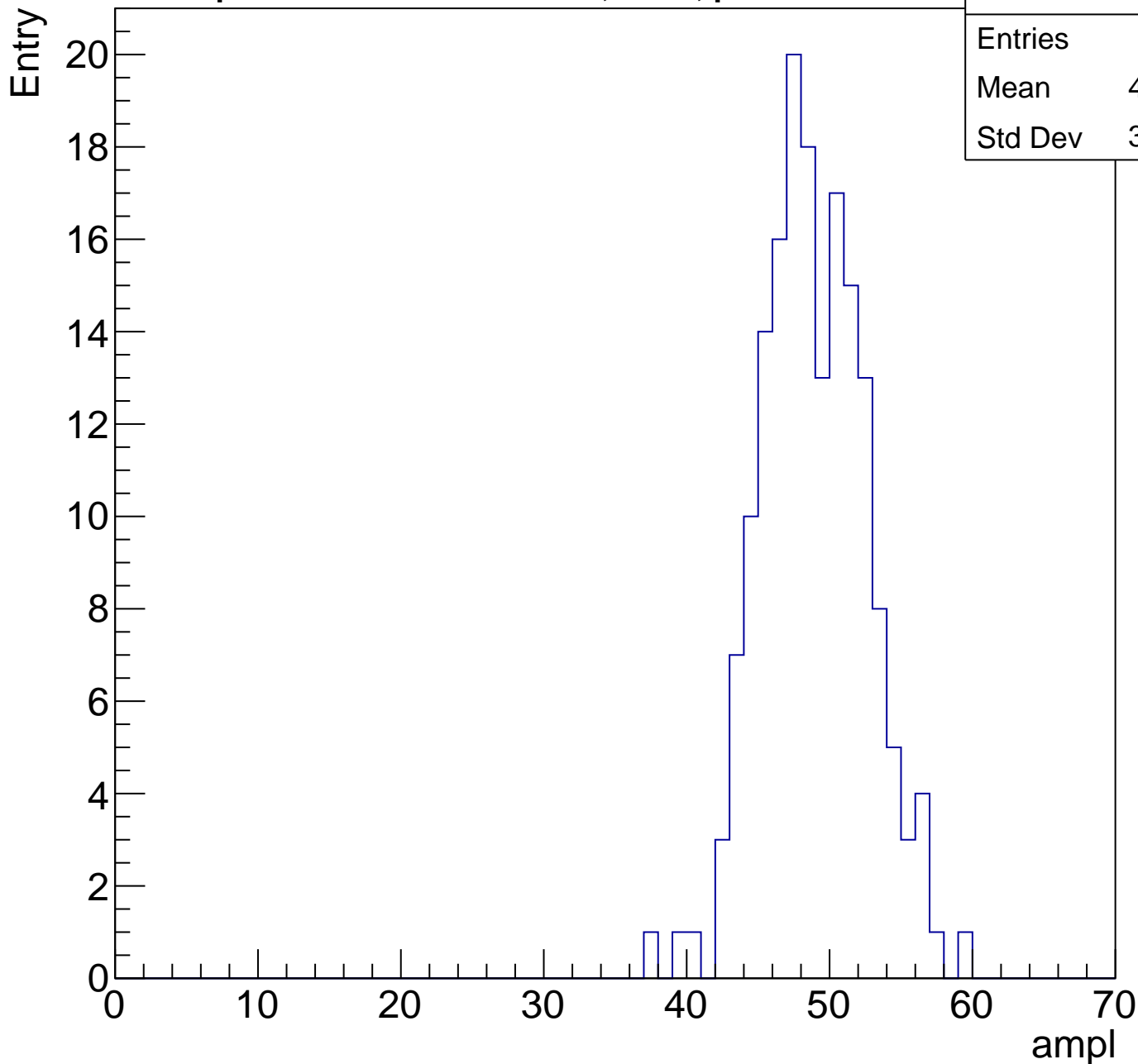
Entry



B0L101S, U20-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	48.42
Std Dev	3.666



B0L101S, U20-ch24, adc4

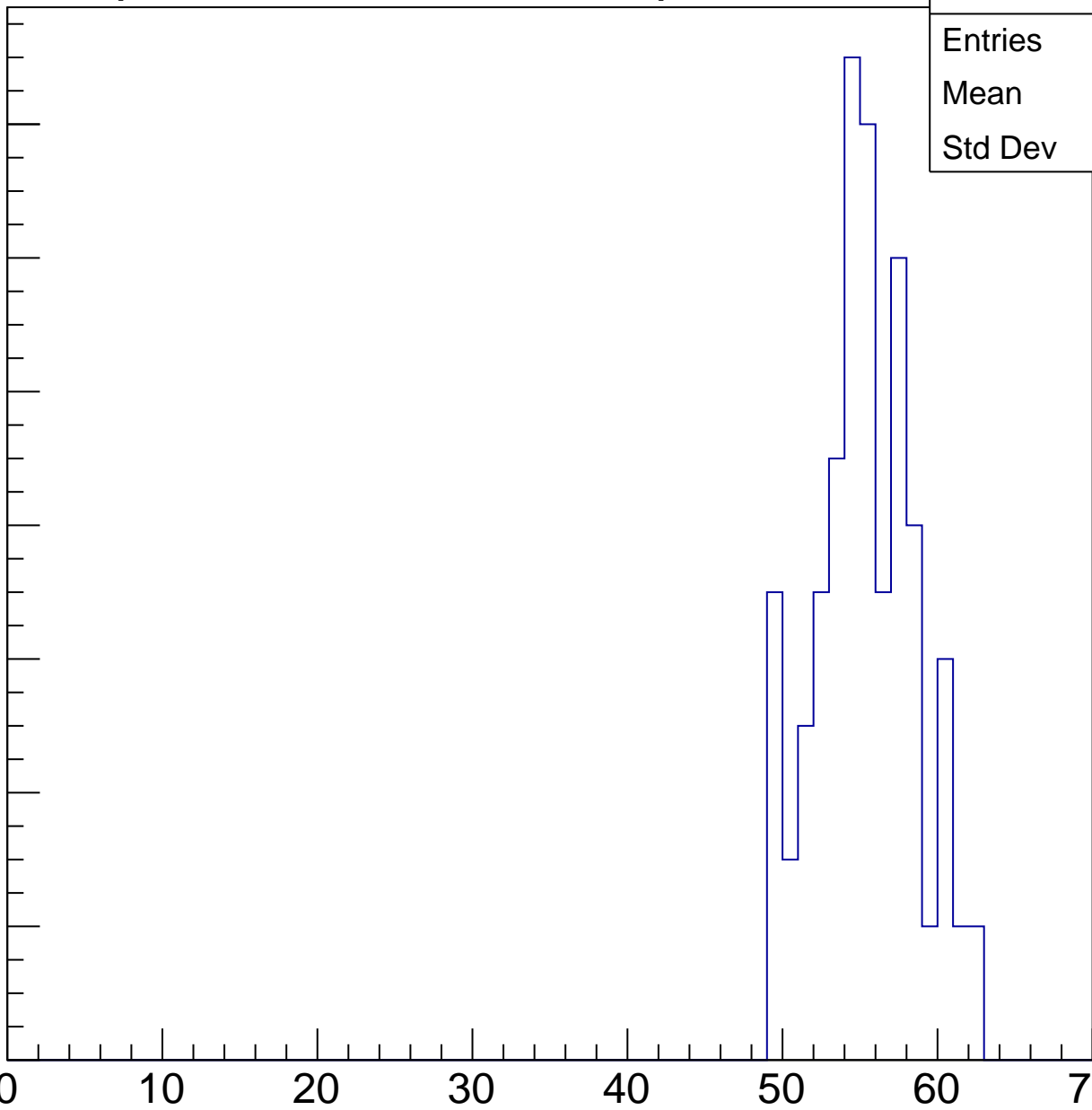
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	54.88
Std Dev	3.176

ampl



B0L101S, U20-ch24, adc5

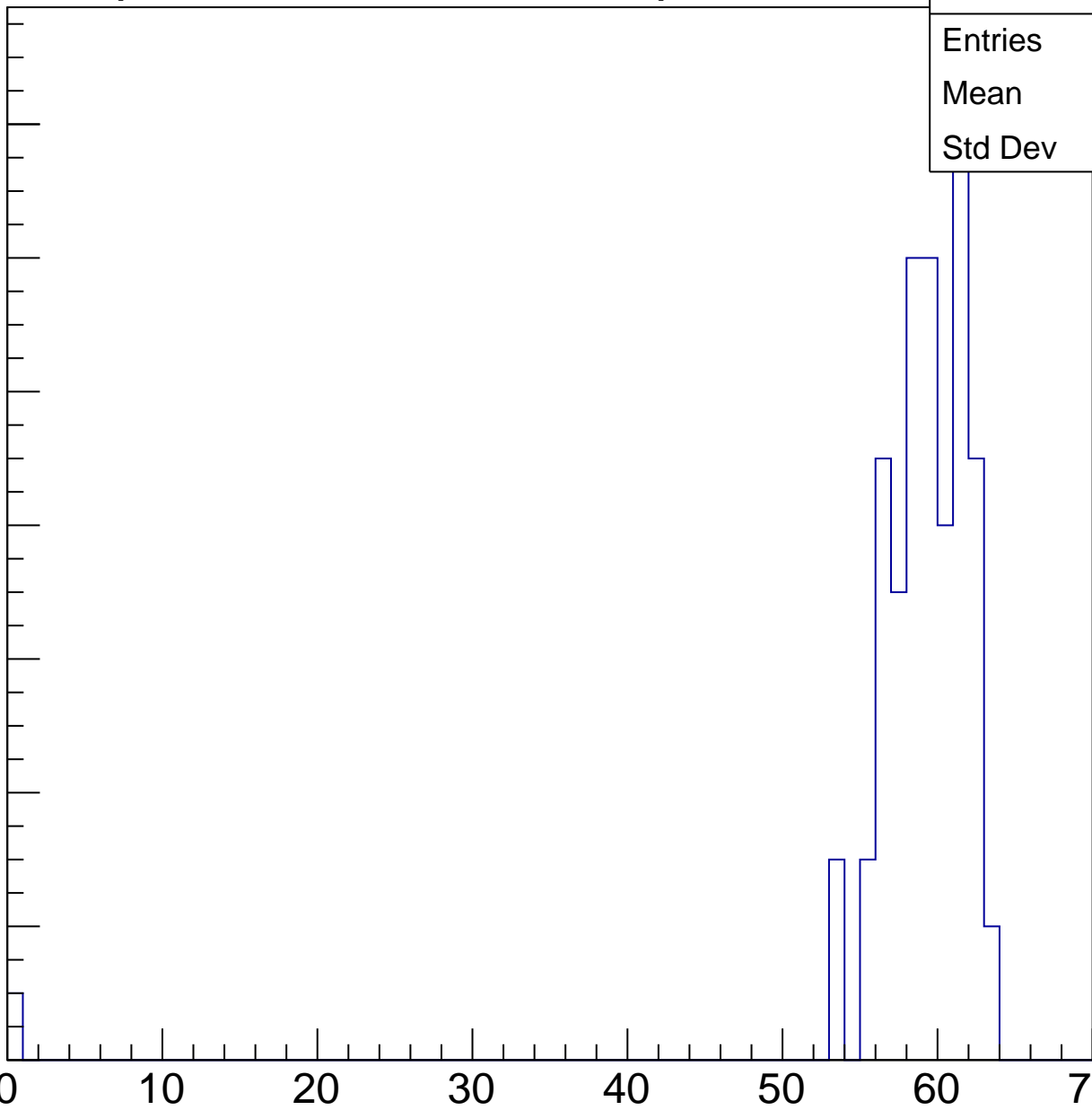
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.15
Std Dev	6.921

ampl



B0L101S, U20-ch24, adc6

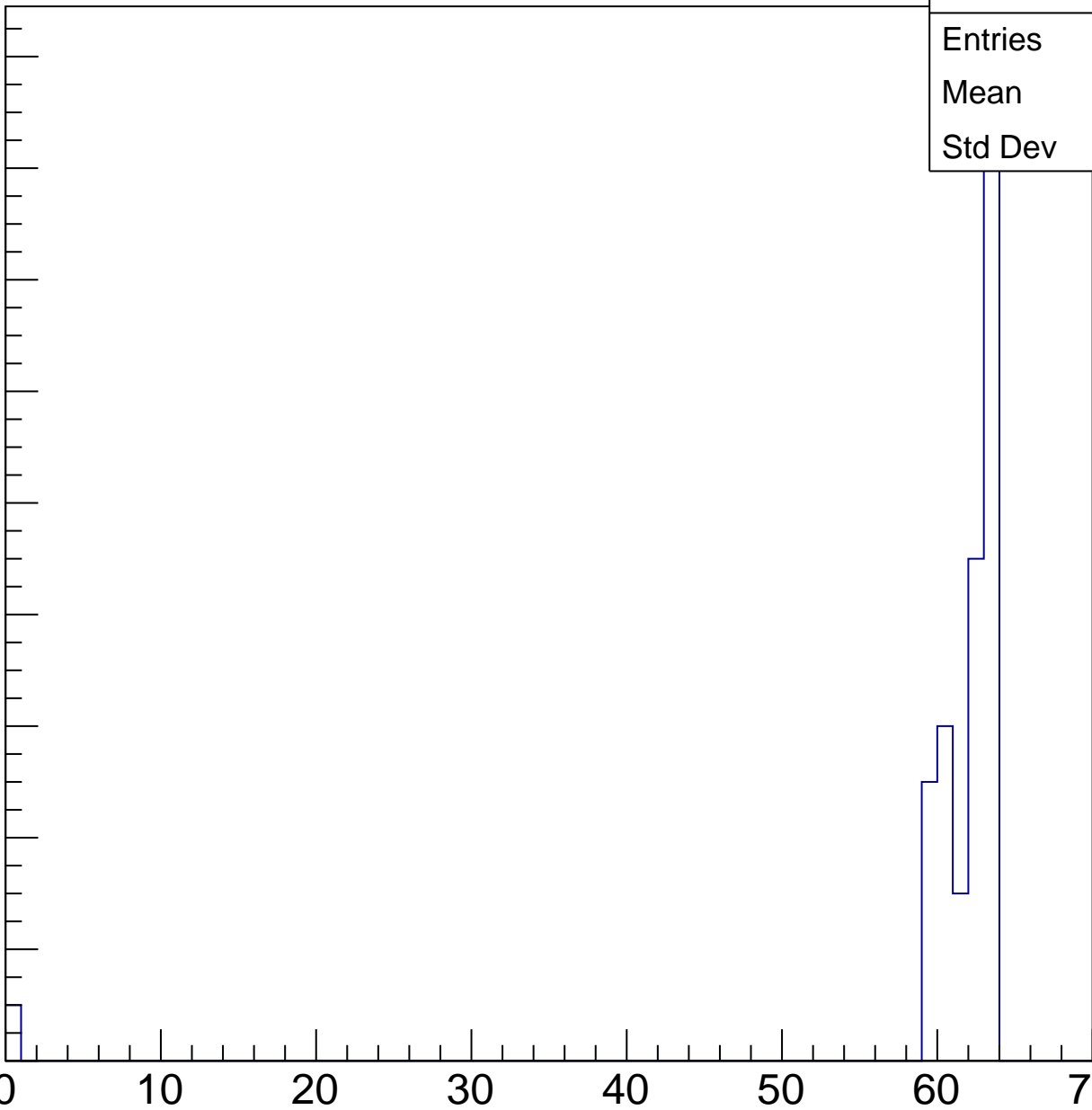
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	42
Mean	60.24
Std Dev	9.516

ampl



B0L101S, U20-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch25, adc0

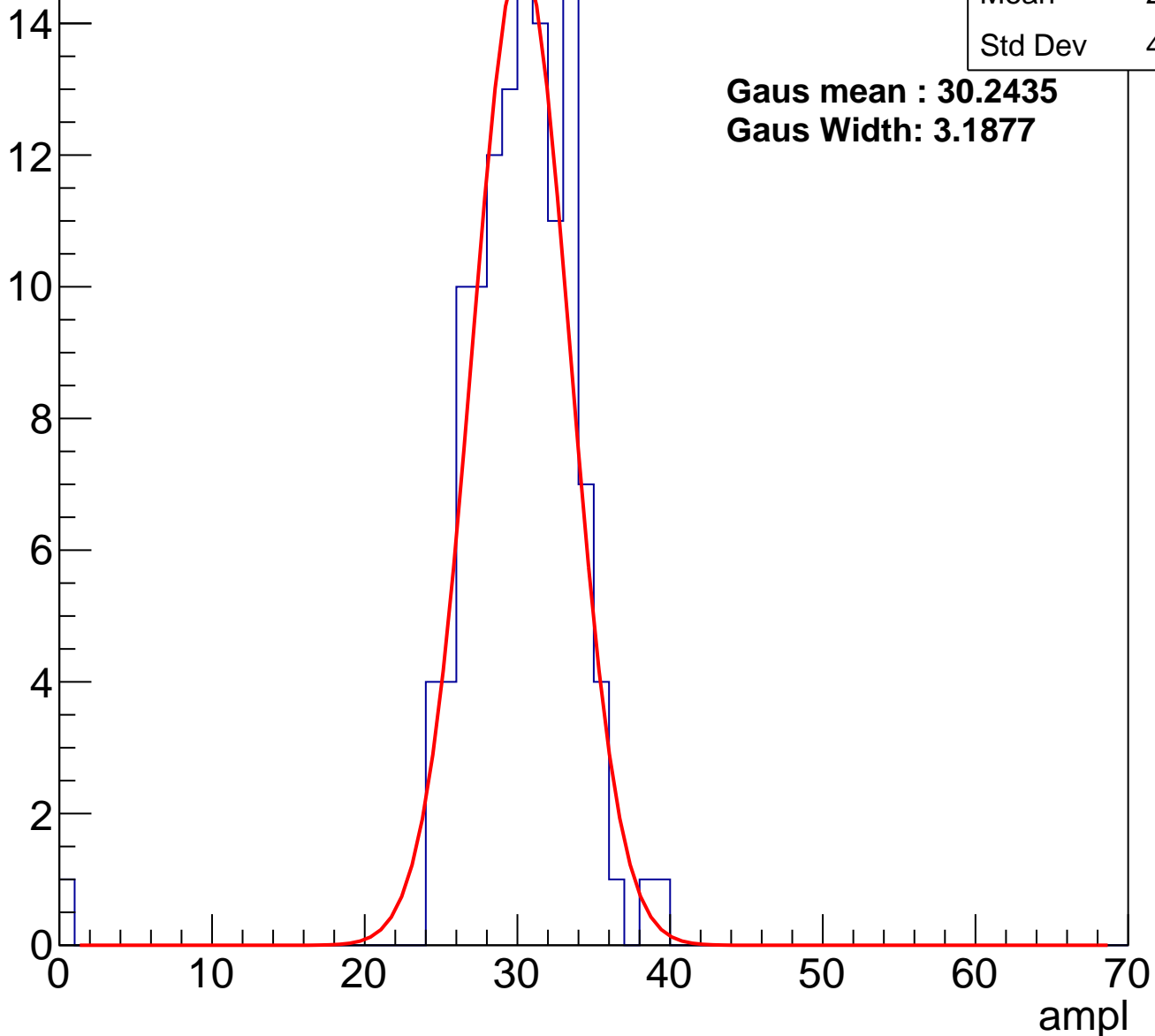
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	29.76
Std Dev	4.067

Gaus mean : 30.2435

Gaus Width: 3.1877

Entry



B0L101S, U20-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.17
Std Dev	4.872

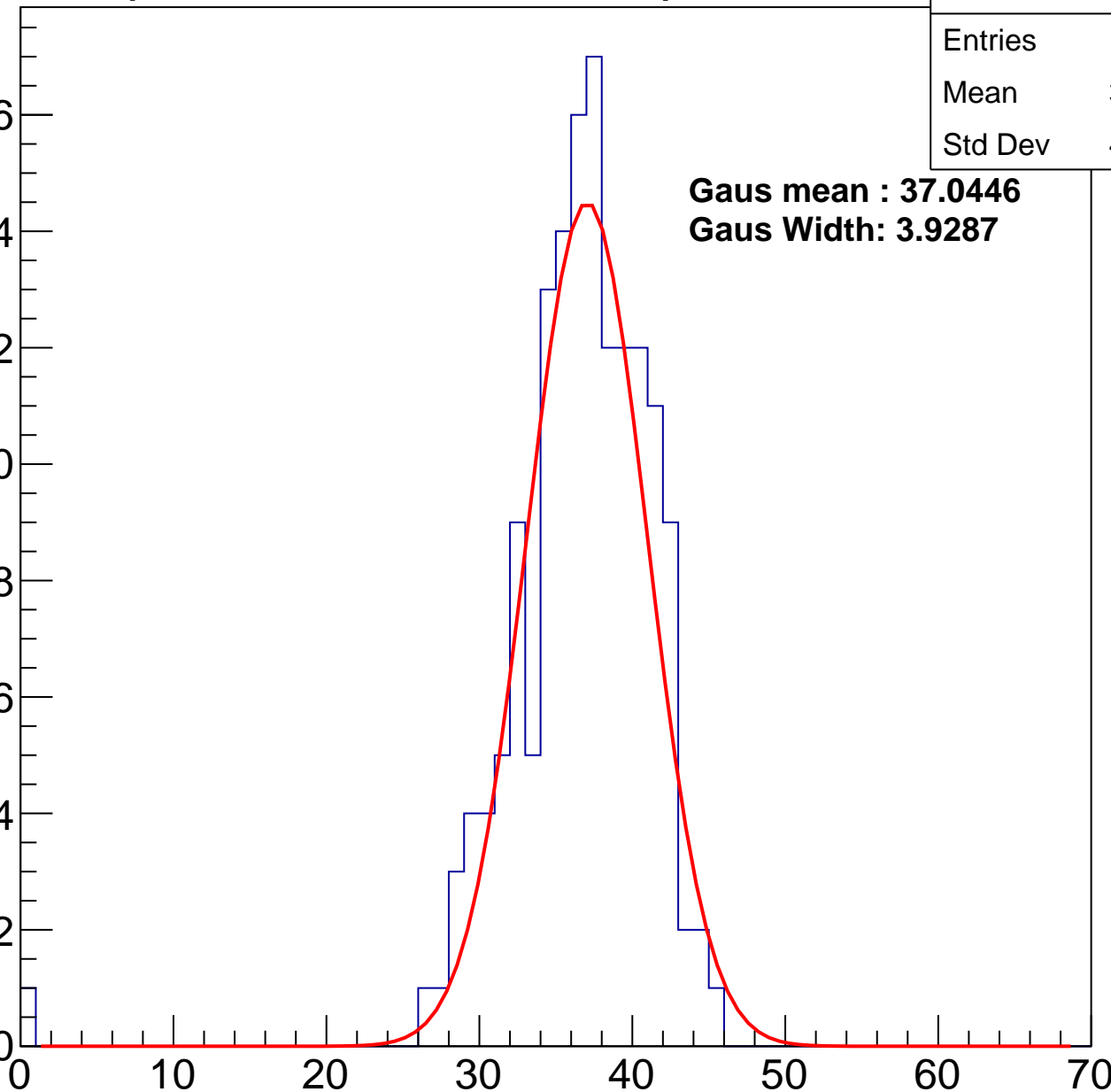
Gaus mean : 37.0446

Gaus Width: 3.9287

Entry

16
14
12
10
8
6
4
2
0

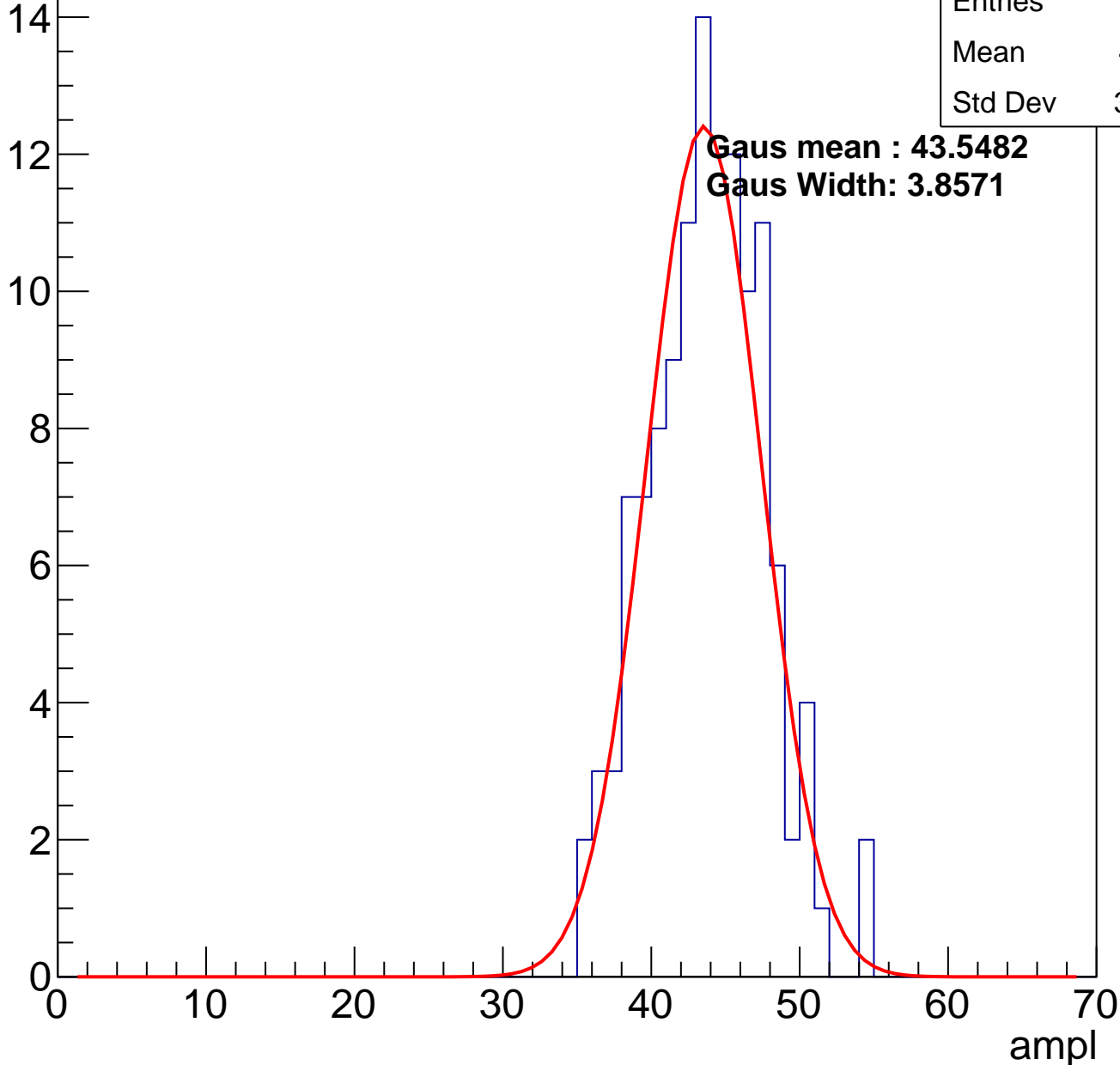
ampl



B0L101S, U20-ch25, adc2

calib_packv5_042523_0143.root, FC#1, port C1

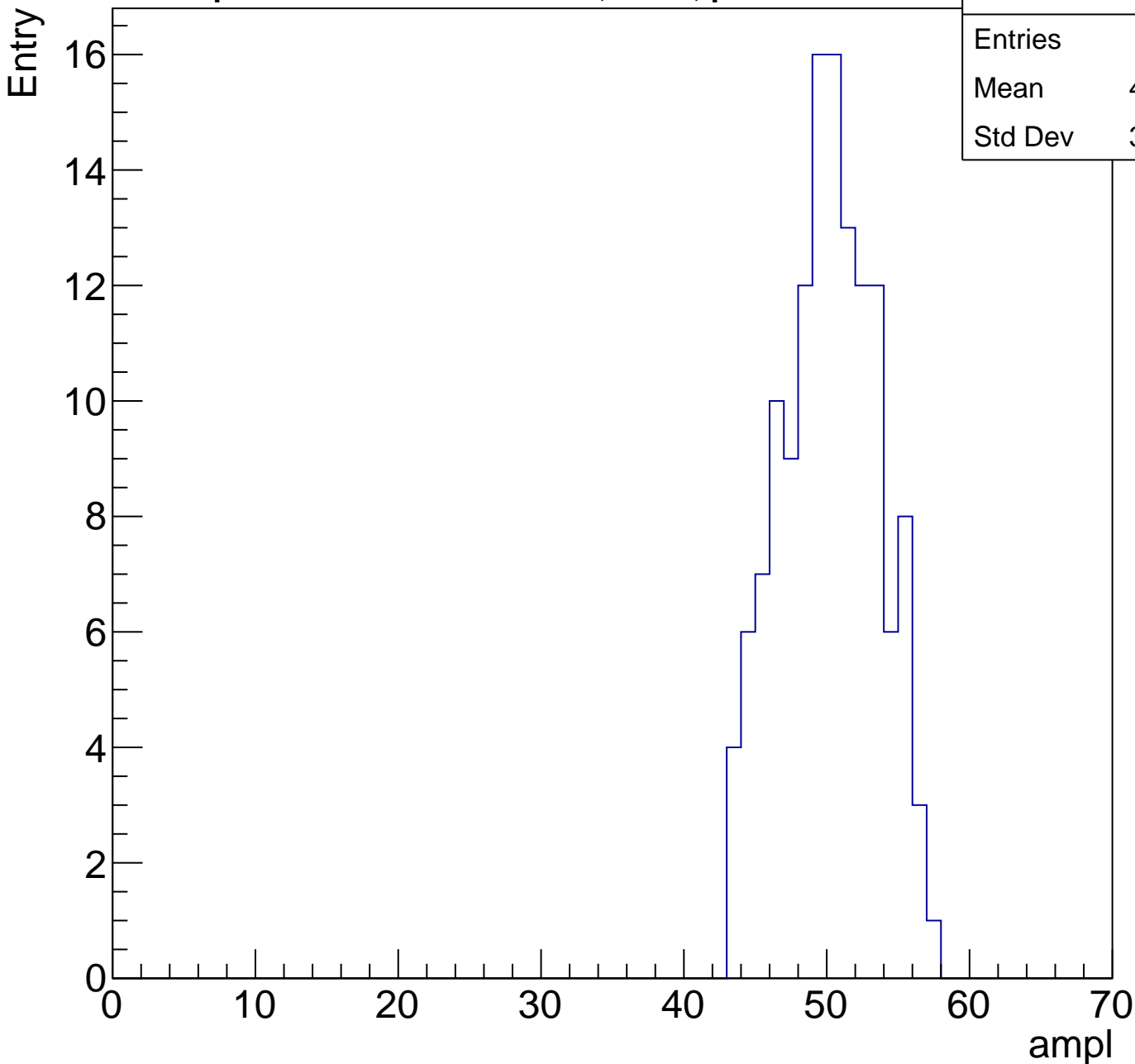
Entry



B0L101S, U20-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	49.67
Std Dev	3.333



B0L101S, U20-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

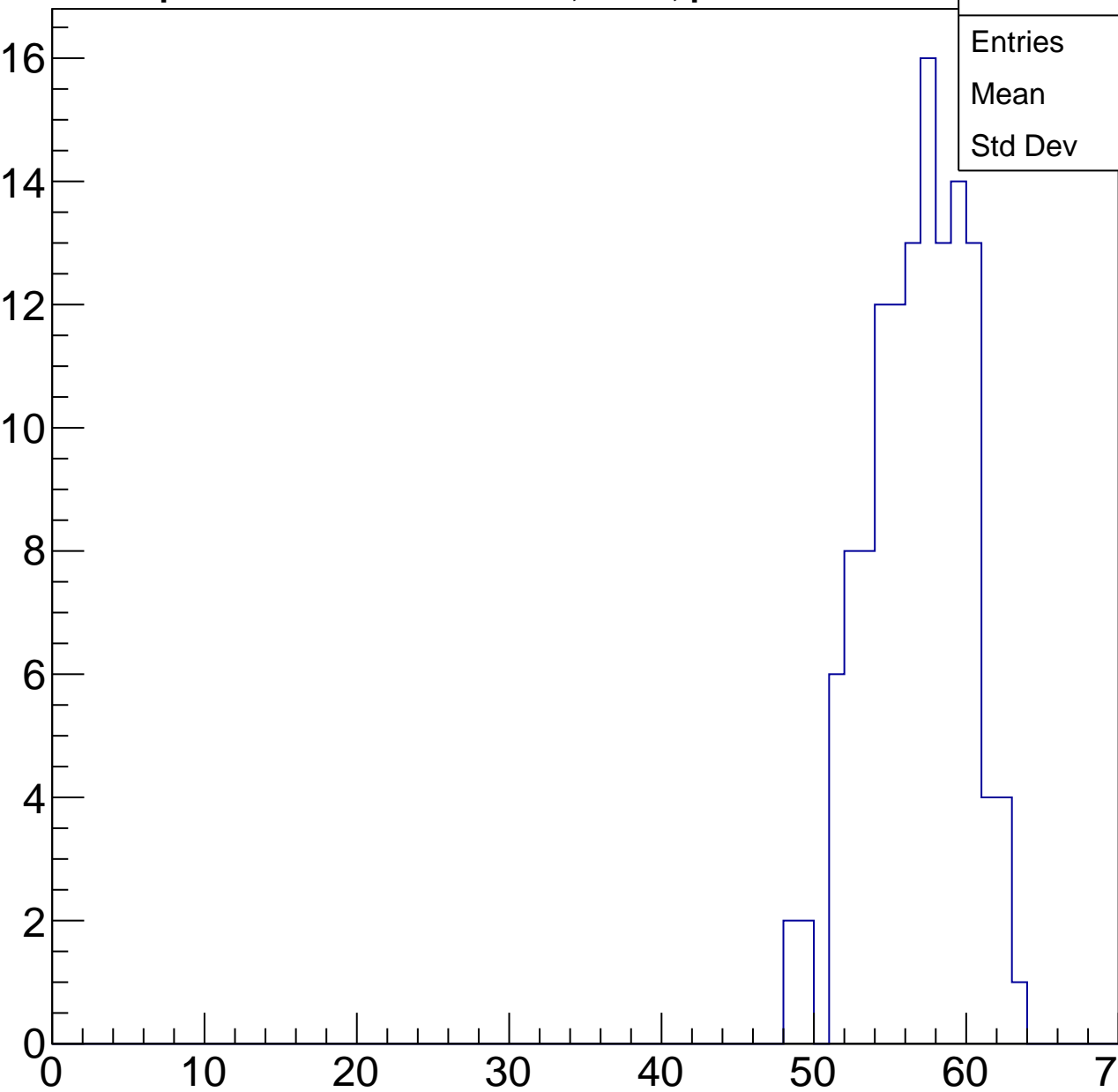
Entries	128
Mean	56.27
Std Dev	3.213

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch25, adc5

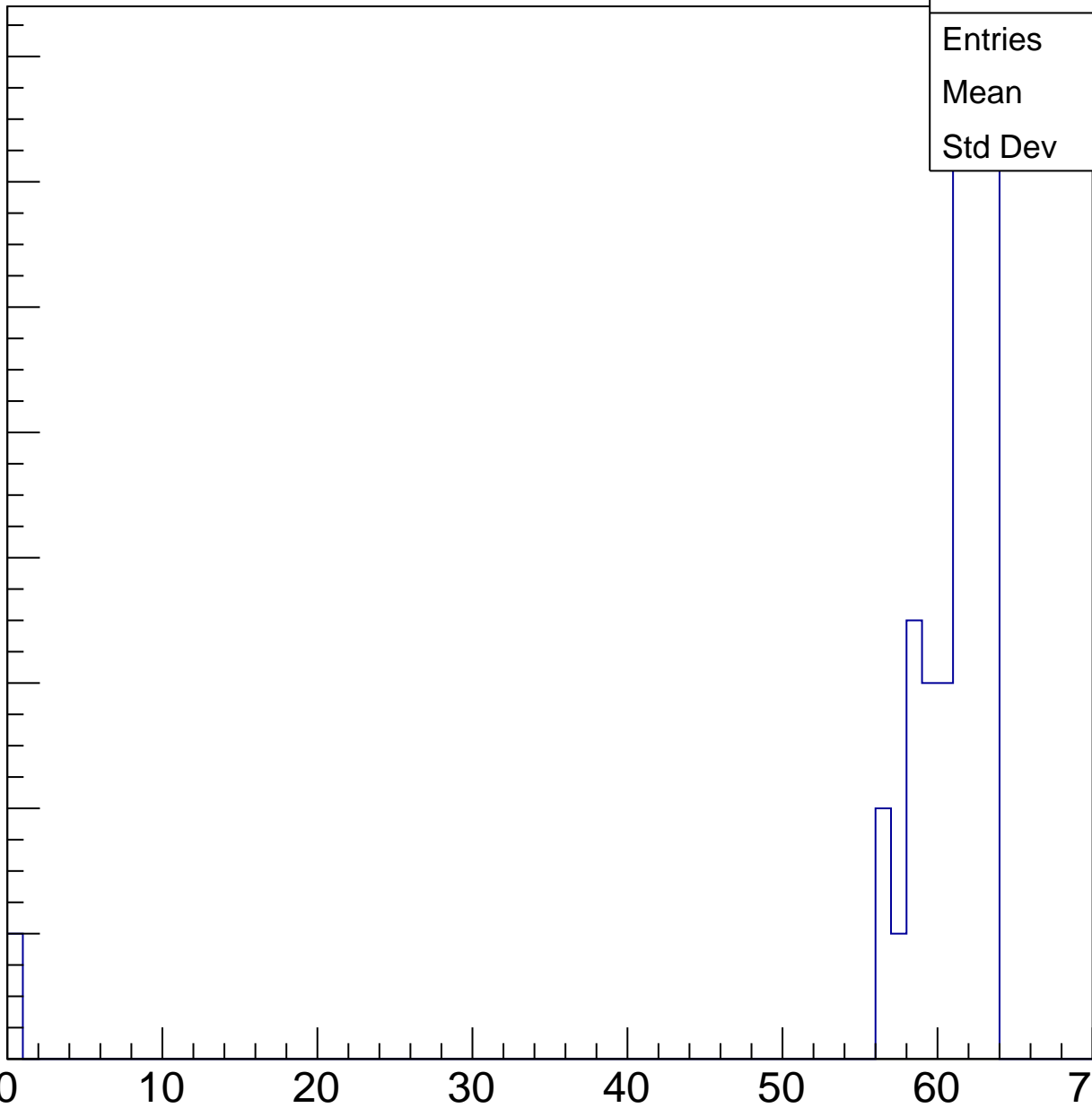
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.08
Std Dev	10.05

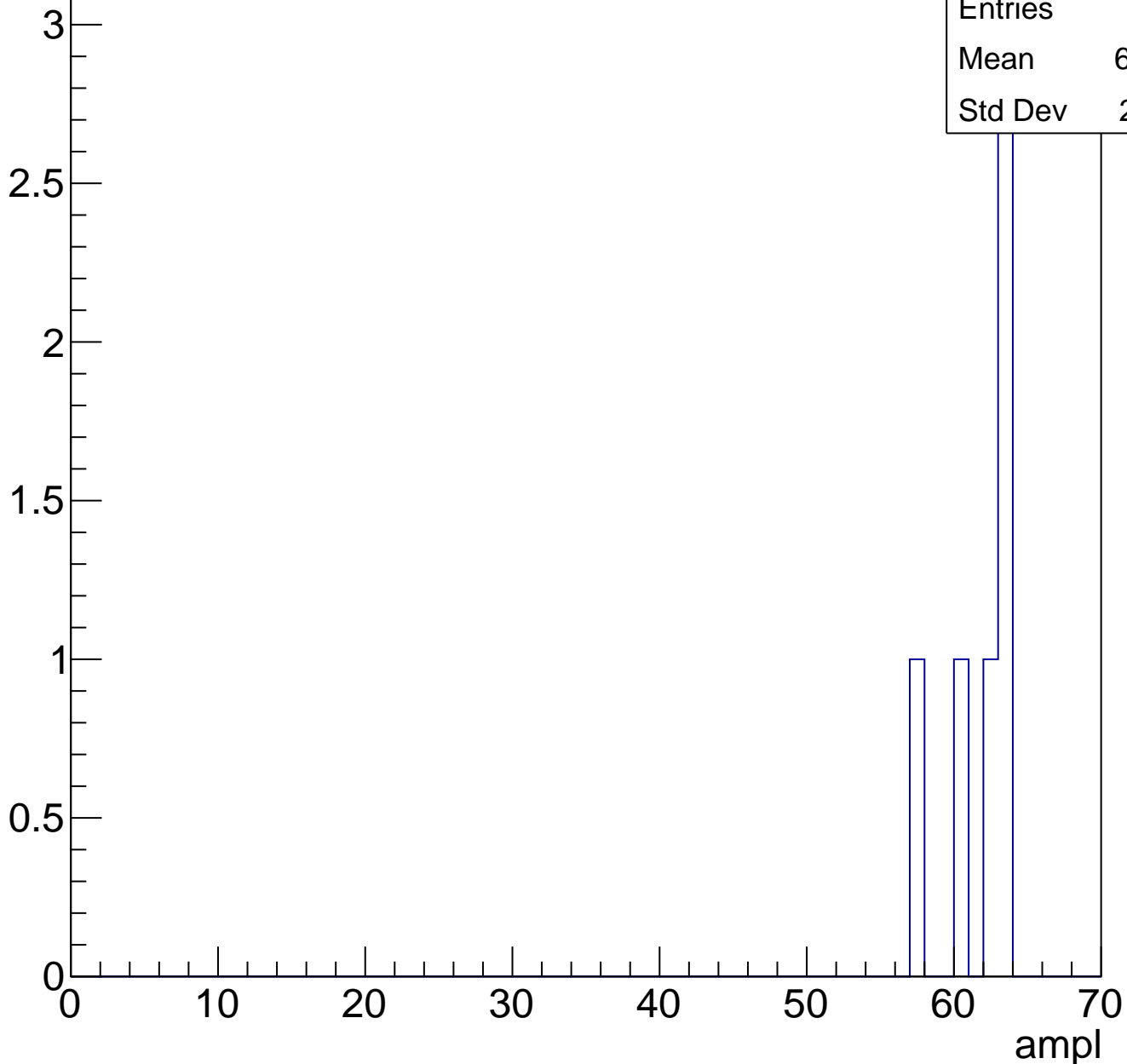
ampl



B0L101S, U20-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U20-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.25
Std Dev	4.178

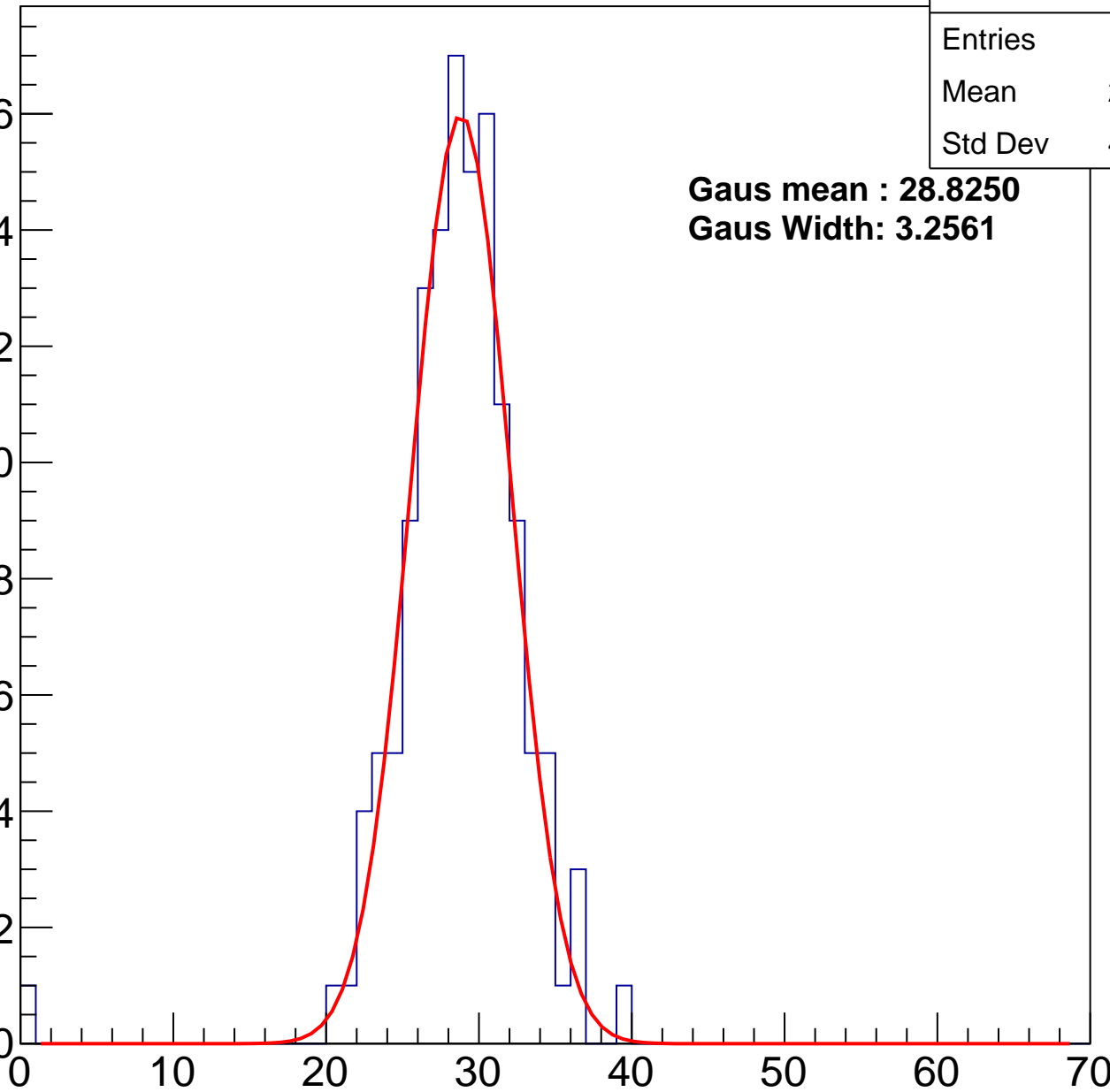
Gaus mean : 28.8250

Gaus Width: 3.2561

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch26, adc1

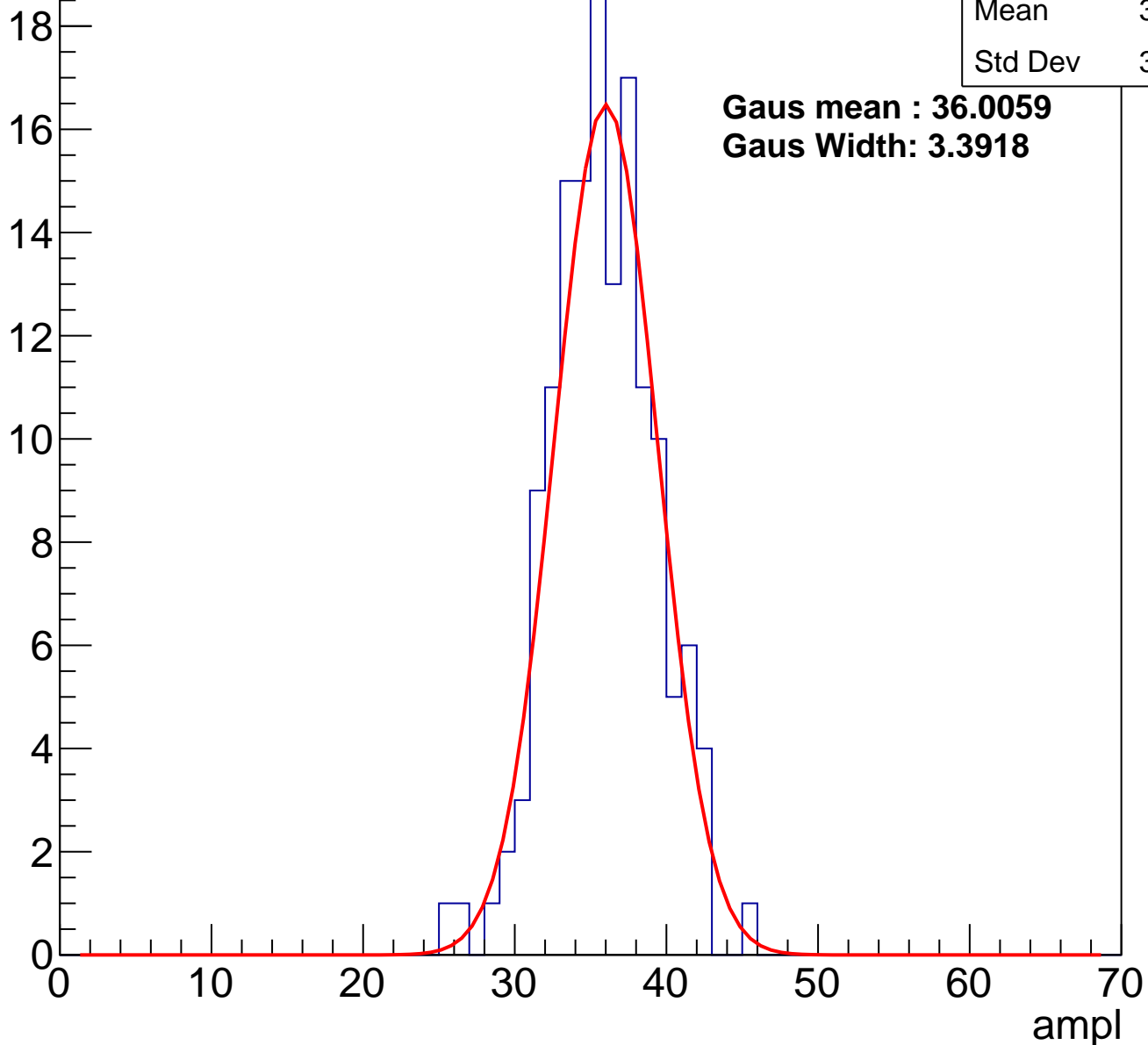
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.36
Std Dev	3.388

Gaus mean : 36.0059

Gaus Width: 3.3918

Entry



B0L101S, U20-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

42.55

Std Dev

3.033

Gaus mean : 43.1623

Gaus Width: 3.3101

0

10

20

30

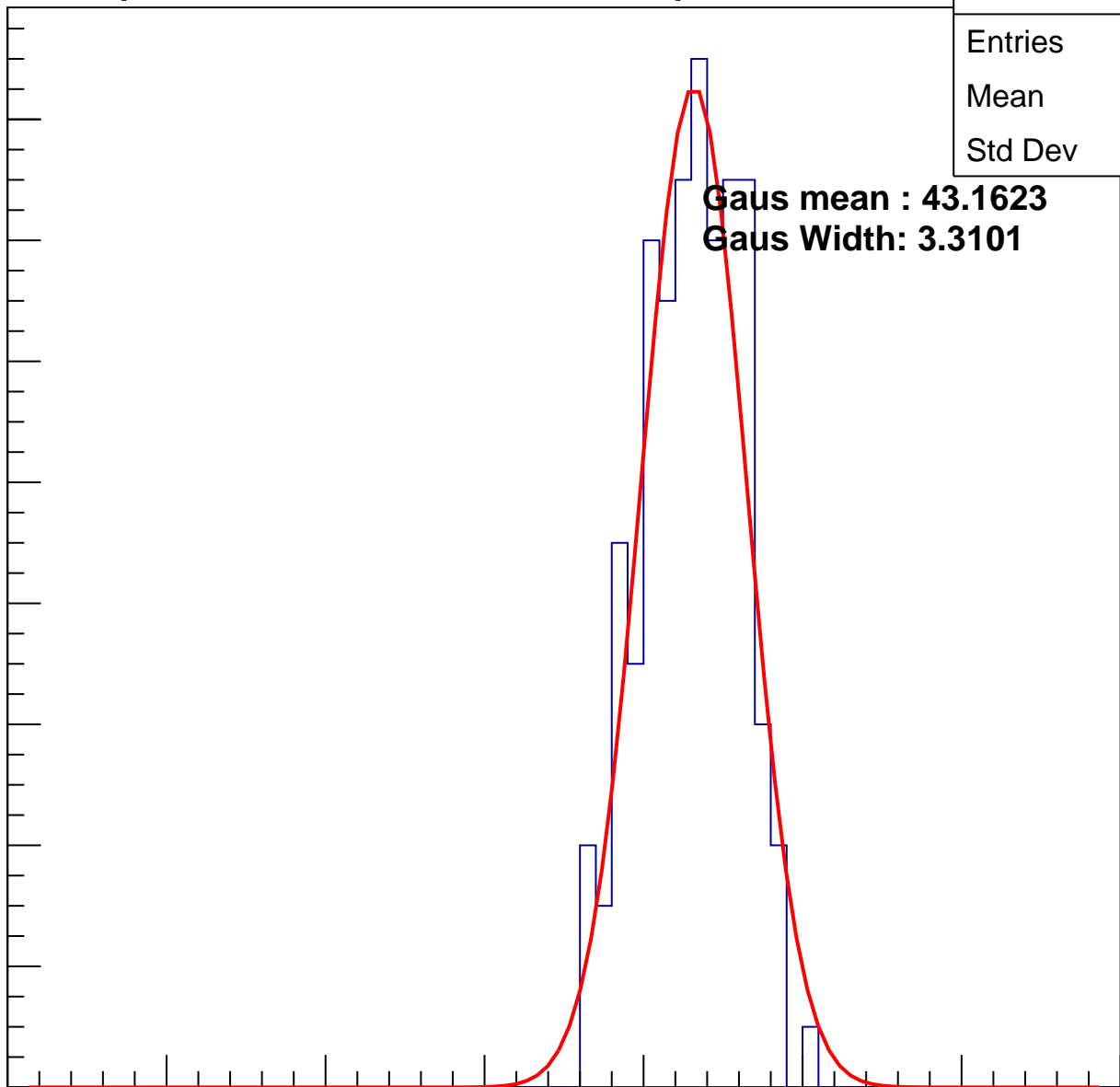
40

50

60

70

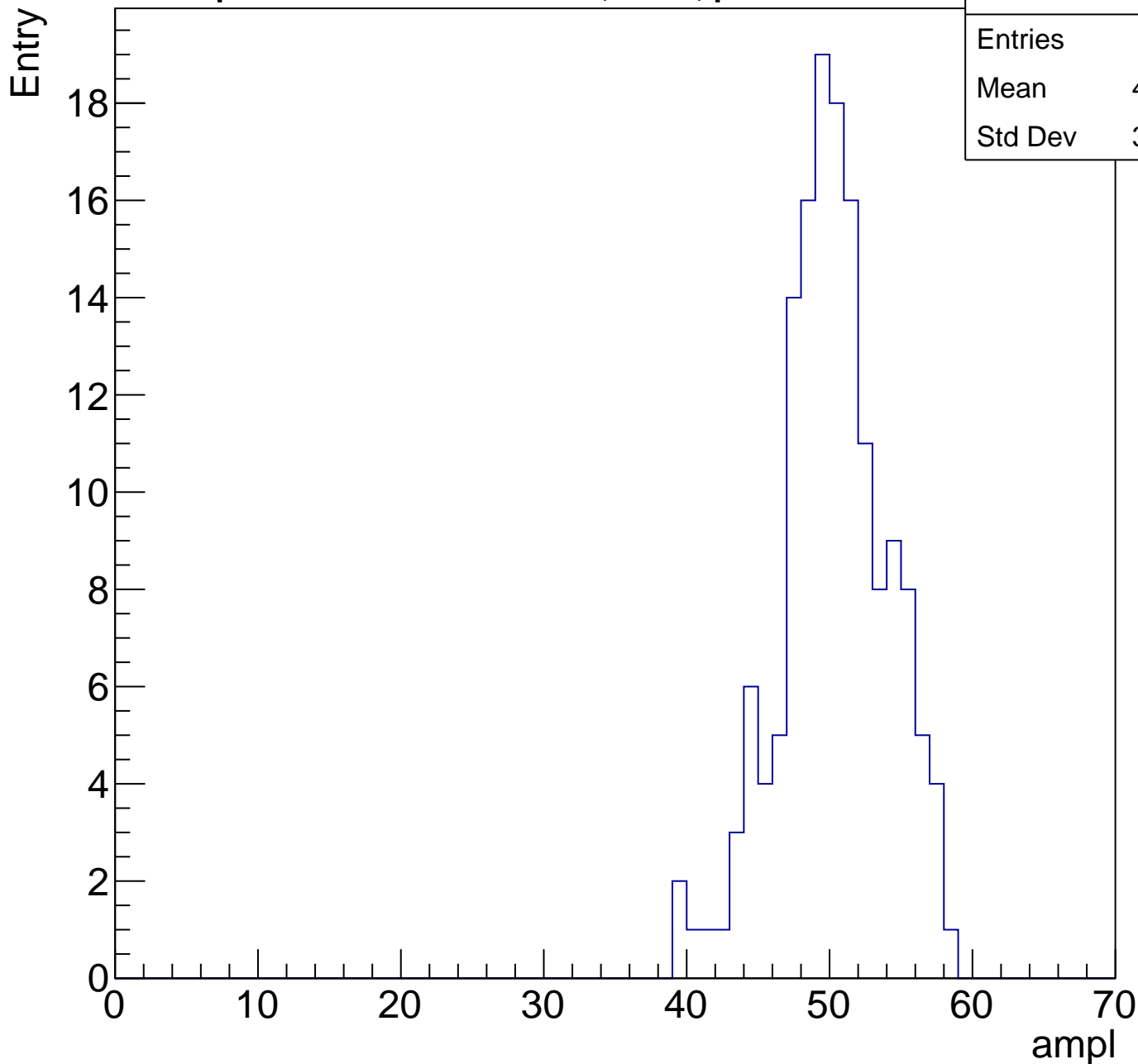
ampl



B0L101S, U20-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	49.77
Std Dev	3.758



B0L101S, U20-ch26, adc4

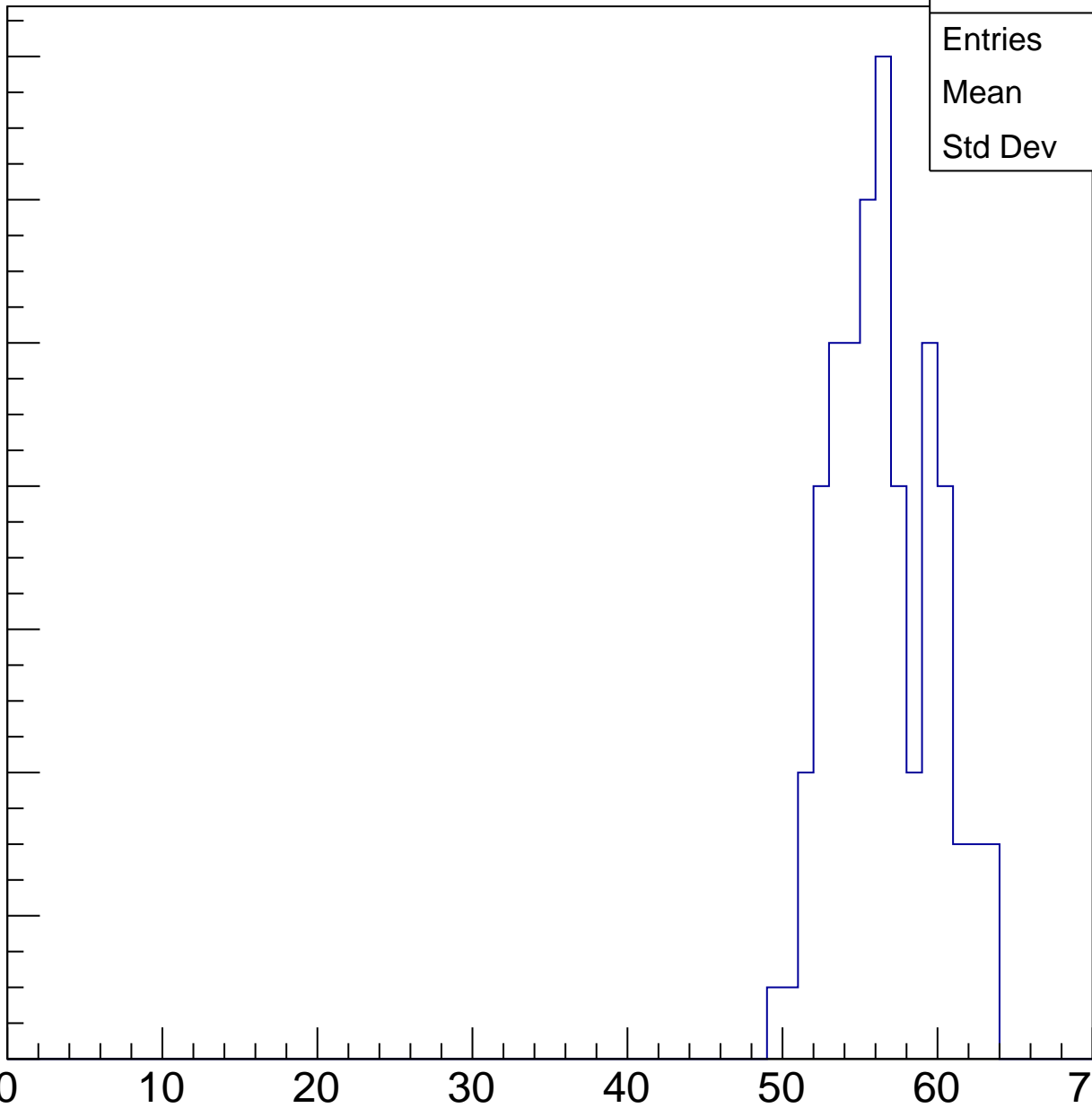
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	56.05
Std Dev	3.22

ampl



B0L101S, U20-ch26, adc5

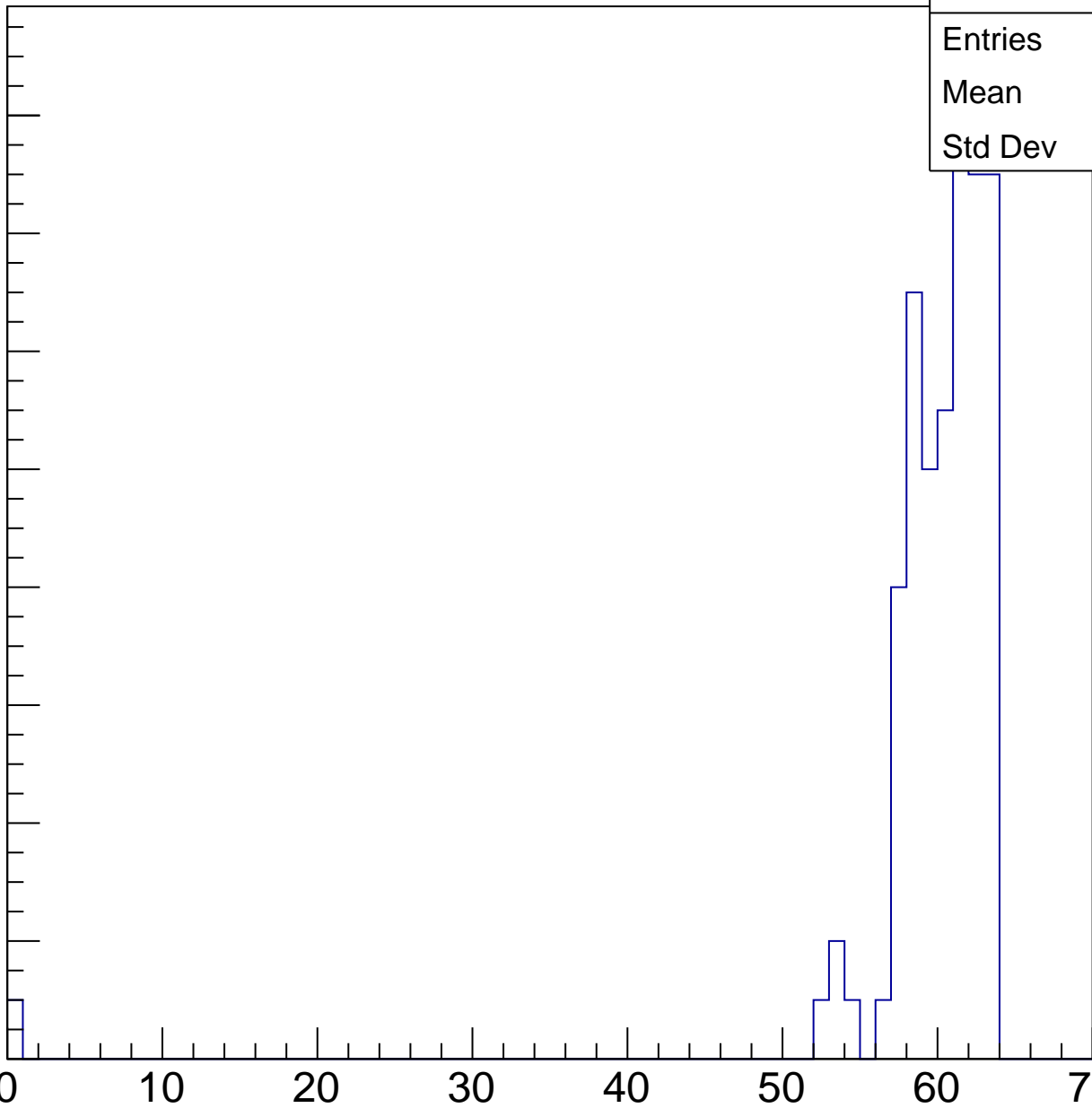
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.37
Std Dev	6.587

ampl



B0L101S, U20-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

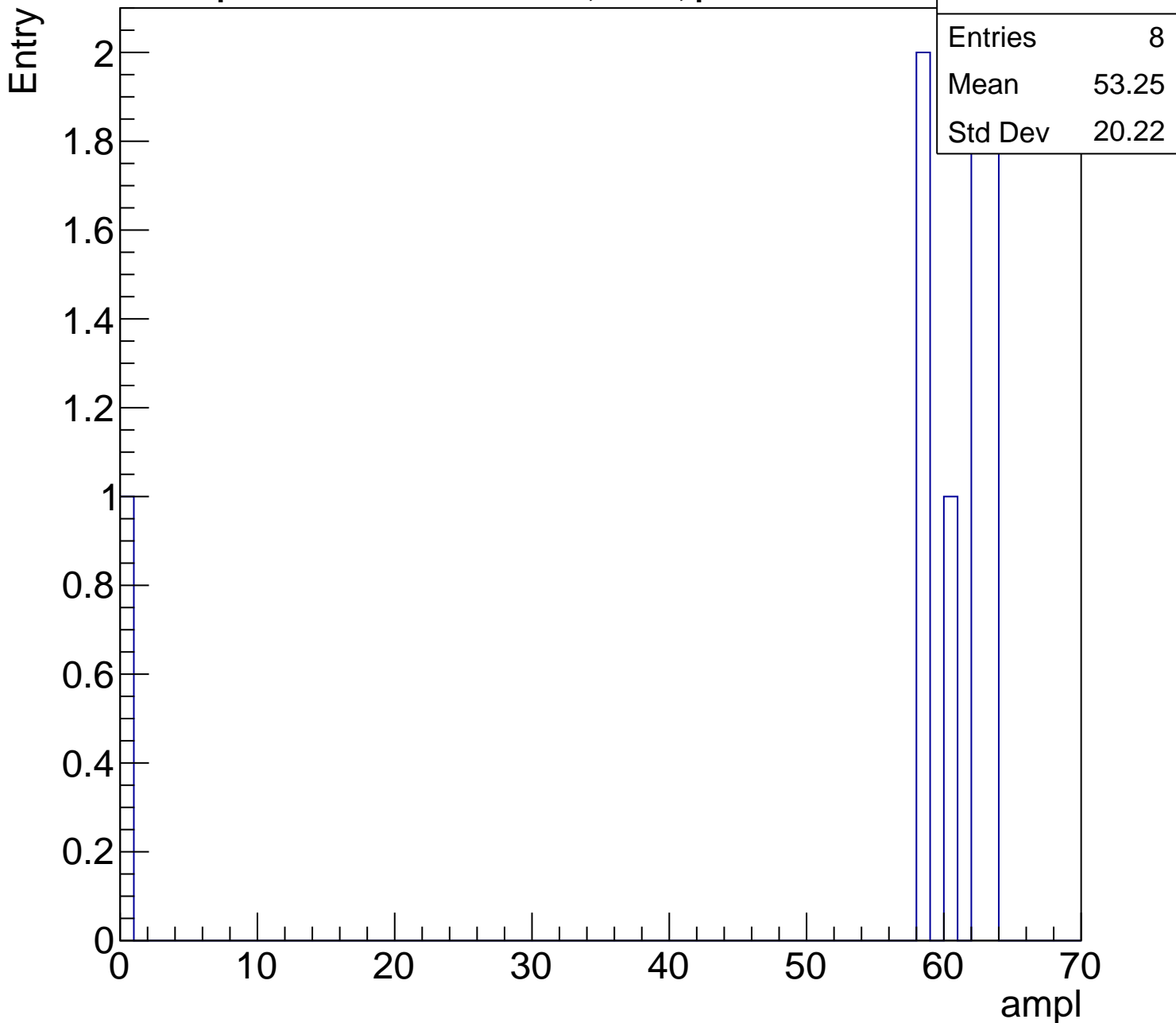
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.25
Std Dev	20.22

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

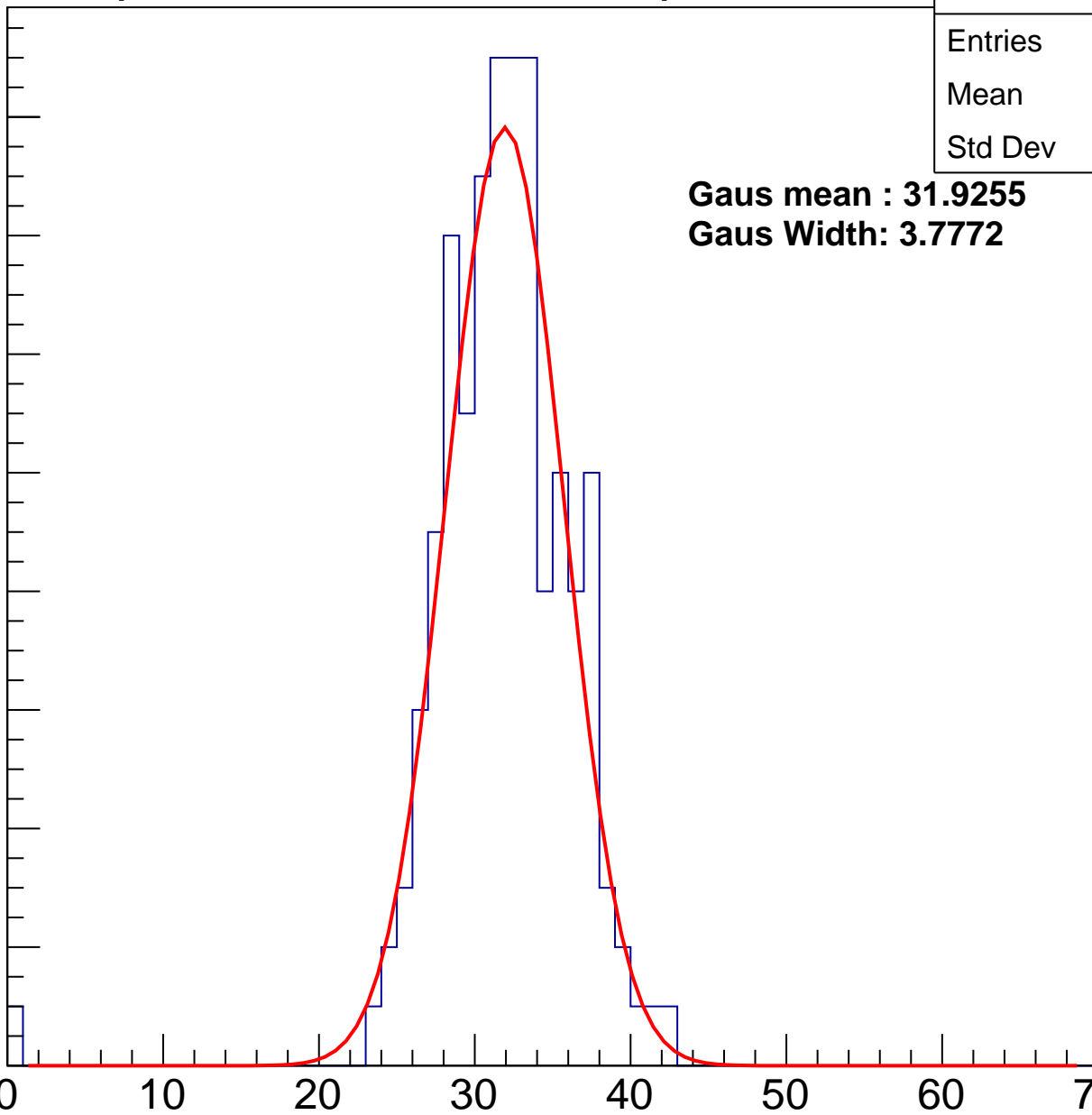
Entry

16
14
12
10
8
6
4
2
0

Entries	157
Mean	31.42
Std Dev	4.469

Gaus mean : 31.9255
Gaus Width: 3.7772

0 10 20 30 40 50 60 70
ampl



B0L101S, U20-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	38.2
Std Dev	3.39

Gaus mean : 38.8576

Gaus Width: 3.5491

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

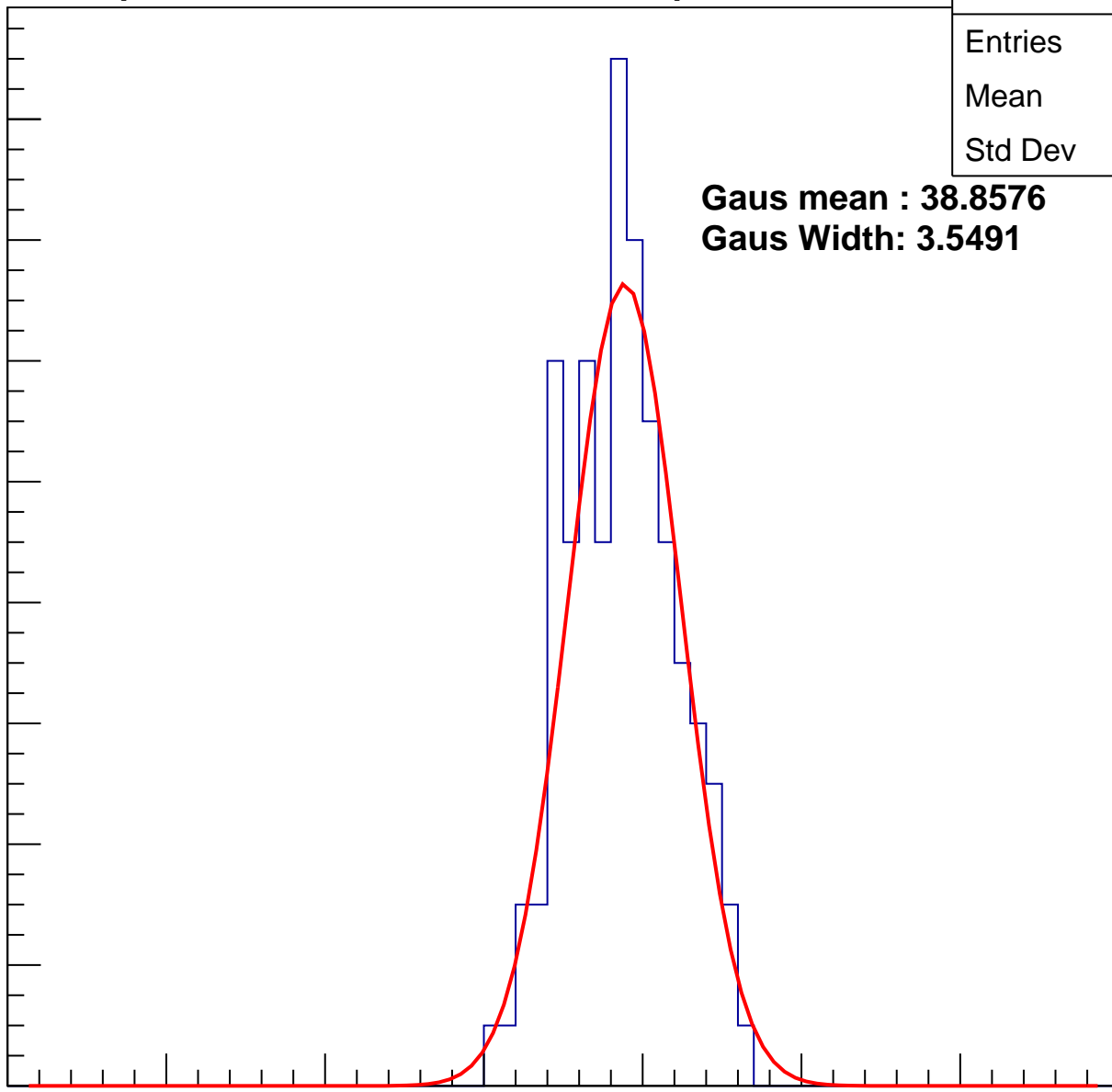
40

50

60

70

ampl



B0L101S, U20-ch27, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

159

Mean

45.47

Std Dev

3.62

Gaus mean : 45.9928

Gaus Width: 4.0294

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

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291

292

293

294

295

296

297

298

299

300

301

302

303

304

305

306

307

308

309

310

311

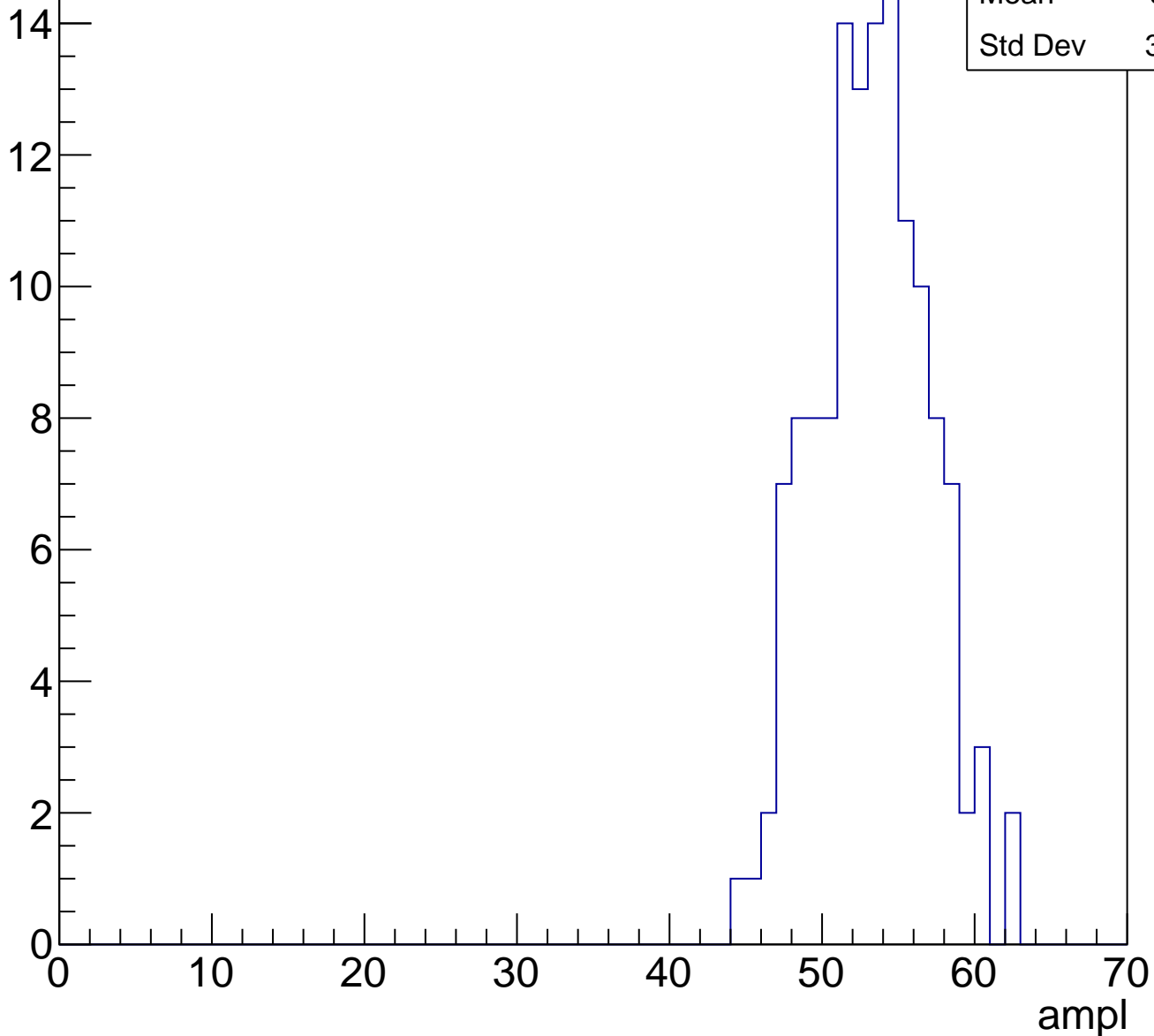
312

313

B0L101S, U20-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch27, adc4

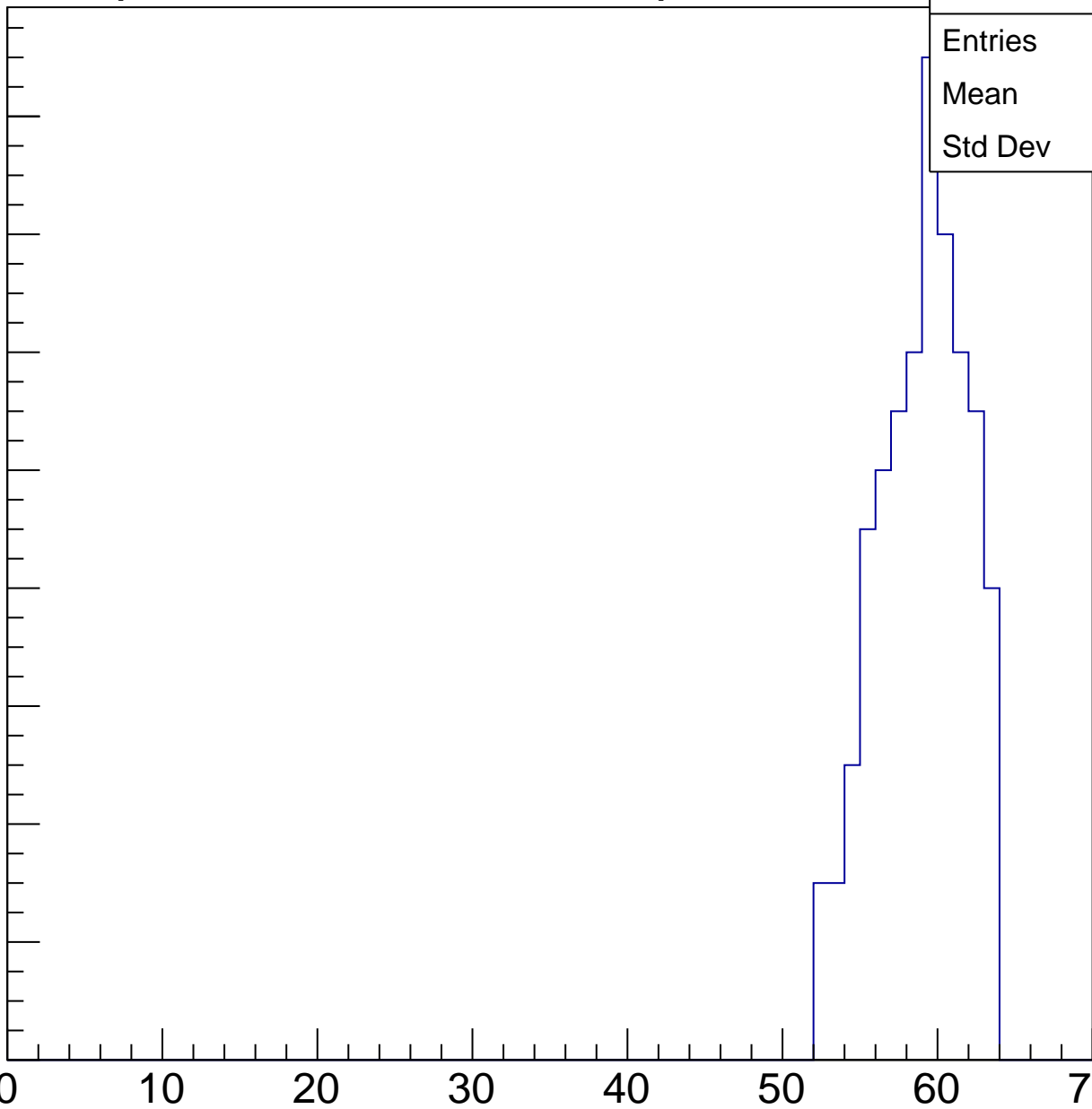
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	58.47
Std Dev	2.839

ampl



B0L101S, U20-ch27, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	35
Mean	58.03
Std Dev	14.37

2

4

6

8

10

12

B0L101S, U20-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L101S, U20-ch28, adc0

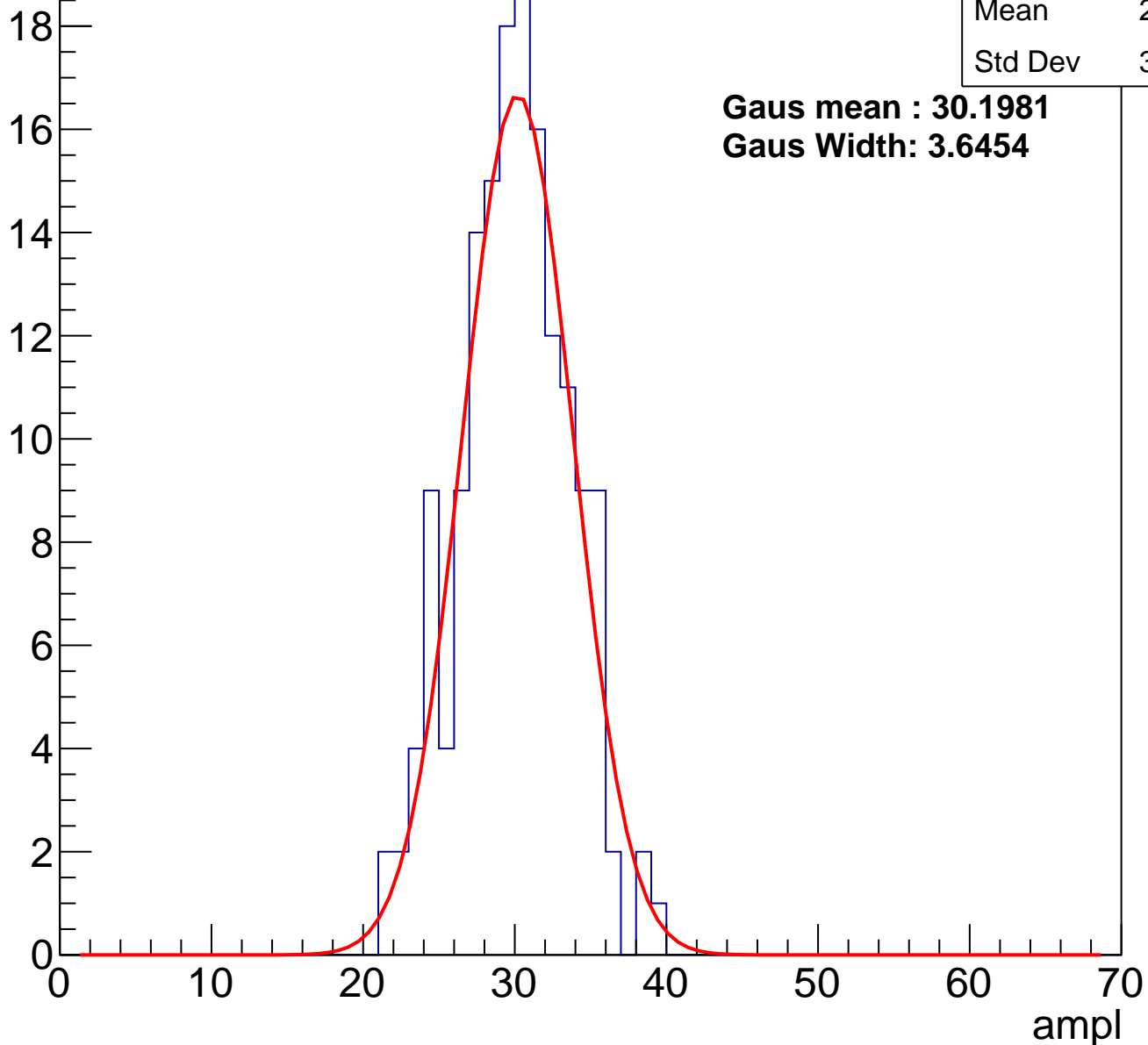
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	29.55
Std Dev	3.592

Gaus mean : 30.1981

Gaus Width: 3.6454

Entry



B0L101S, U20-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	36.55
Std Dev	3.035

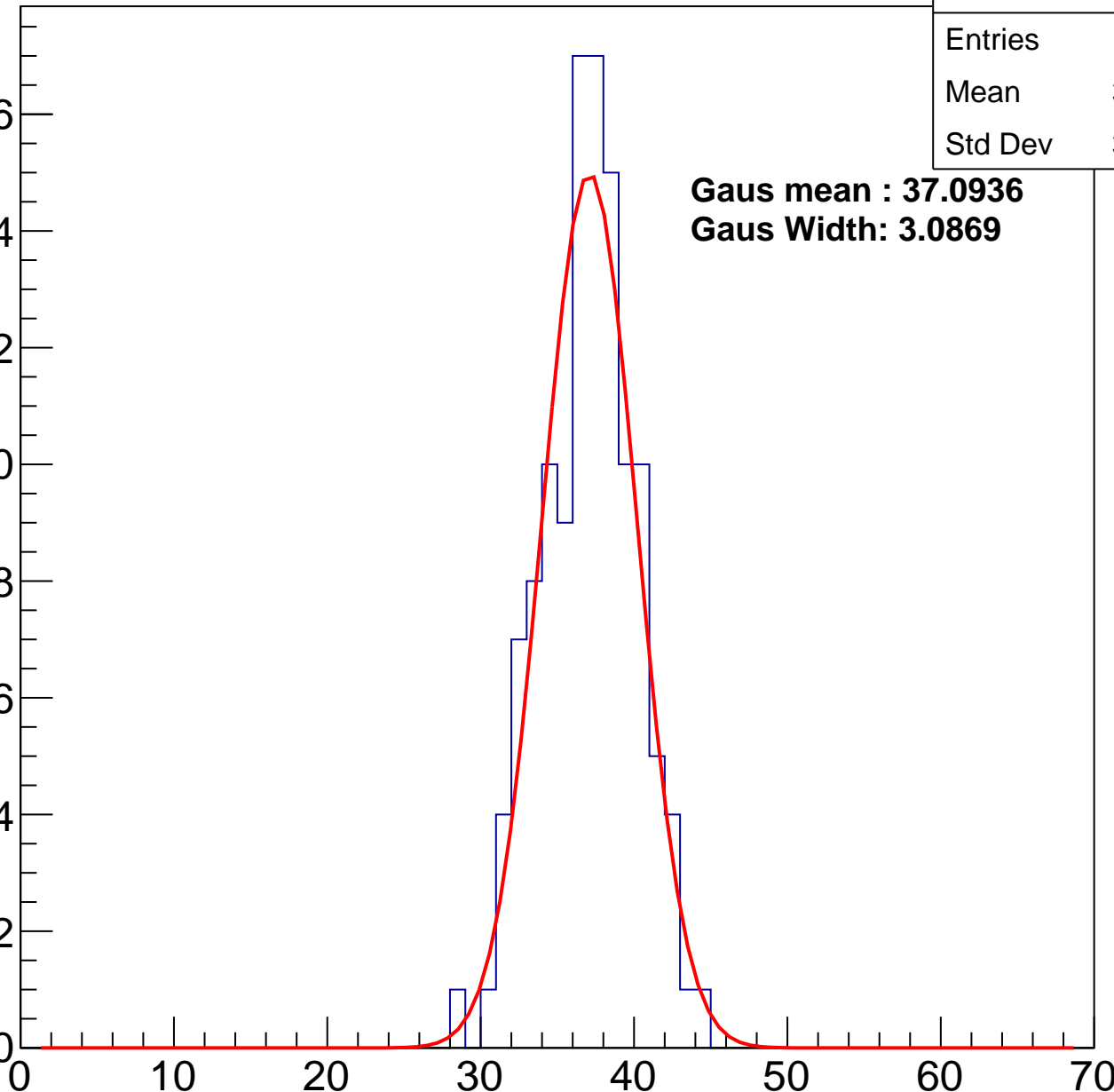
Gaus mean : 37.0936

Gaus Width: 3.0869

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch28, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

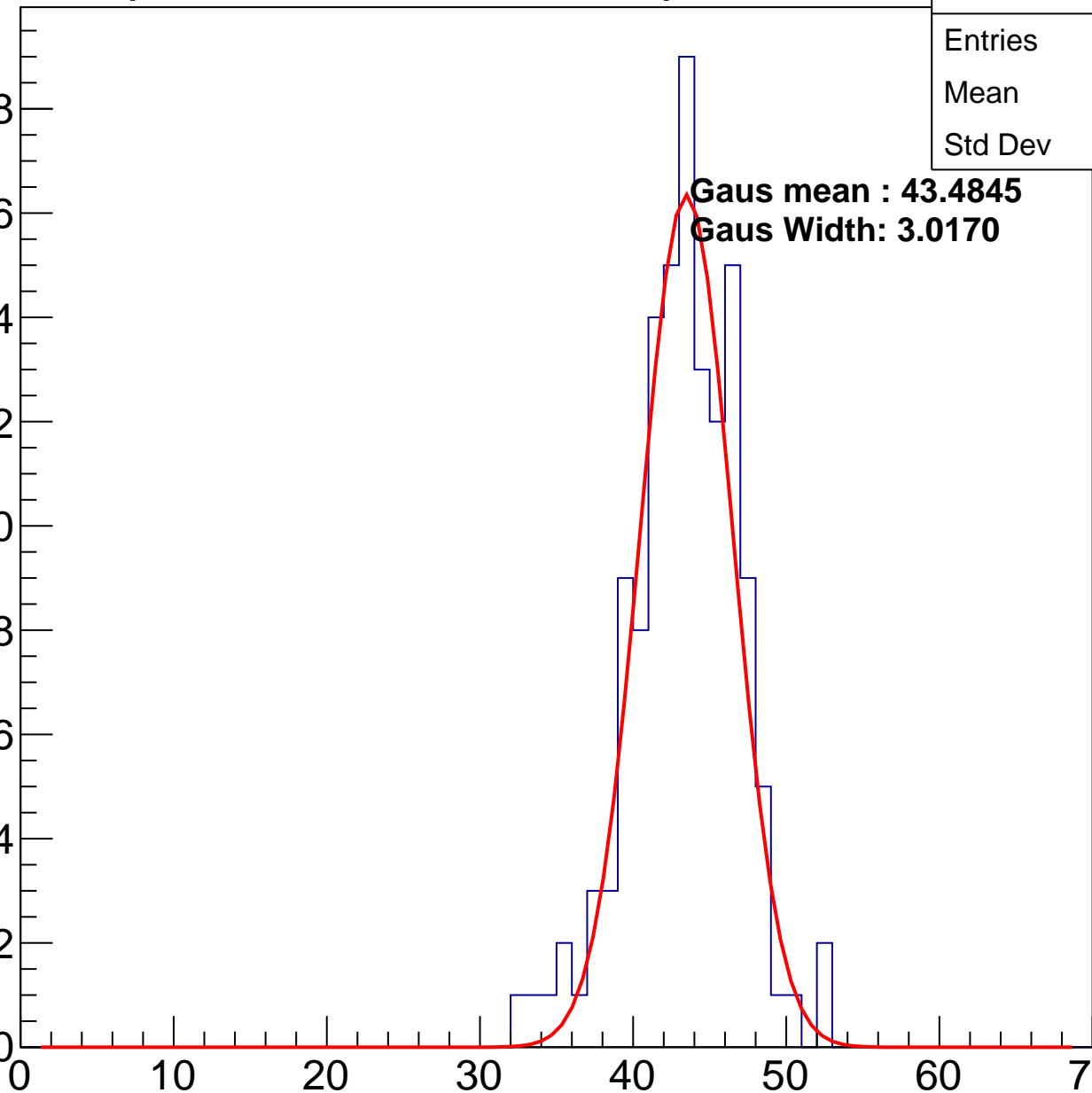
18
16
14
12
10
8
6
4
2
0

Entries	135
Mean	42.89
Std Dev	3.506

Gaus mean : 43.4845

Gaus Width: 3.0170

ampl

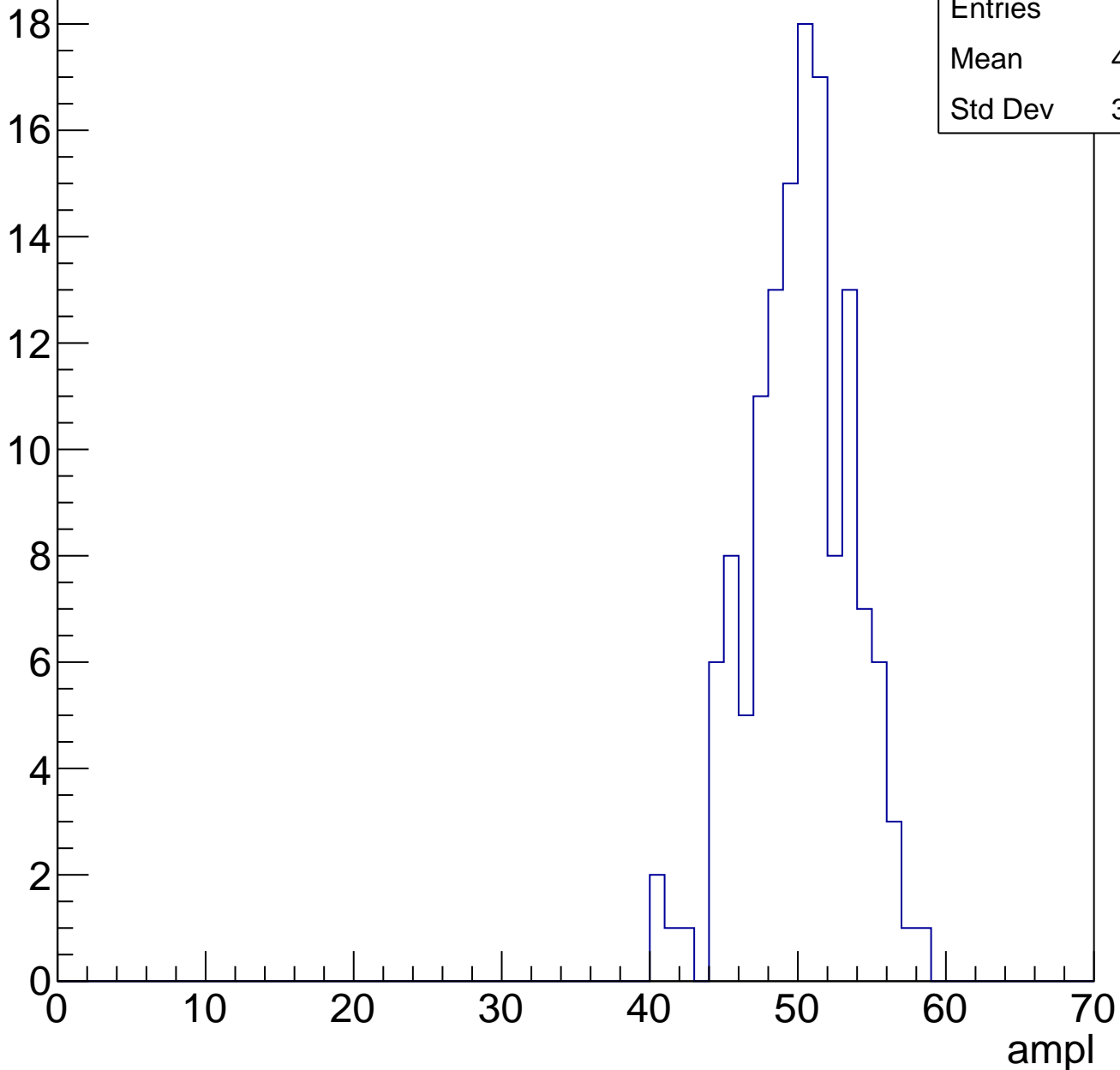


B0L101S, U20-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	49.68
Std Dev	3.474

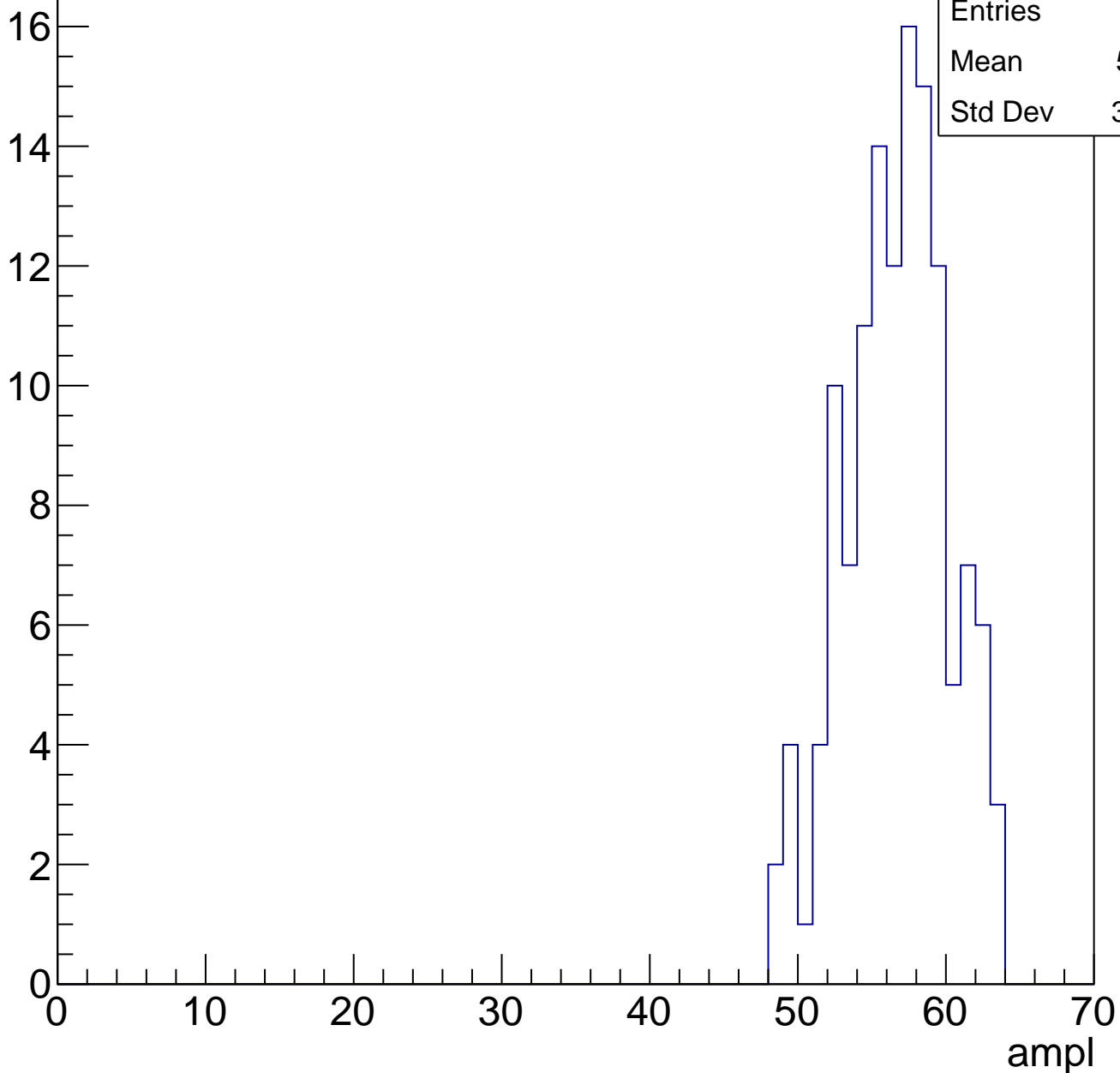
Entry



B0L101S, U20-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	129
Mean	56.21
Std Dev	3.452

B0L101S, U20-ch28, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

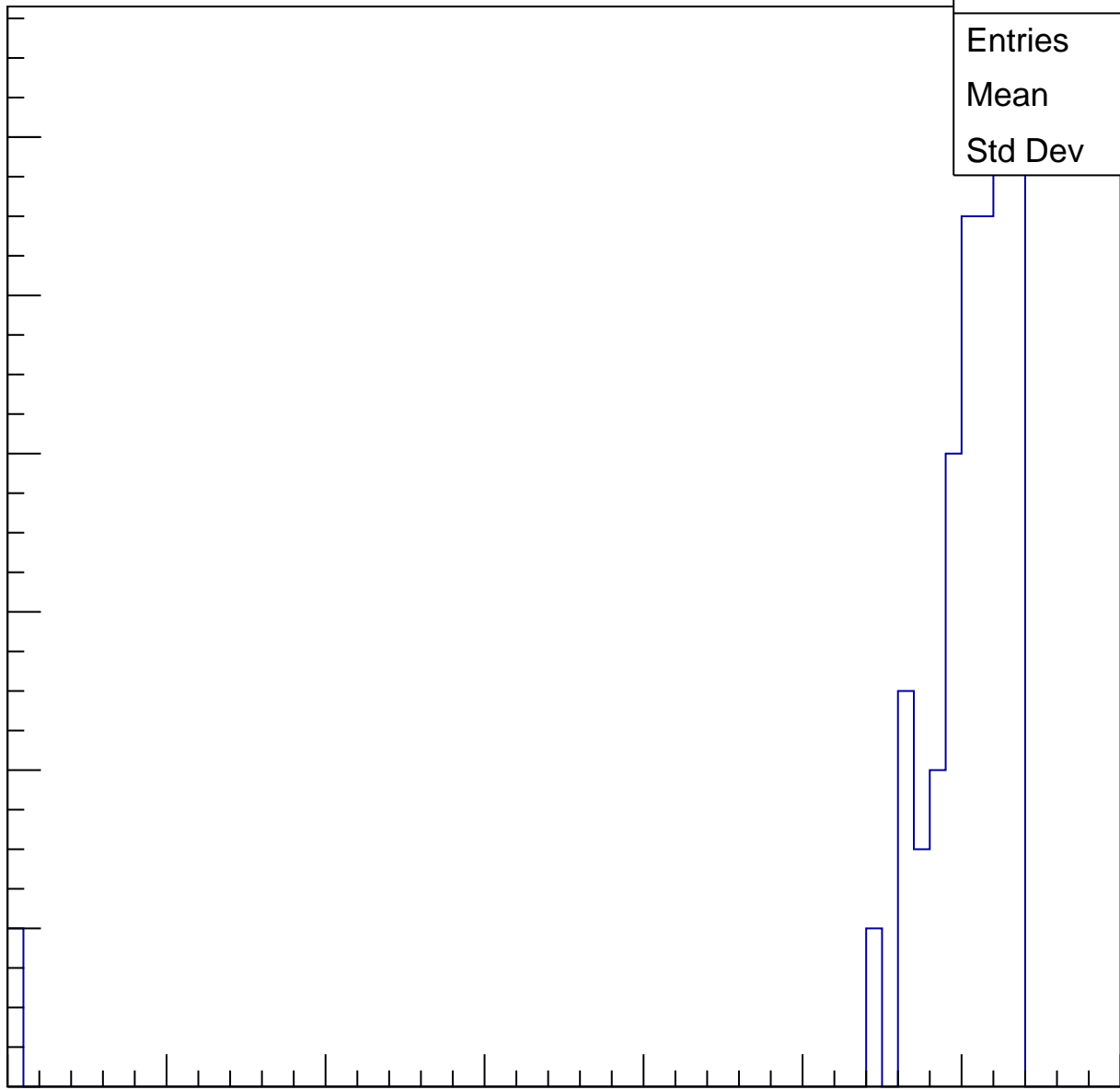
2

0

Entries	72
Mean	58.6
Std Dev	10.17

ampl

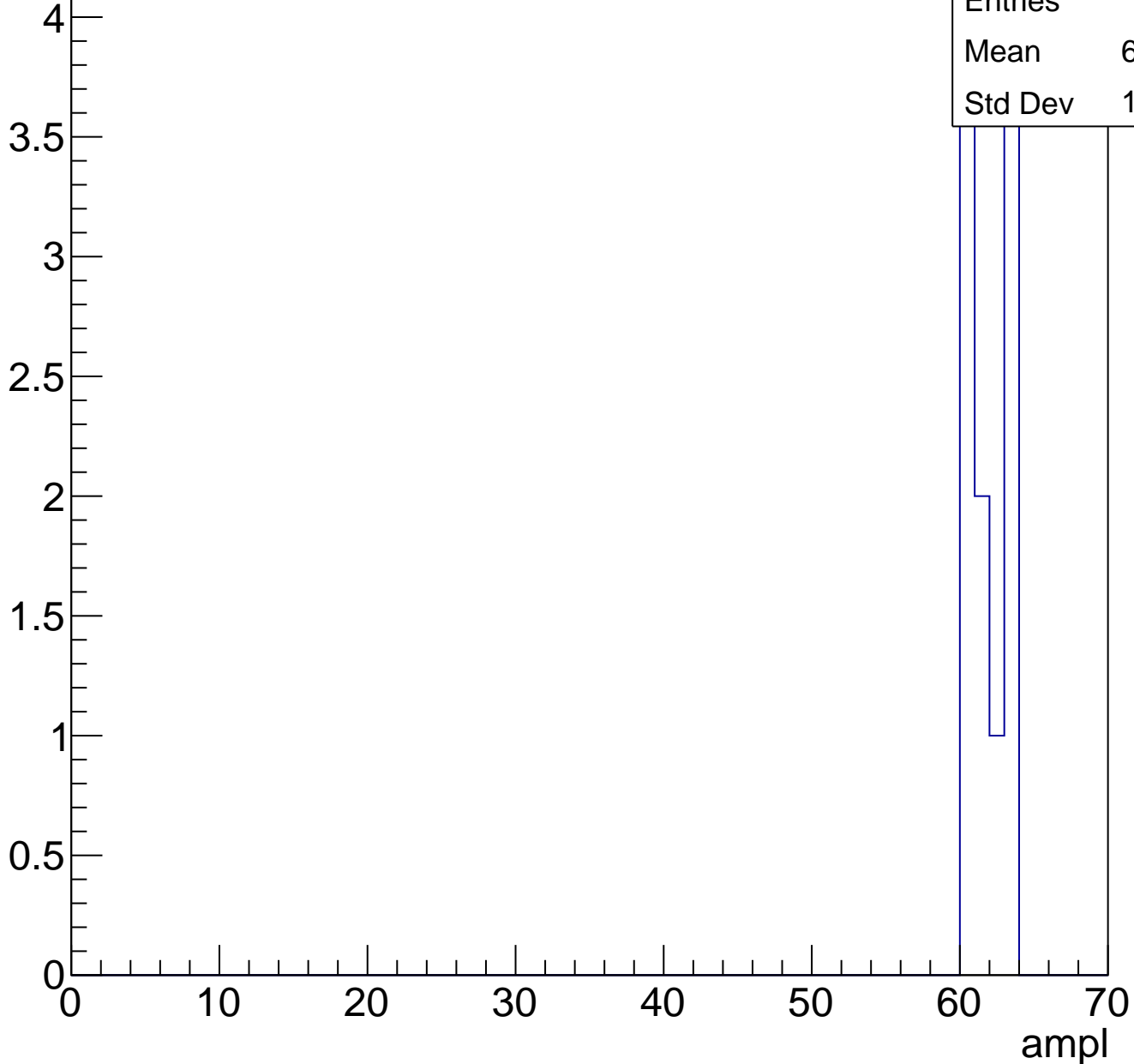
0 10 20 30 40 50 60 70



B0L101S, U20-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L101S, U20-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	30.22
Std Dev	3.586

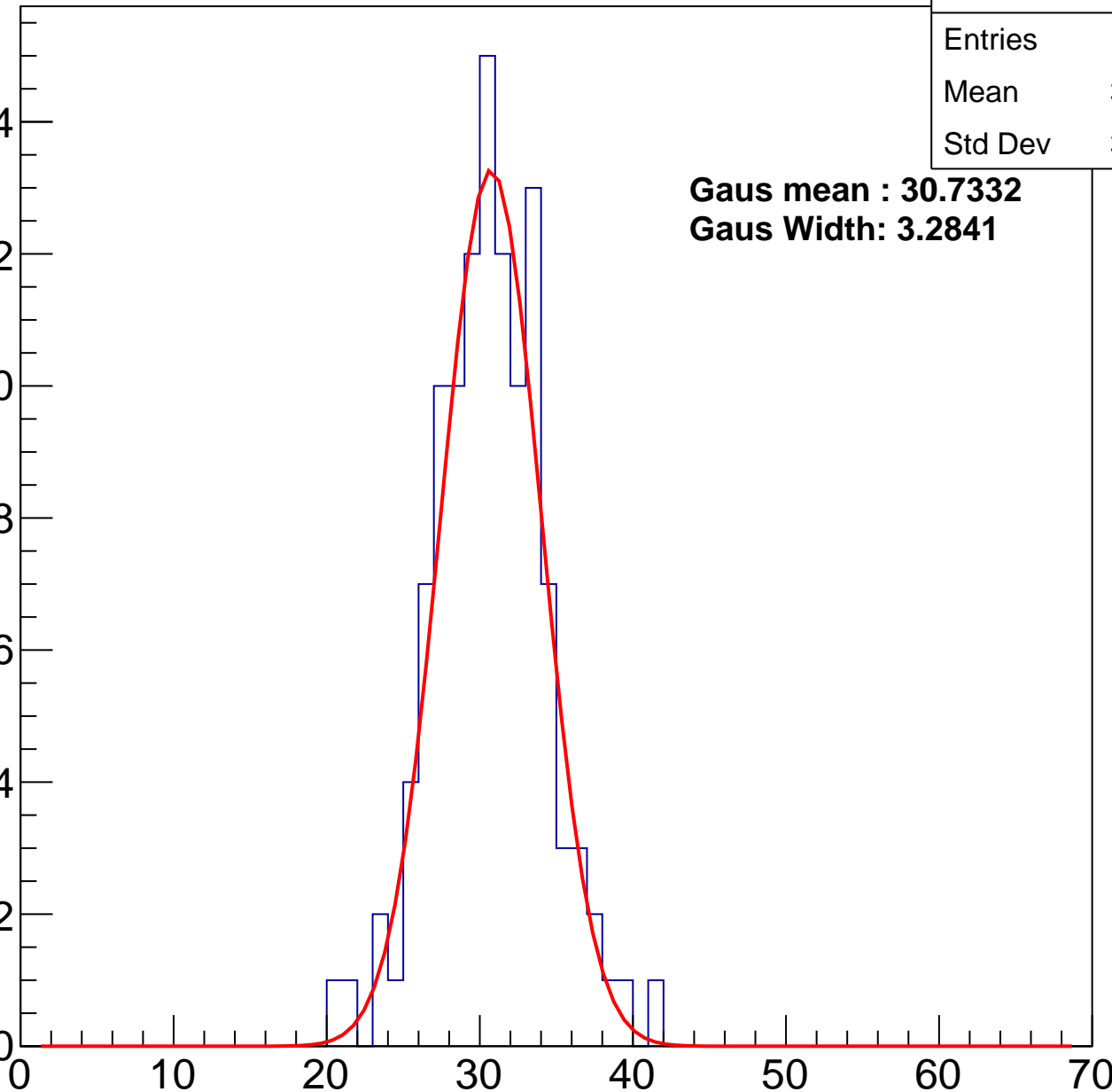
Gaus mean : 30.7332

Gaus Width: 3.2841

Entry

14
12
10
8
6
4
2
0

ampl



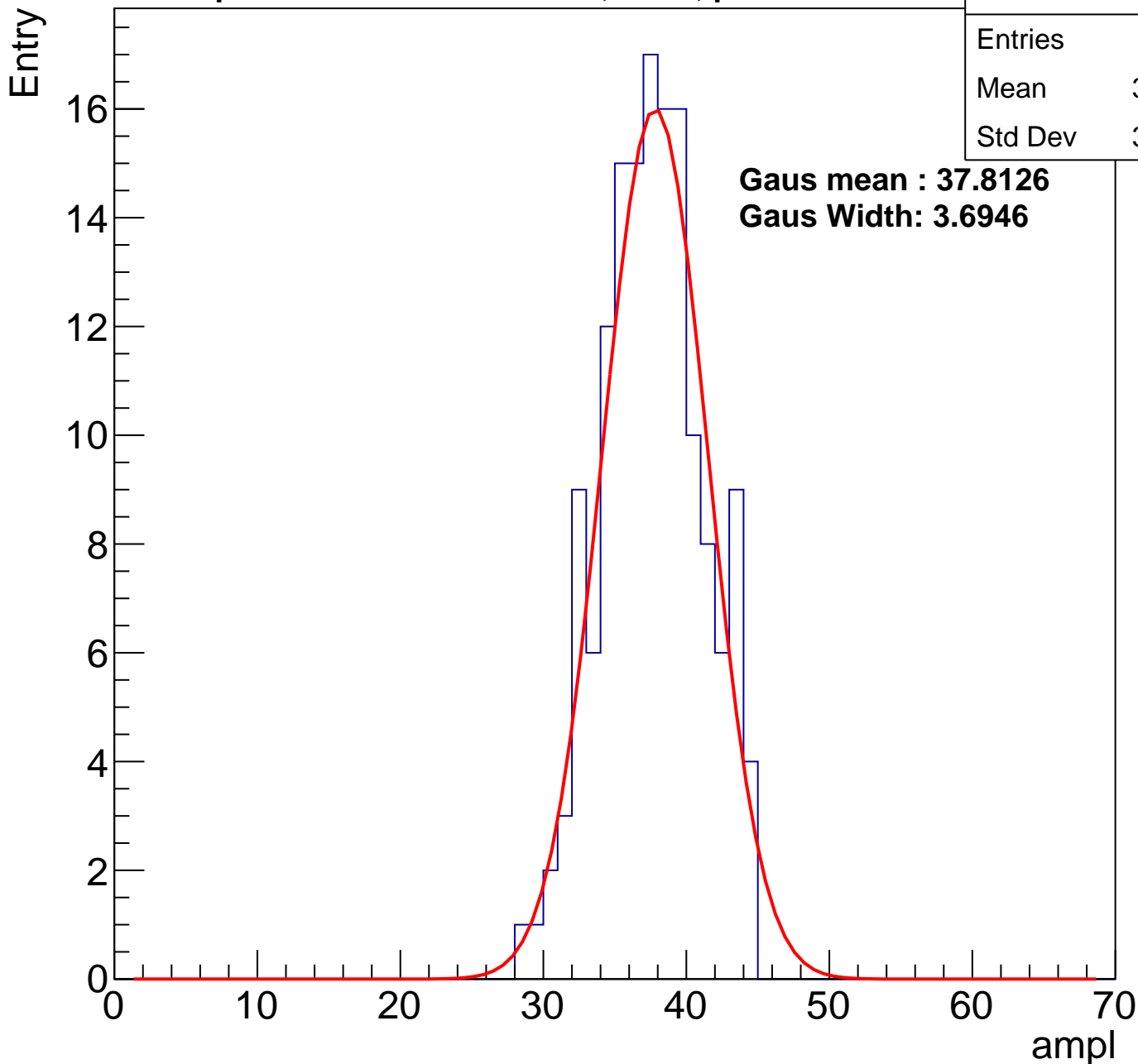
B0L101S, U20-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	37.15
Std Dev	3.483

Gaus mean : 37.8126

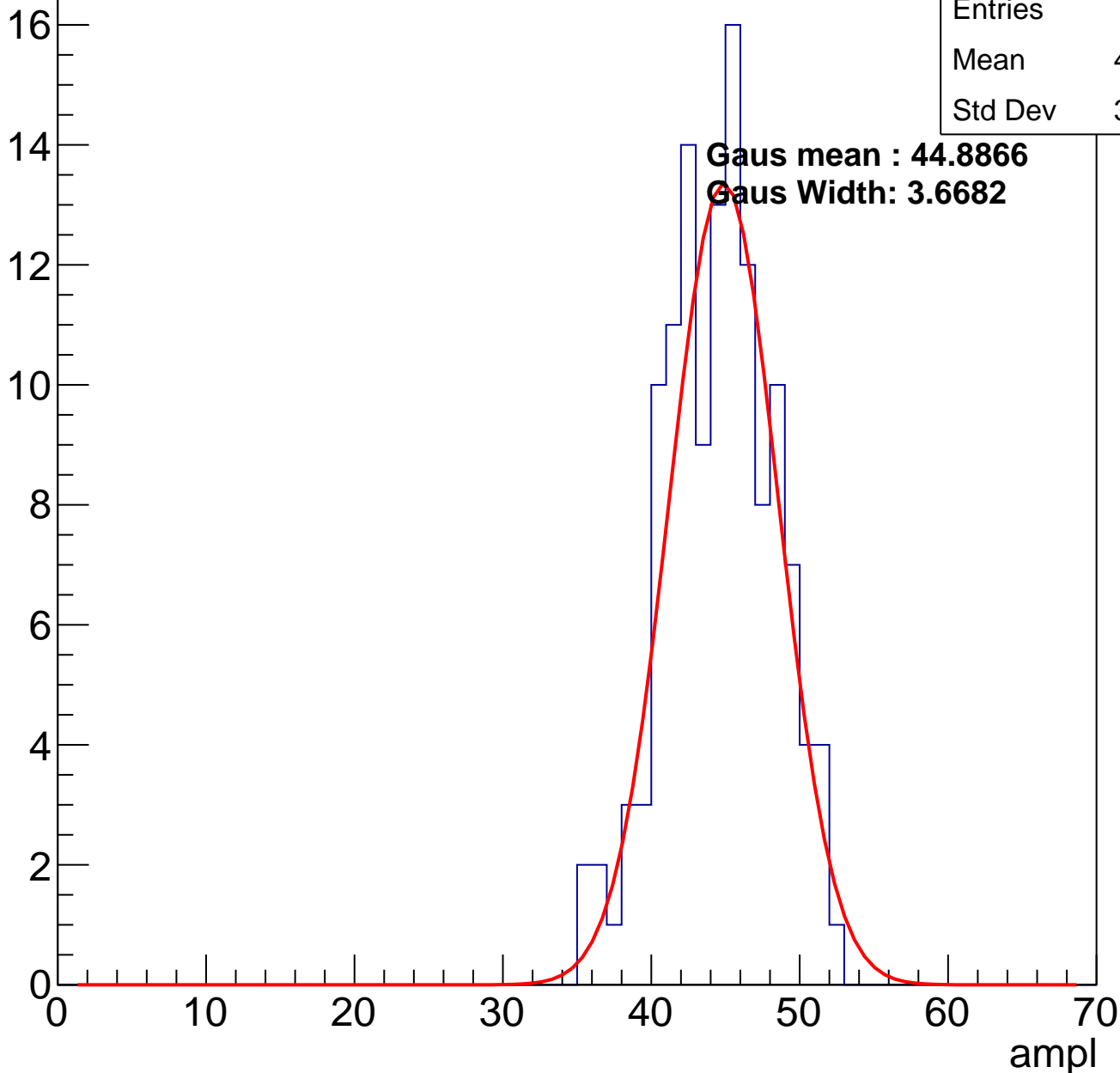
Gaus Width: 3.6946



B0L101S, U20-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

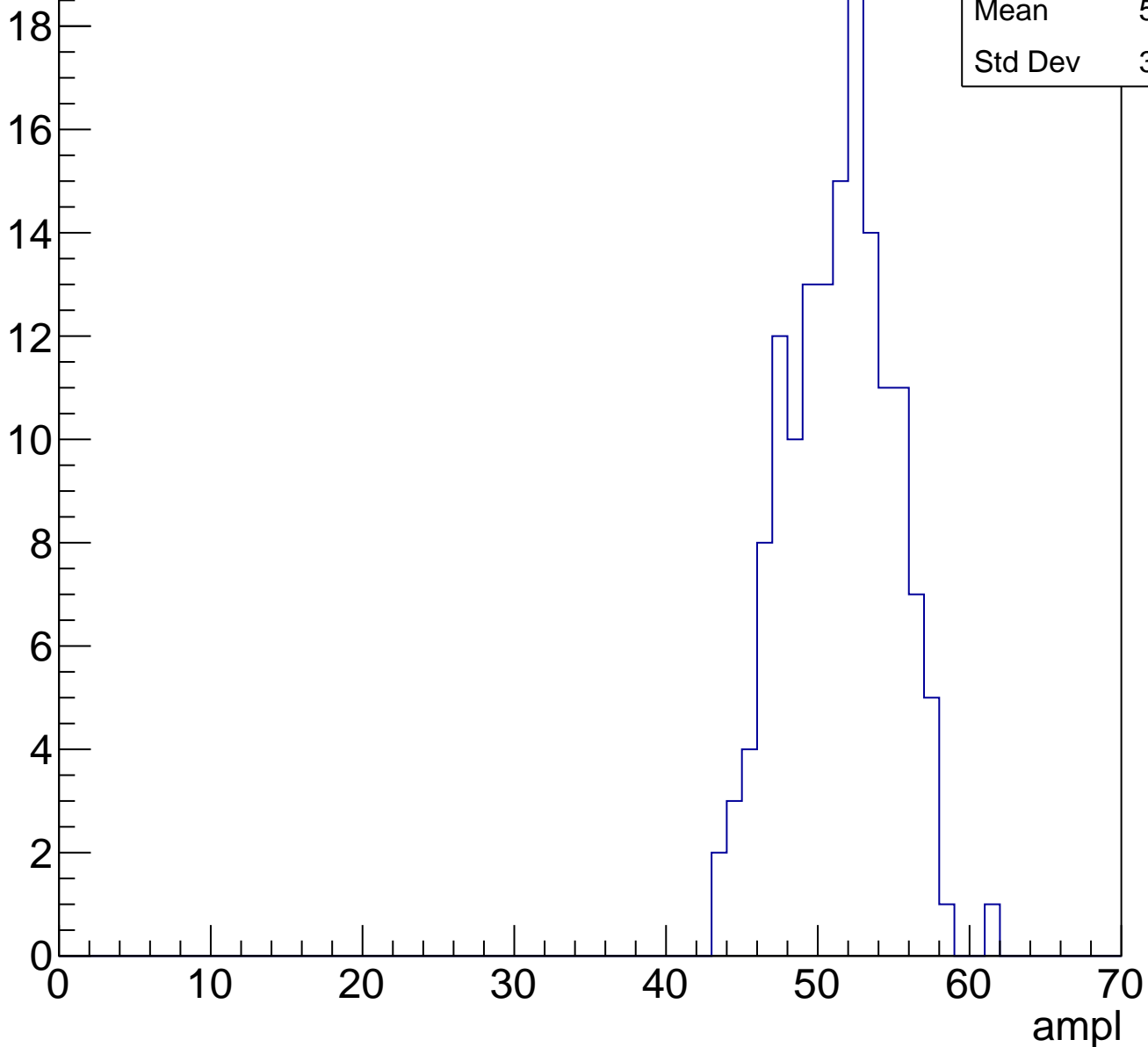


B0L101S, U20-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	50.92
Std Dev	3.494

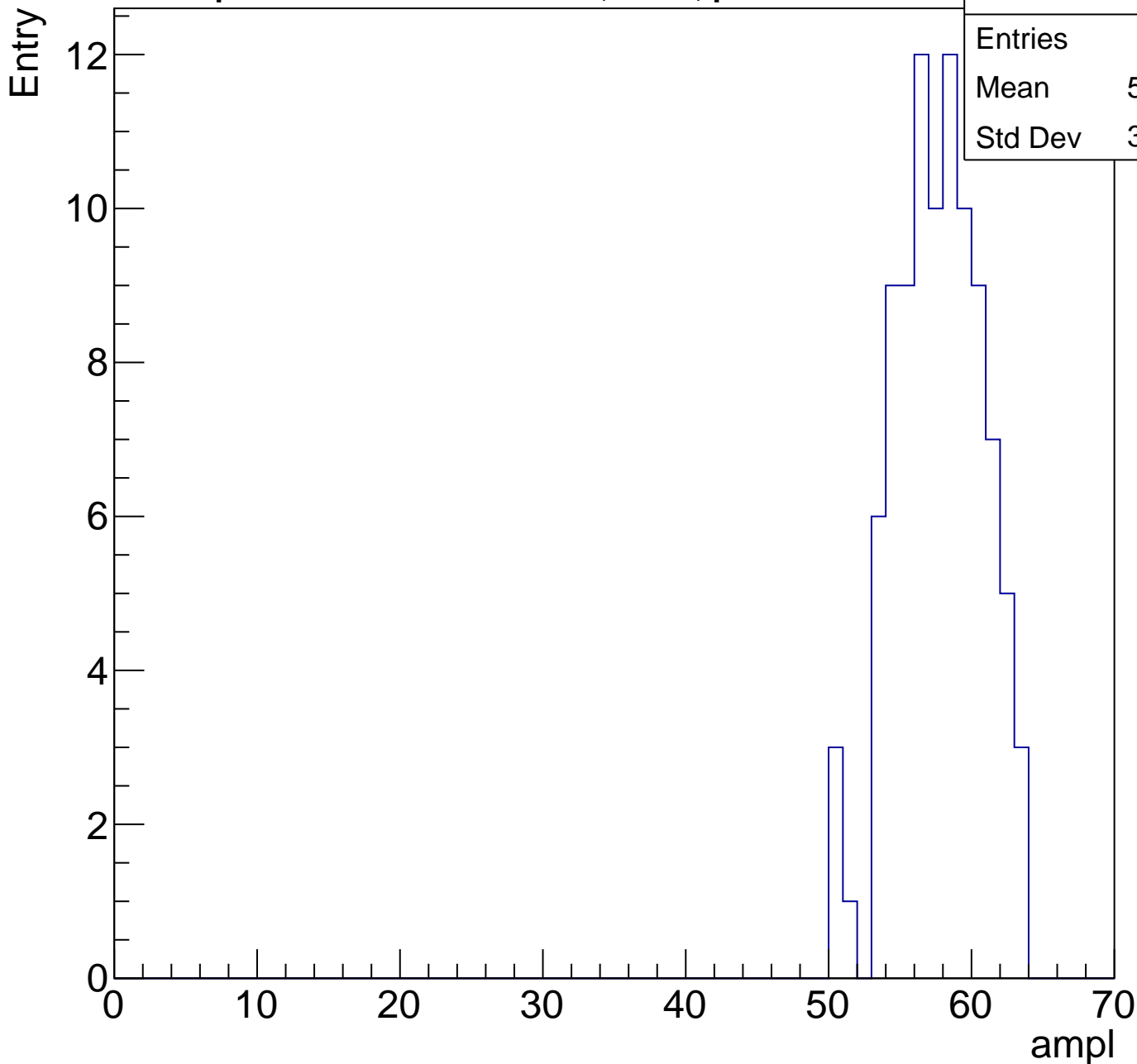
Entry



B0L101S, U20-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	57.23
Std Dev	3.029



B0L101S, U20-ch29, adc5

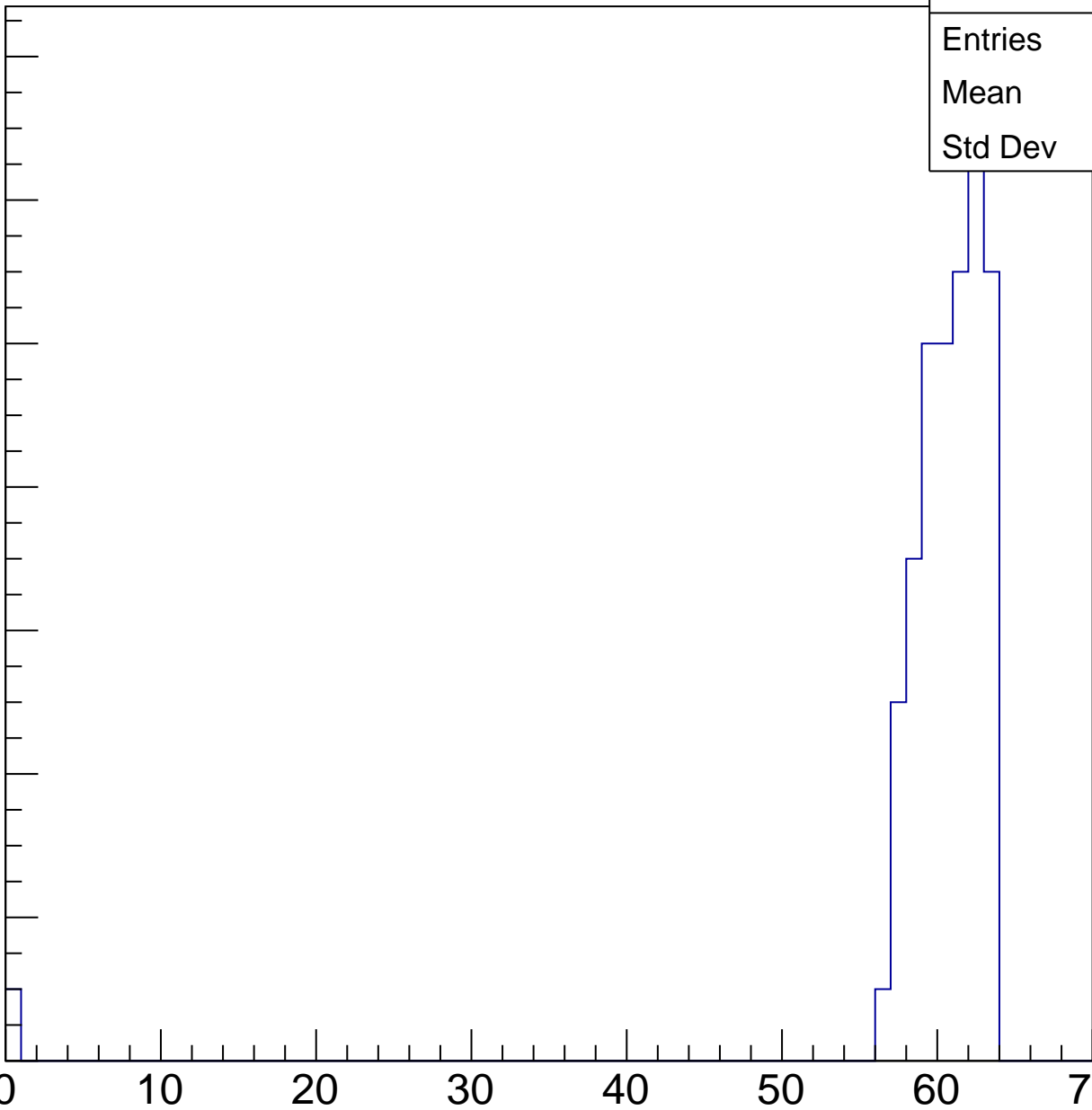
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.56
Std Dev	7.417

ampl

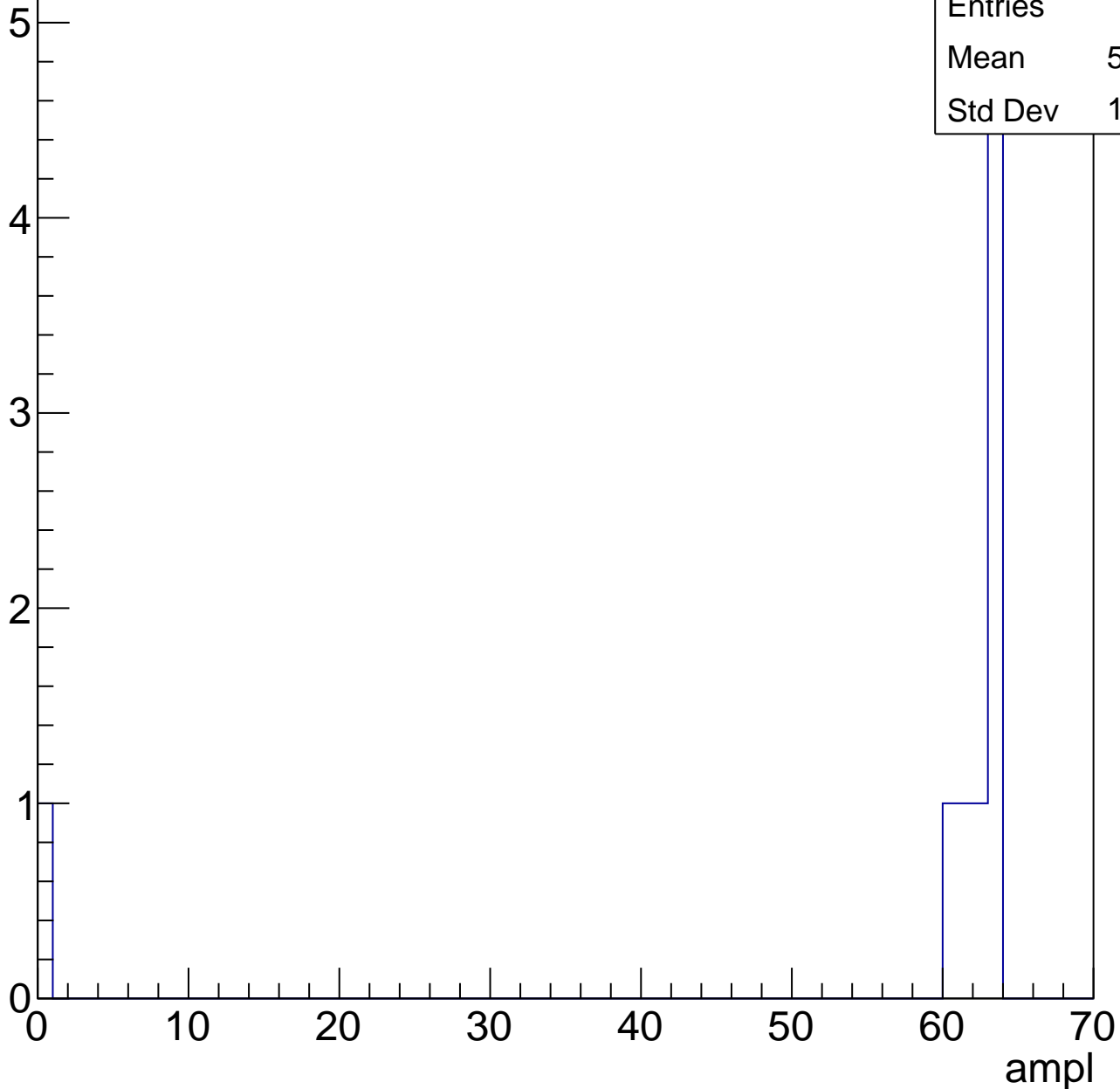


B0L101S, U20-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

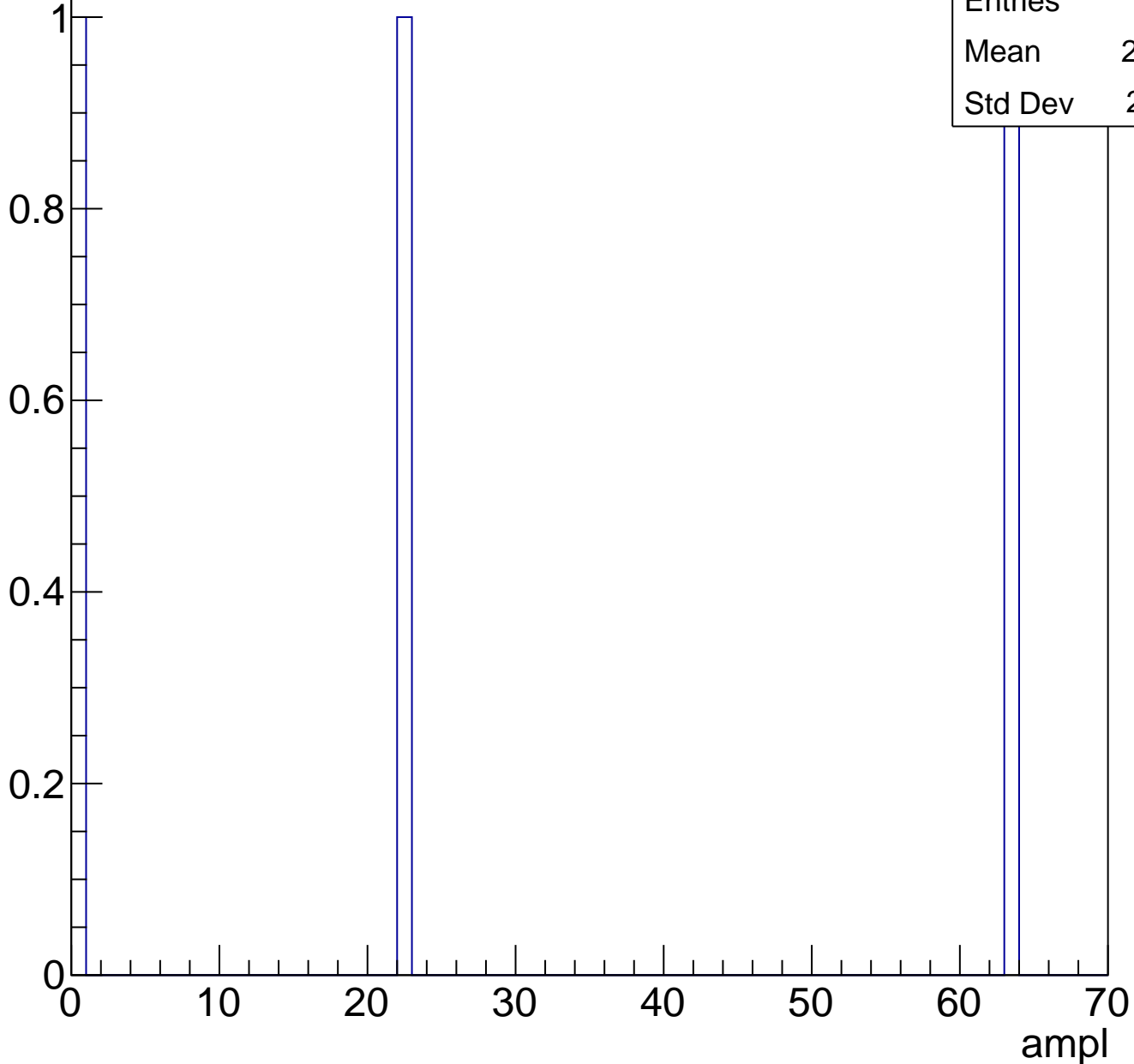
Entries	9
Mean	55.33
Std Dev	19.59



B0L101S, U20-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B0L101S, U20-ch30, adc0

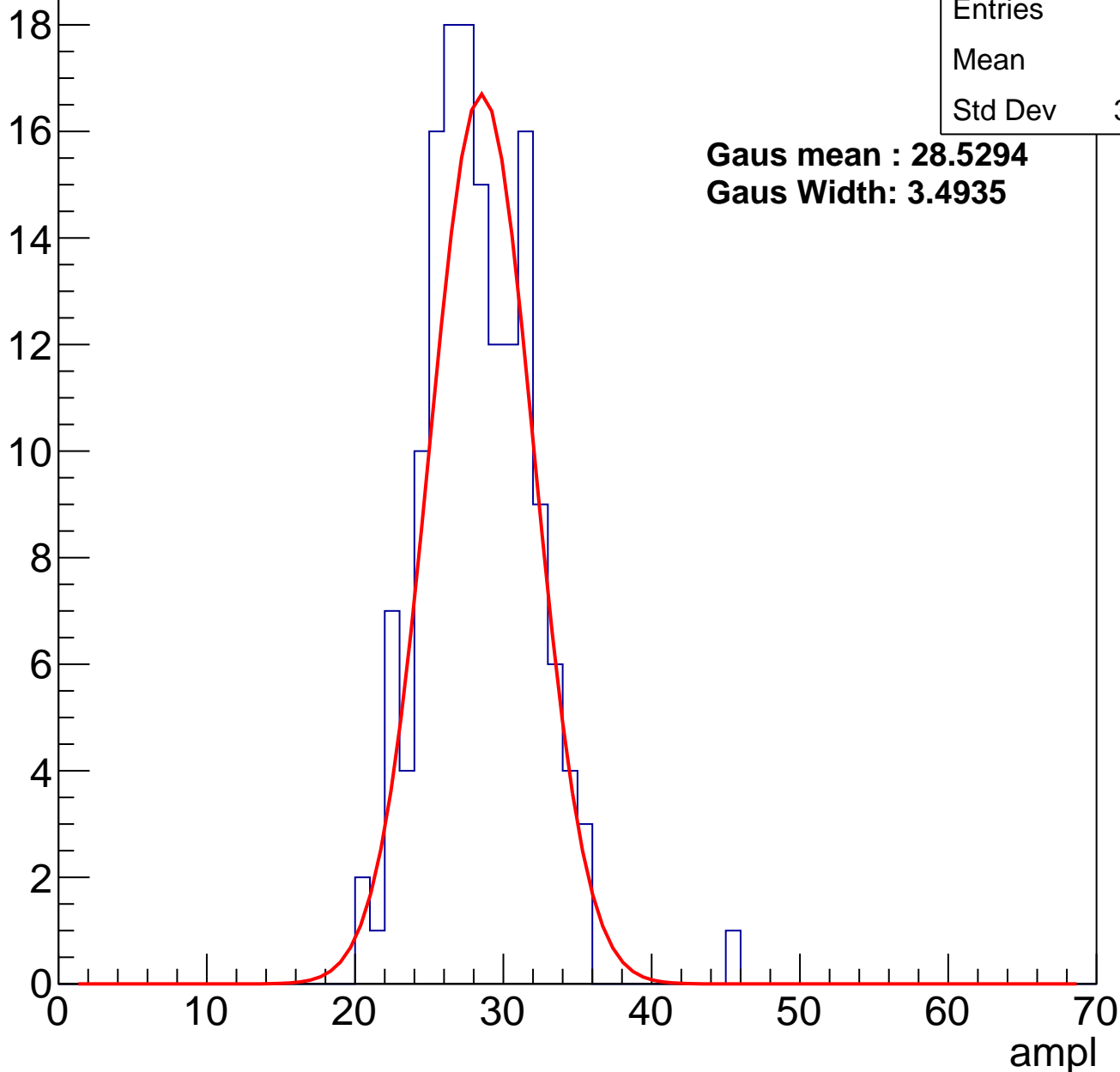
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	27.9
Std Dev	3.603

Gaus mean : 28.5294

Gaus Width: 3.4935

Entry



B0L101S, U20-ch30, adc1

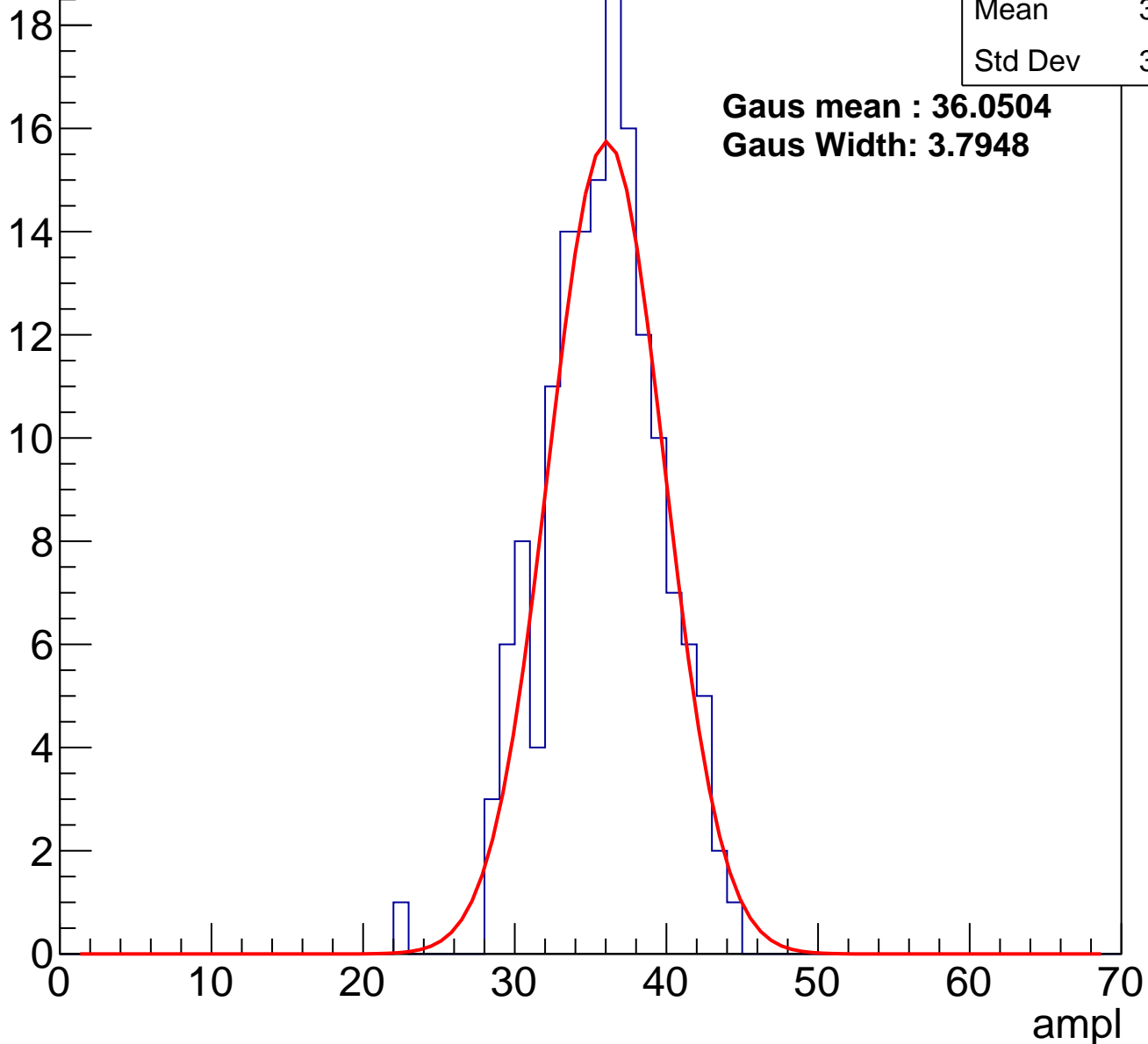
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	35.37
Std Dev	3.728

Gaus mean : 36.0504

Gaus Width: 3.7948

Entry



B0L101S, U20-ch30, adc2

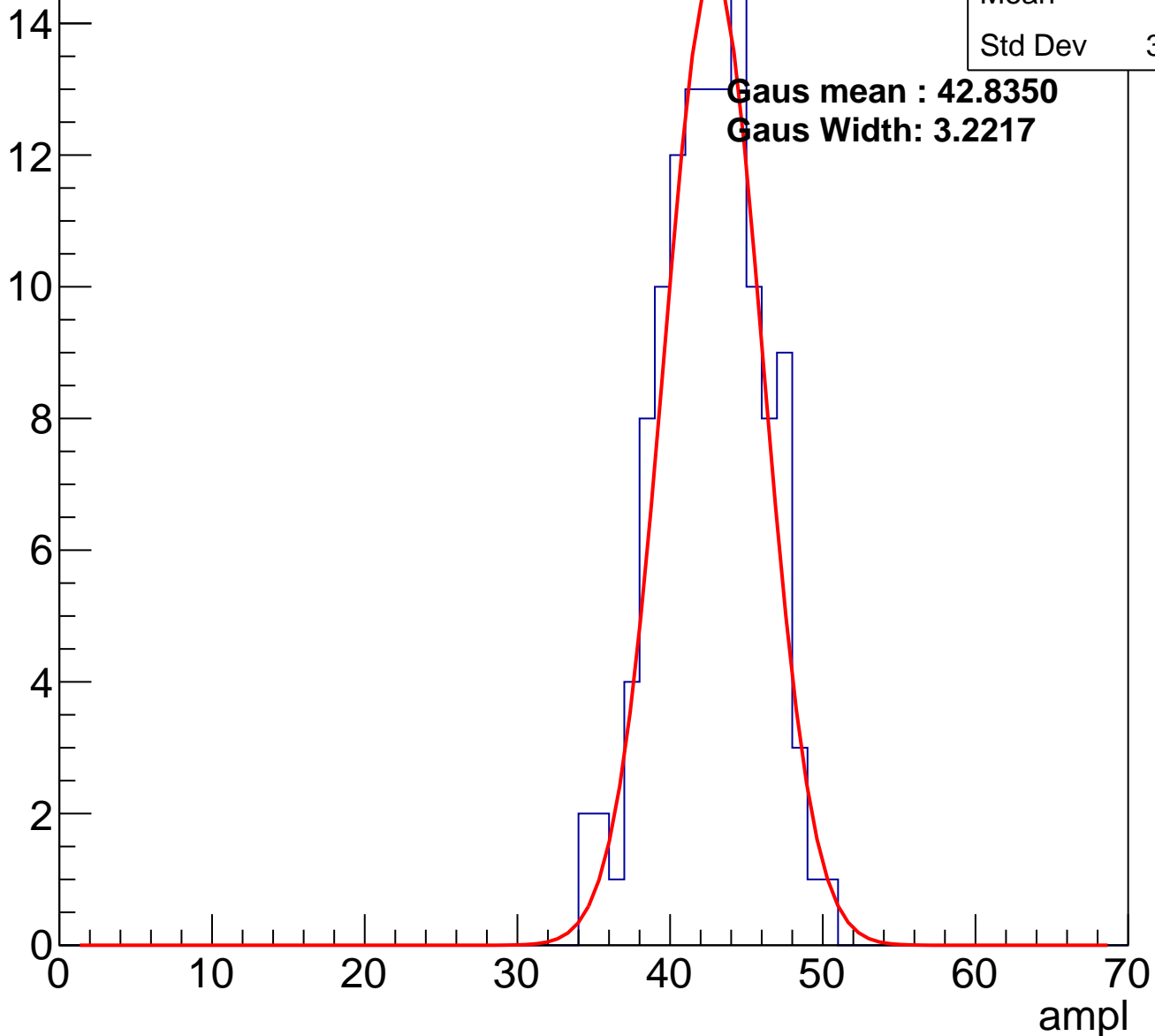
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	42.22
Std Dev	3.313

Gaus mean : 42.8350

Gaus Width: 3.2217

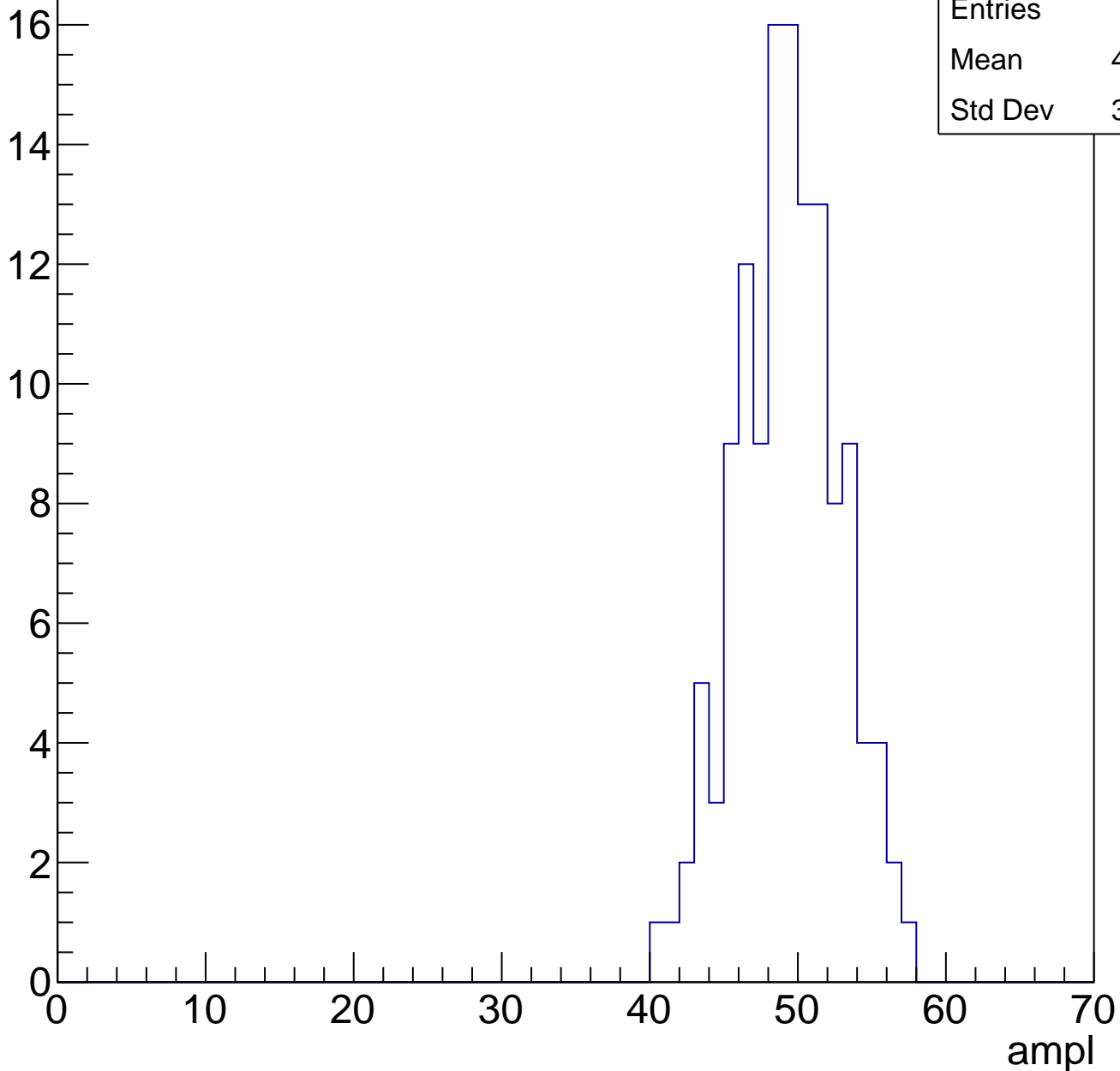
Entry



B0L101S, U20-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	128
Mean	48.87
Std Dev	3.408

B0L101S, U20-ch30, adc4

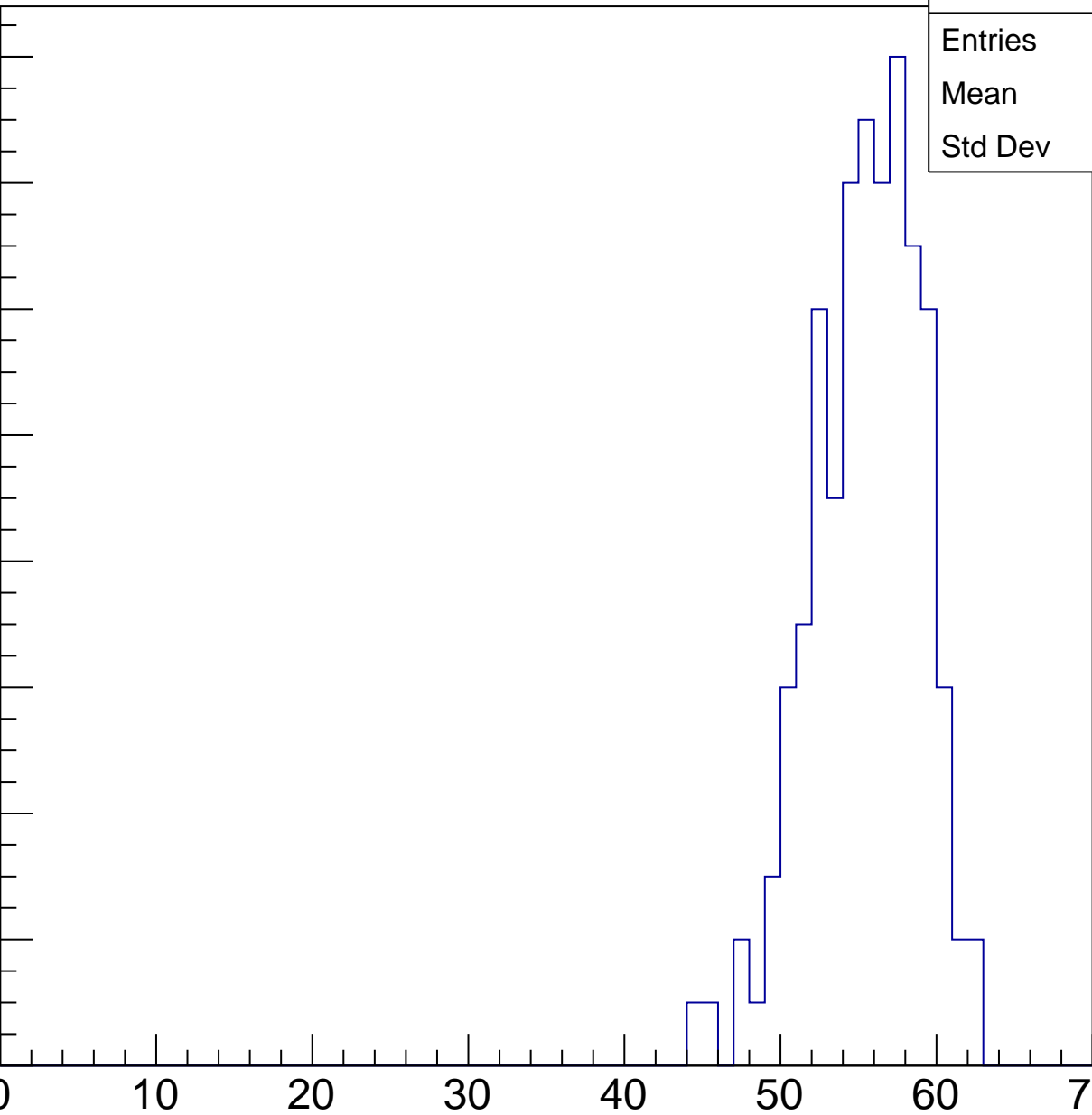
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	55.01
Std Dev	3.465

ampl



B0L101S, U20-ch30, adc5

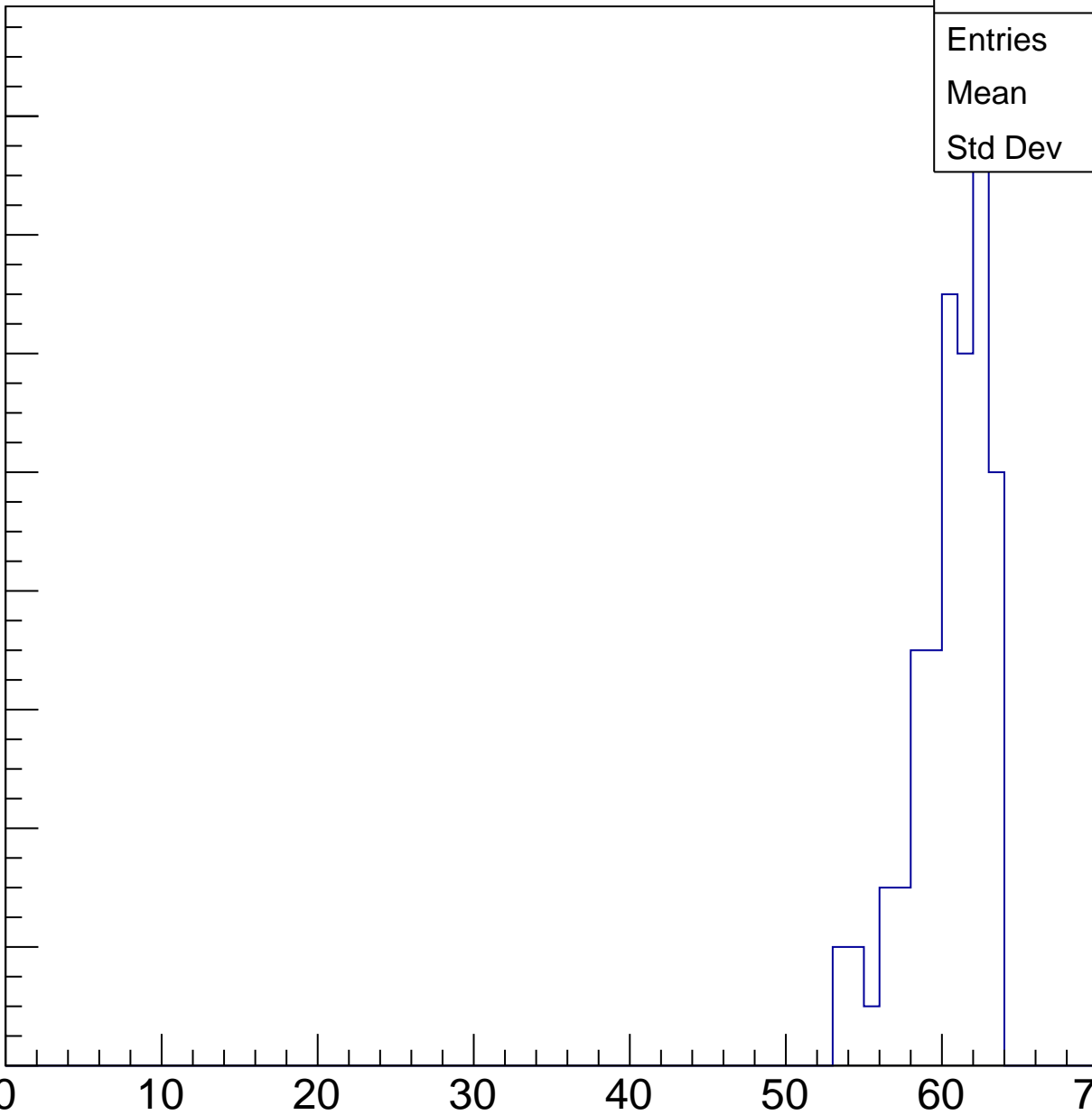
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	60.04
Std Dev	2.483

ampl



B0L101S, U20-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	22
Mean	56.05
Std Dev	17.8

Entry

10

8

6

4

2

0

0

10

20

30

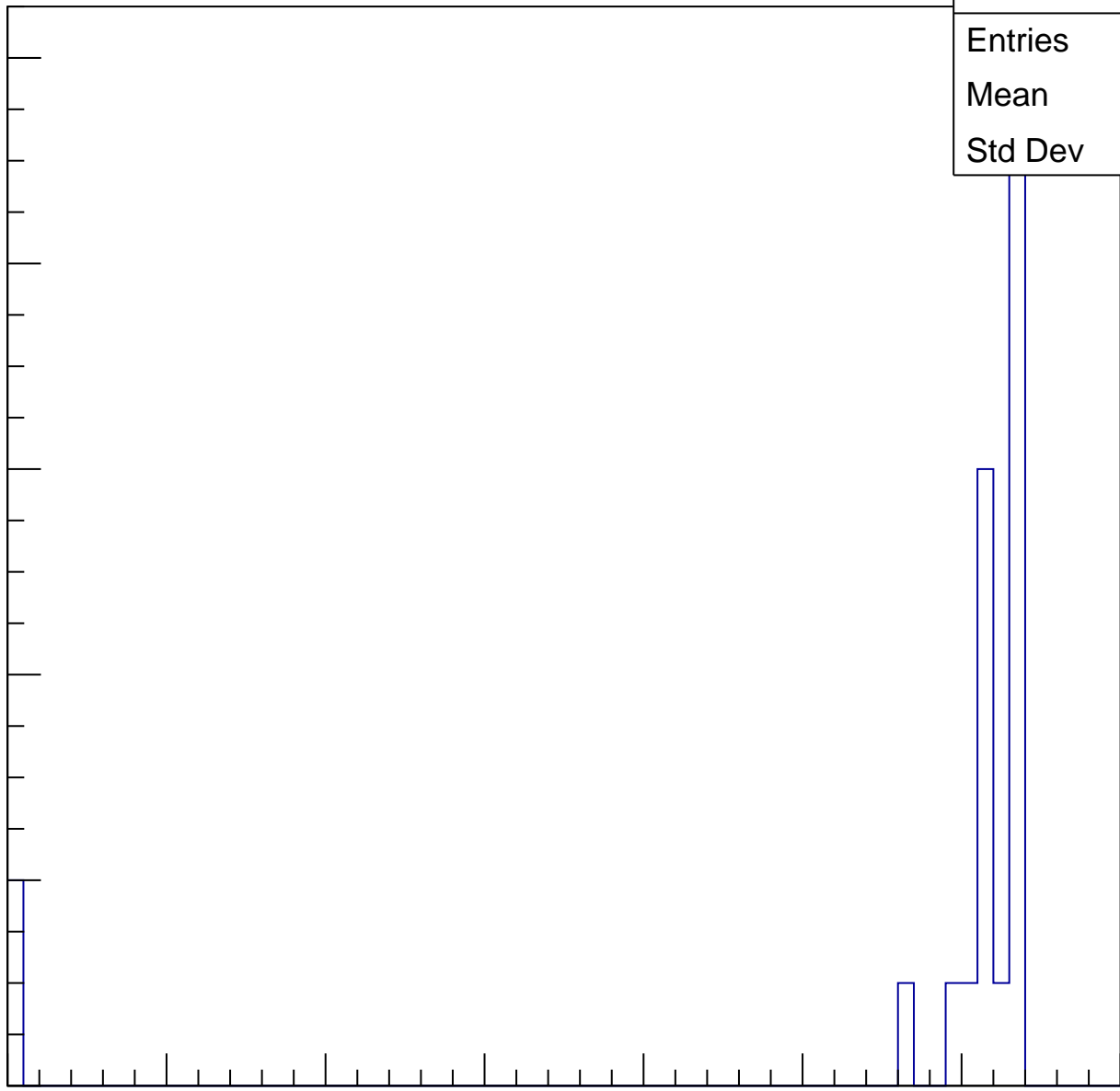
40

50

60

70

ampl



B0L101S, U20-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch31, adc0

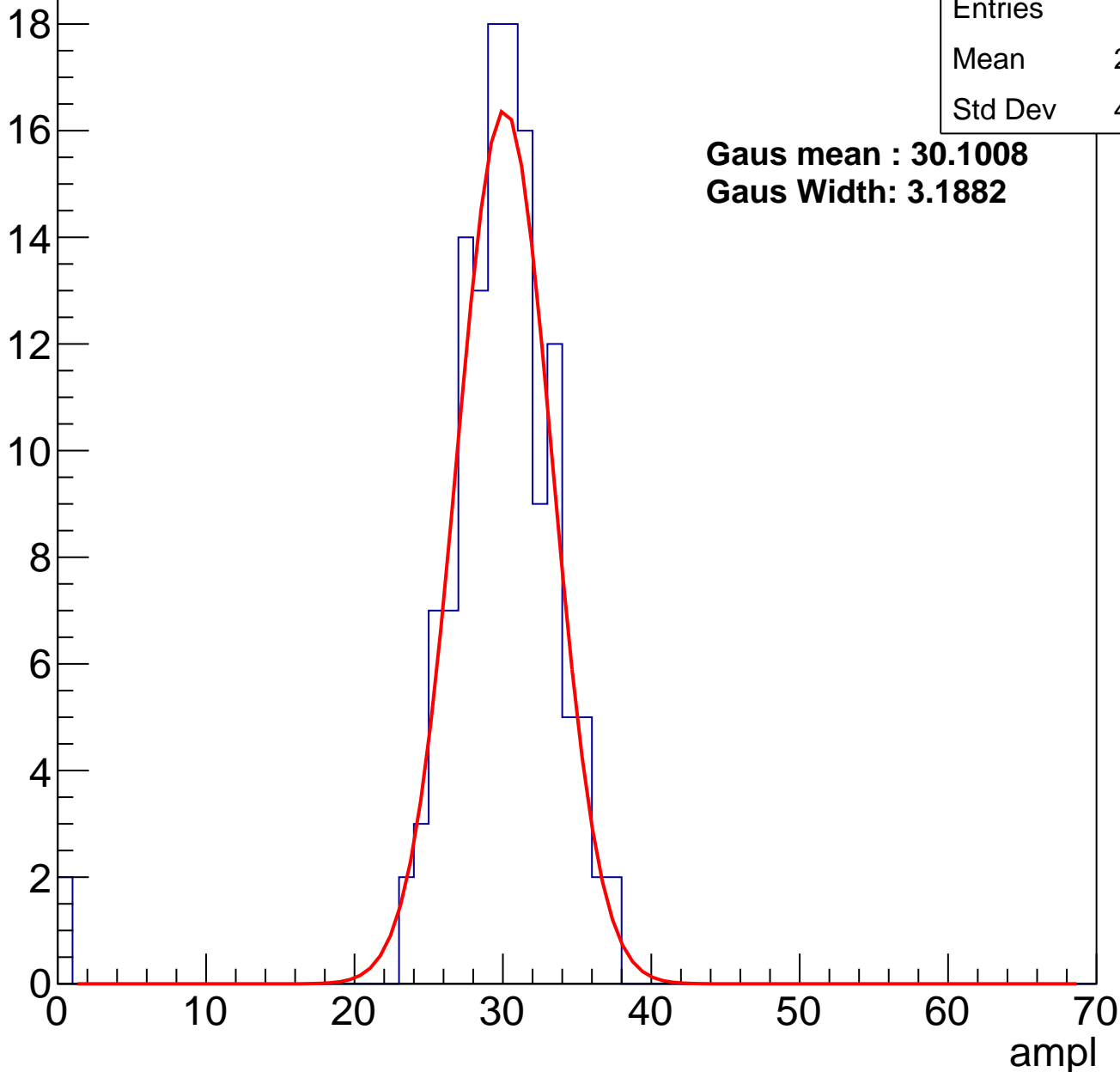
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	29.26
Std Dev	4.675

Gaus mean : 30.1008

Gaus Width: 3.1882

Entry



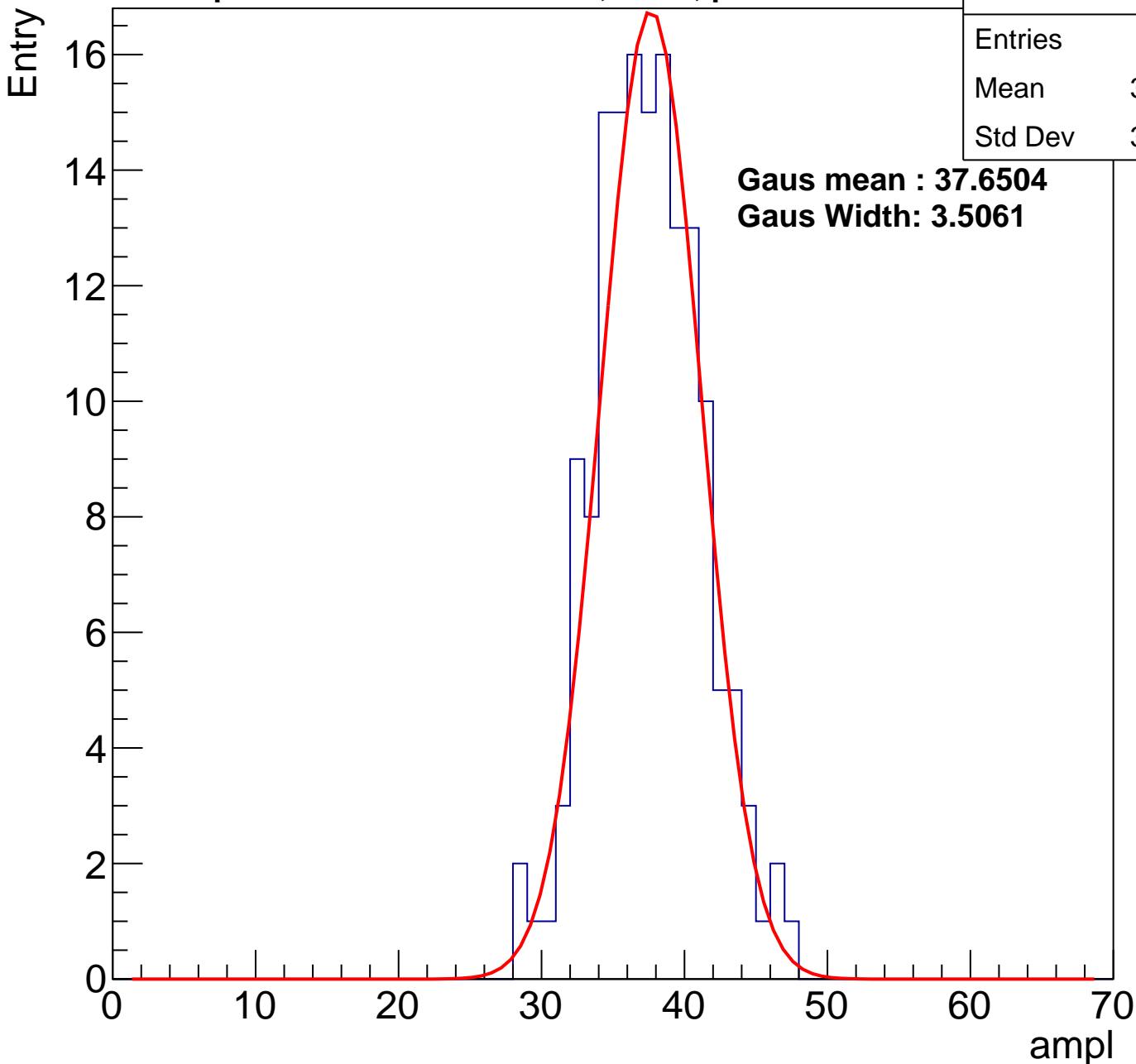
B0L101S, U20-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	37.09
Std Dev	3.665

Gaus mean : 37.6504

Gaus Width: 3.5061



B0L101S, U20-ch31, adc2

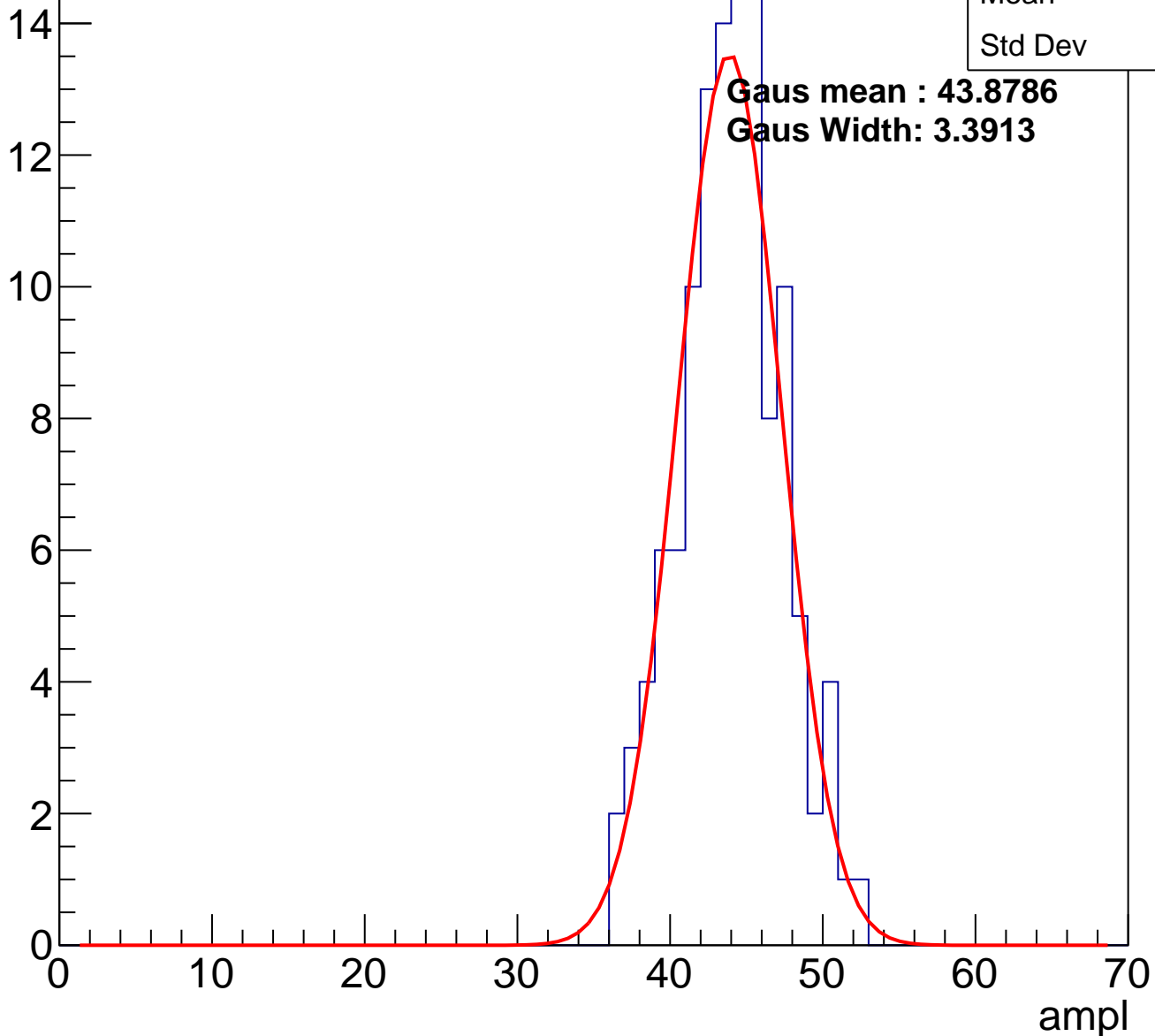
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	43.54
Std Dev	3.33

Gaus mean : 43.8786

Gaus Width: 3.3913

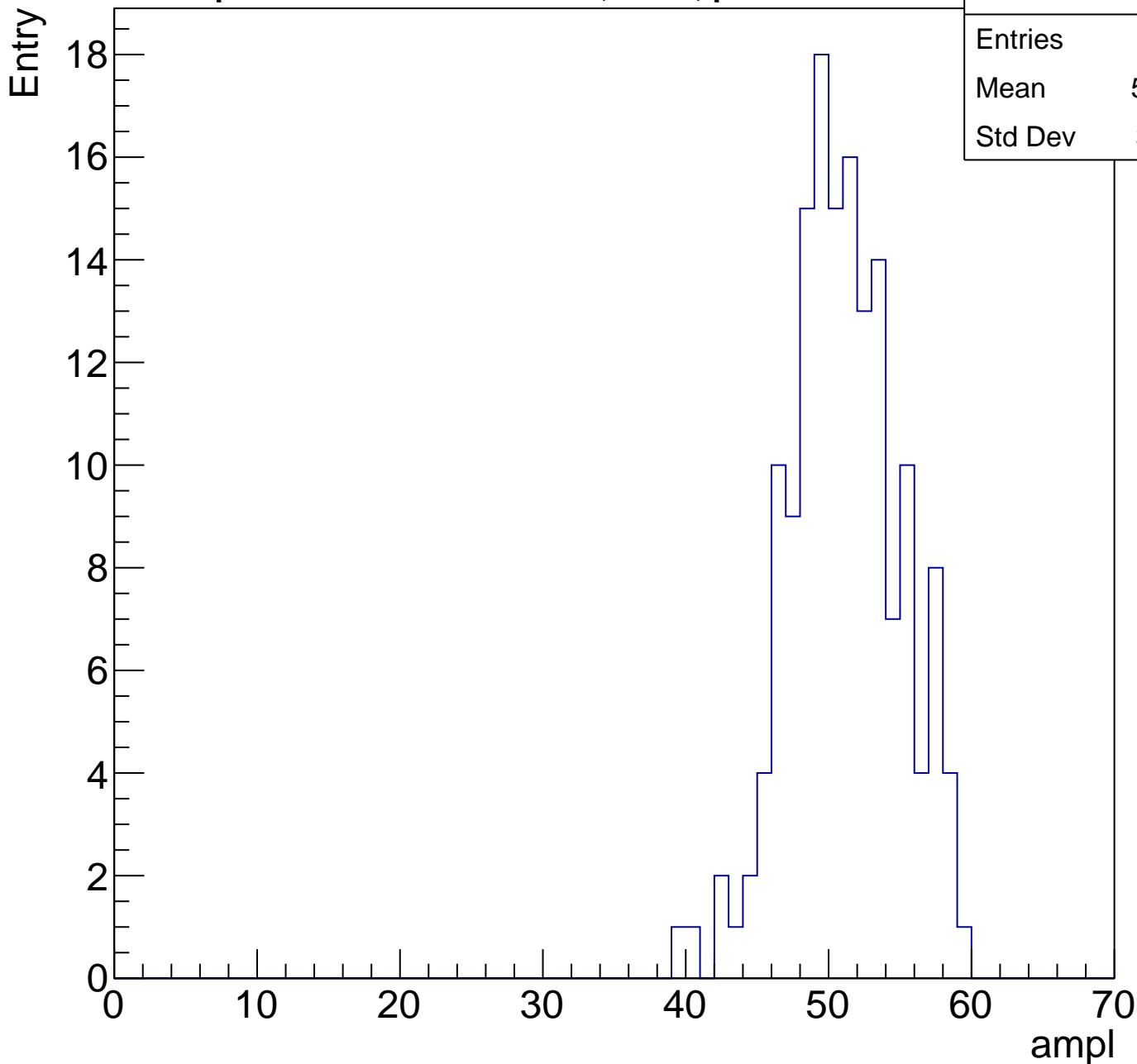
Entry



B0L101S, U20-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	50.59
Std Dev	3.821

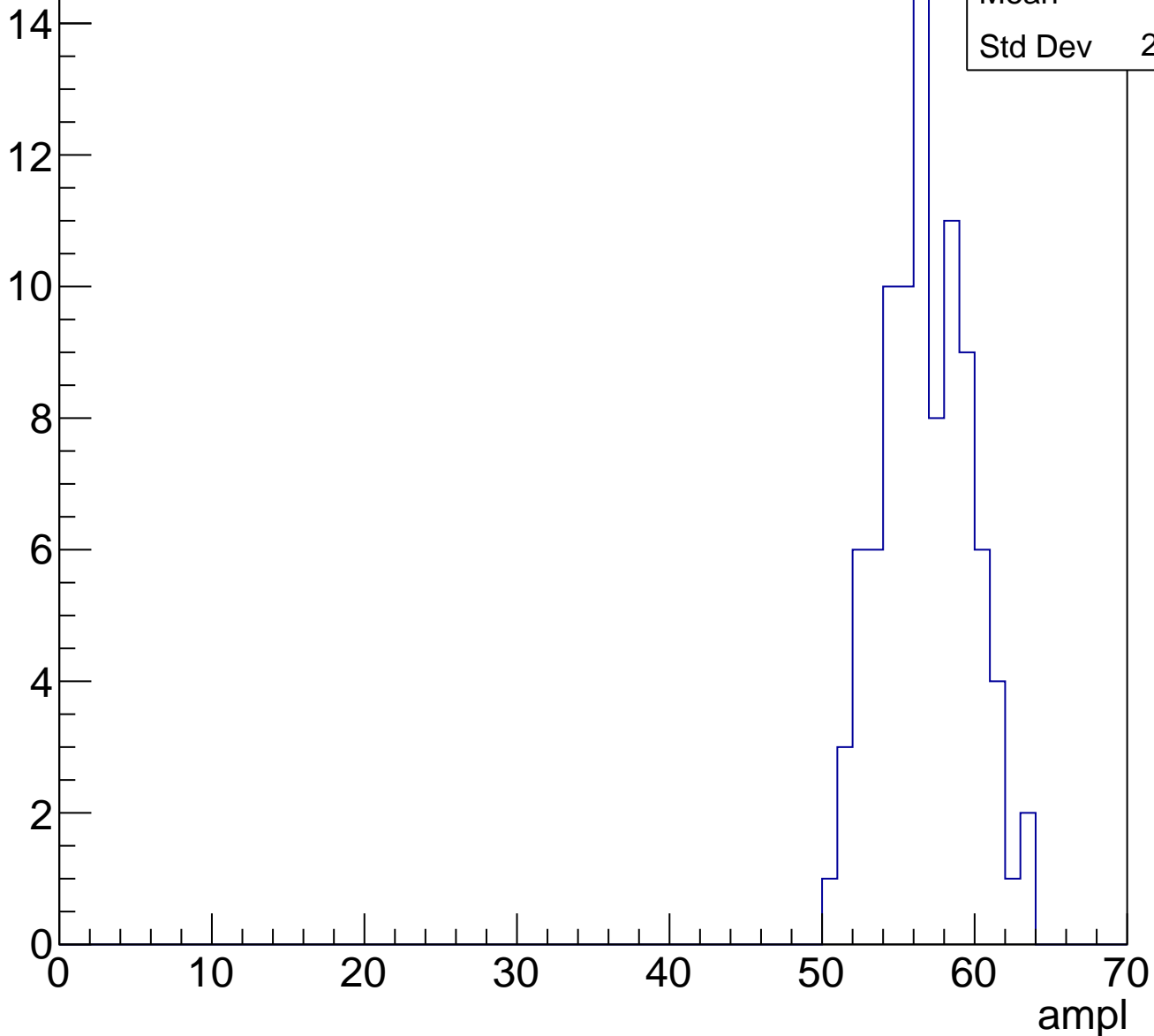


B0L101S, U20-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	92
Mean	56.3
Std Dev	2.885

Entry



B0L101S, U20-ch31, adc5

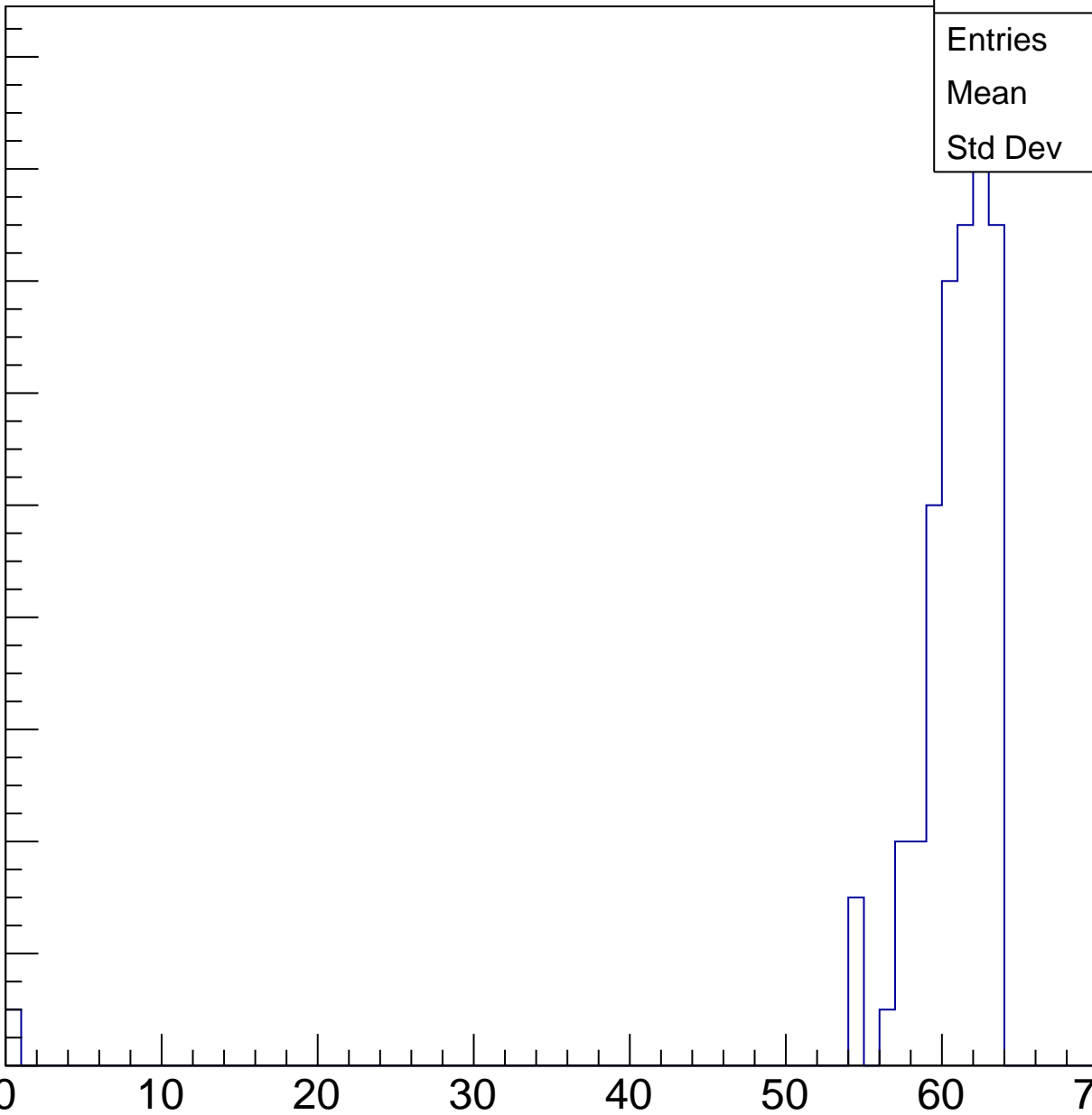
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.81
Std Dev	6.864

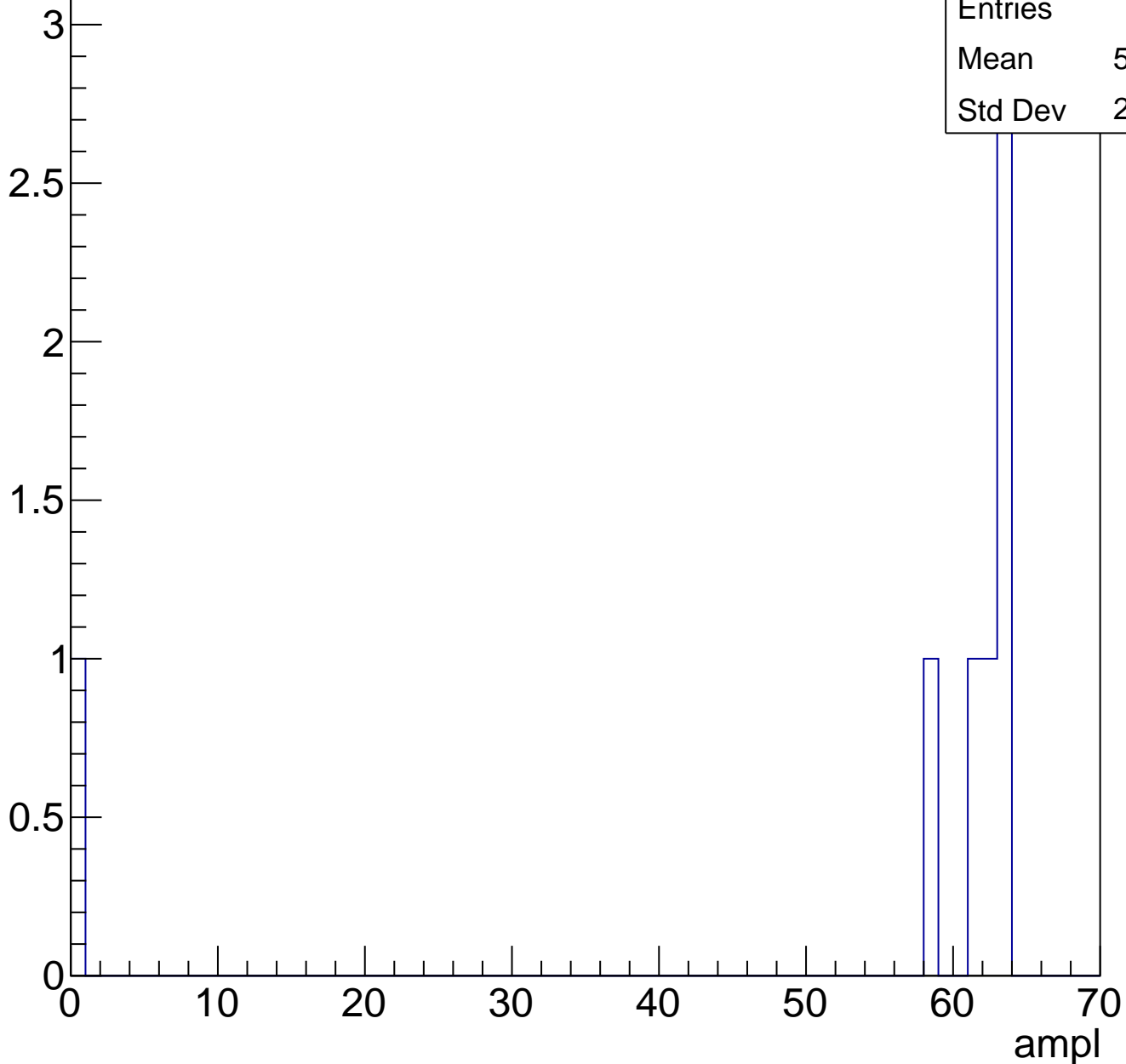
ampl



B0L101S, U20-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	28.7
Std Dev	4.228

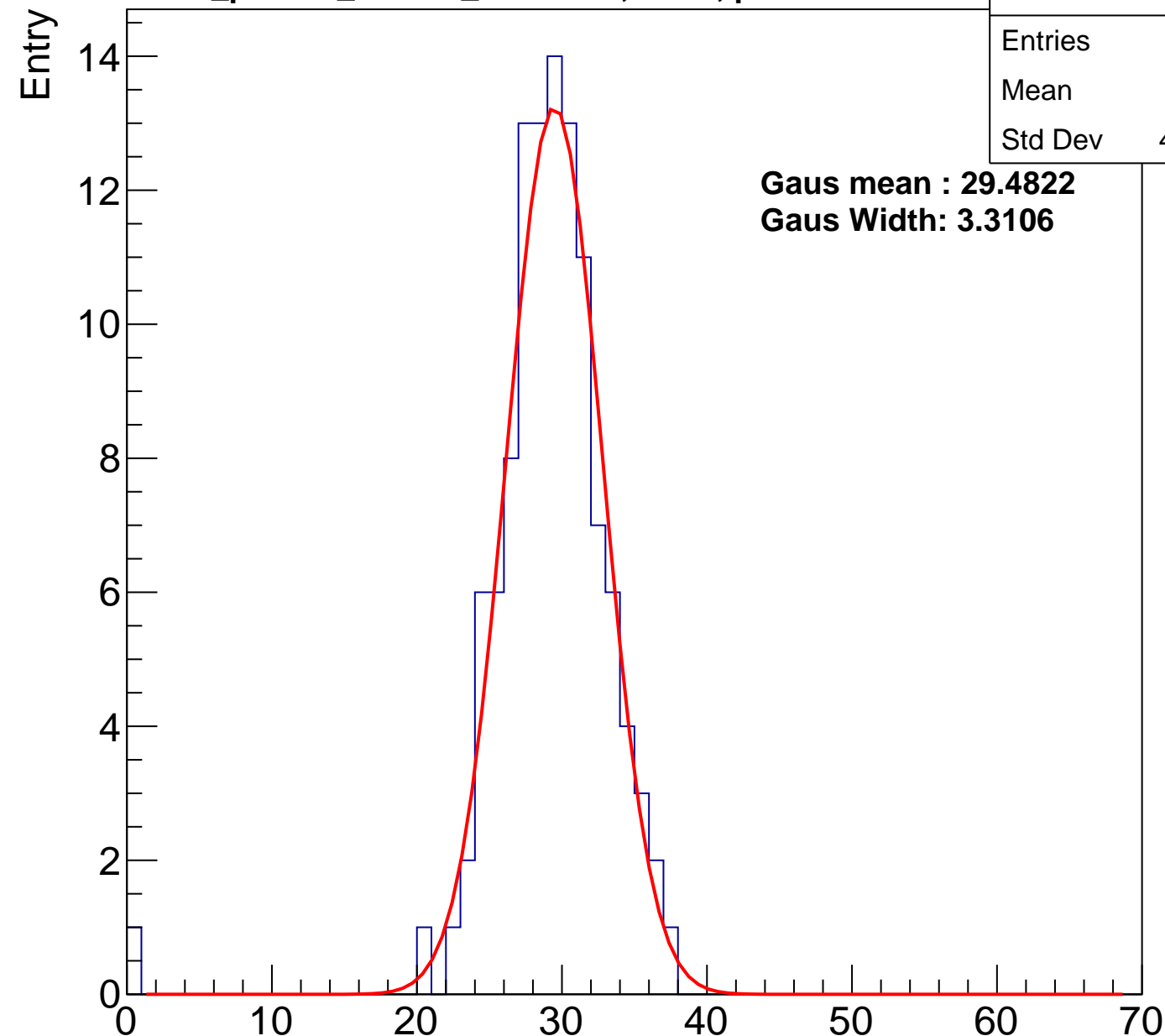
Gaus mean : 29.4822

Gaus Width: 3.3106

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch32, adc1

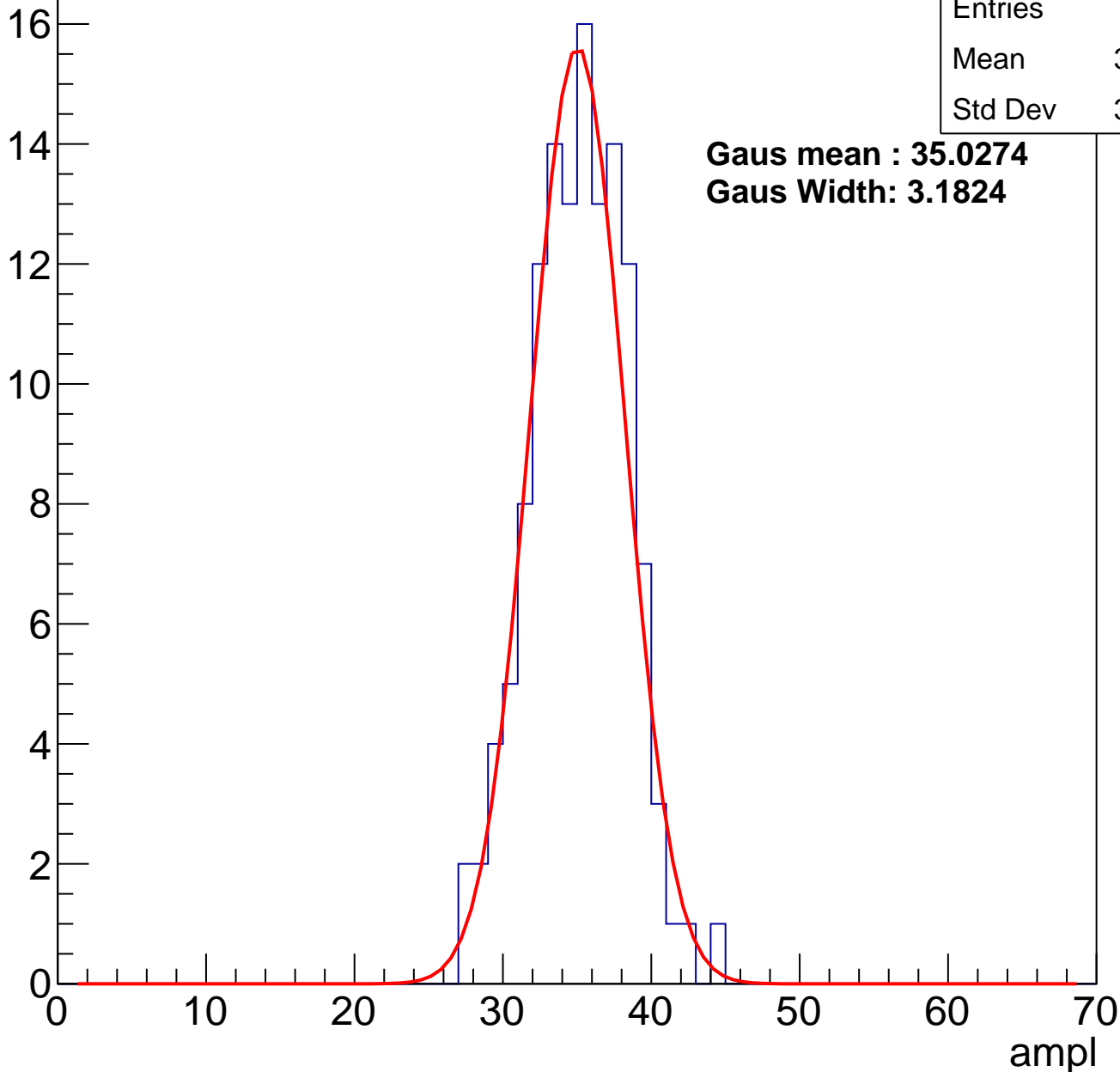
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	34.64
Std Dev	3.203

Gaus mean : 35.0274

Gaus Width: 3.1824

Entry



B0L101S, U20-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

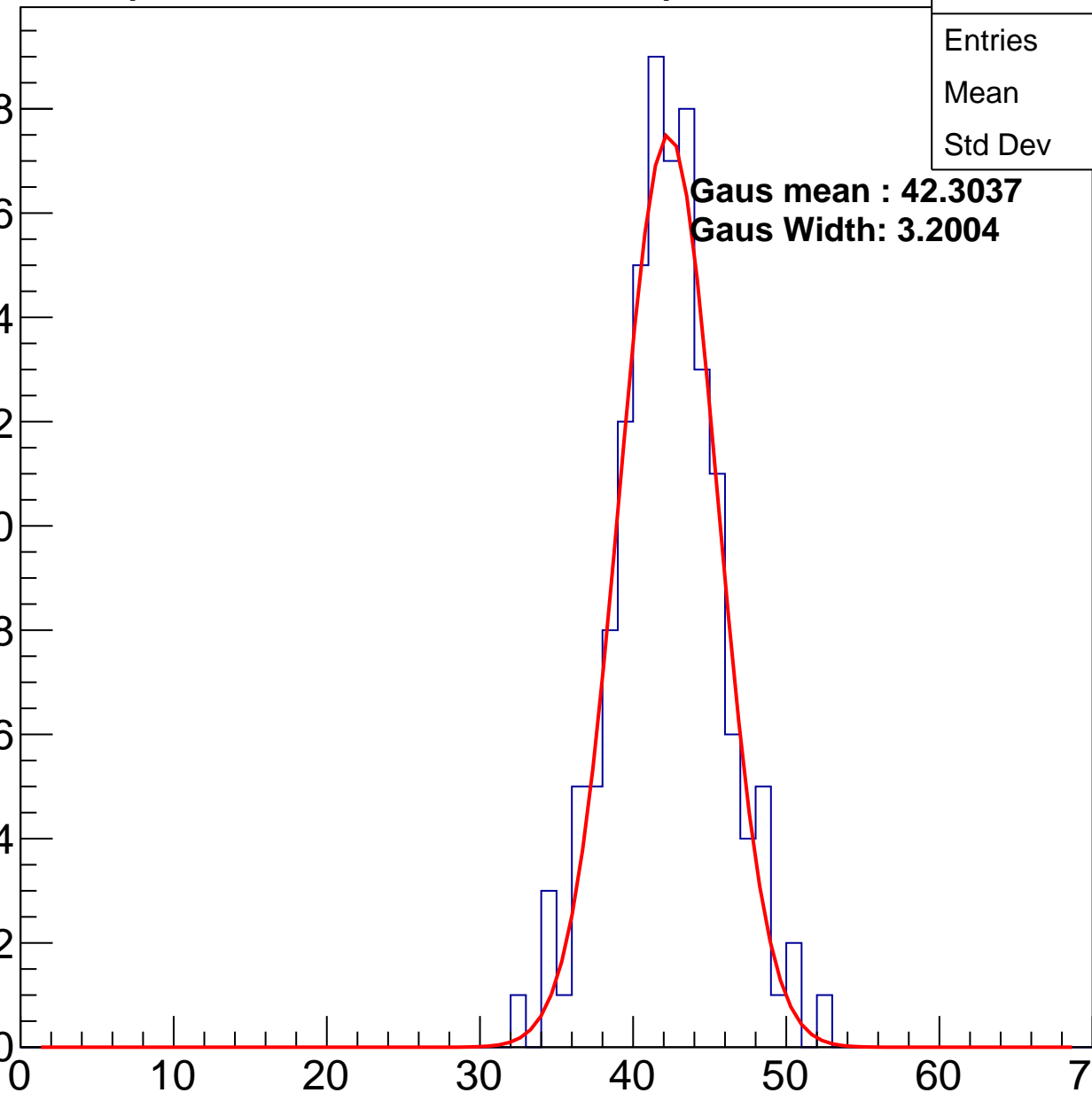
18
16
14
12
10
8
6
4
2
0

Entries	147
Mean	41.8
Std Dev	3.477

Gaus mean : 42.3037

Gaus Width: 3.2004

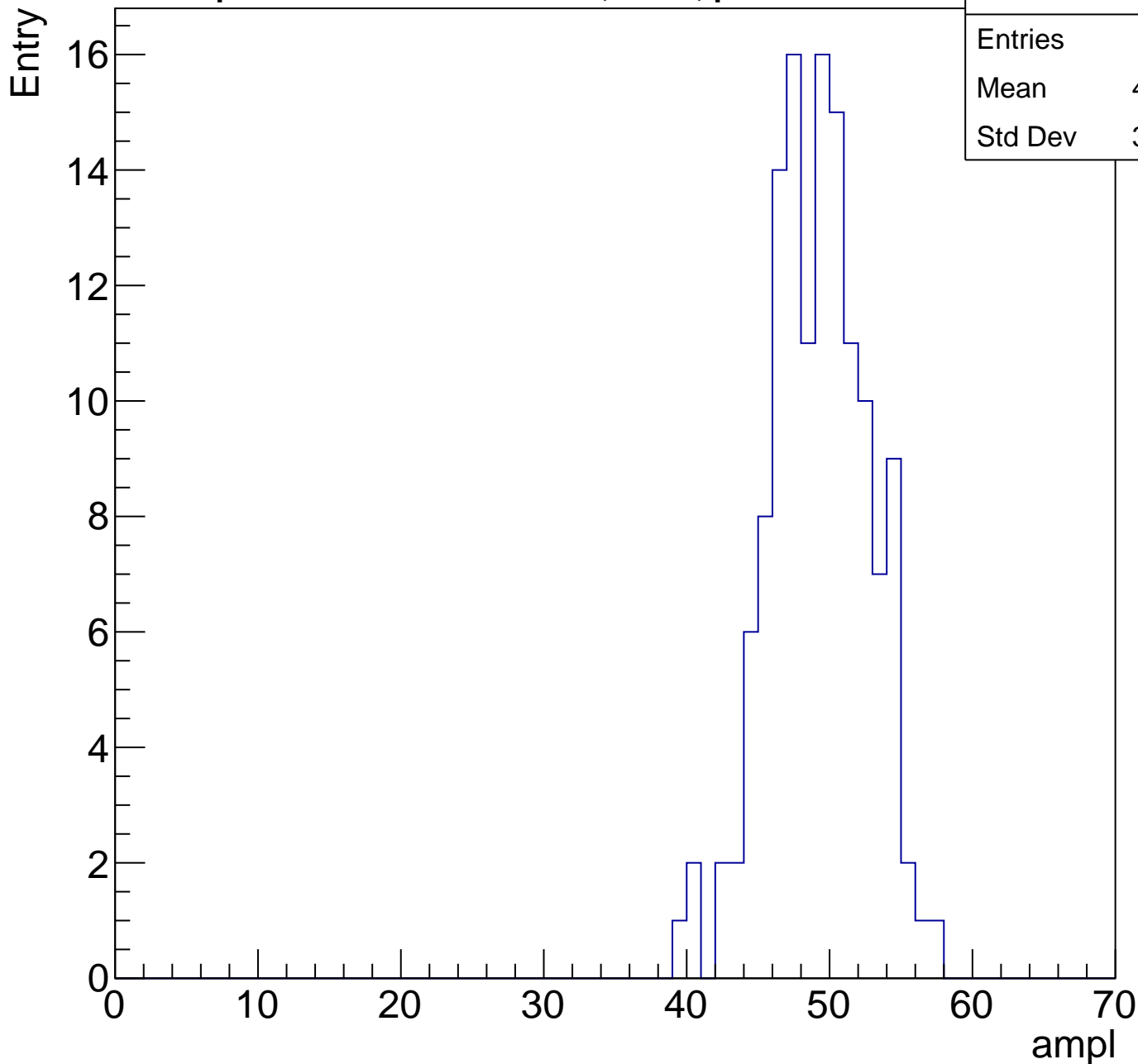
ampl



B0L101S, U20-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	48.75
Std Dev	3.427



B0L101S, U20-ch32, adc4

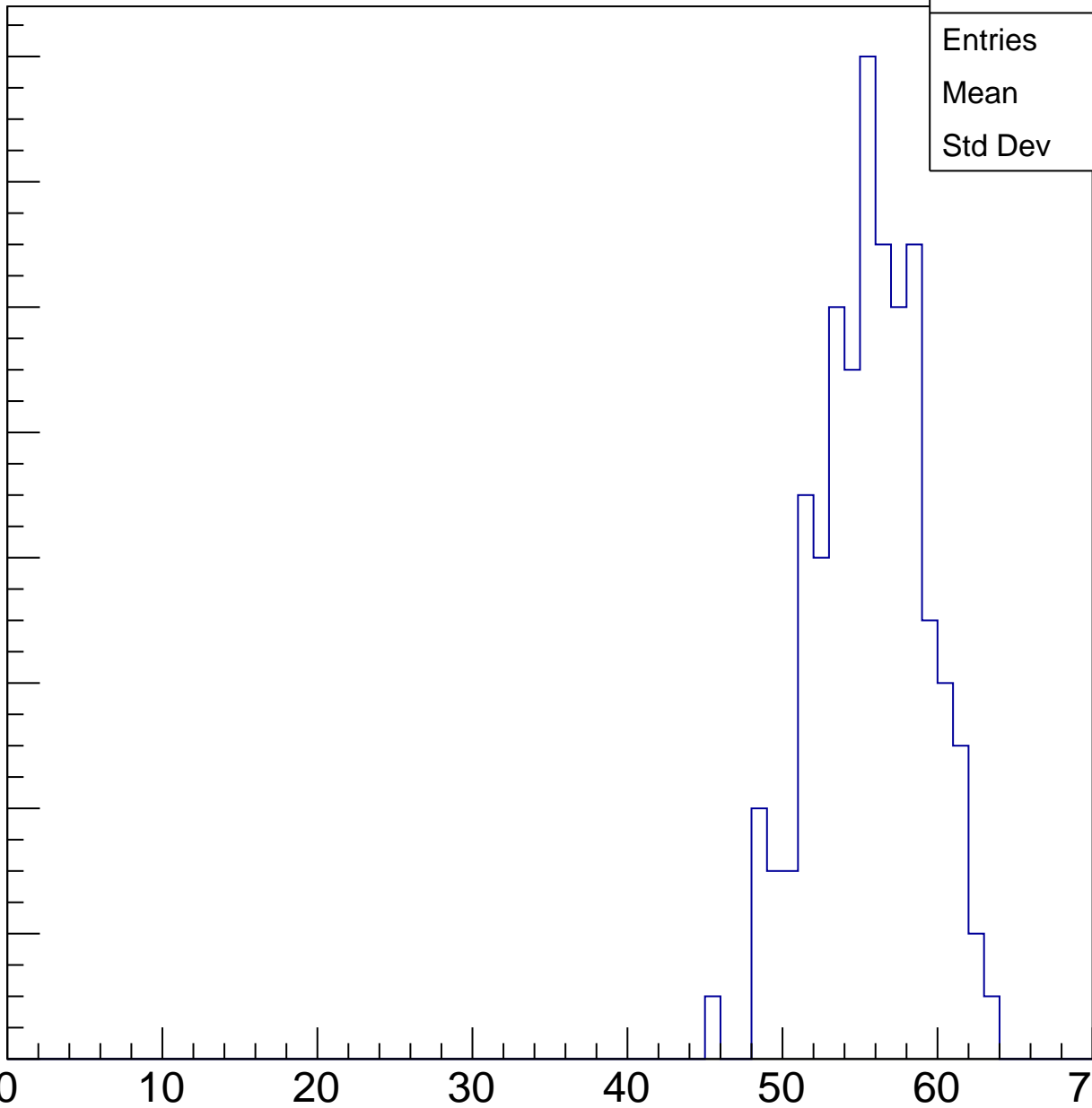
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	55.16
Std Dev	3.488

ampl



B0L101S, U20-ch32, adc5

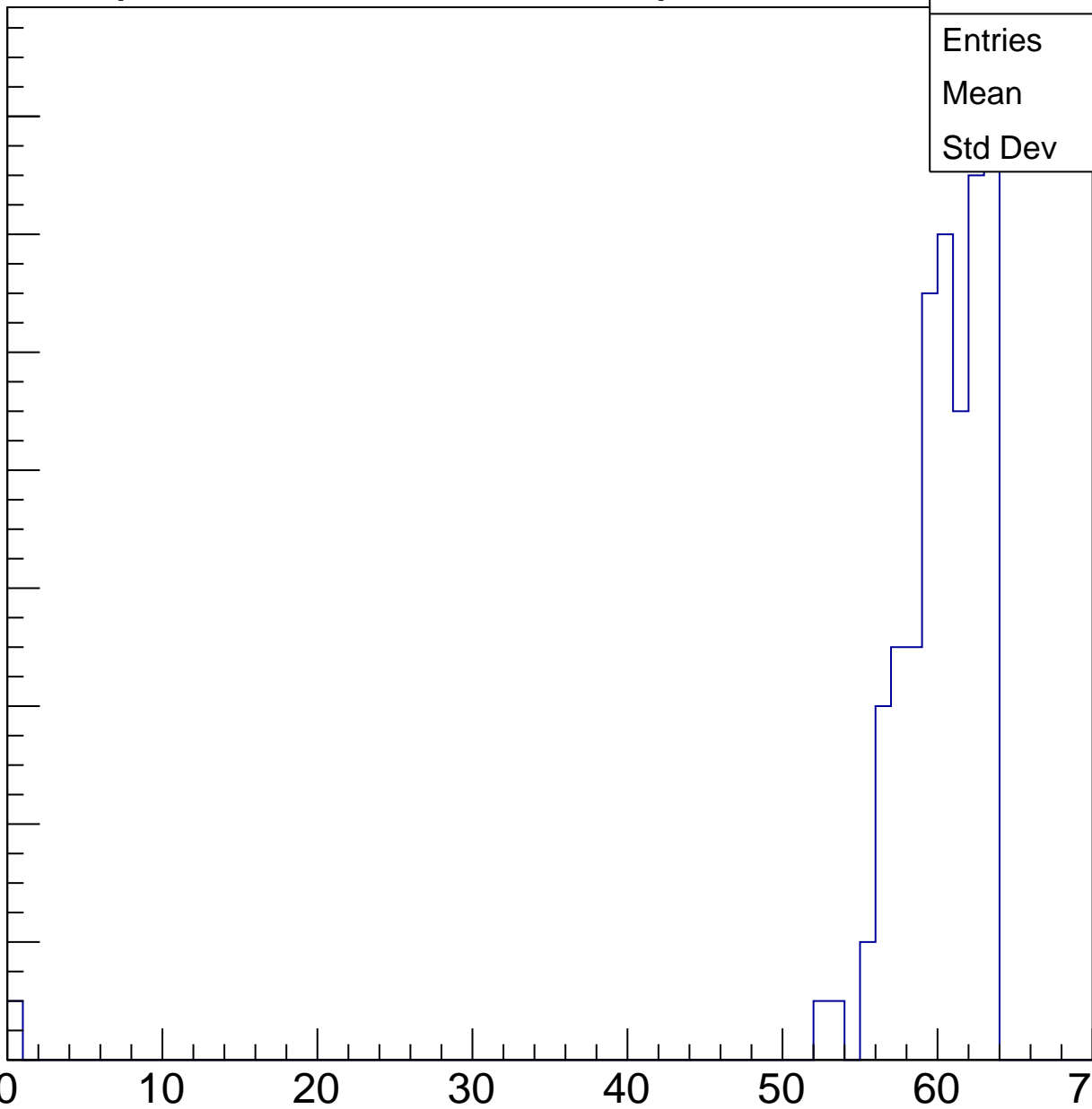
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.32
Std Dev	6.603

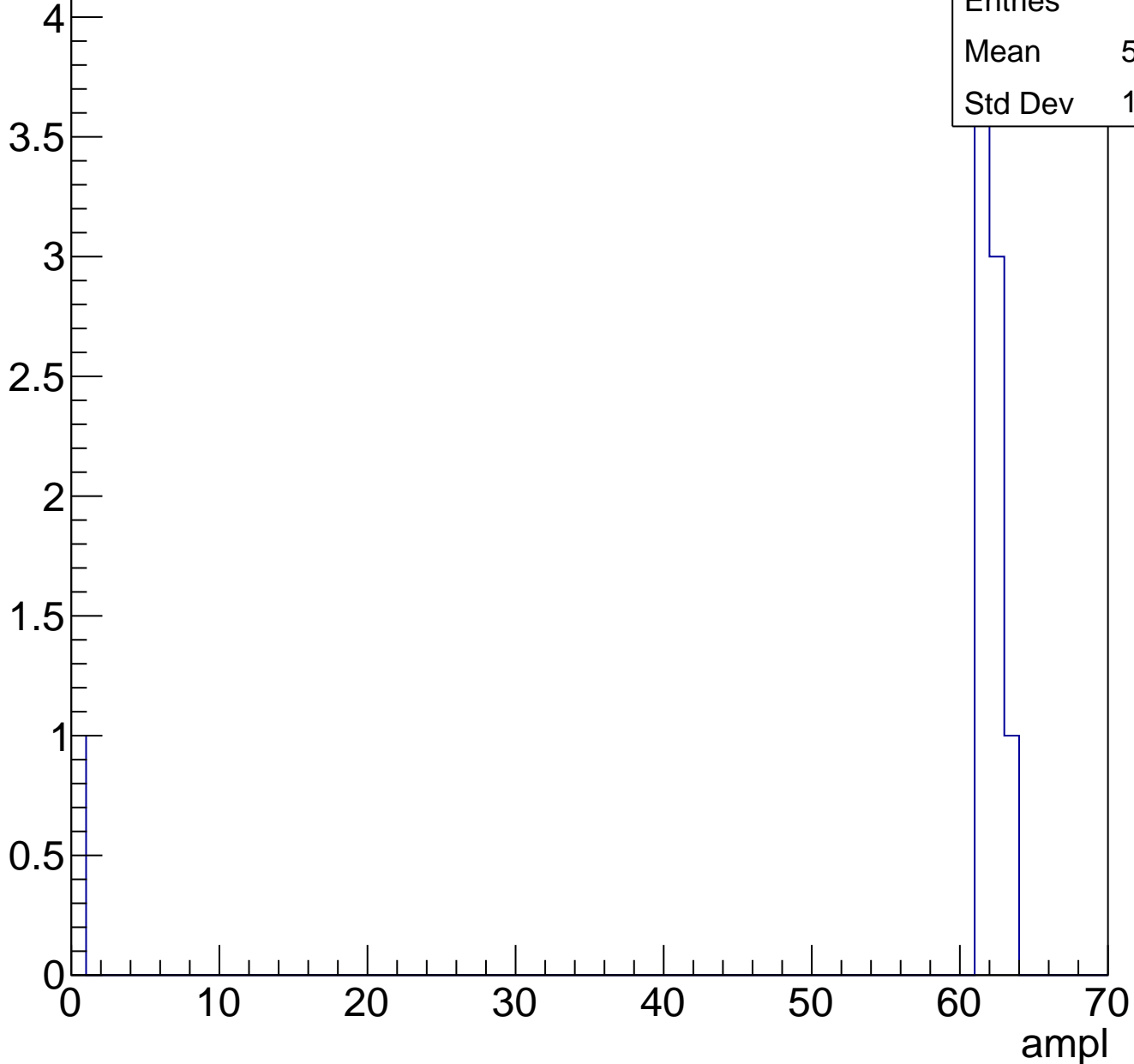
ampl



B0L101S, U20-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

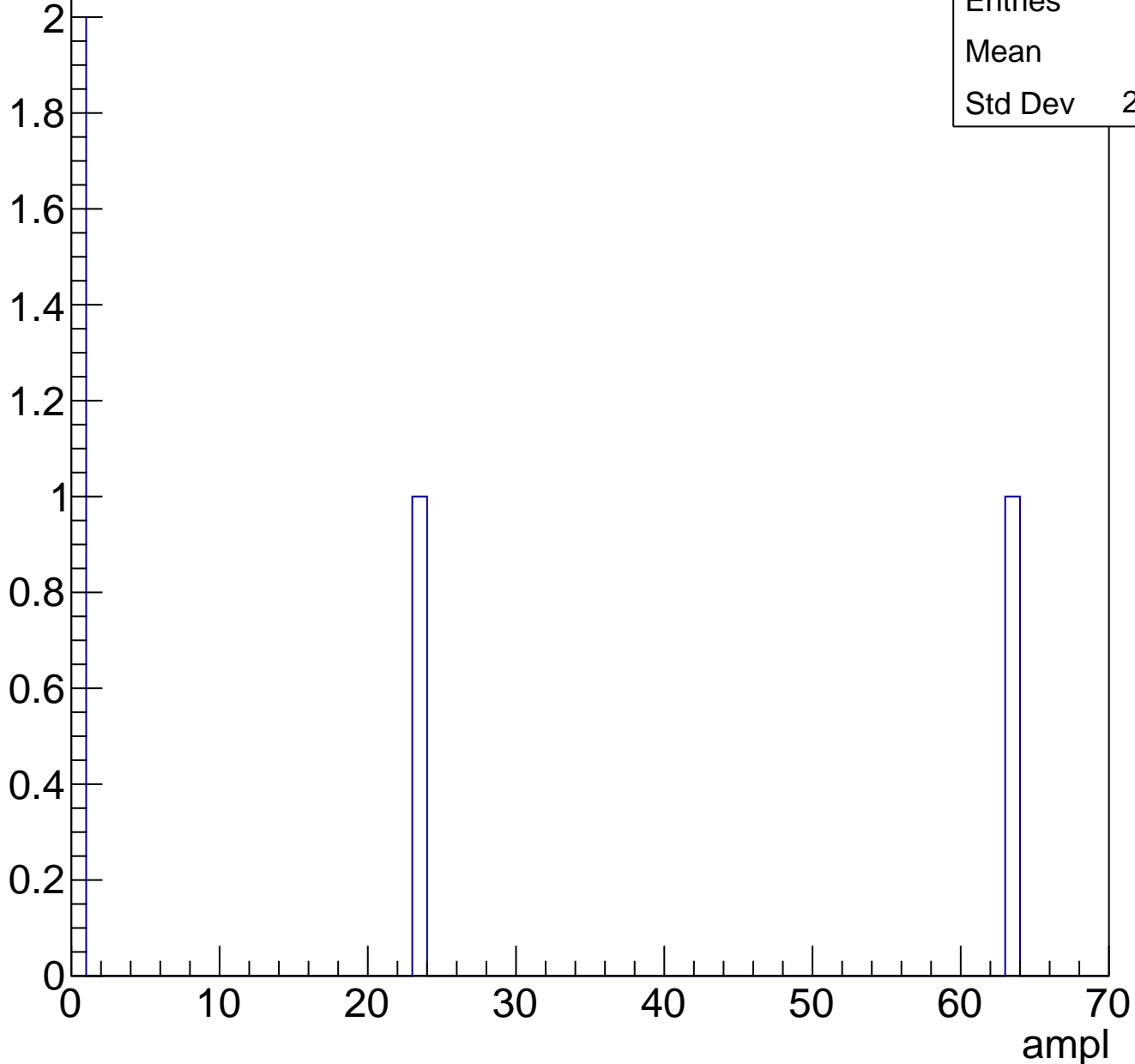


Entries	9
Mean	54.78
Std Dev	19.38

B0L101S, U20-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	21.5
Std Dev	25.73

B0L101S, U20-ch33, adc0

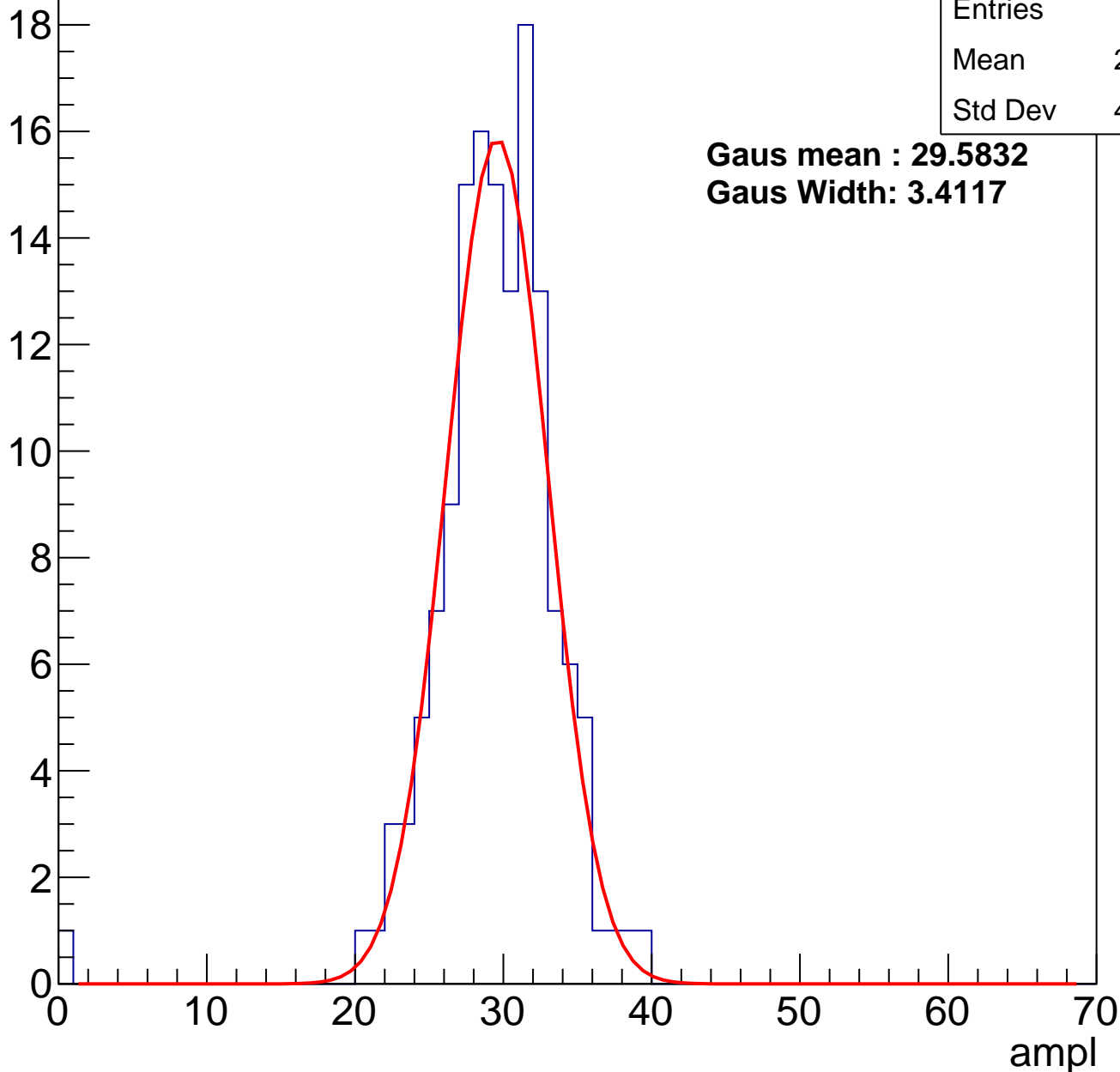
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.99
Std Dev	4.257

Gaus mean : 29.5832

Gaus Width: 3.4117

Entry



B0L101S, U20-ch33, adc1

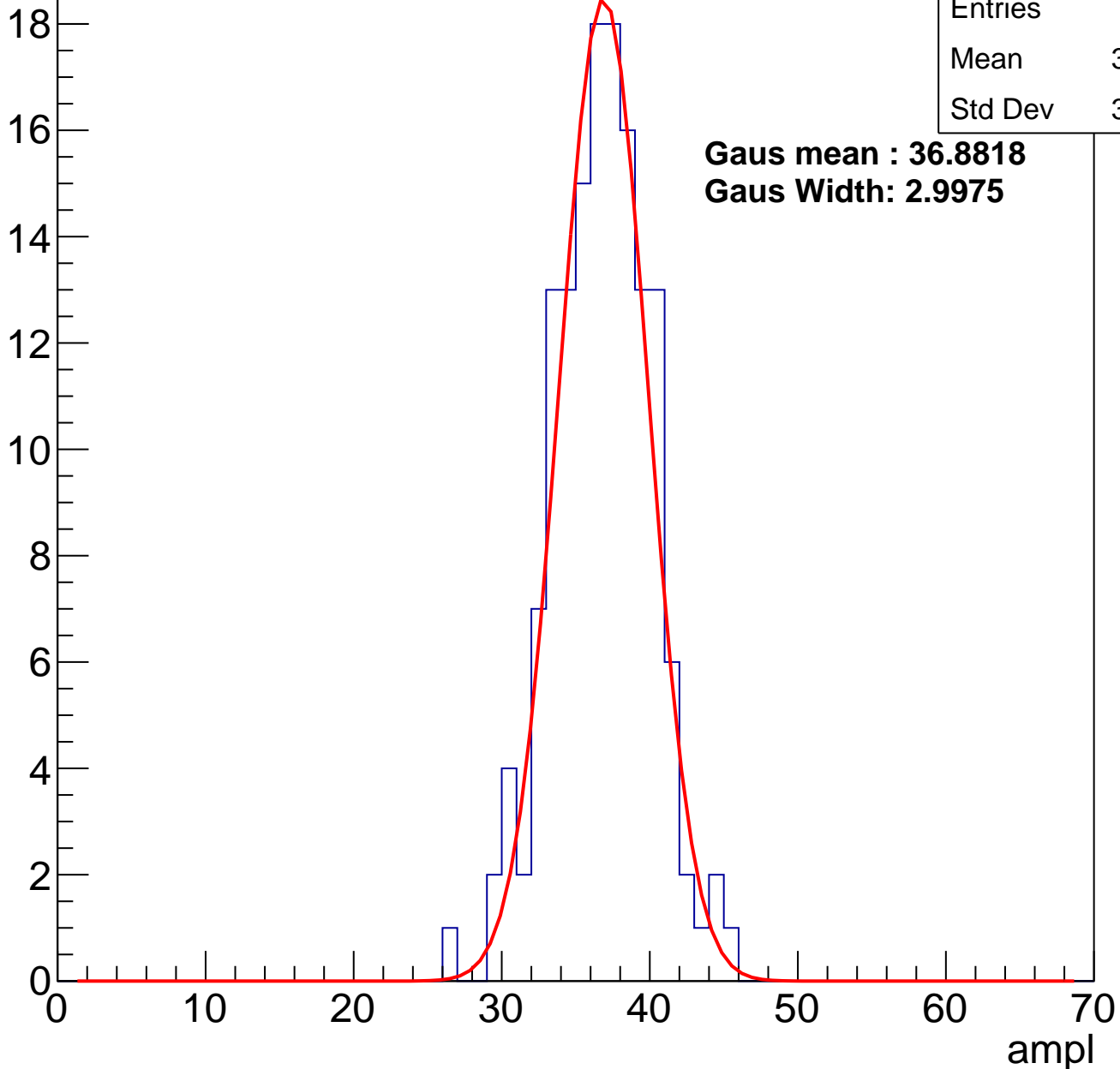
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	36.33
Std Dev	3.256

Gaus mean : 36.8818

Gaus Width: 2.9975

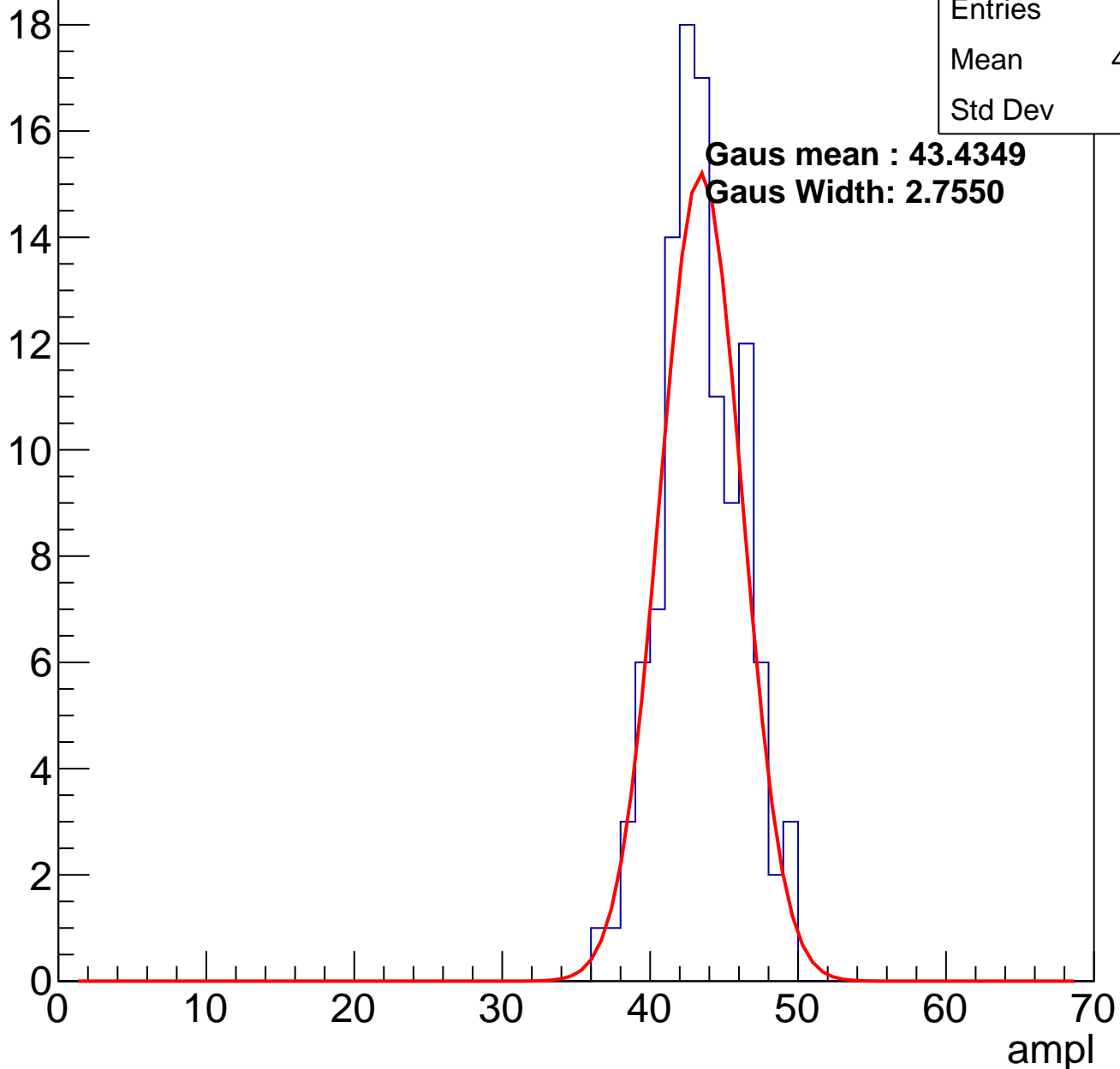
Entry



B0L101S, U20-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

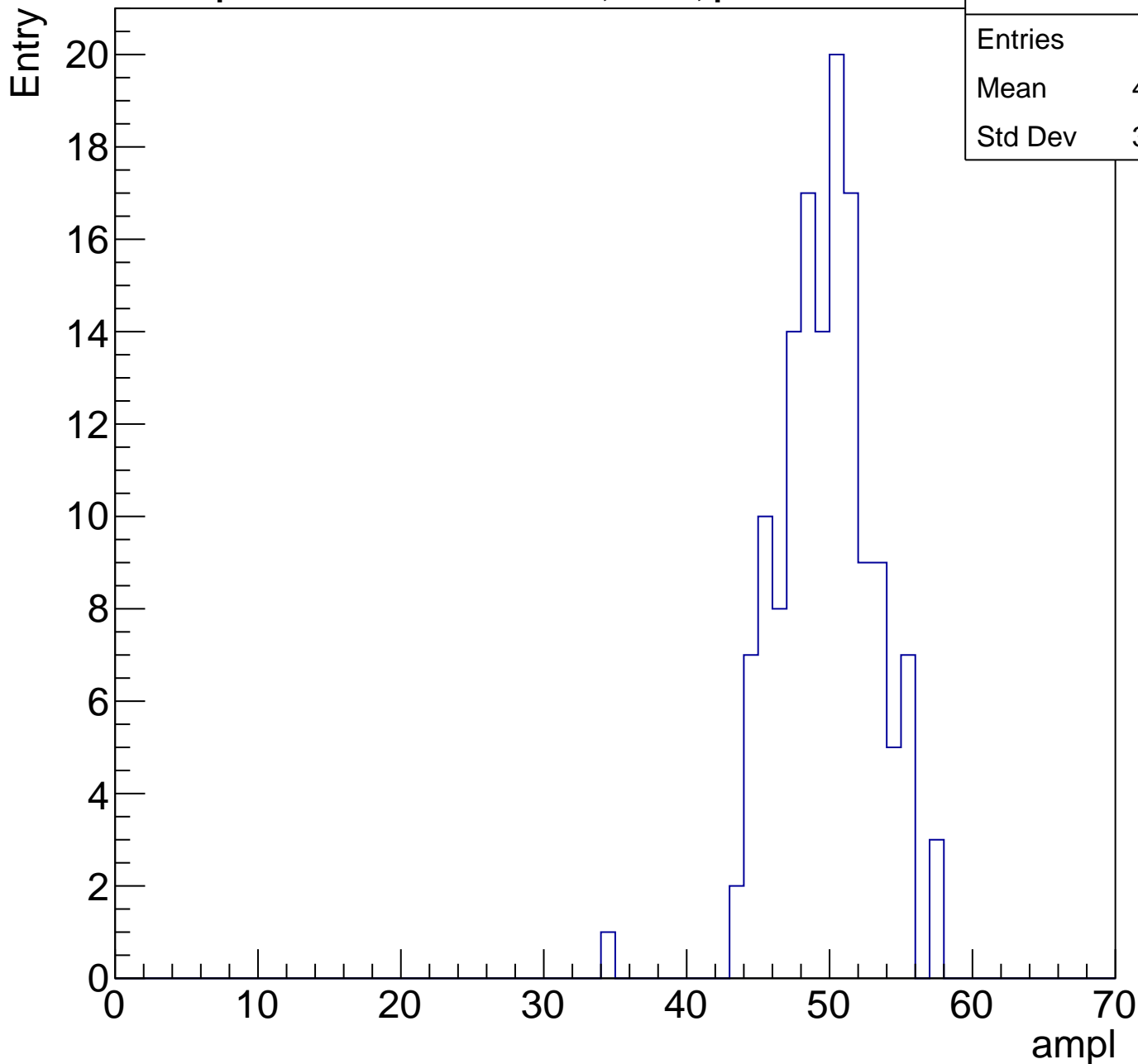
Entry



B0L101S, U20-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	49.26
Std Dev	3.396



B0L101S, U20-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

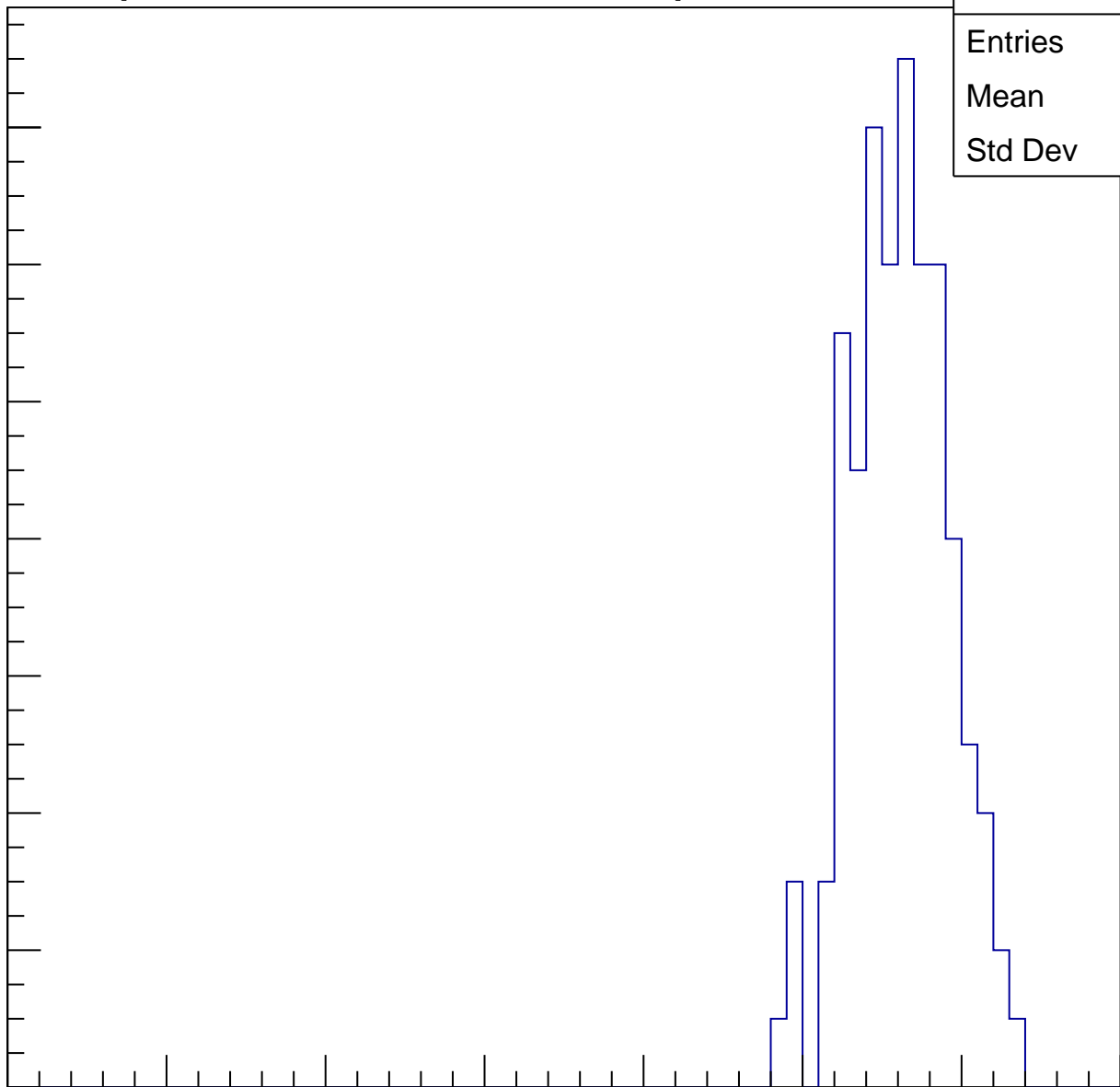
Entries	112
Mean	55.68
Std Dev	3.036

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch33, adc5

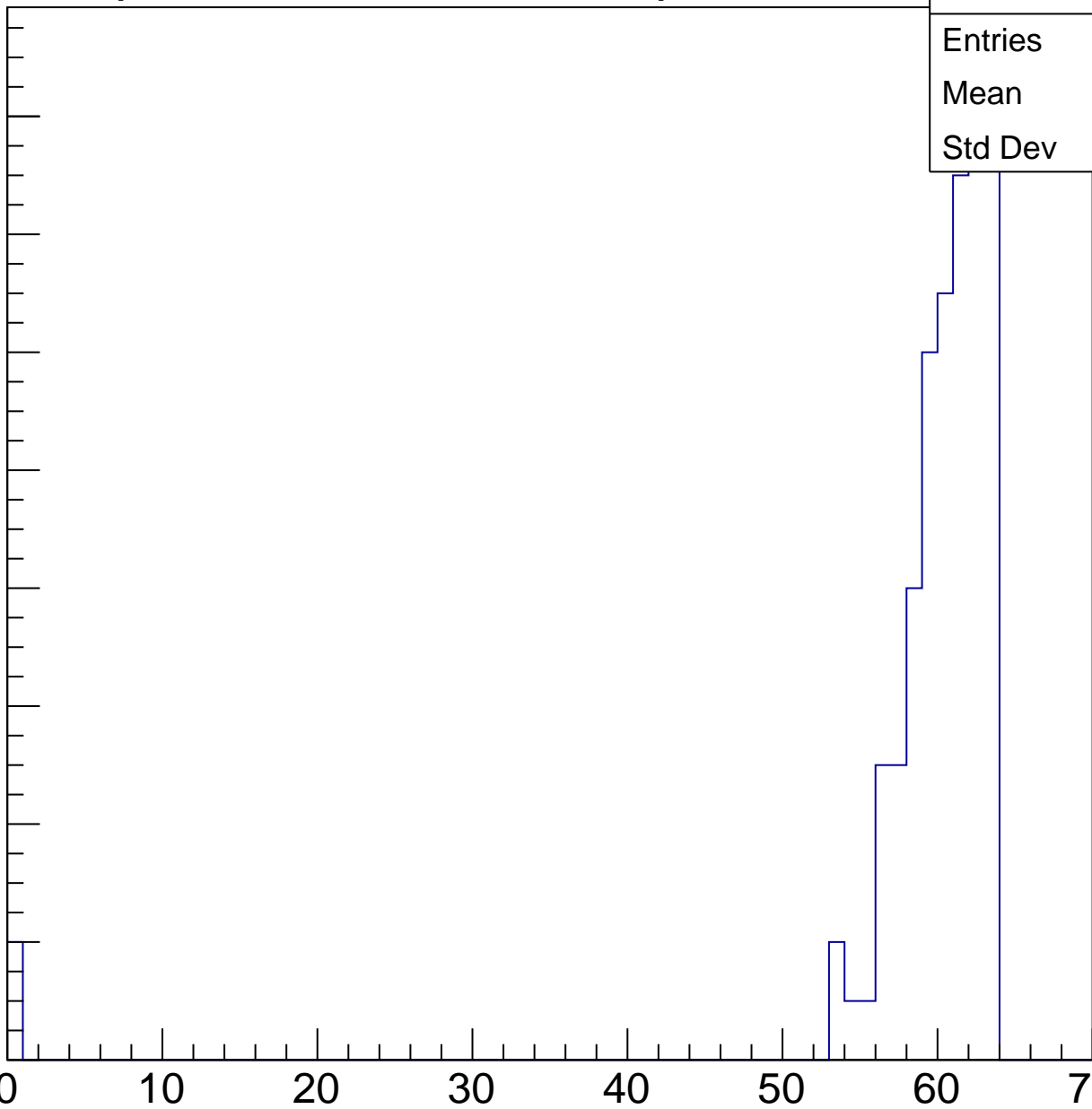
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.87
Std Dev	8.872

ampl



B0L101S, U20-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

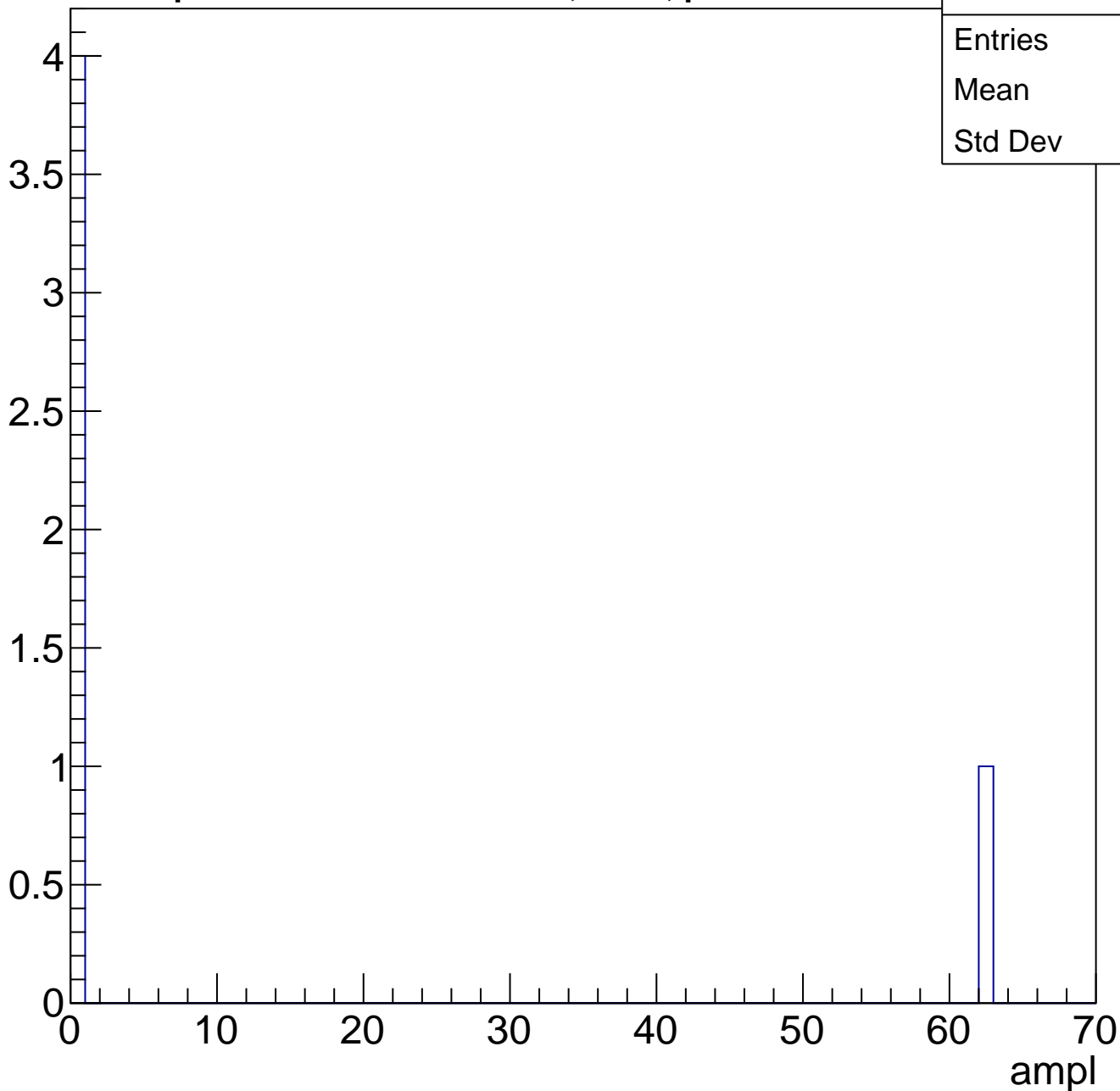
ampl

0 10 20 30 40 50 60 70

B0L101S, U20-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch34, adc0

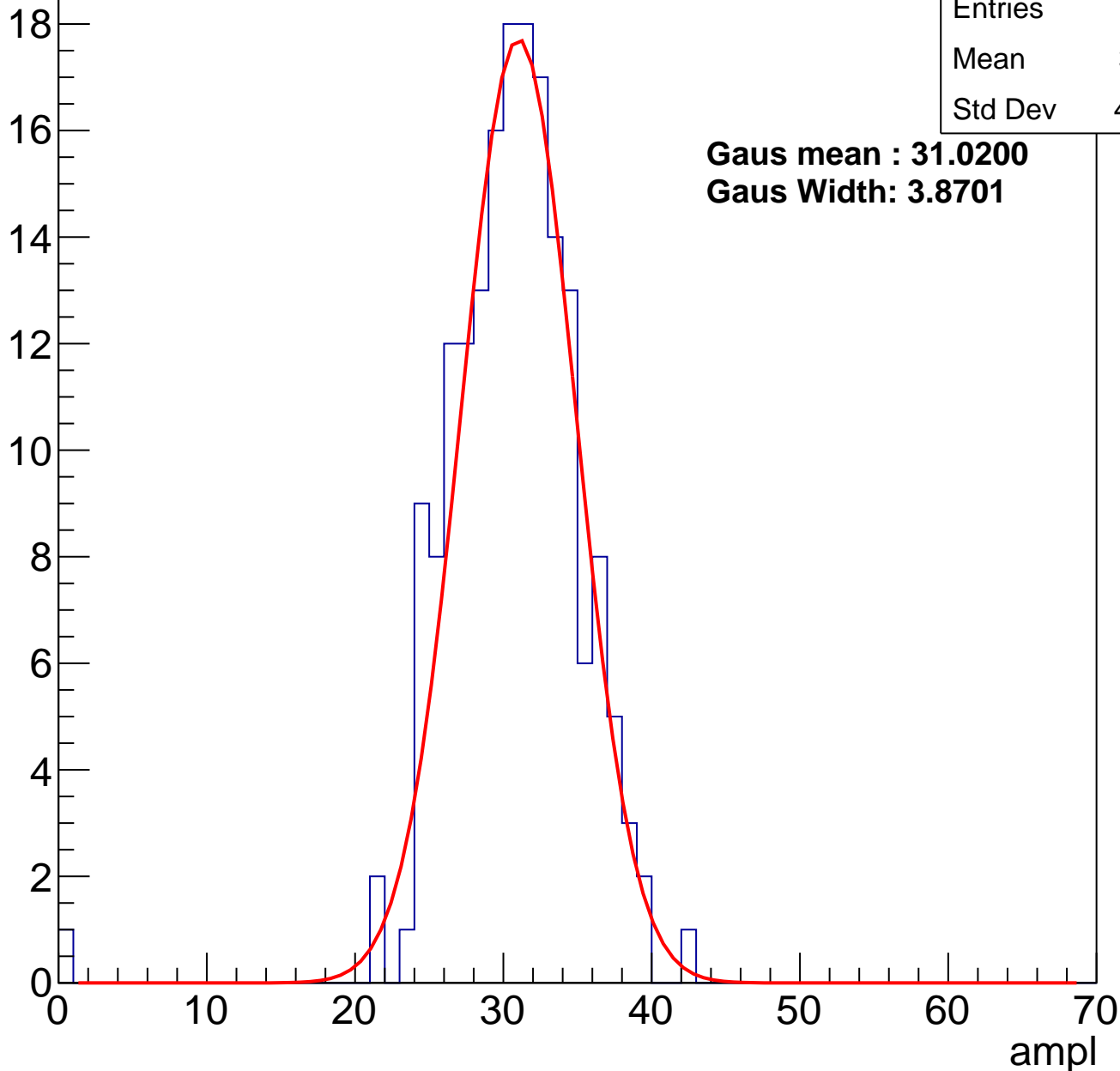
calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	30.21
Std Dev	4.482

Gaus mean : 31.0200

Gaus Width: 3.8701

Entry



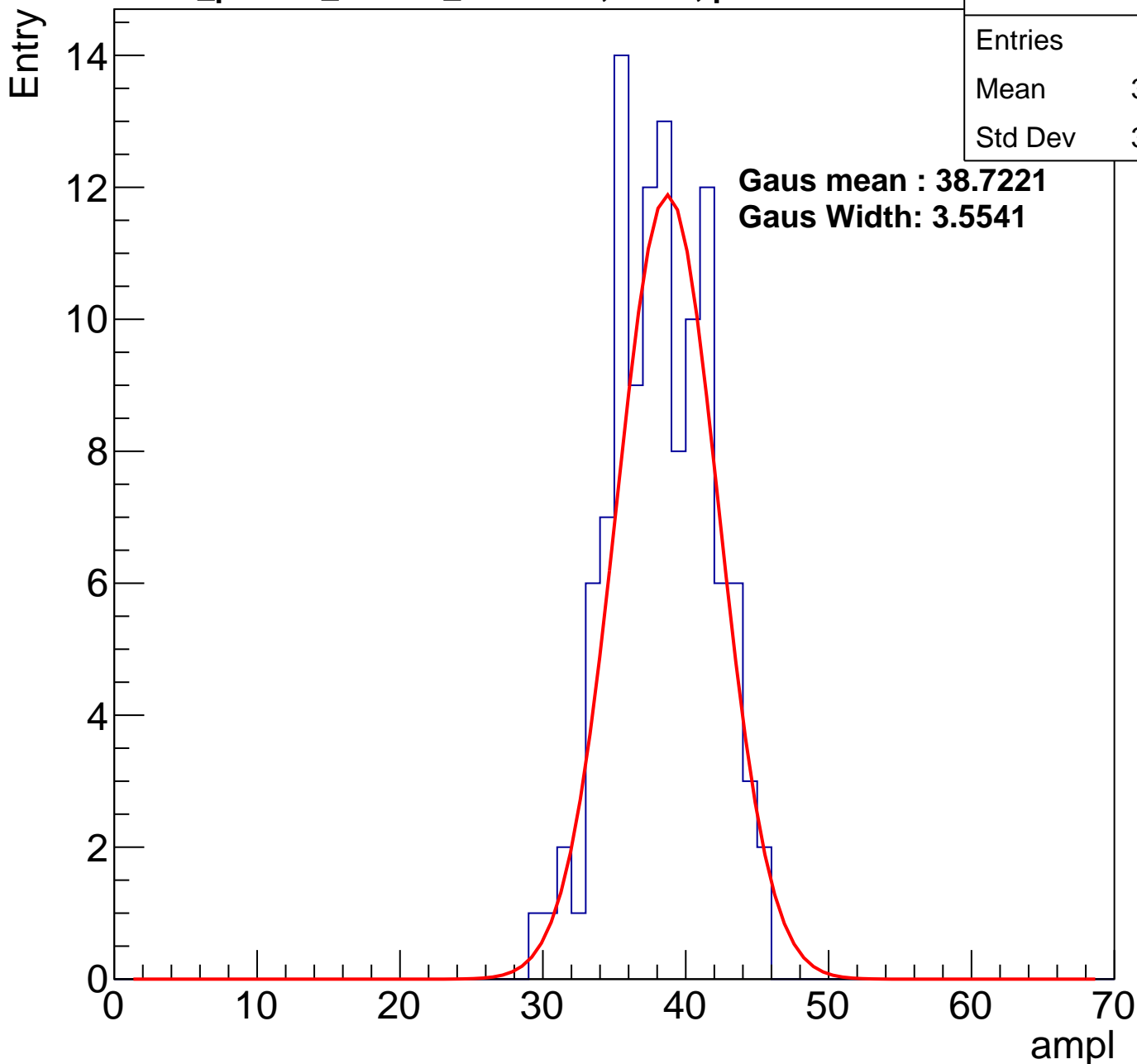
B0L101S, U20-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	37.85
Std Dev	3.418

Gaus mean : 38.7221

Gaus Width: 3.5541



B0L101S, U20-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

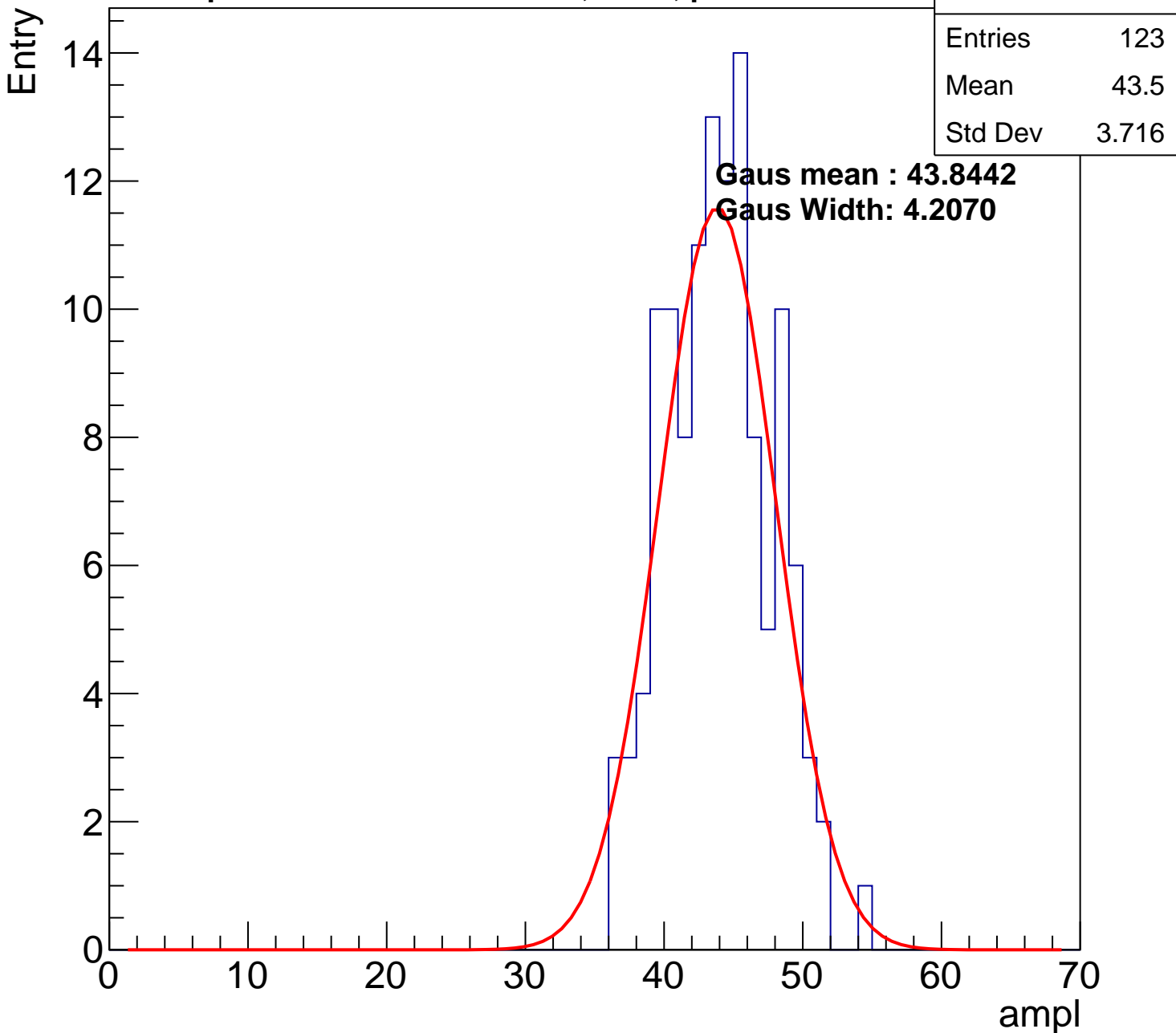
Entries	123
Mean	43.5
Std Dev	3.716

Gaus mean : 43.8442

Gaus Width: 4.2070

ampl

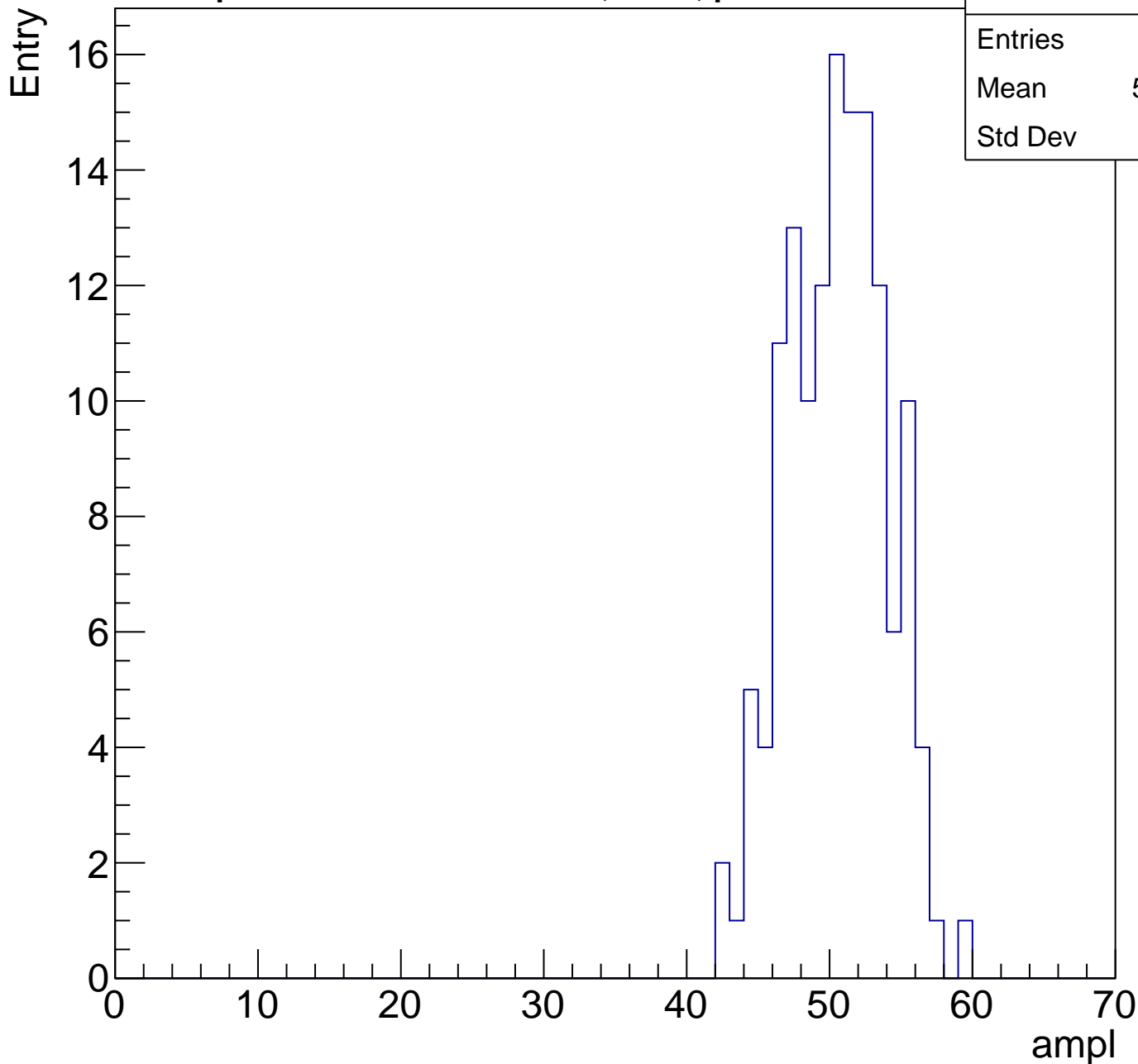
0 10 20 30 40 50 60 70



B0L101S, U20-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

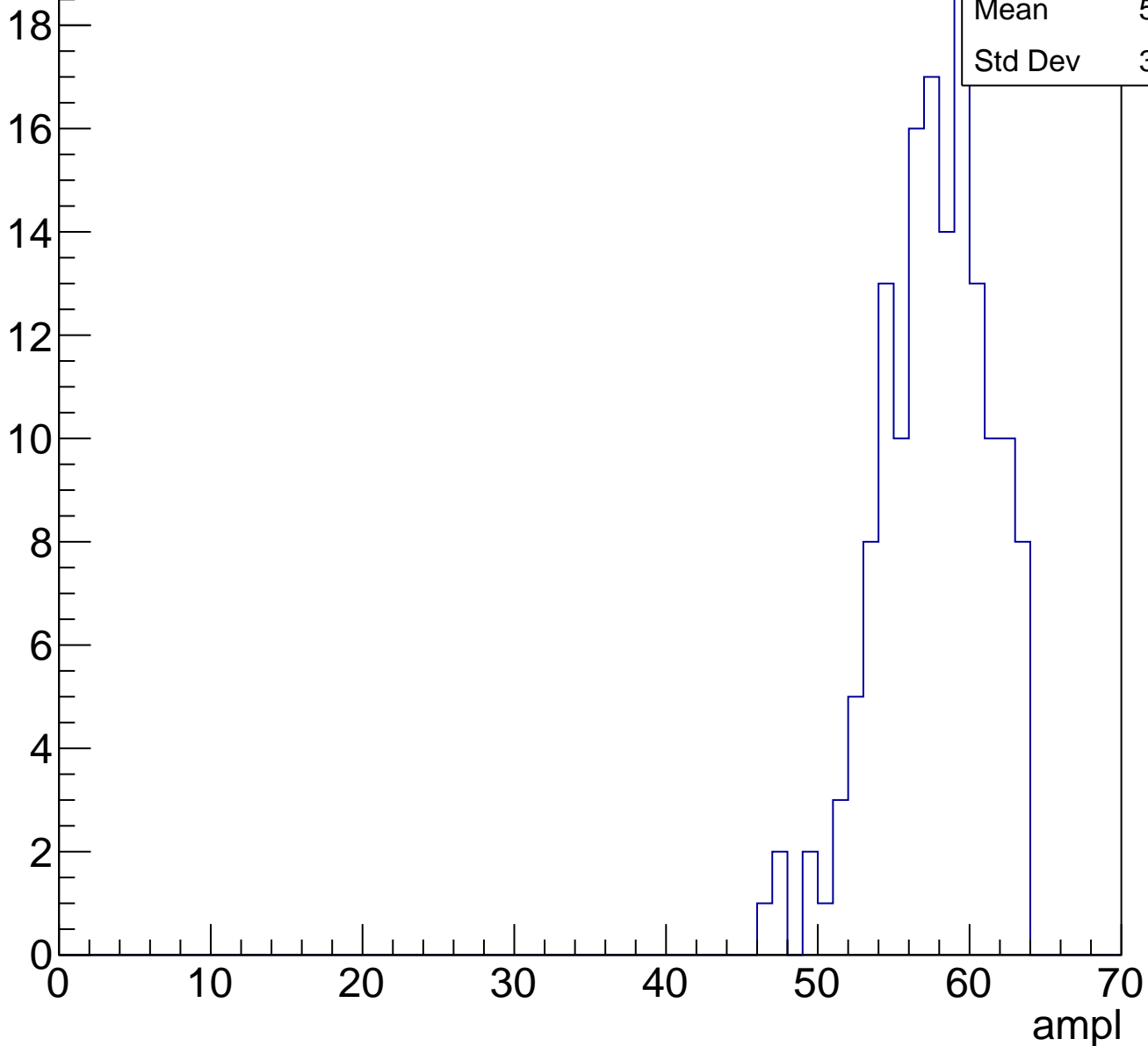
Entries	138
Mean	50.05
Std Dev	3.41



B0L101S, U20-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

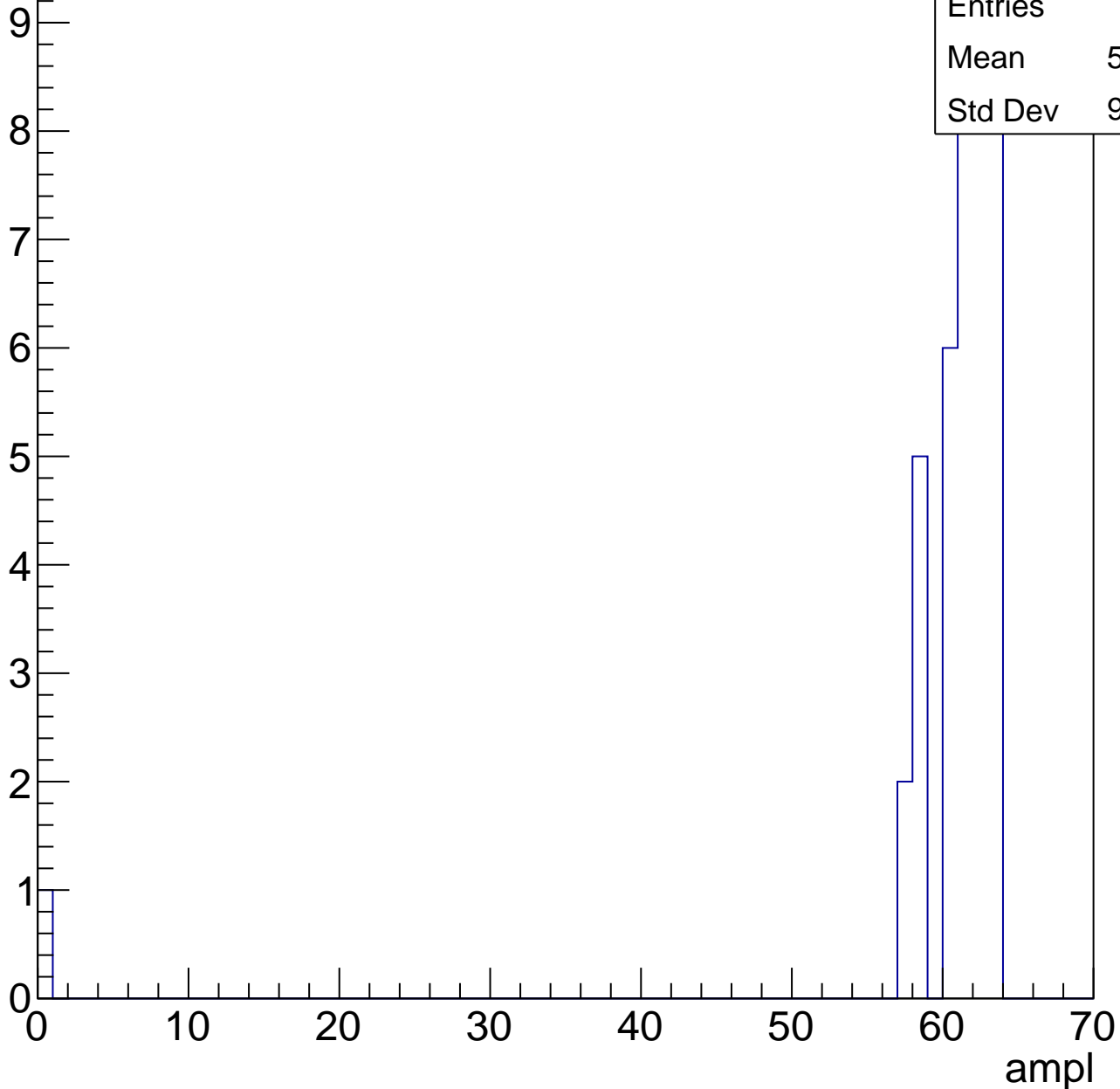


B0L101S, U20-ch34, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

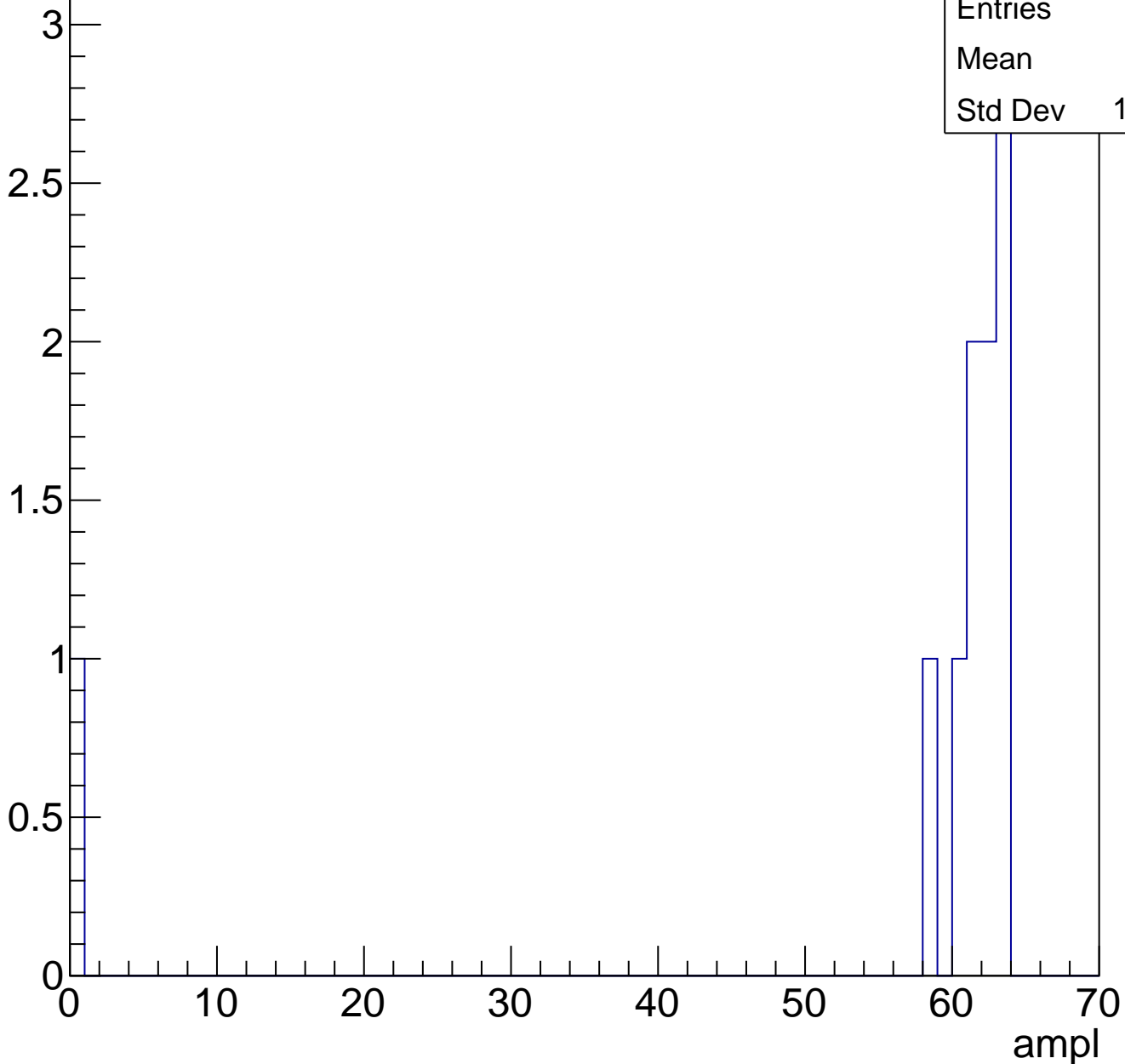
Entries	39
Mean	59.36
Std Dev	9.797



B0L101S, U20-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

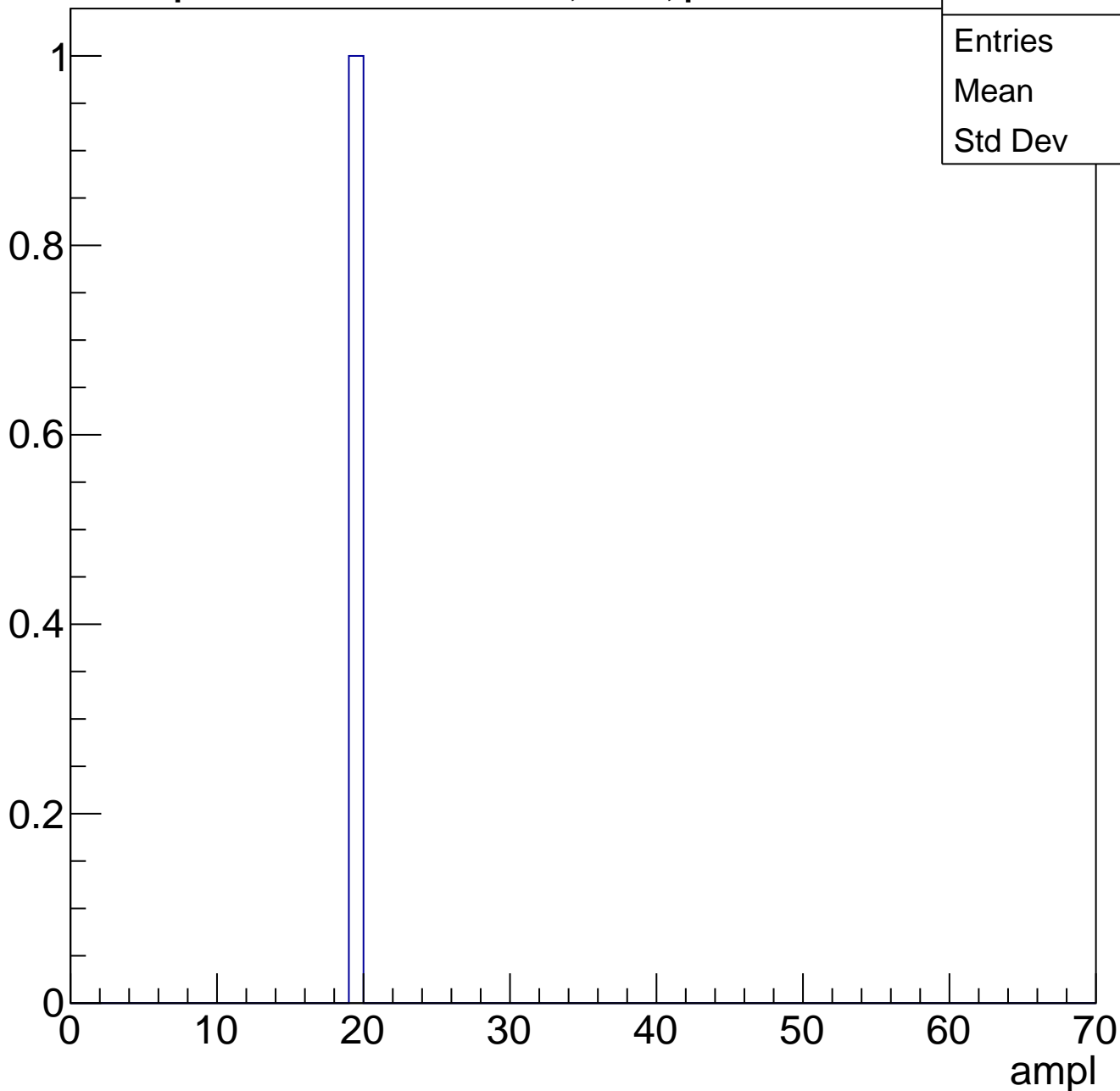
Entry



B0L101S, U20-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	19
Std Dev	0

B0L101S, U20-ch35, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	29.3
Std Dev	3.92

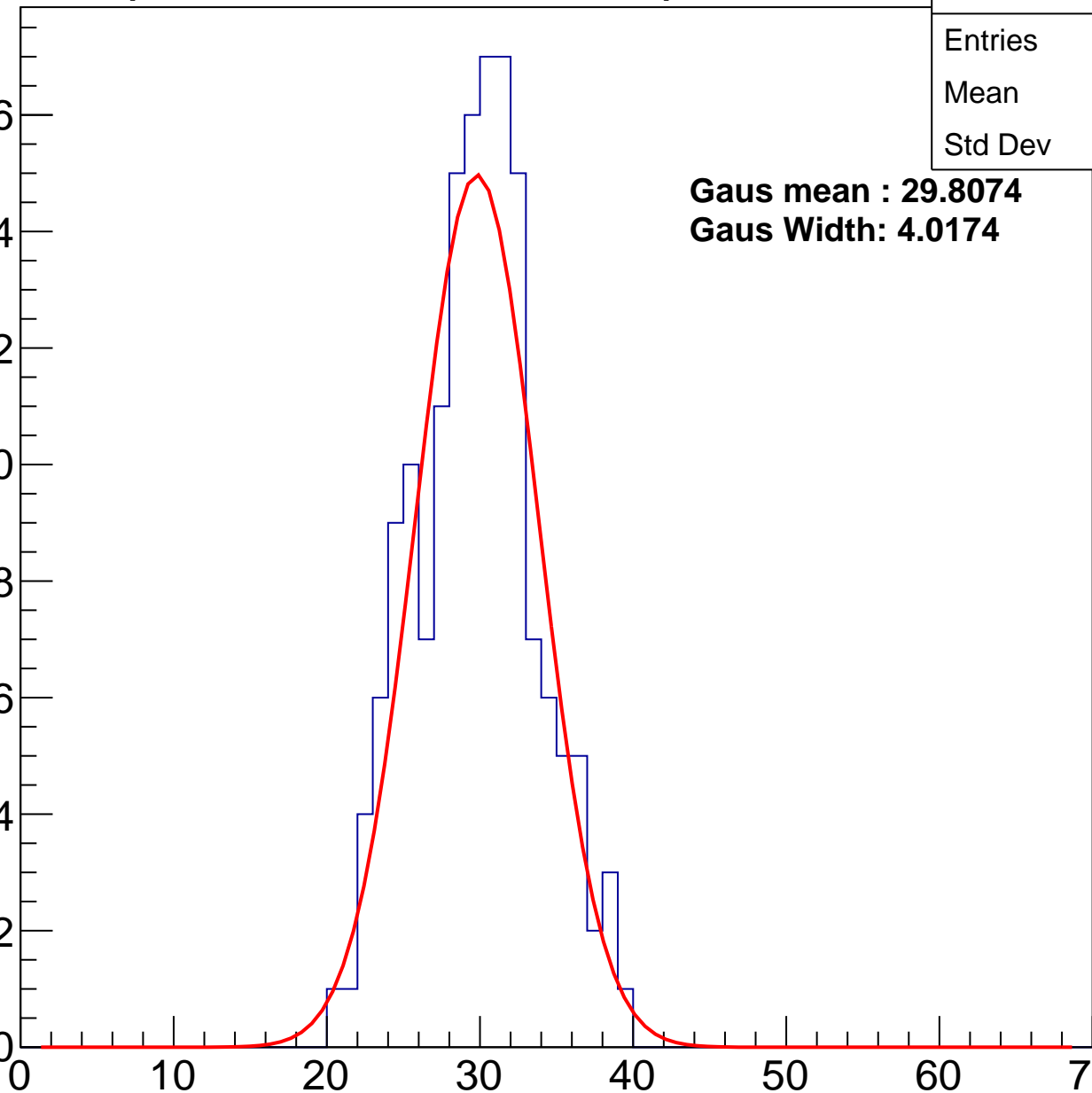
Gaus mean : 29.8074

Gaus Width: 4.0174

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch35, adc1

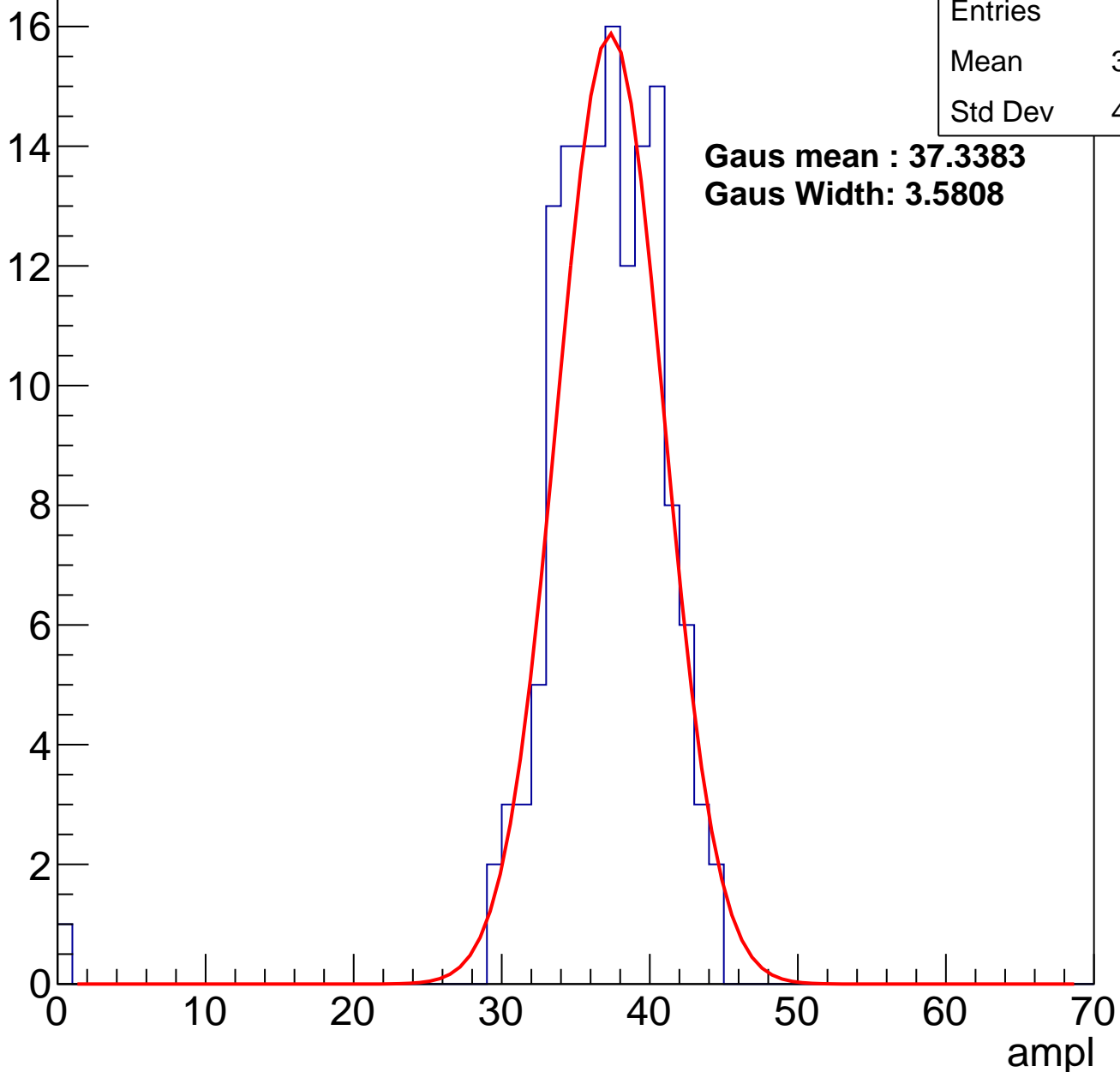
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	36.49
Std Dev	4.495

Gaus mean : 37.3383

Gaus Width: 3.5808

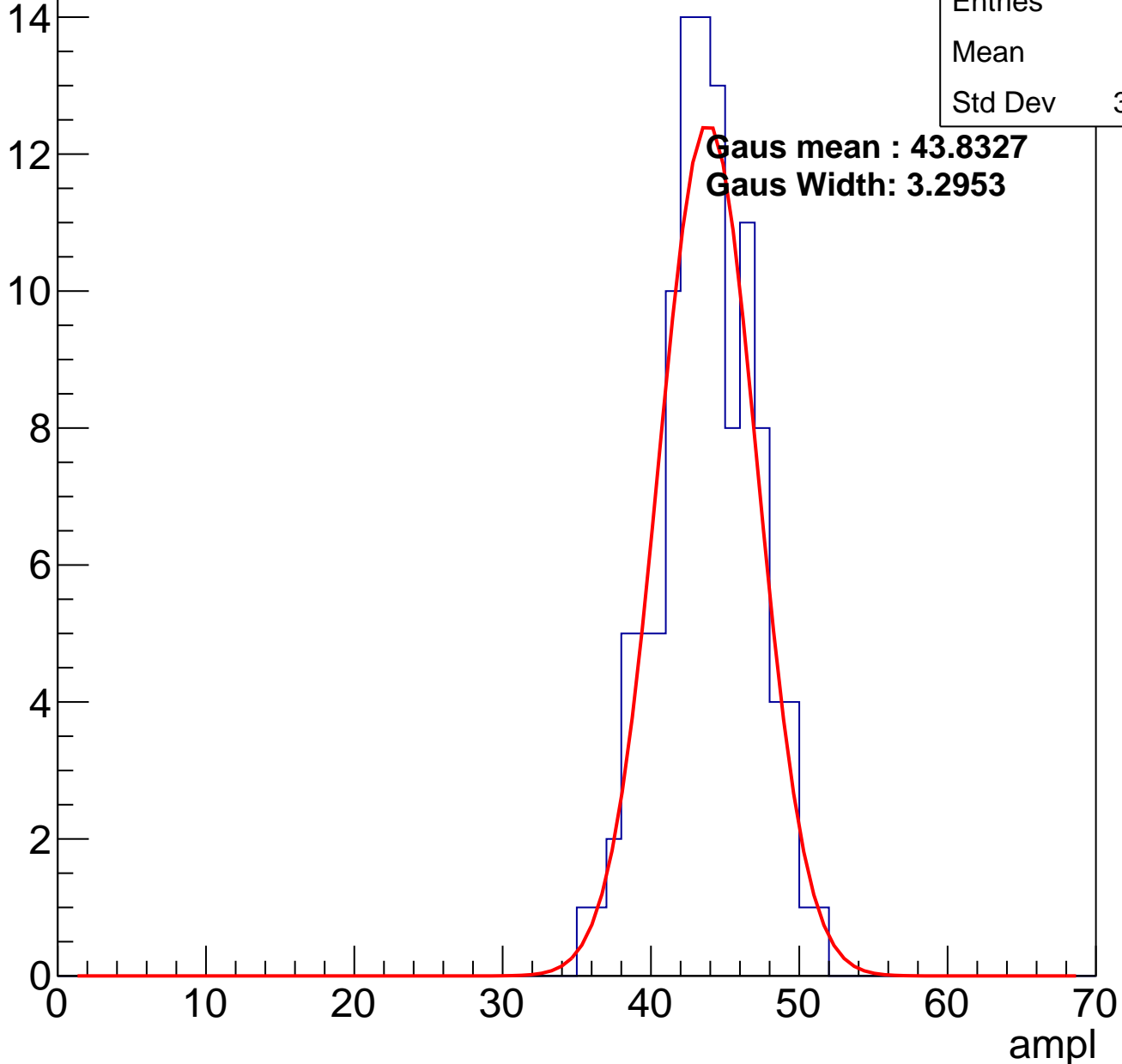
Entry



B0L101S, U20-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

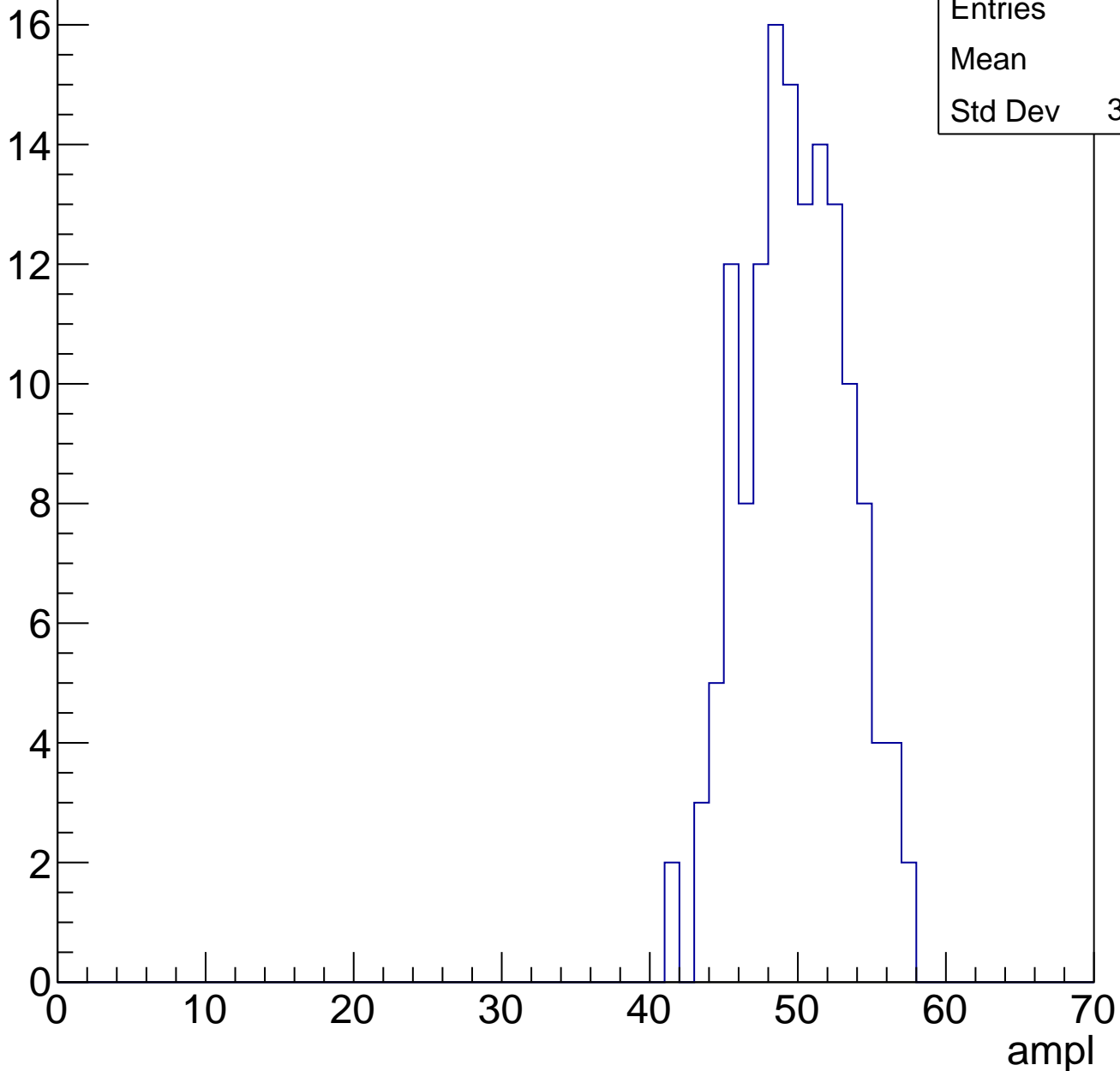
Entry



B0L101S, U20-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch35, adc4

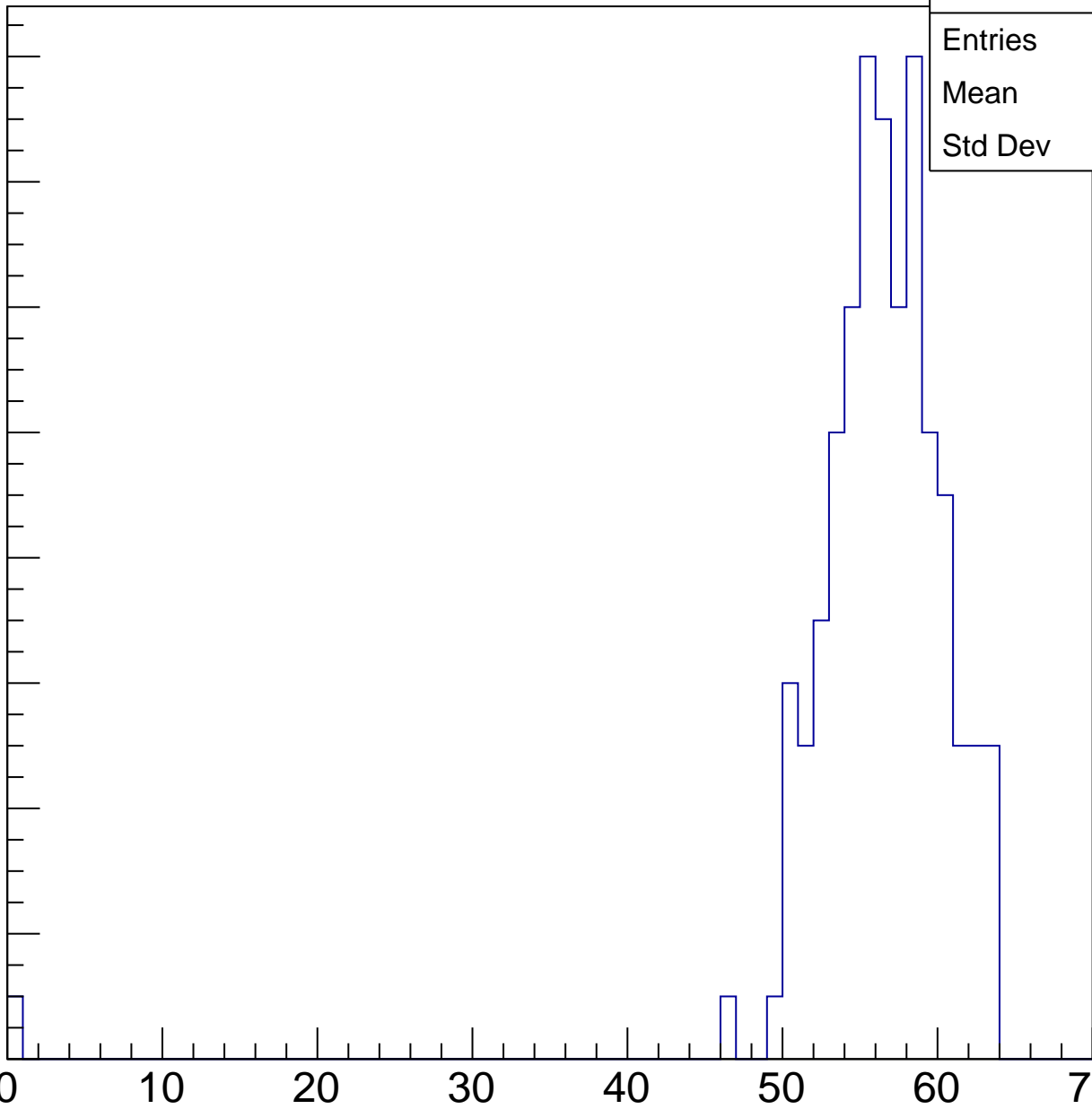
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	55.76
Std Dev	5.906

ampl



B0L101S, U20-ch35, adc5

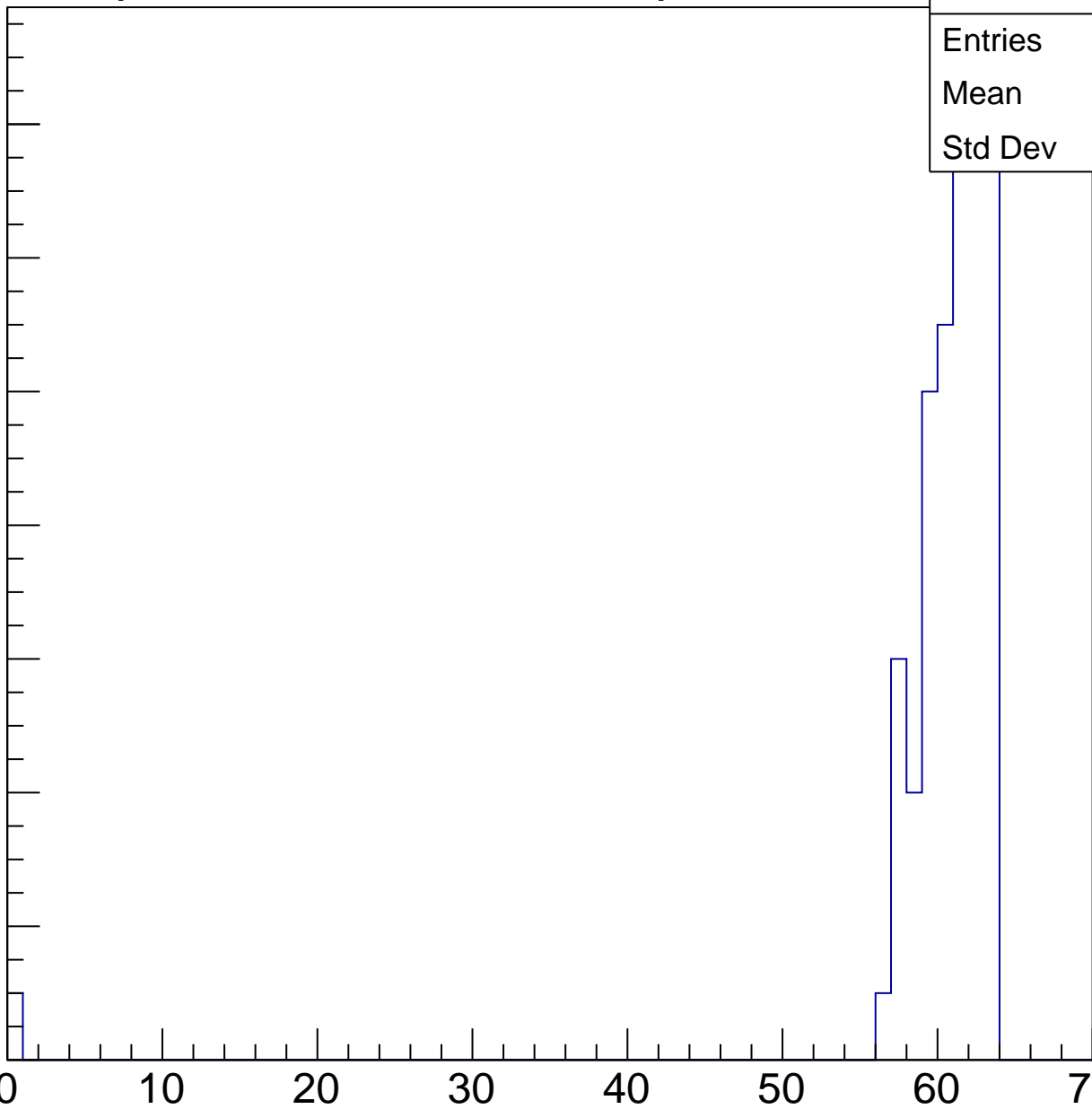
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.8
Std Dev	7.154

ampl



B0L101S, U20-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch36, adc0

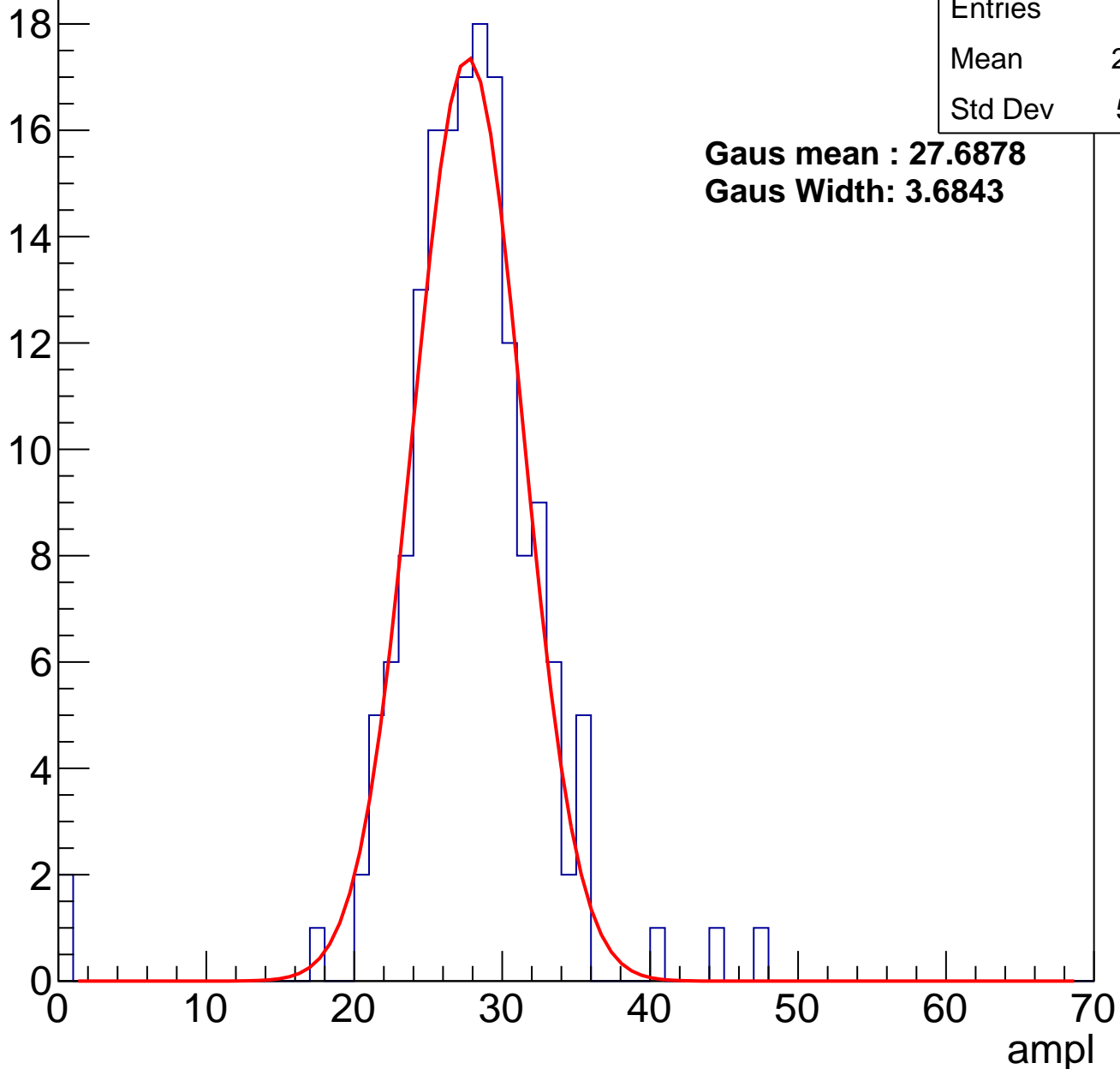
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	27.29
Std Dev	5.111

Gaus mean : 27.6878

Gaus Width: 3.6843

Entry



B0L101S, U20-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	34.9
Std Dev	3.487

Gaus mean : 35.4487

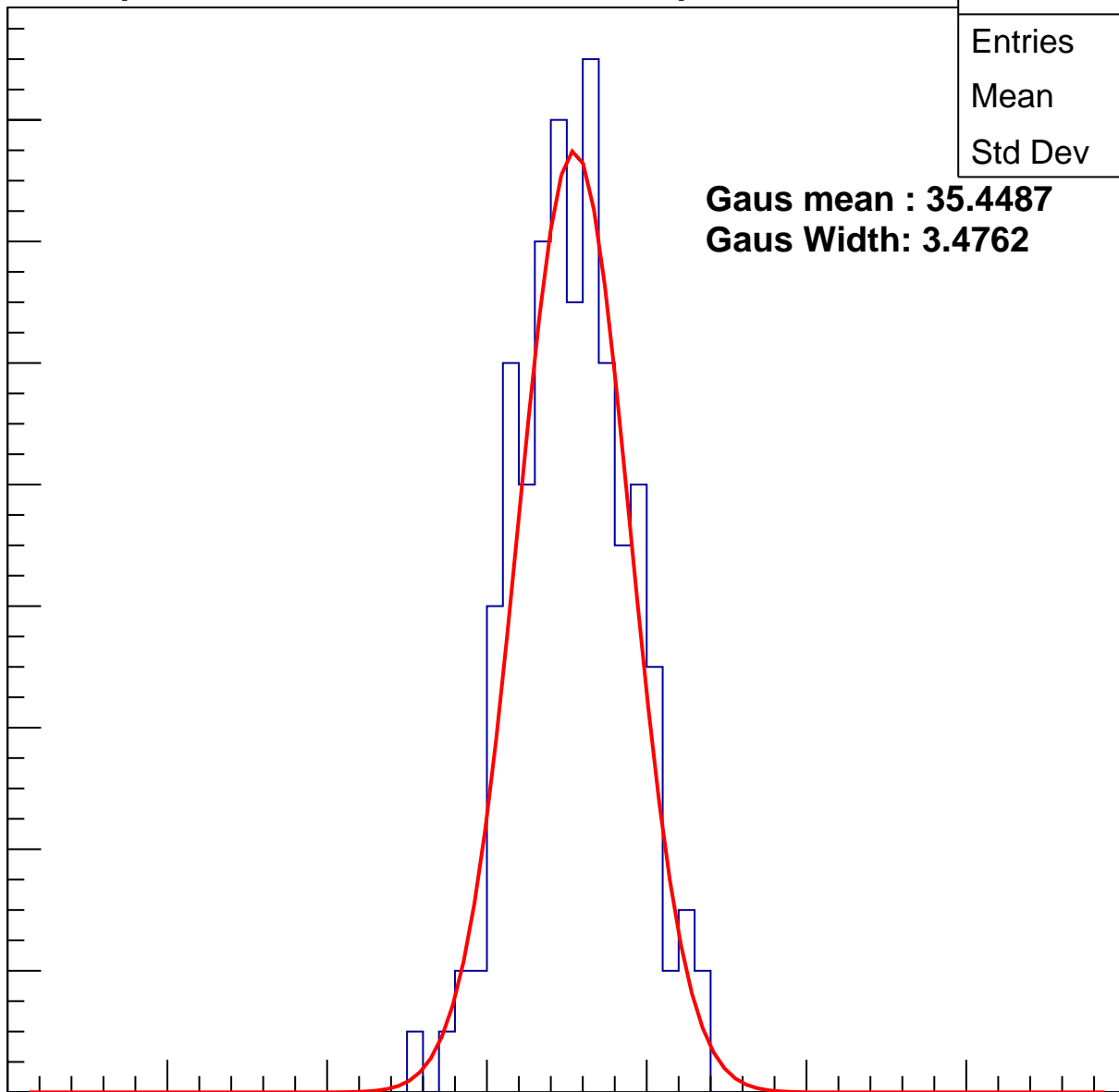
Gaus Width: 3.4762

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

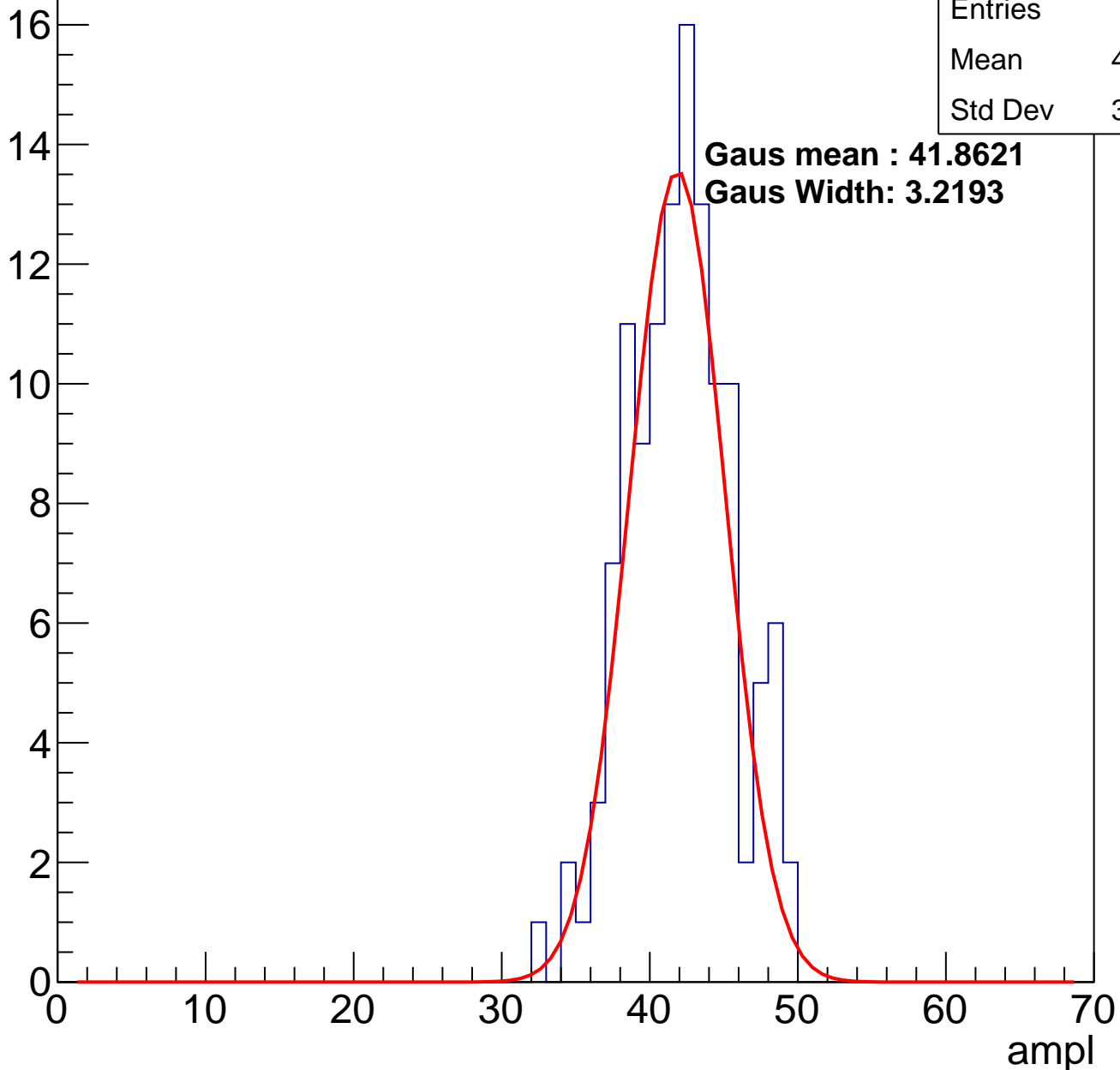
ampl



B0L101S, U20-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

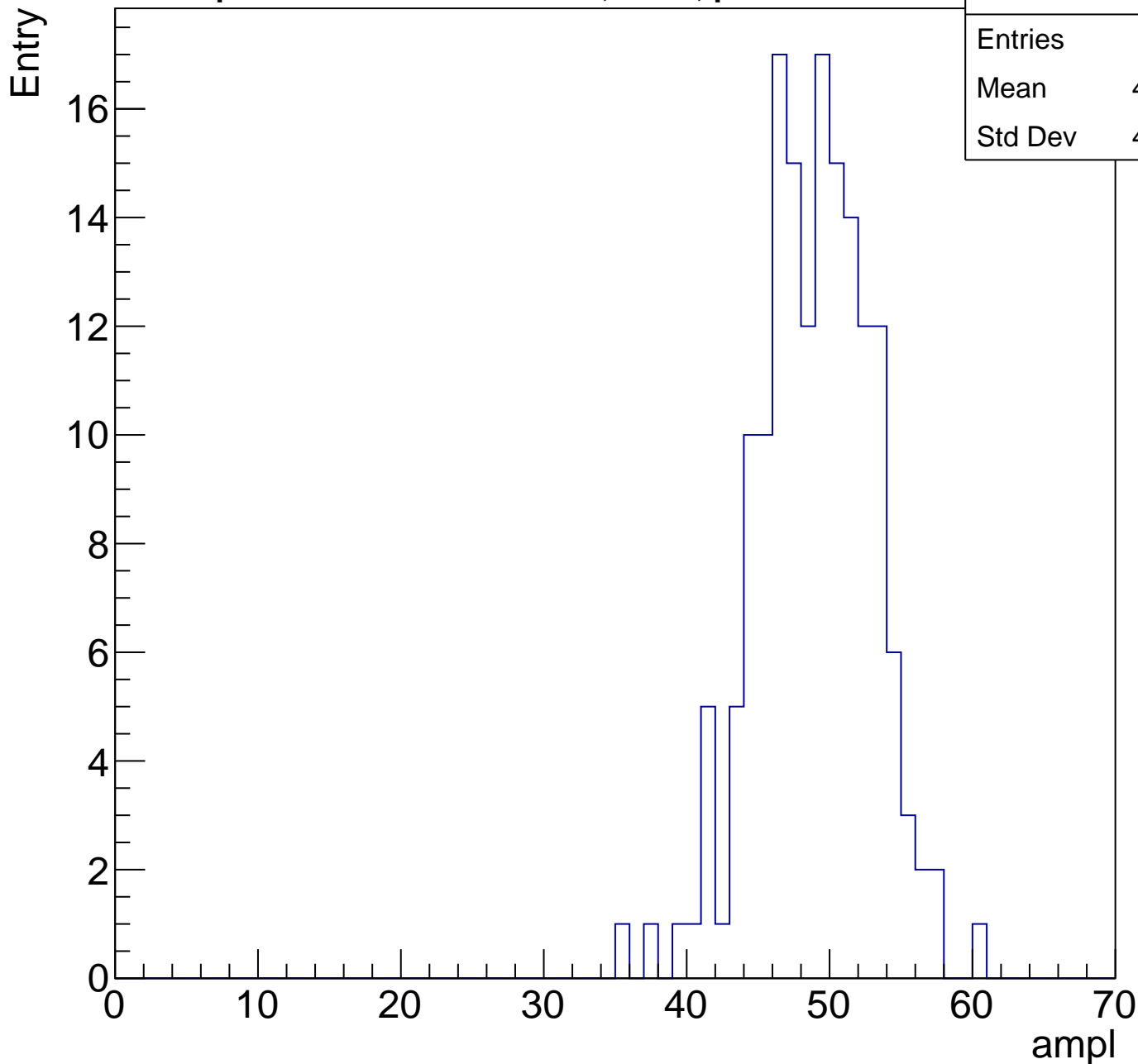
Entry



B0L101S, U20-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

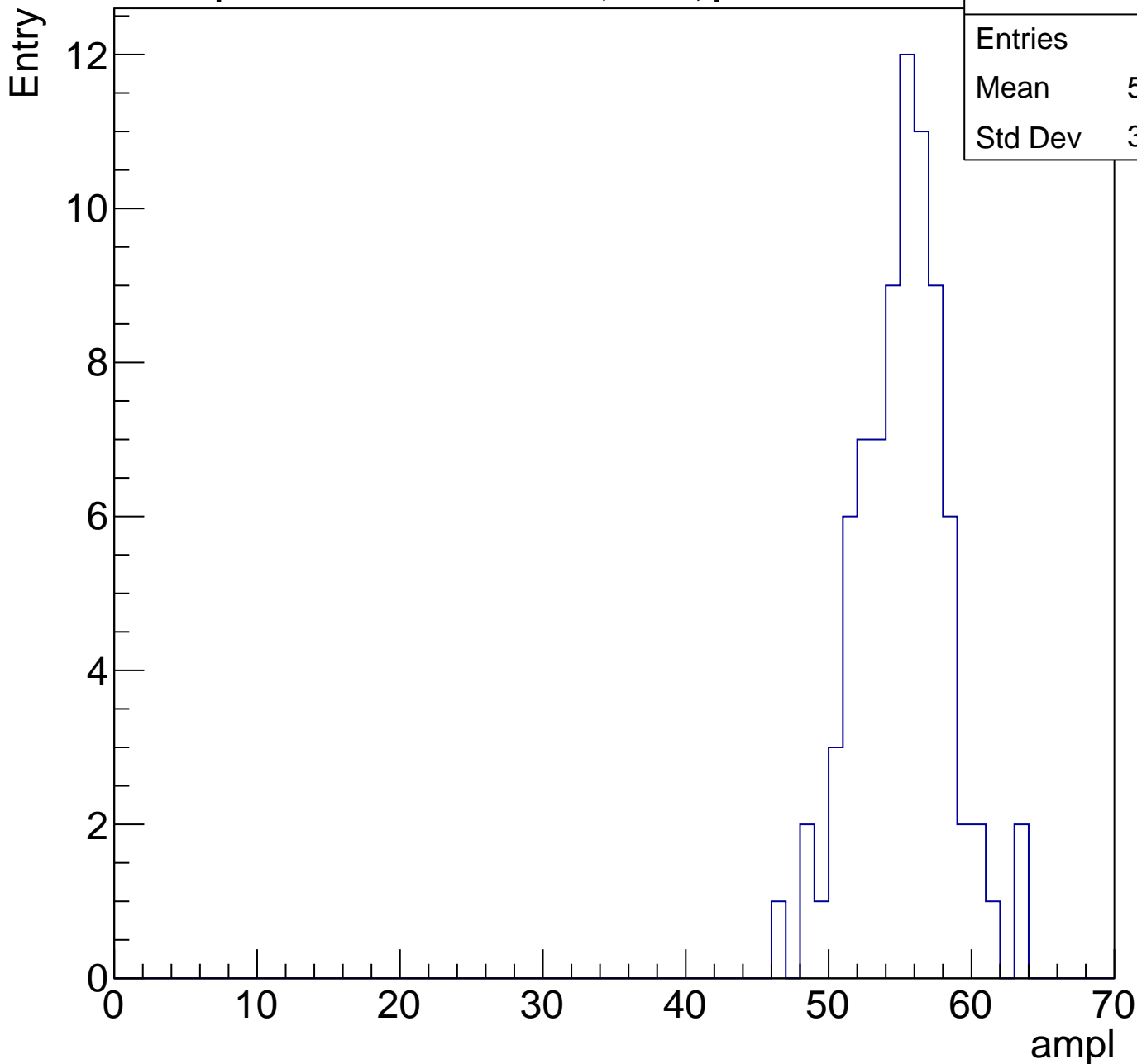
Entries	163
Mean	48.45
Std Dev	4.019



B0L101S, U20-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	81
Mean	54.69
Std Dev	3.192



B0L101S, U20-ch36, adc5

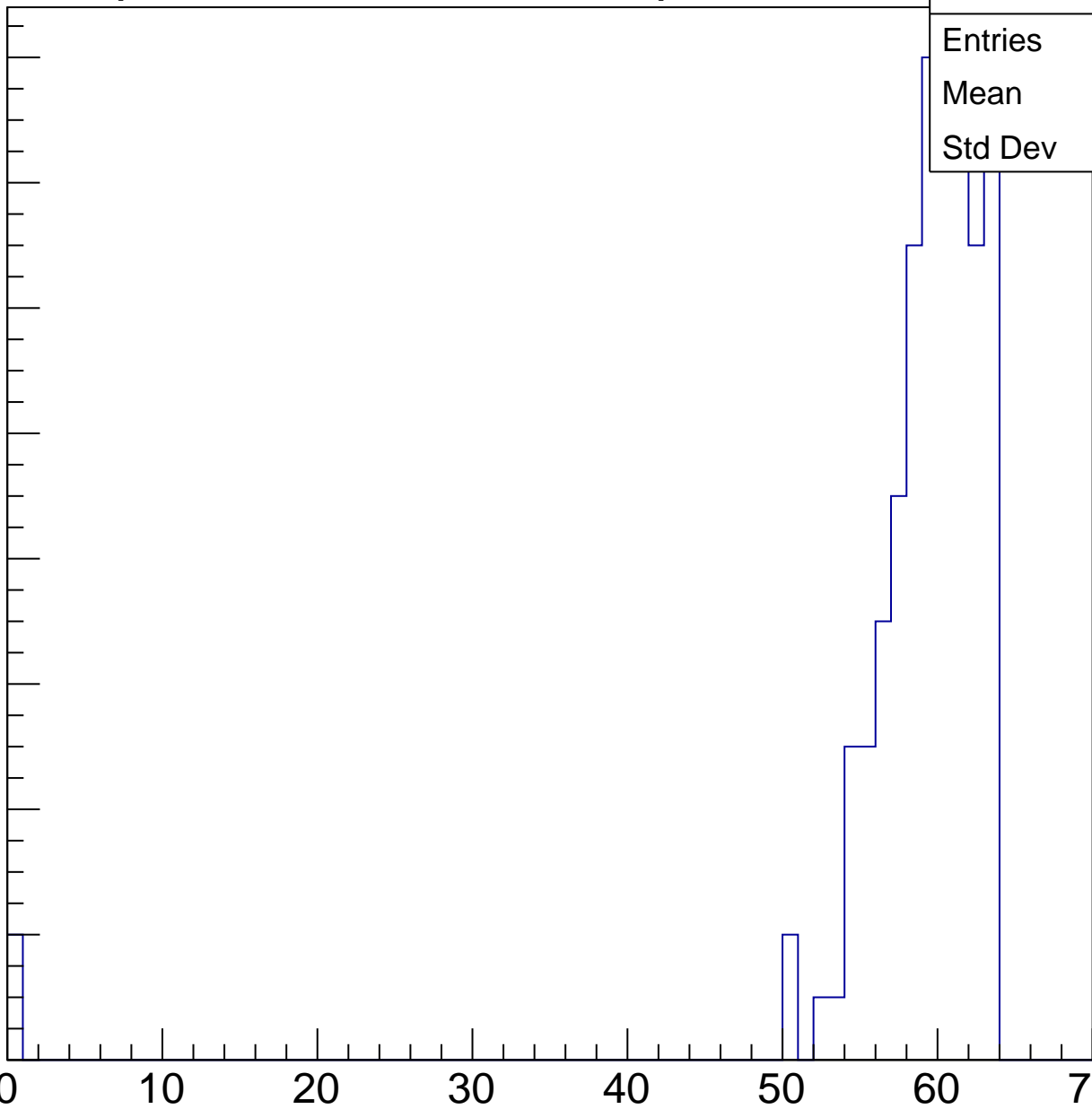
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	58.18
Std Dev	8.071

ampl

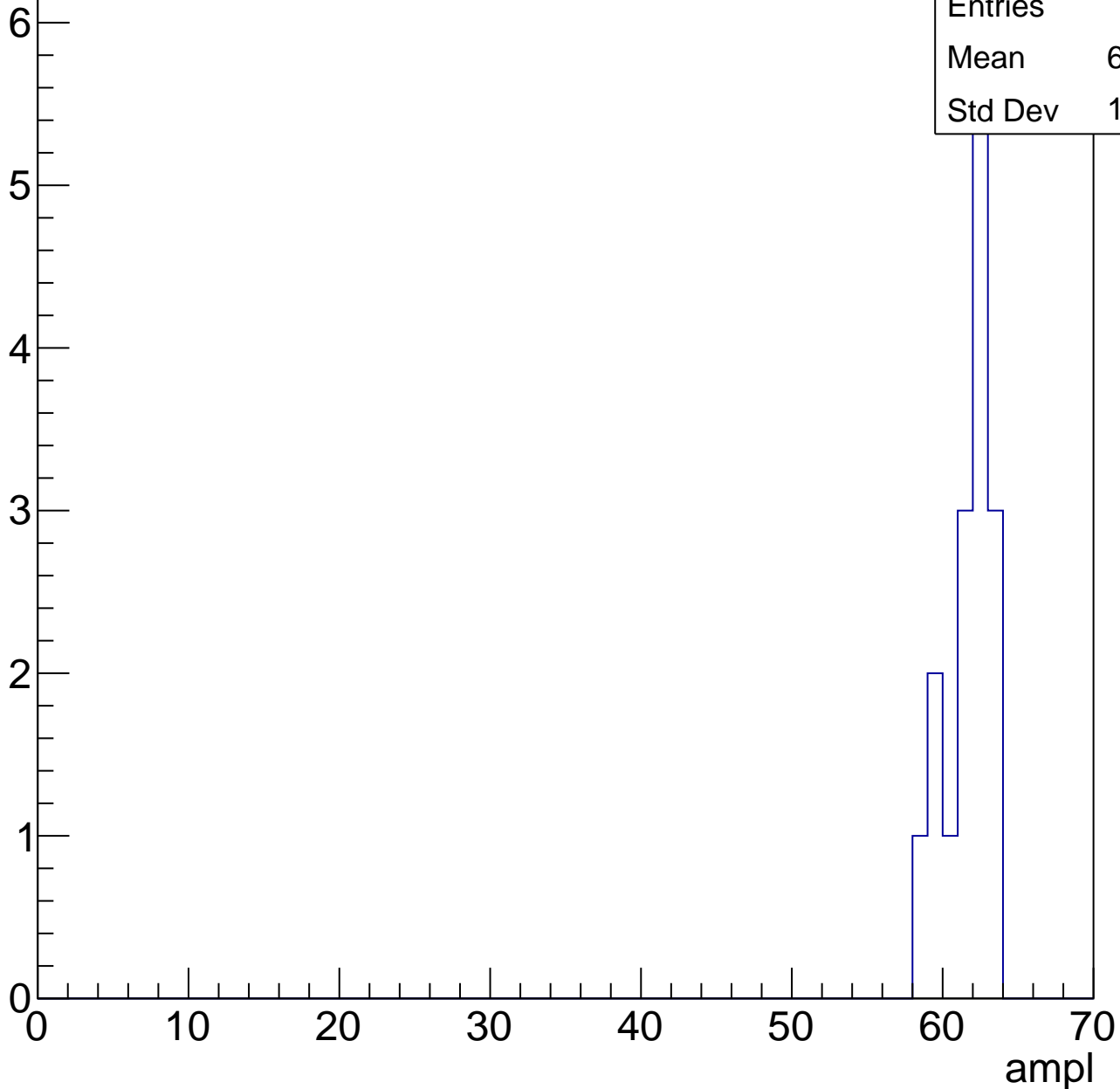


B0L101S, U20-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

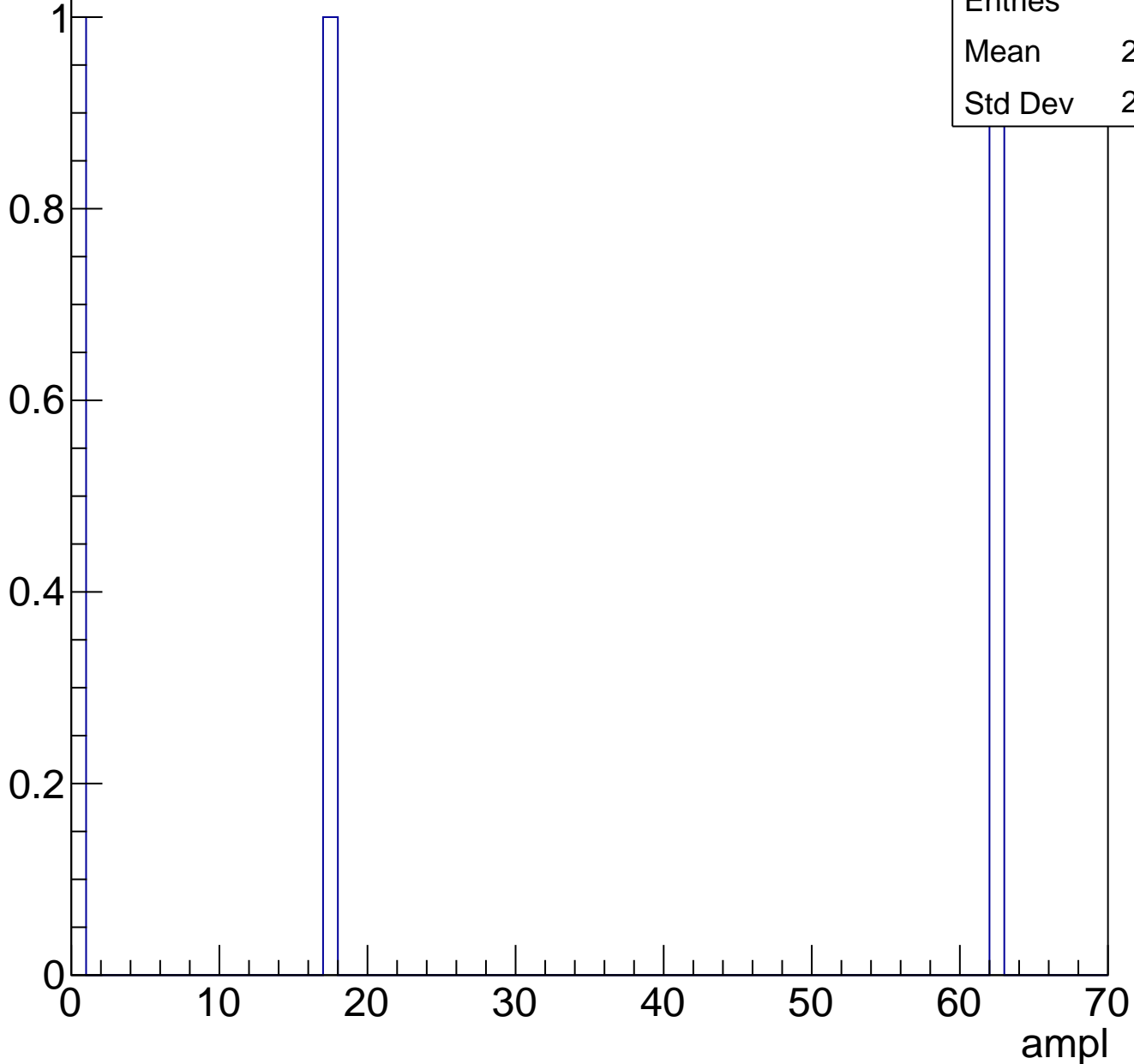
Entries	16
Mean	61.25
Std Dev	1.479



B0L101S, U20-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.33
Std Dev	26.16

B0L101S, U20-ch37, adc0

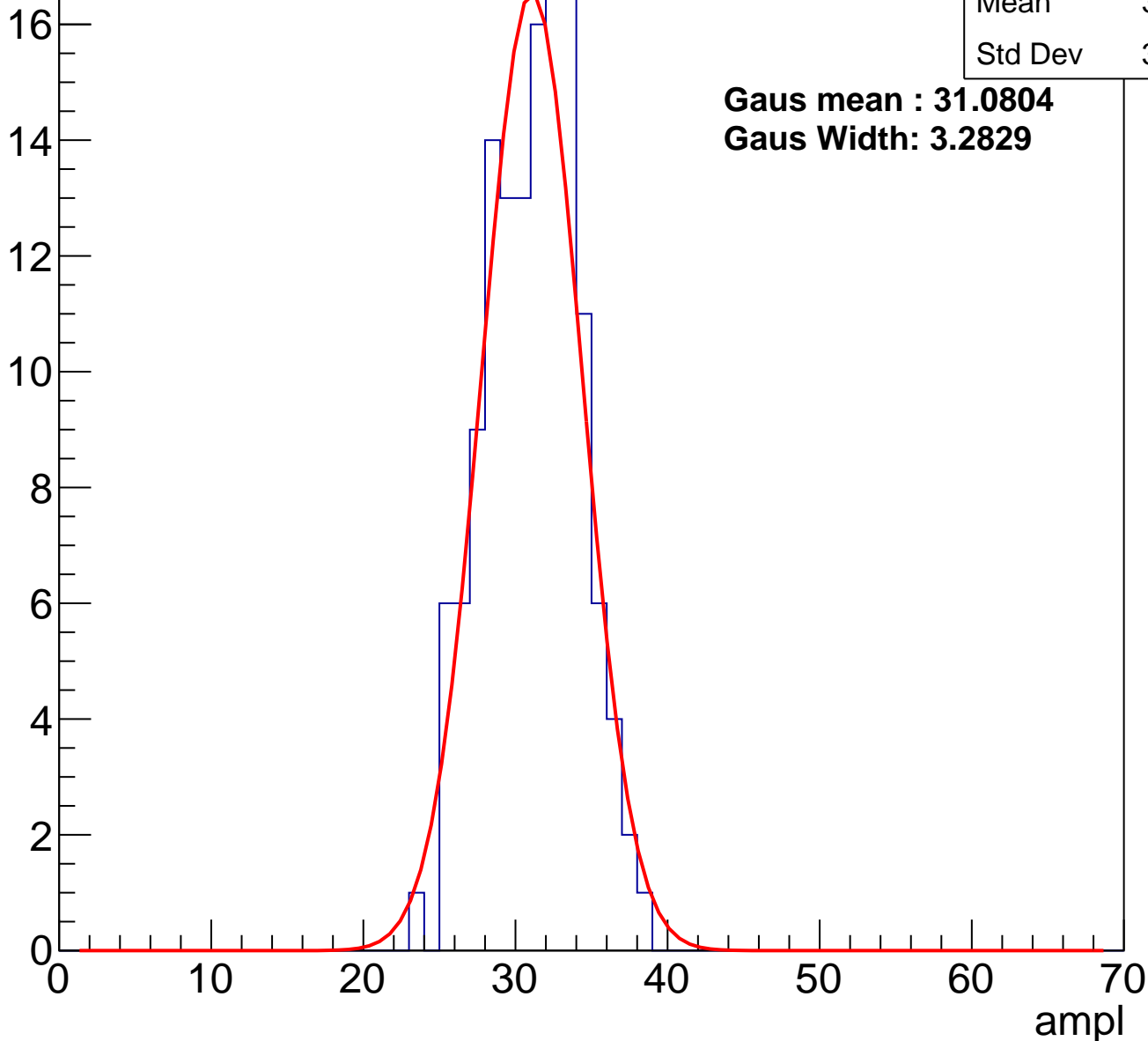
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	136
Mean	30.68
Std Dev	3.034

Gaus mean : 31.0804

Gaus Width: 3.2829



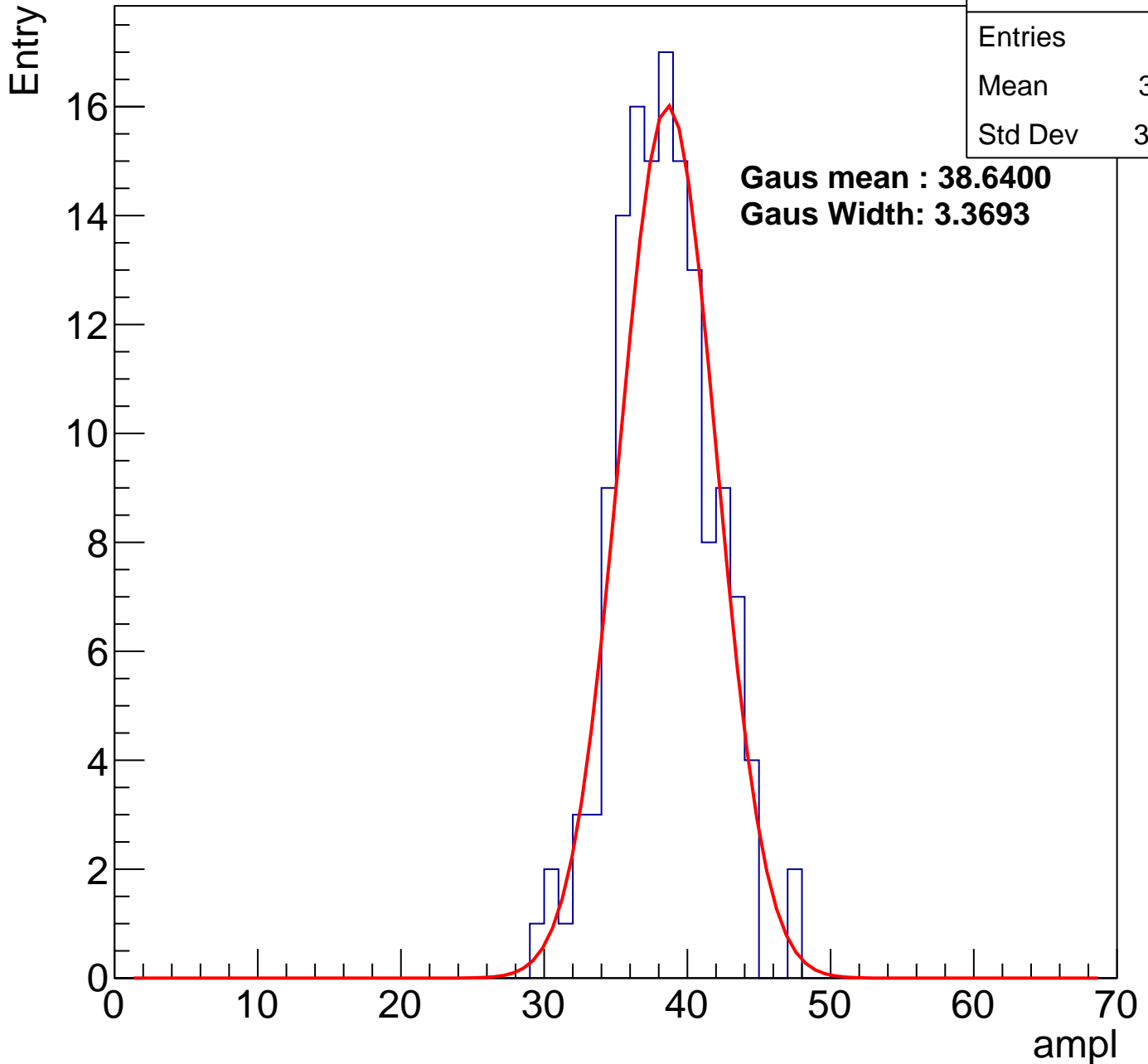
B0L101S, U20-ch37, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	37.91
Std Dev	3.343

Gaus mean : 38.6400

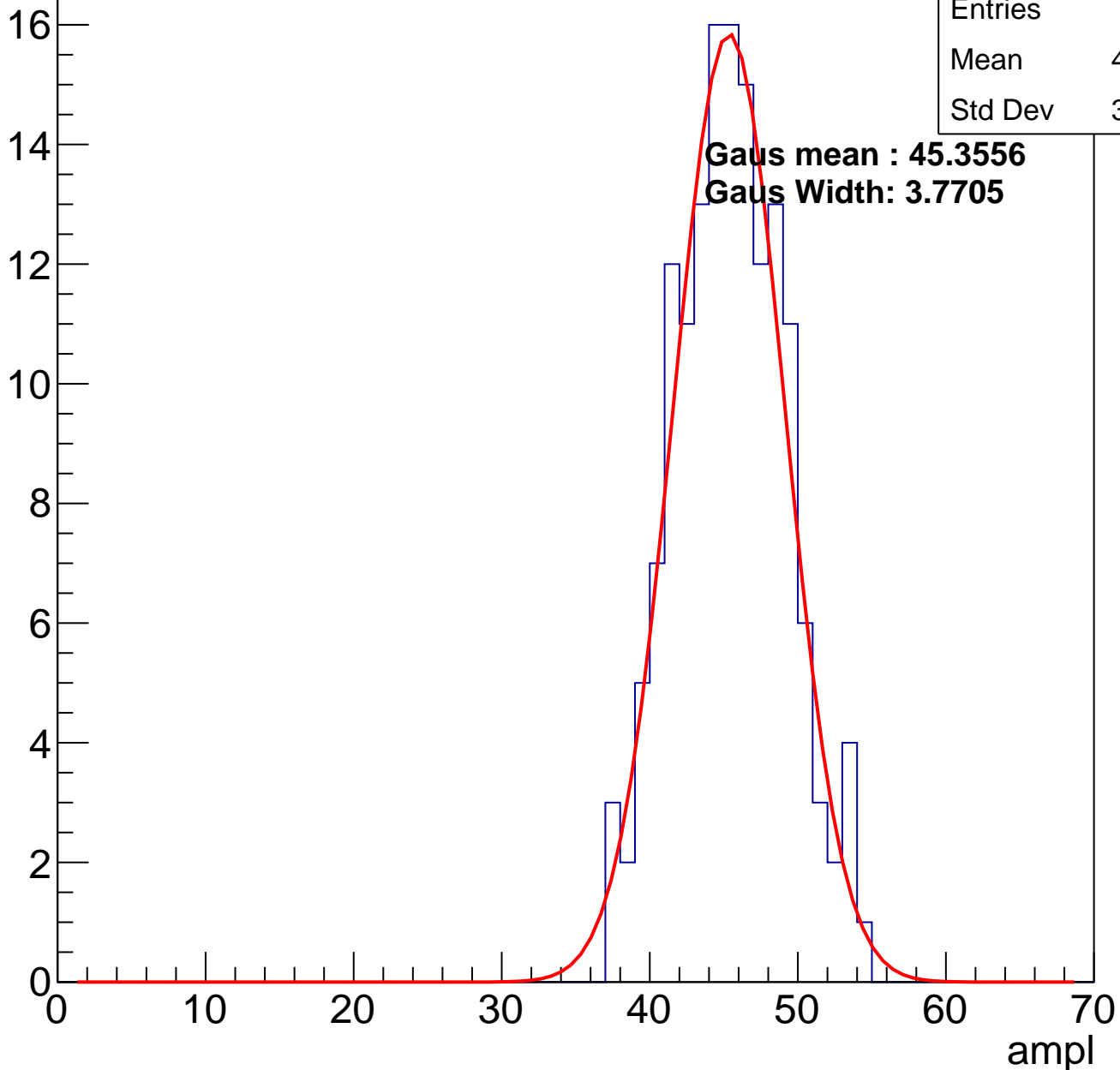
Gaus Width: 3.3693



B0L101S, U20-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

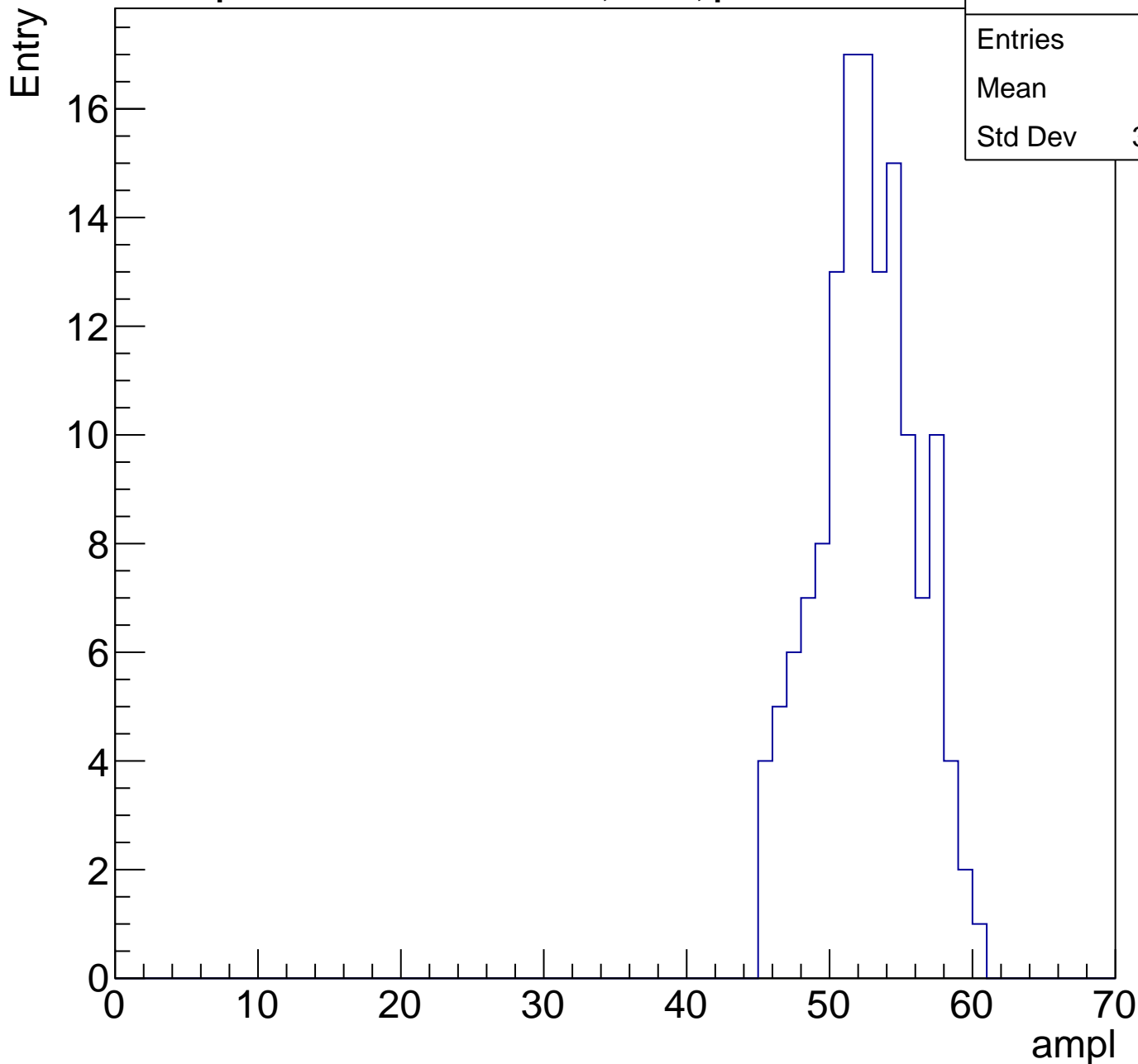
Entry



B0L101S, U20-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	52.1
Std Dev	3.402



B0L101S, U20-ch37, adc4

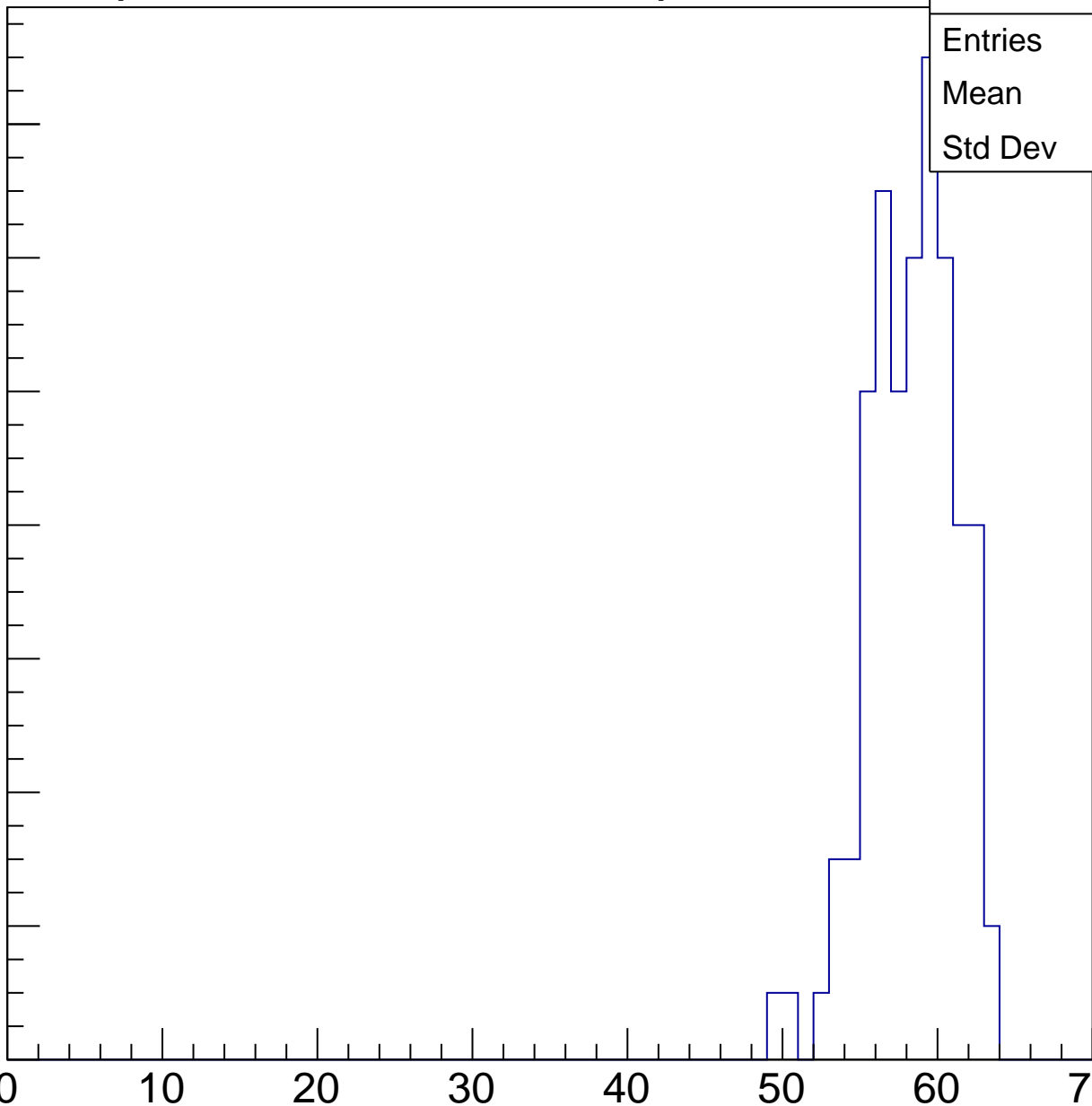
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	57.89
Std Dev	2.785

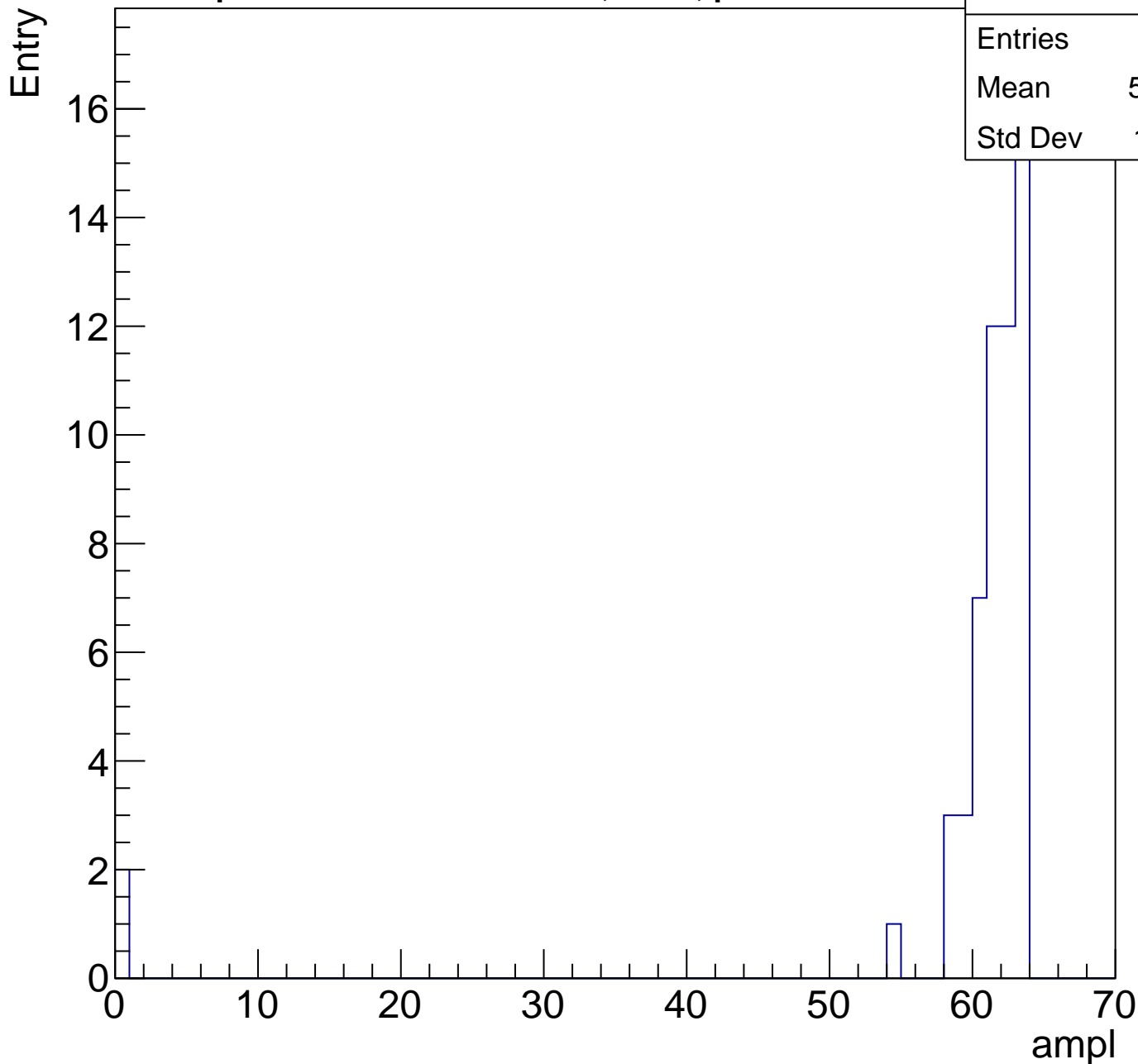
ampl



B0L101S, U20-ch37, adc5

calib_packv5_042523_0143.root, FC#1, port C1

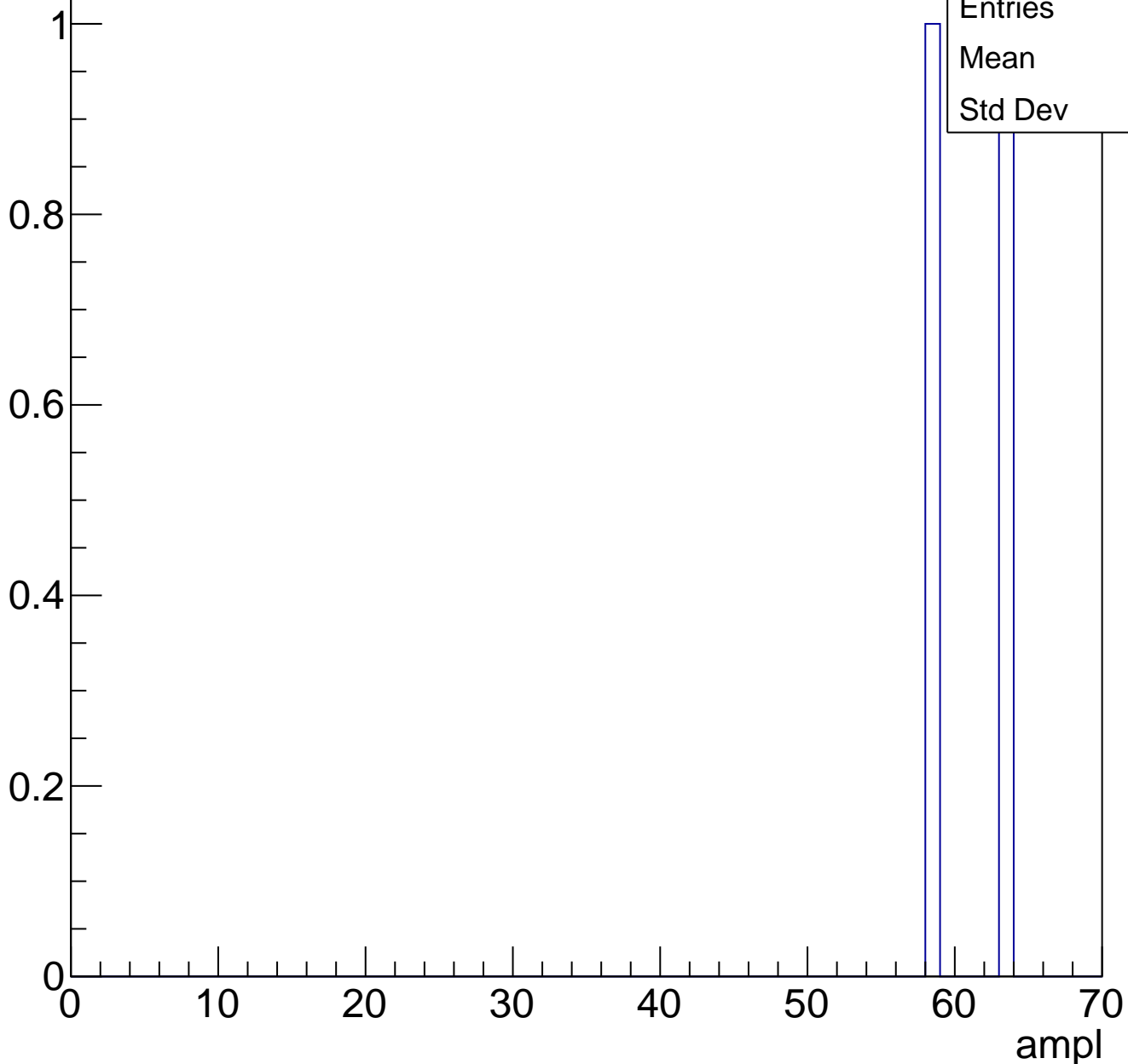
Entries	57
Mean	59.16
Std Dev	11.41



B0L101S, U20-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch38, adc0

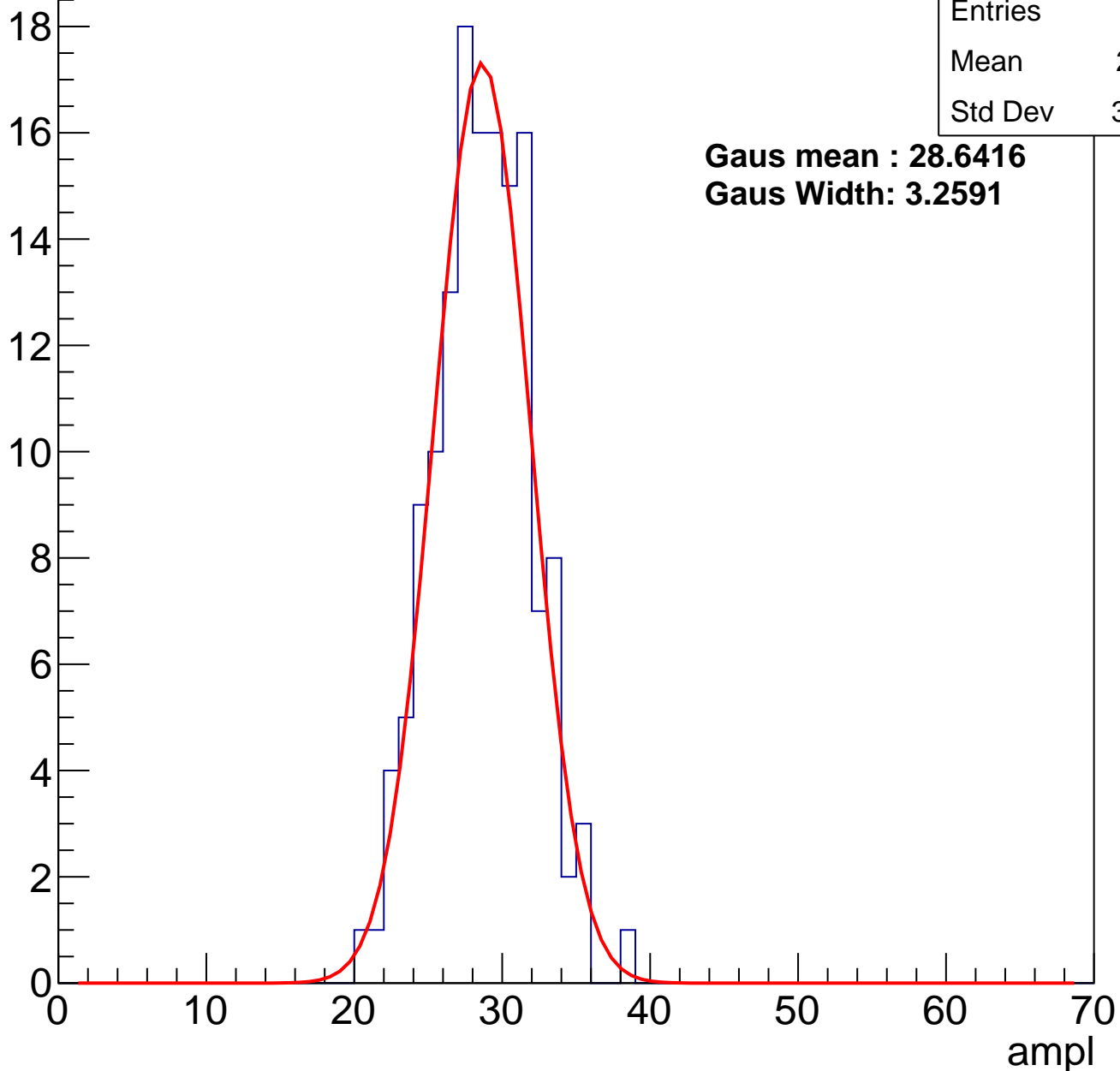
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.21
Std Dev	3.249

Gaus mean : 28.6416

Gaus Width: 3.2591

Entry



B0L101S, U20-ch38, adc1

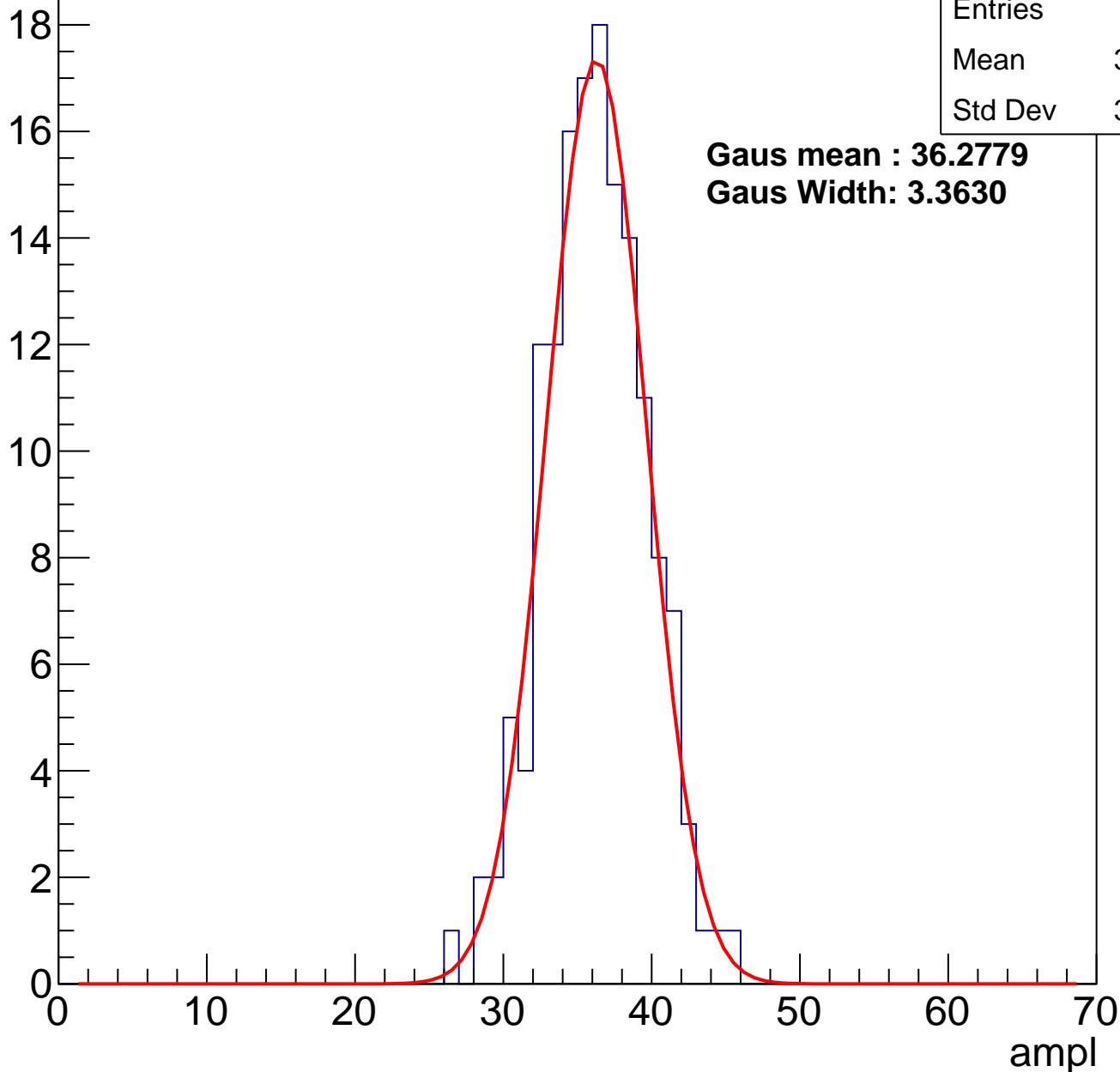
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	35.75
Std Dev	3.418

Gaus mean : 36.2779

Gaus Width: 3.3630

Entry



B0L101S, U20-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

103

Mean

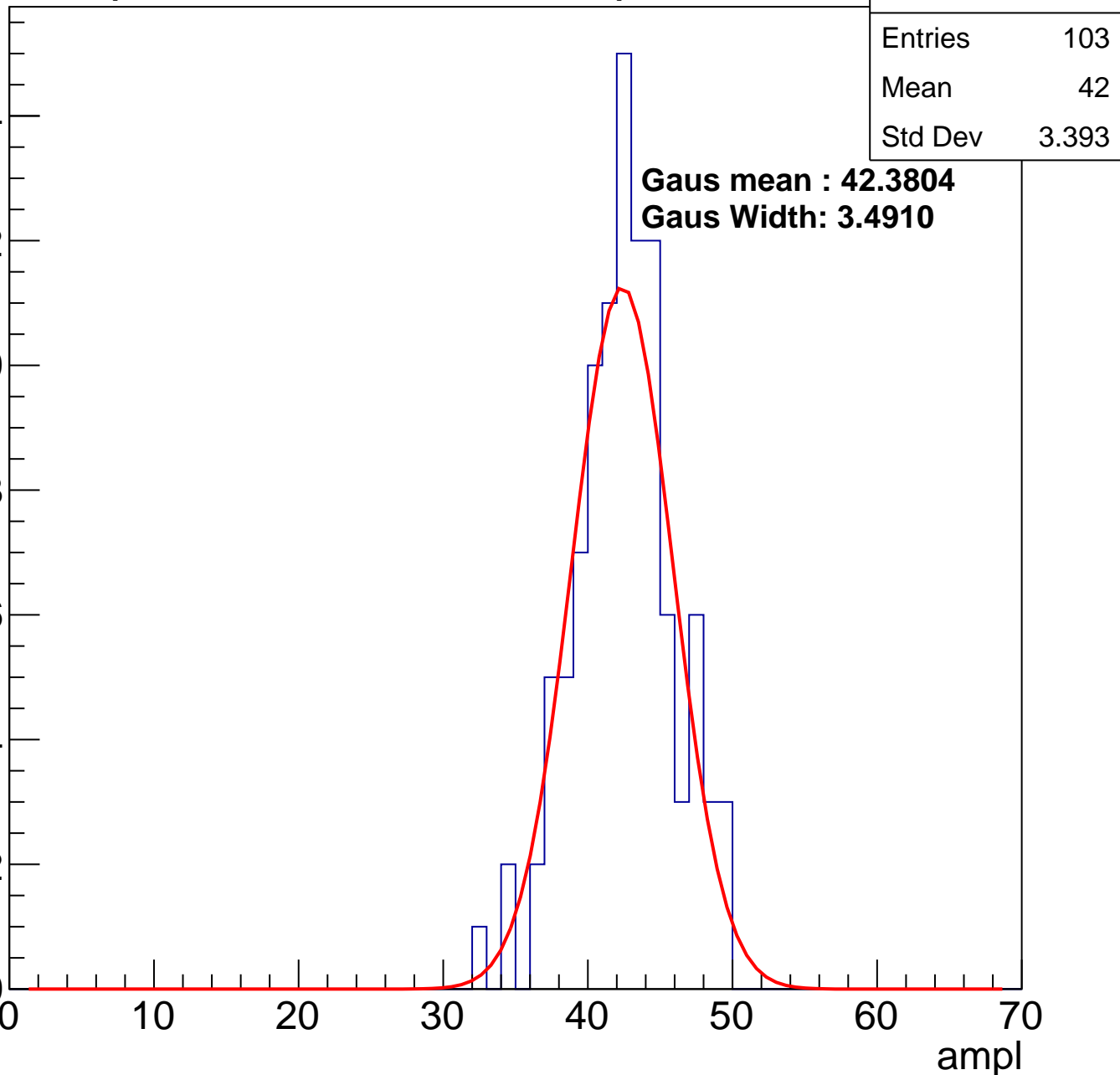
42

Std Dev

3.393

Gaus mean : 42.3804

Gaus Width: 3.4910



B0L101S, U20-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	48.23
Std Dev	3.645

Entry

16

14

12

10

8

6

4

2

0

0

10

20

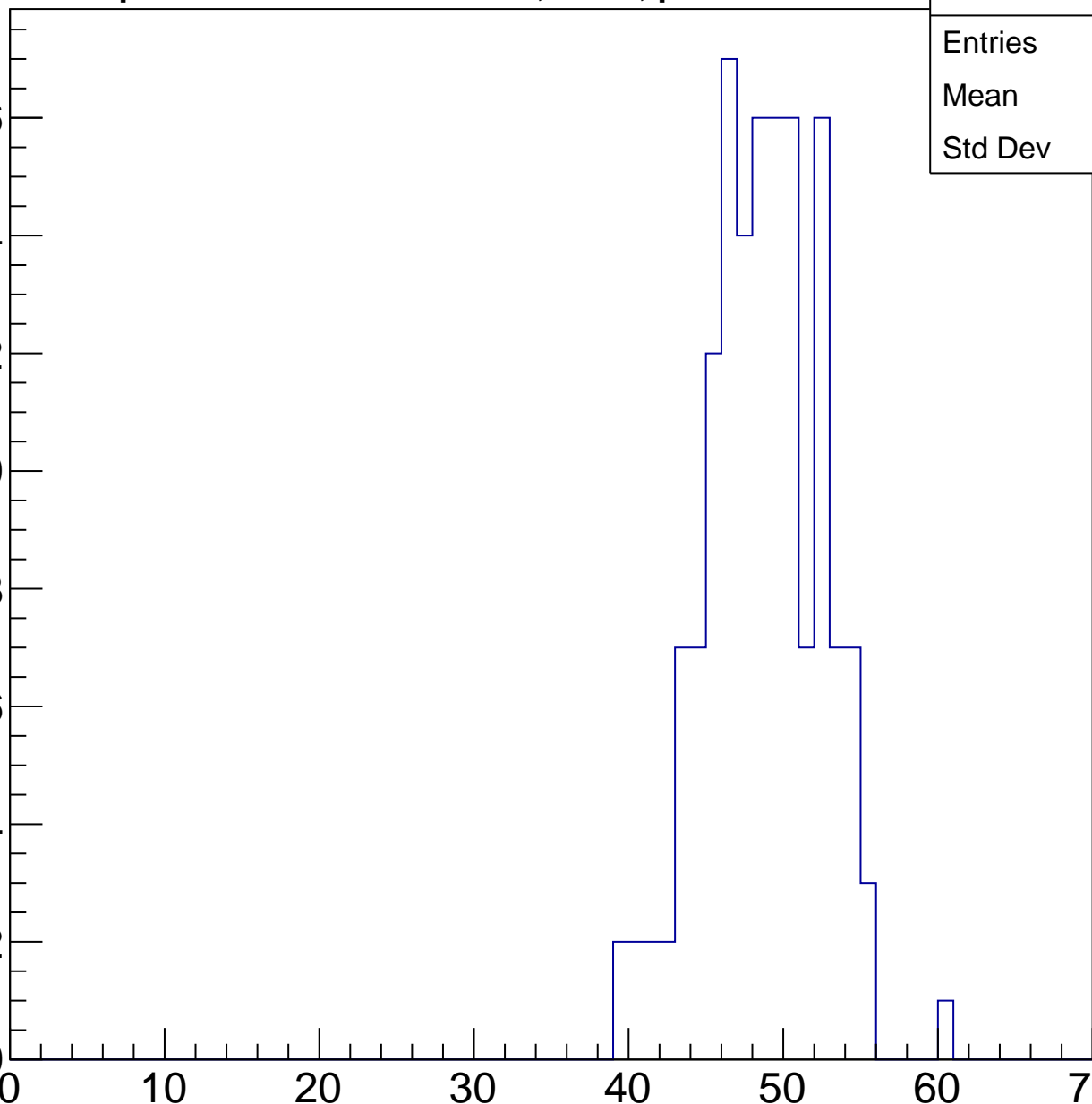
30

40

50

60

ampl



B0L101S, U20-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

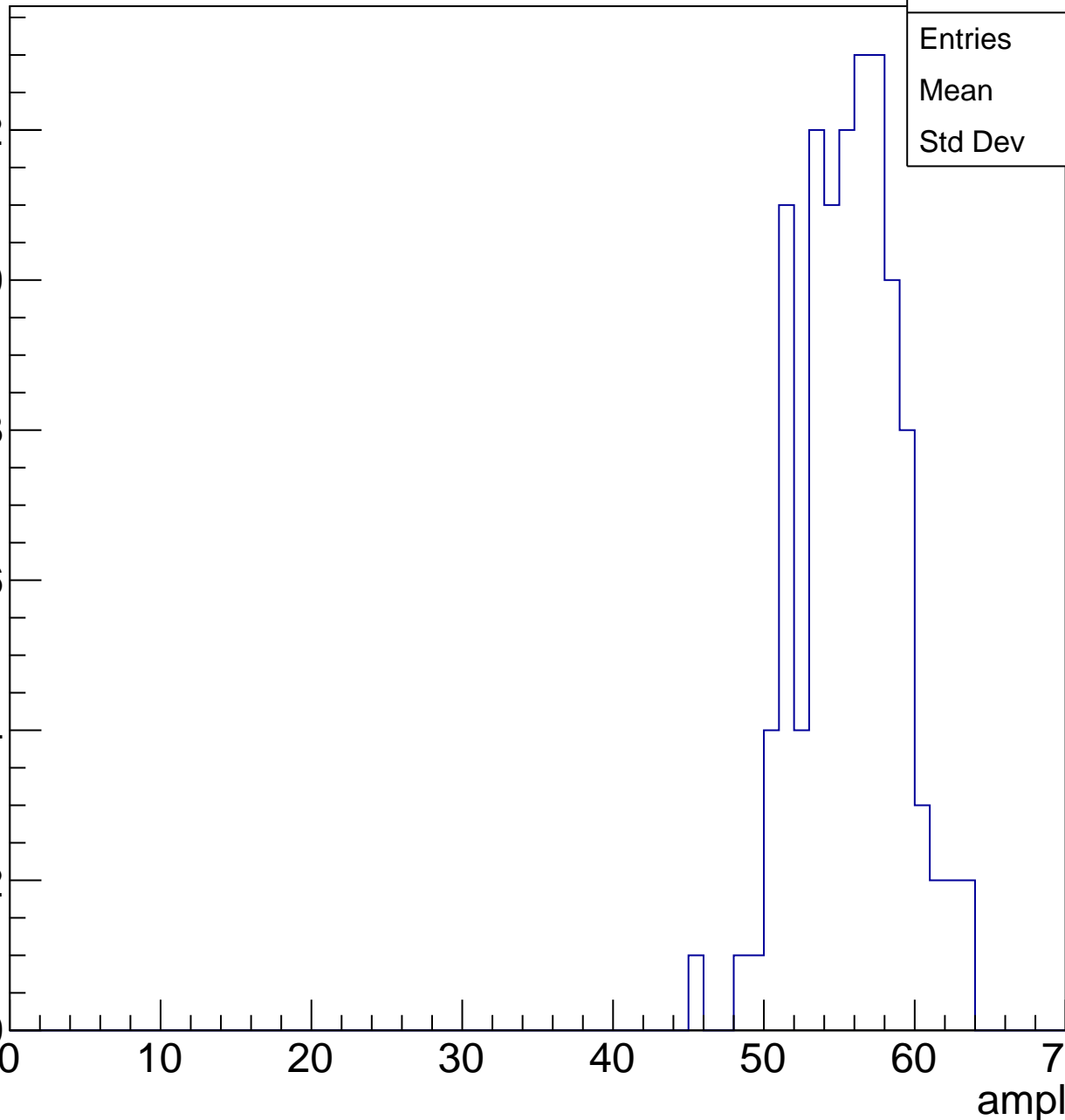
40

50

60

ampl

Entries	110
Mean	55.22
Std Dev	3.312



B0L101S, U20-ch38, adc5

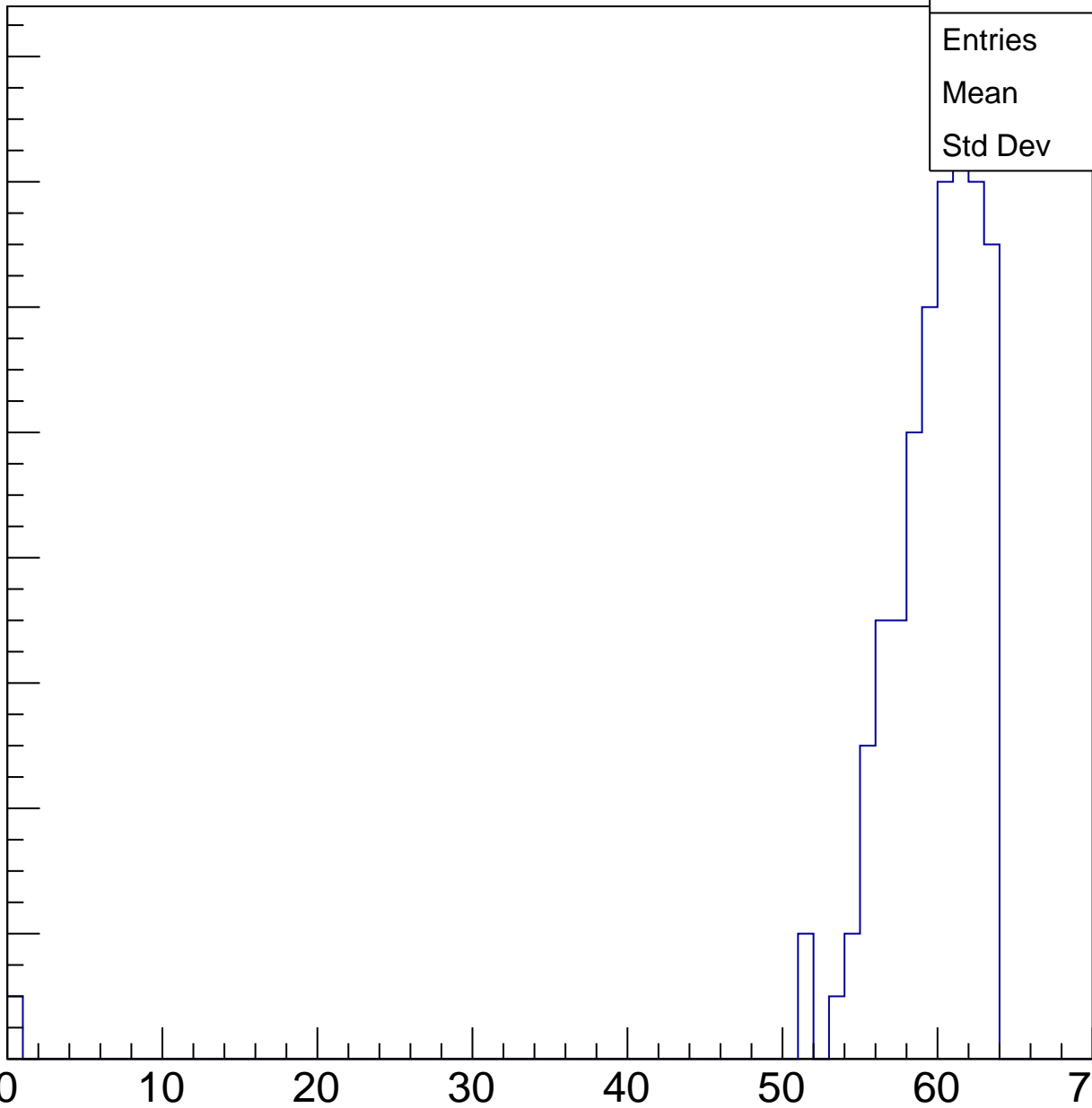
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.85
Std Dev	6.419

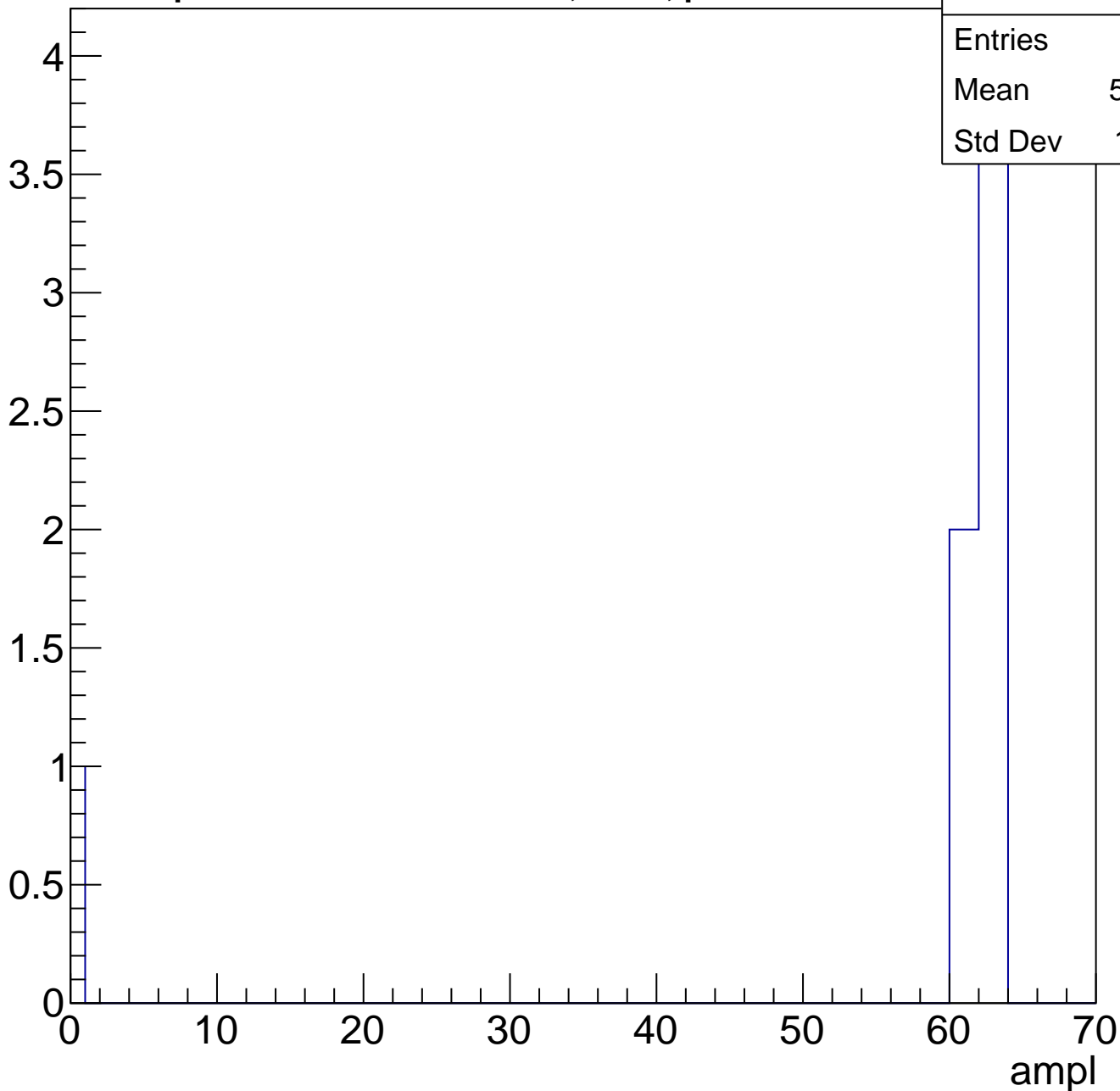
ampl



B0L101S, U20-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

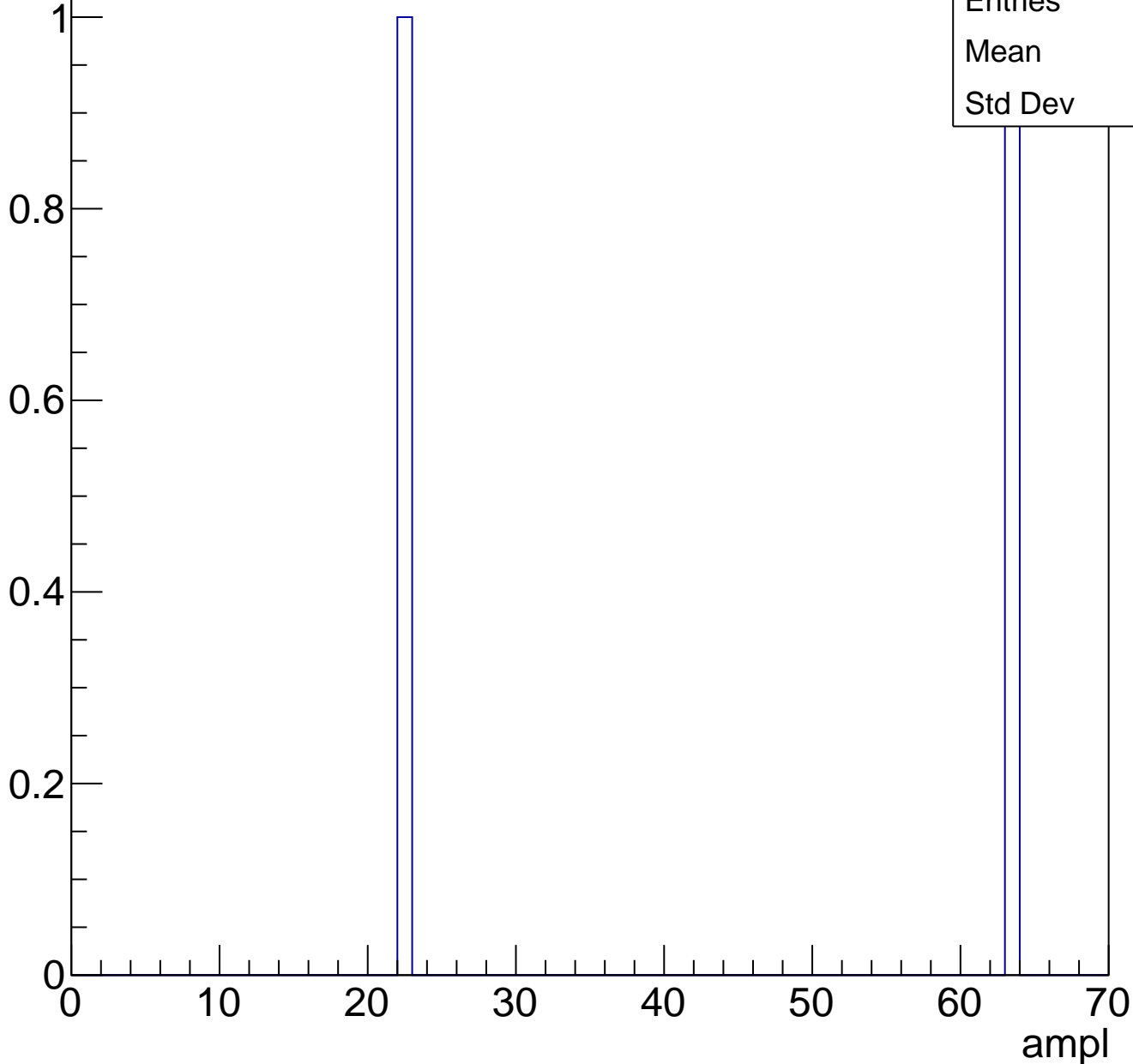
Entry



B0L101S, U20-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch39, adc0

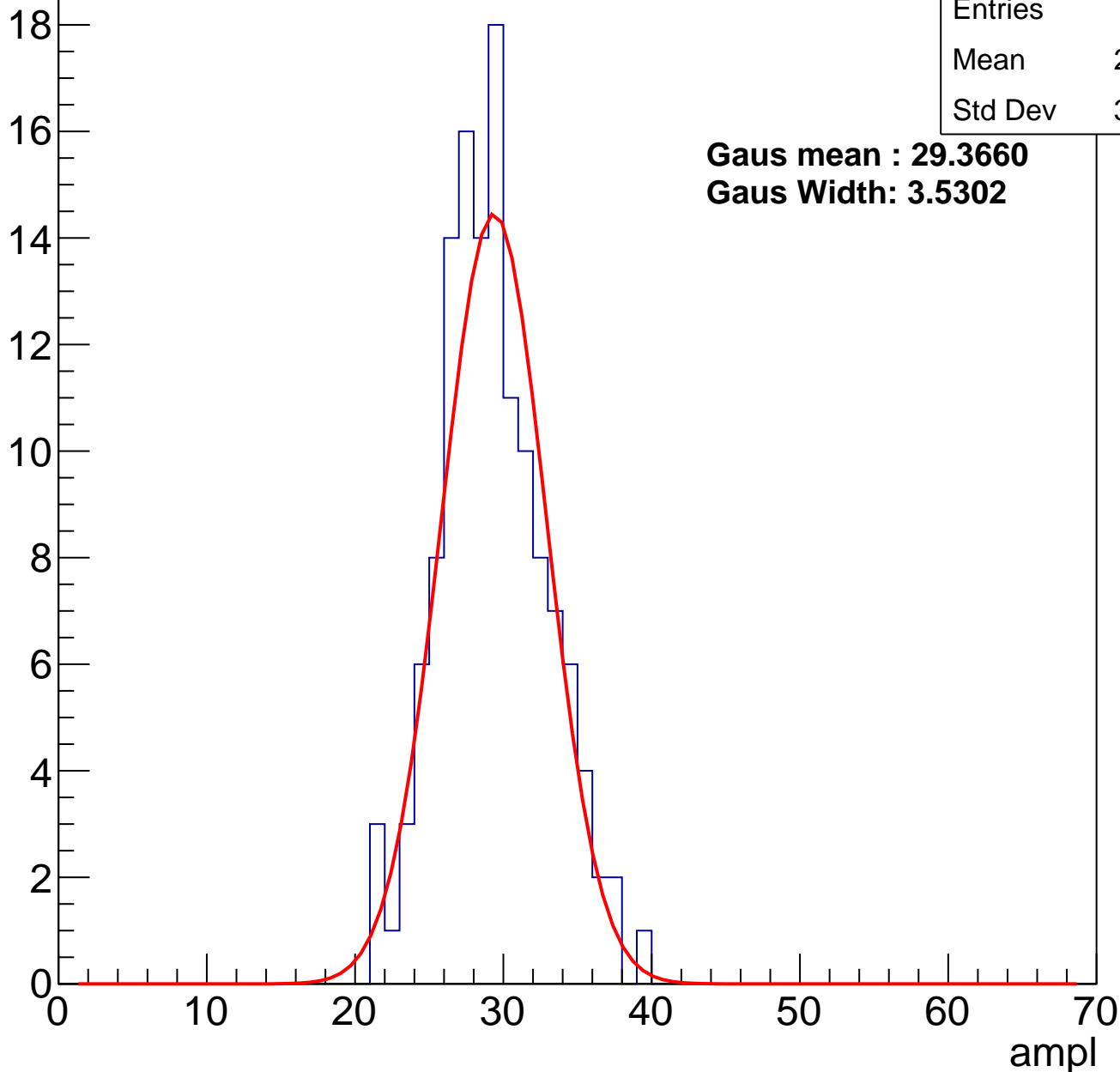
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	28.84
Std Dev	3.543

Gaus mean : 29.3660

Gaus Width: 3.5302

Entry



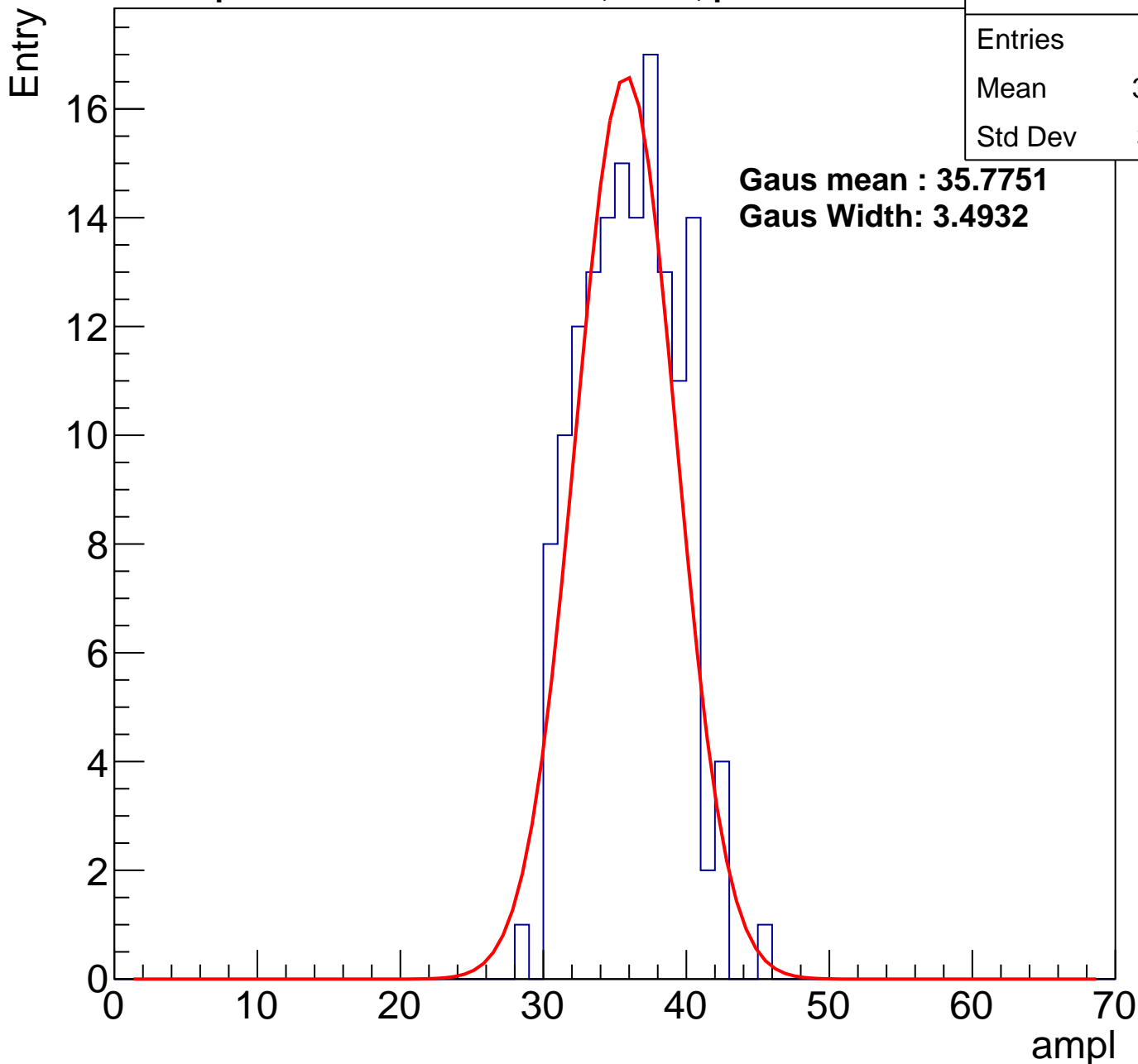
B0L101S, U20-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	35.59
Std Dev	3.301

Gaus mean : 35.7751

Gaus Width: 3.4932



B0L101S, U20-ch39, adc2

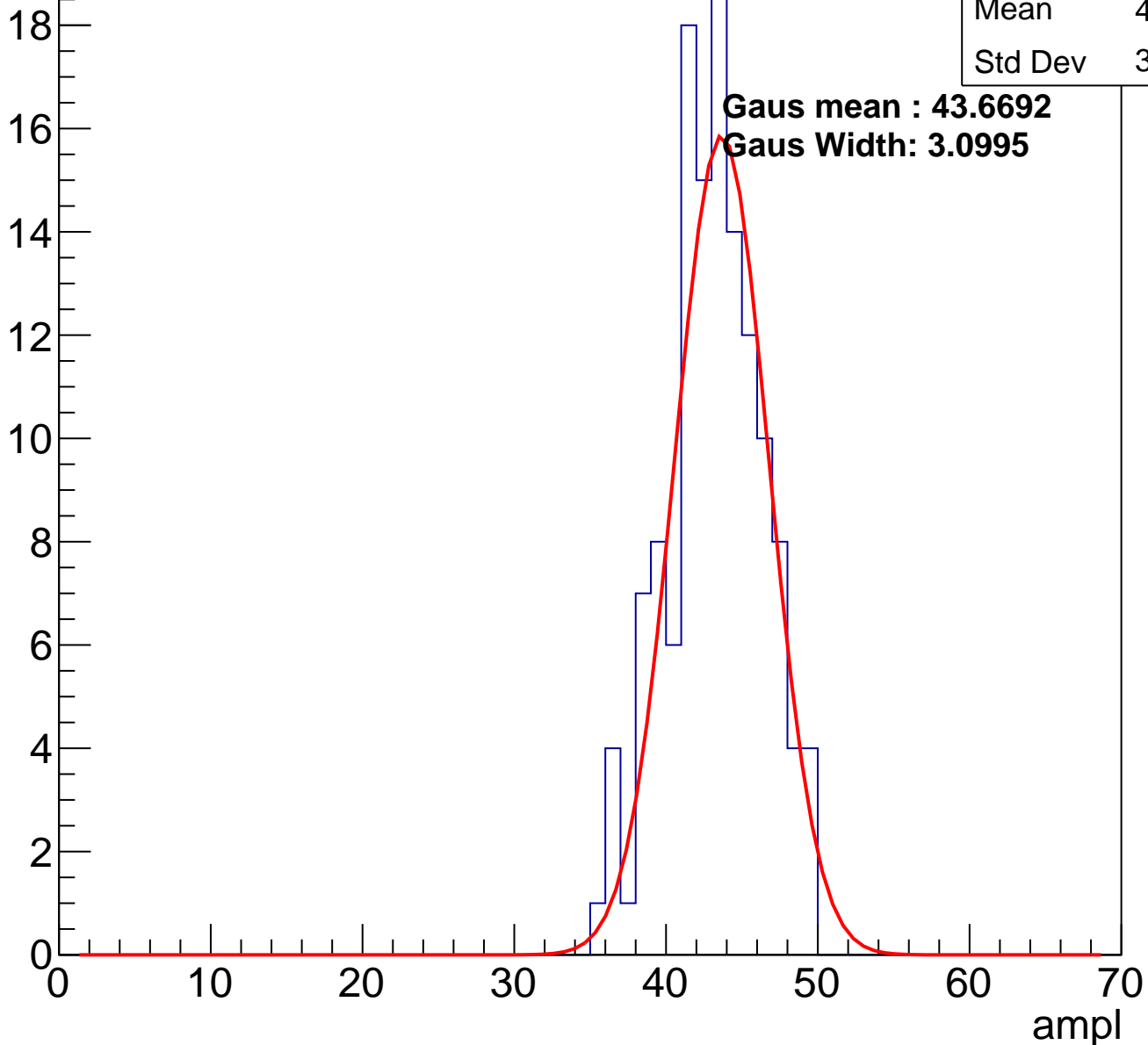
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	42.74
Std Dev	3.095

Gaus mean : 43.6692

Gaus Width: 3.0995

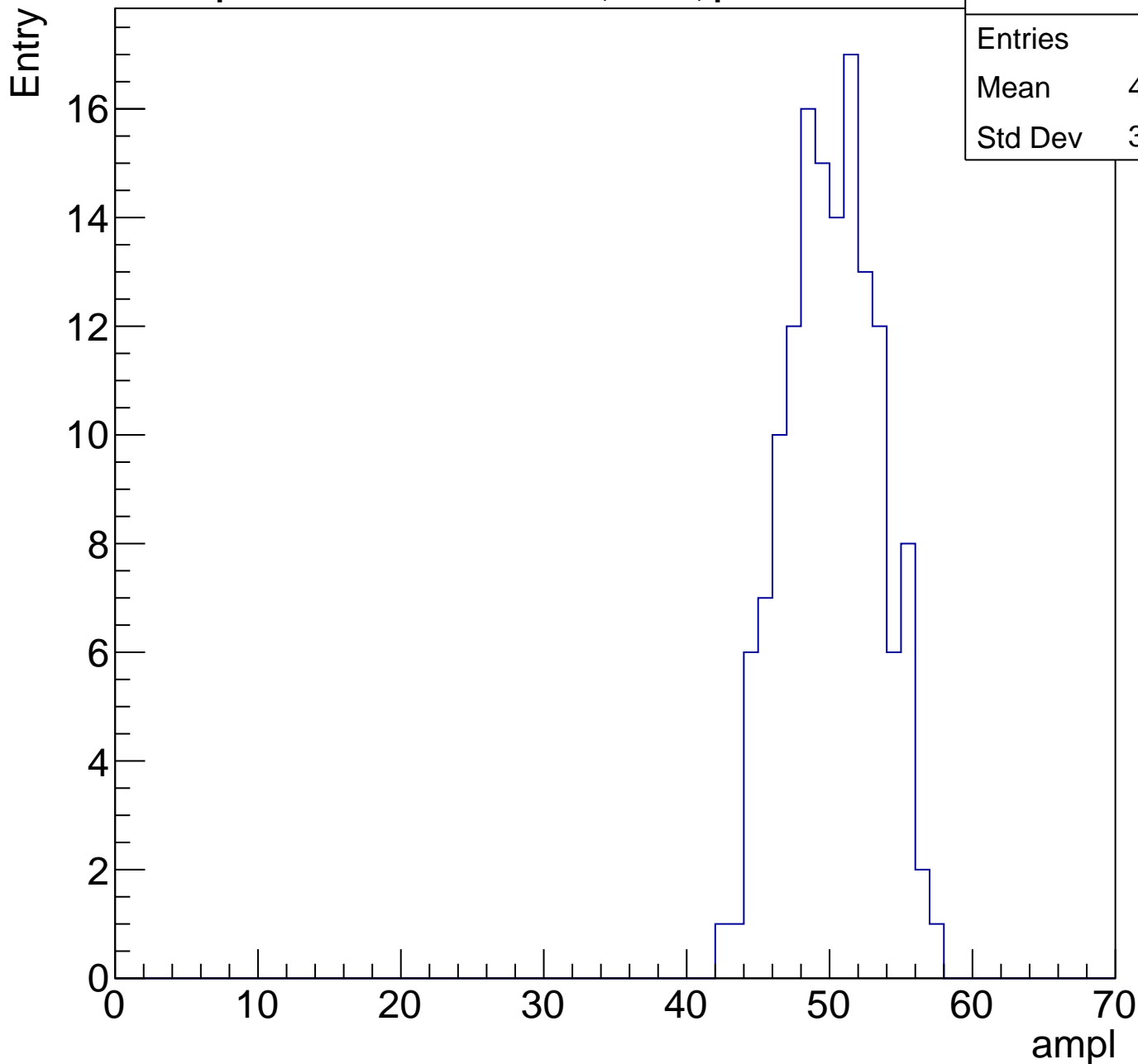
Entry



B0L101S, U20-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

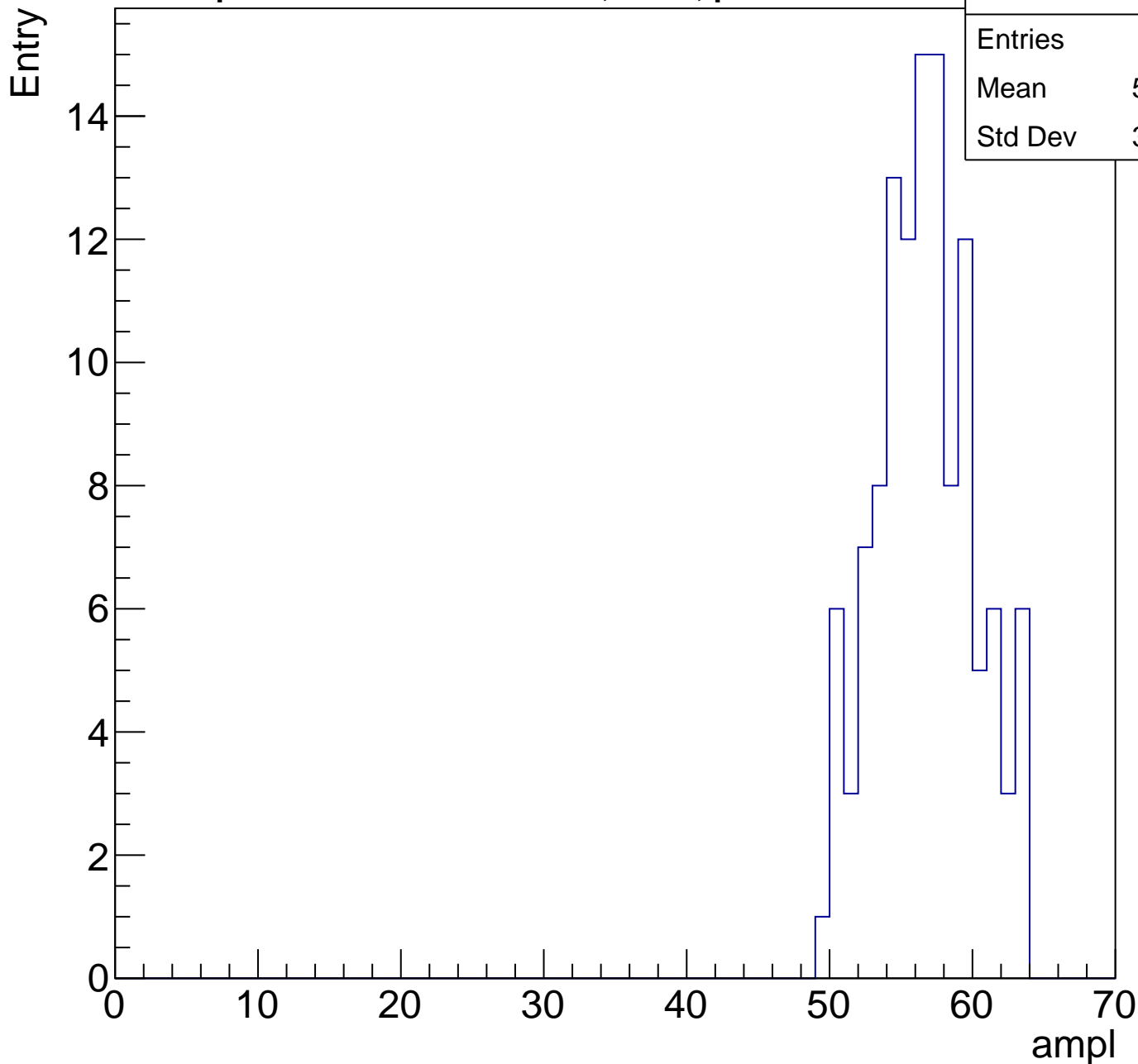
Entries	141
Mean	49.67
Std Dev	3.184



B0L101S, U20-ch39, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	56.24
Std Dev	3.376



B0L101S, U20-ch39, adc5

calib_packv5_042523_0143.root, FC#1, port C1

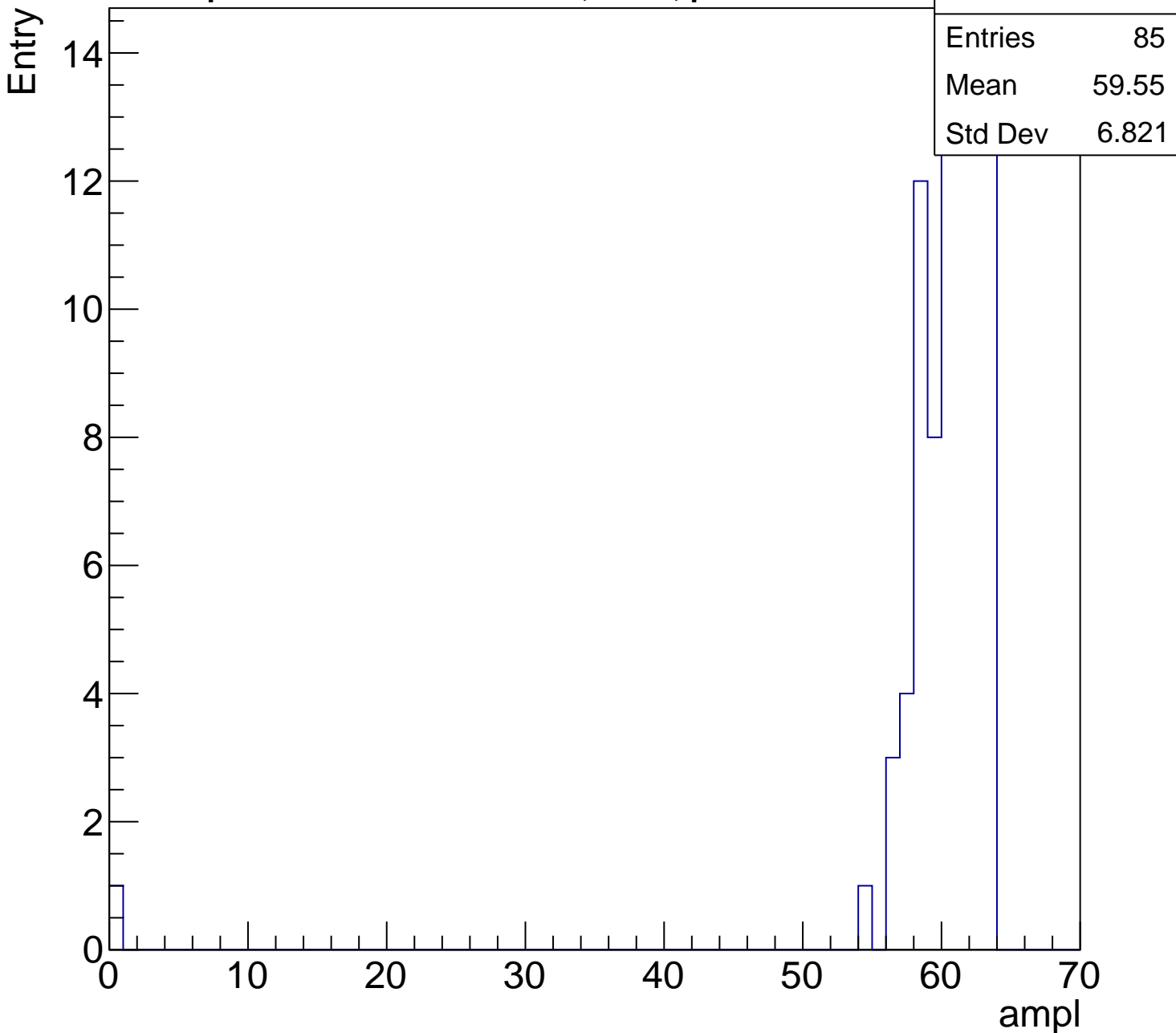
Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	59.55
Std Dev	6.821

ampl

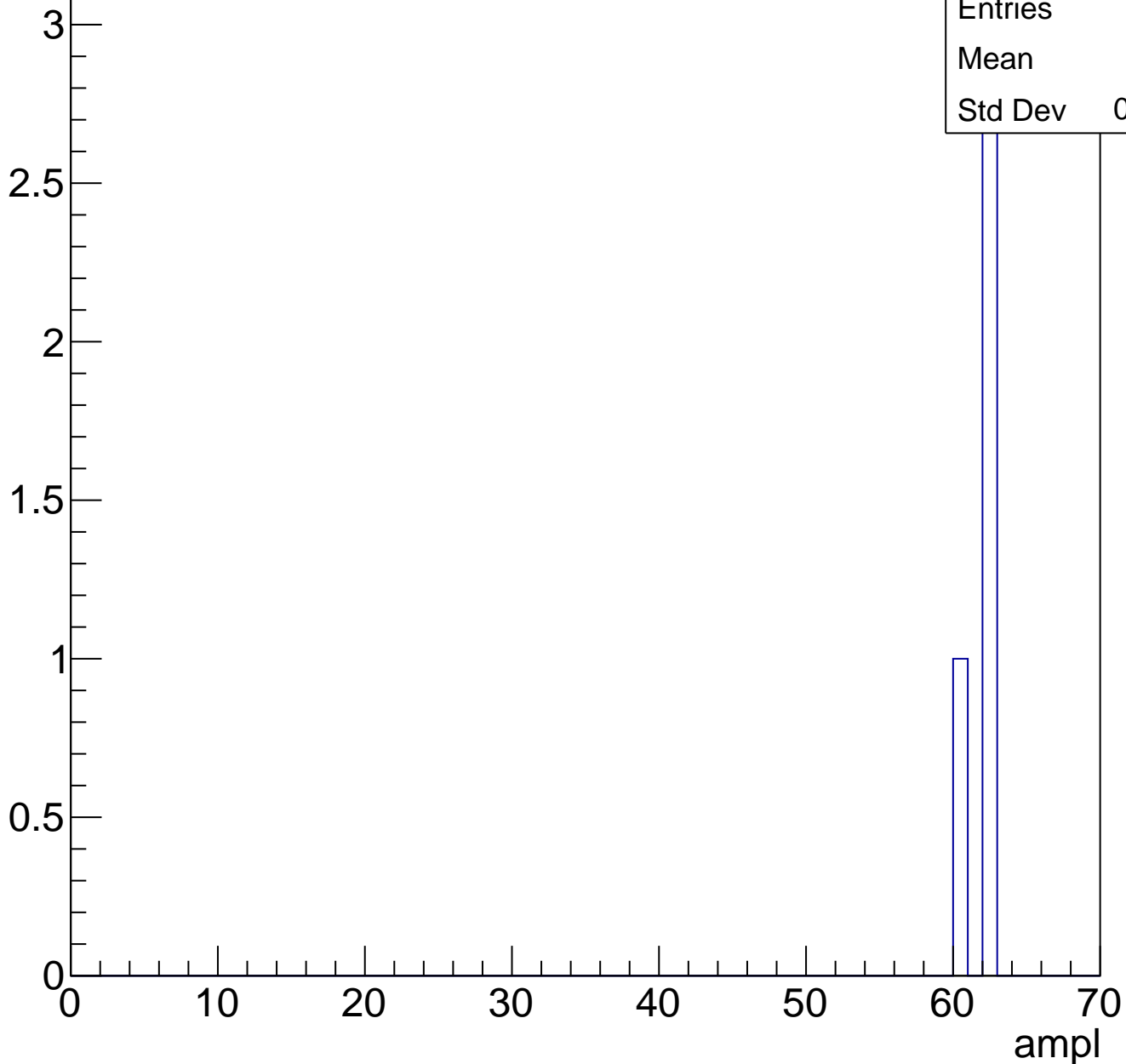
0 10 20 30 40 50 60 70



B0L101S, U20-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

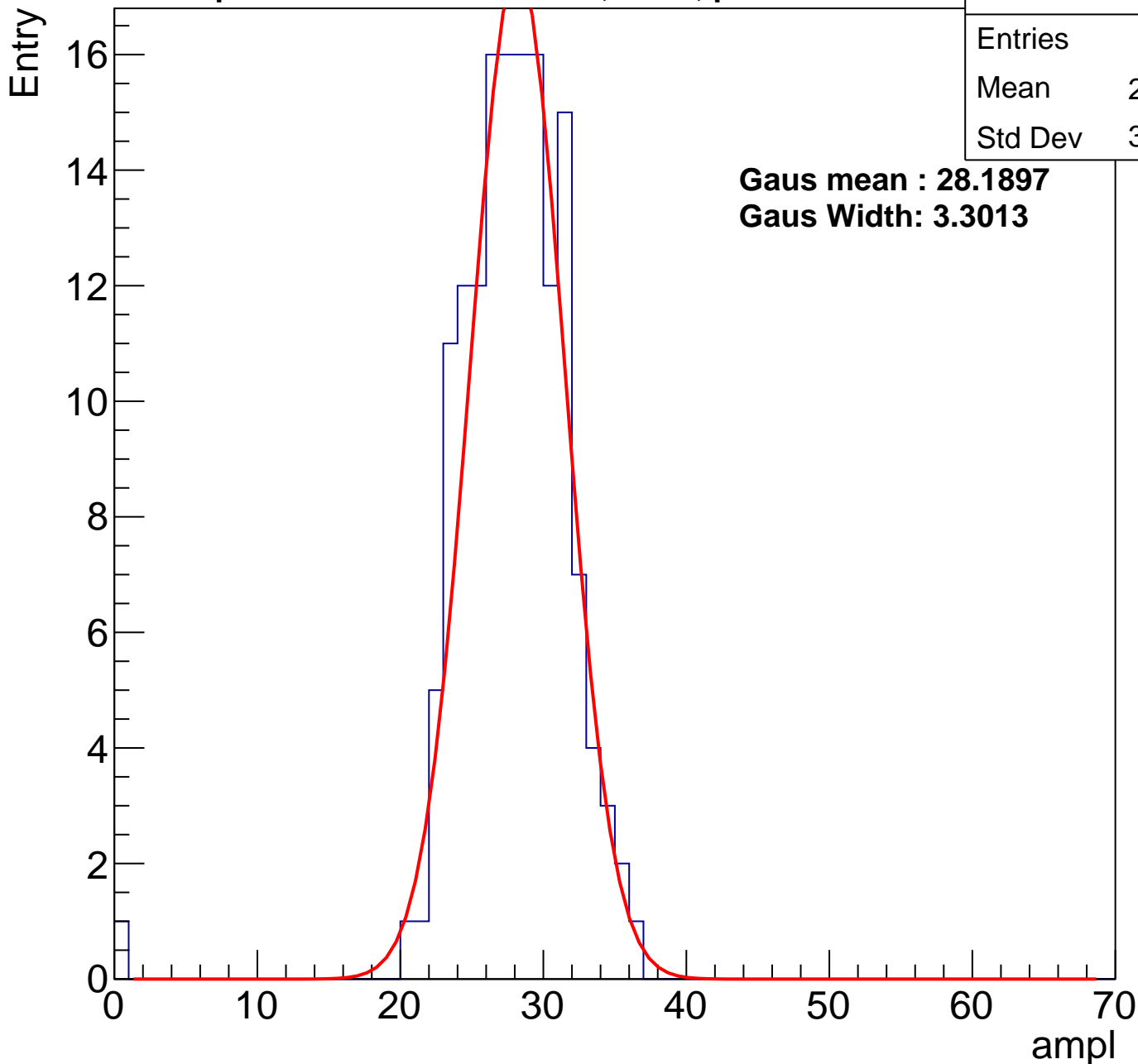
B0L101S, U20-ch40, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	27.42
Std Dev	3.956

Gaus mean : 28.1897

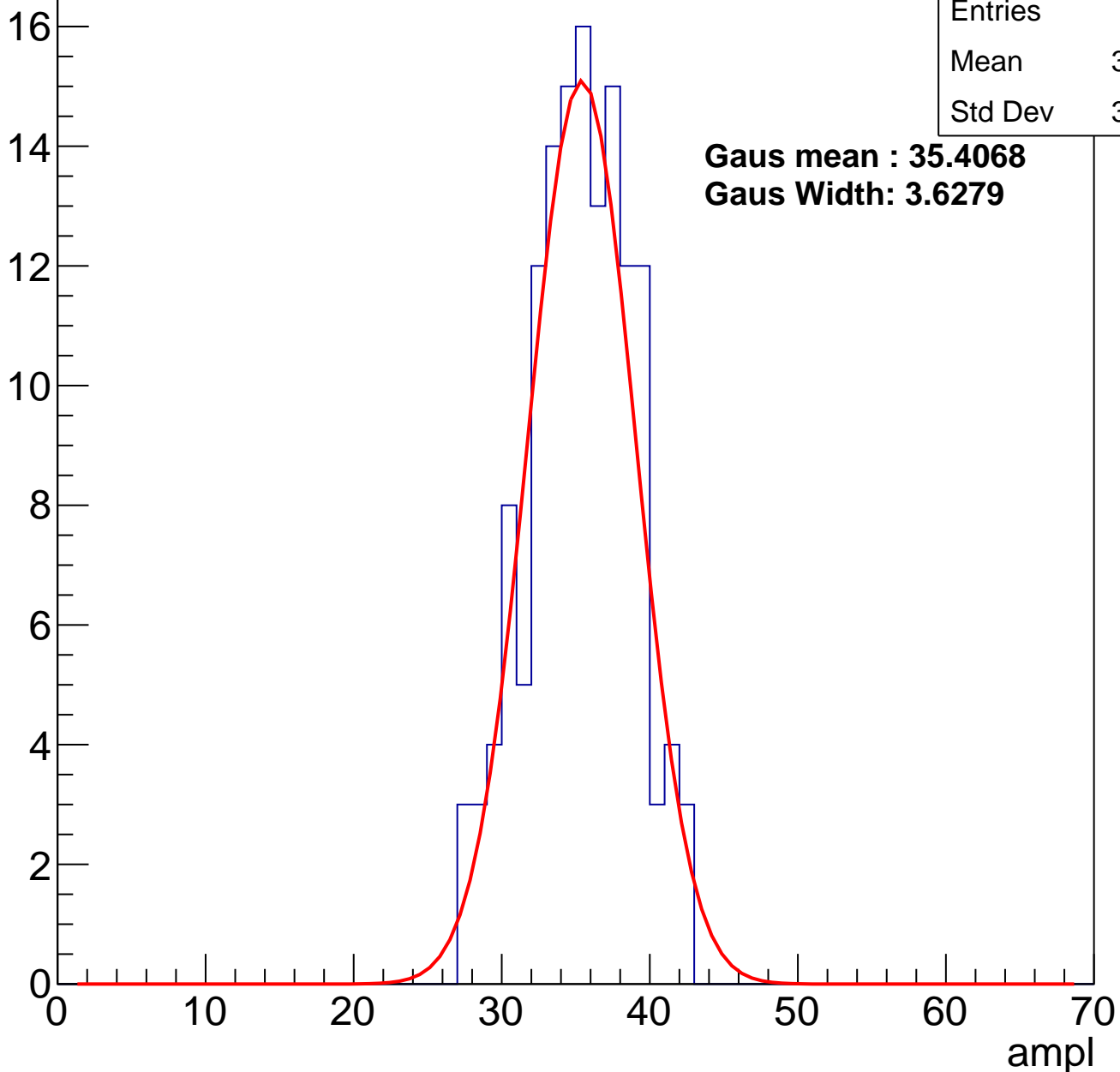
Gaus Width: 3.3013



B0L101S, U20-ch40, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

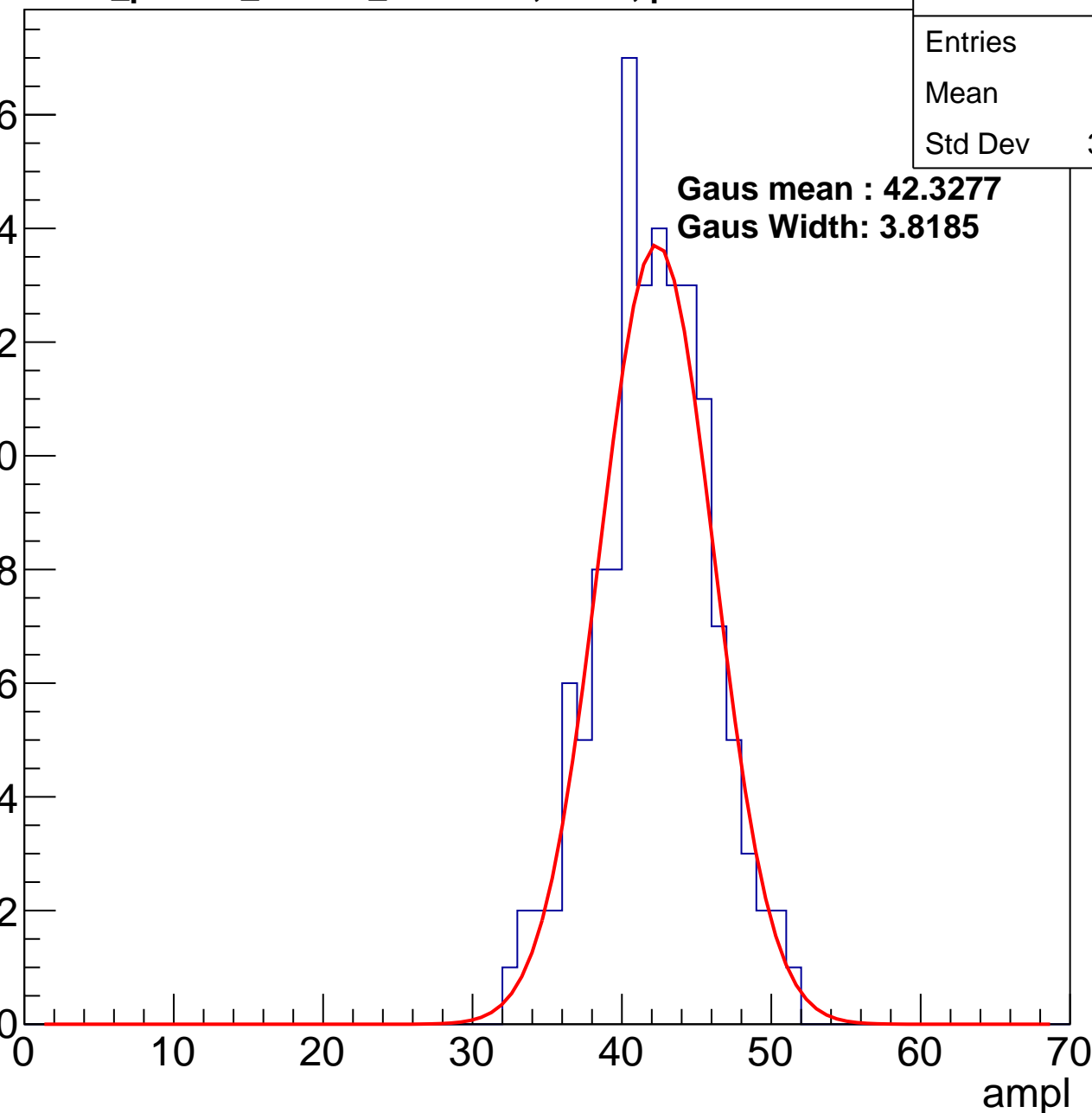
Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	41.7
Std Dev	3.765

Gaus mean : 42.3277

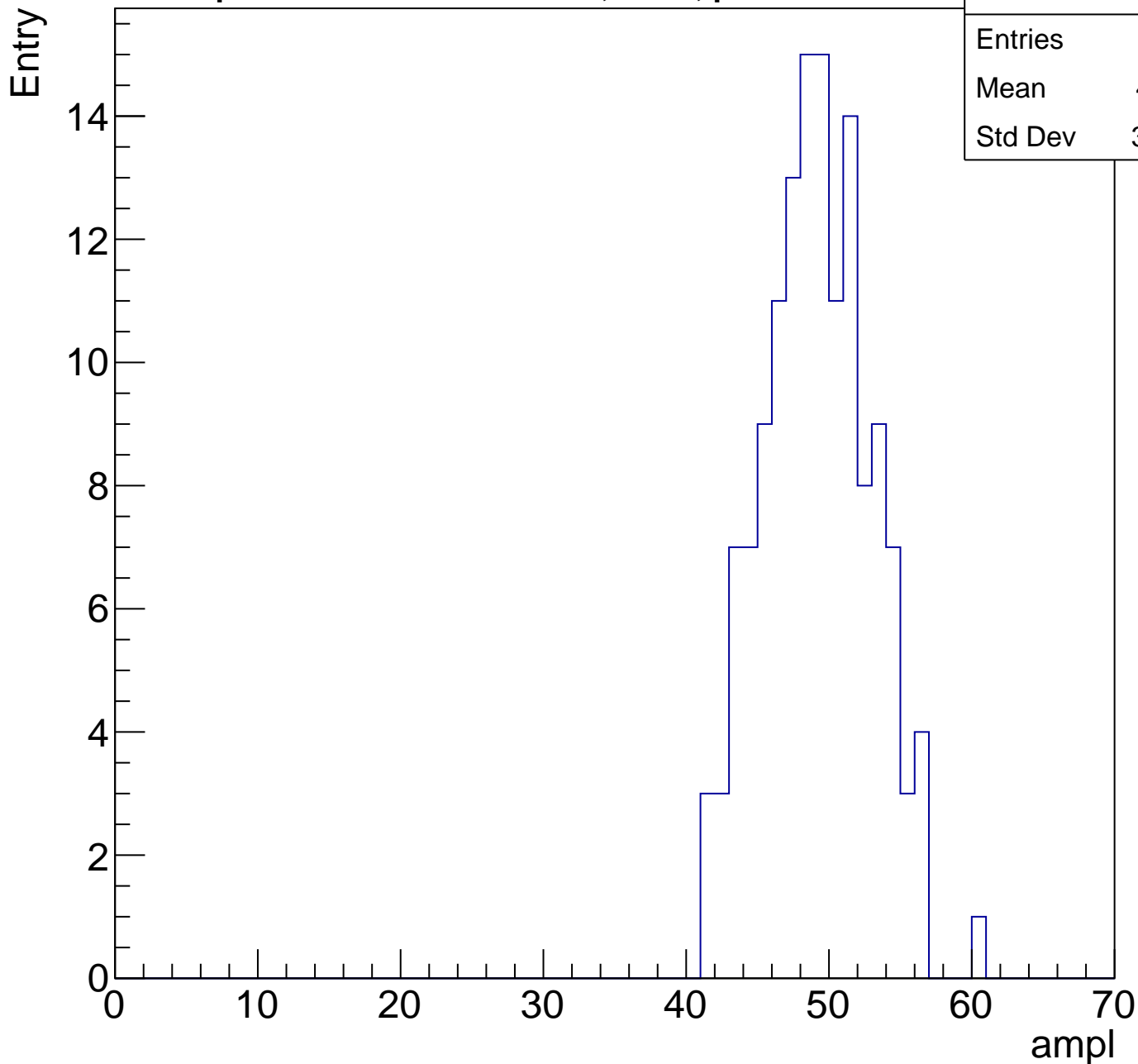
Gaus Width: 3.8185



B0L101S, U20-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

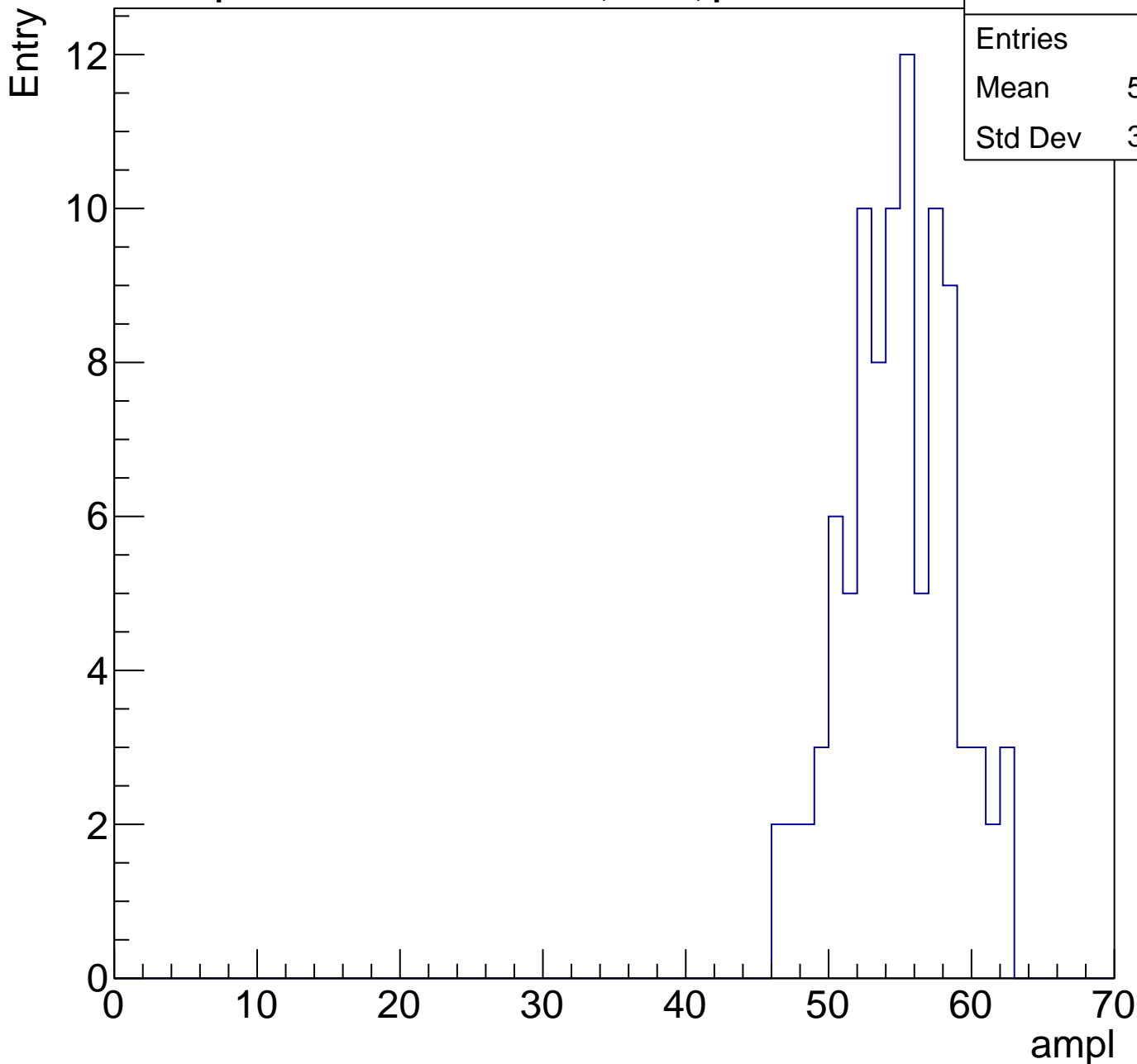
Entries	140
Mean	48.71
Std Dev	3.729



B0L101S, U20-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	54.37
Std Dev	3.682



B0L101S, U20-ch40, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	104
Mean	58.11
Std Dev	6.427

0

2

4

6

8

10

12

B0L101S, U20-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

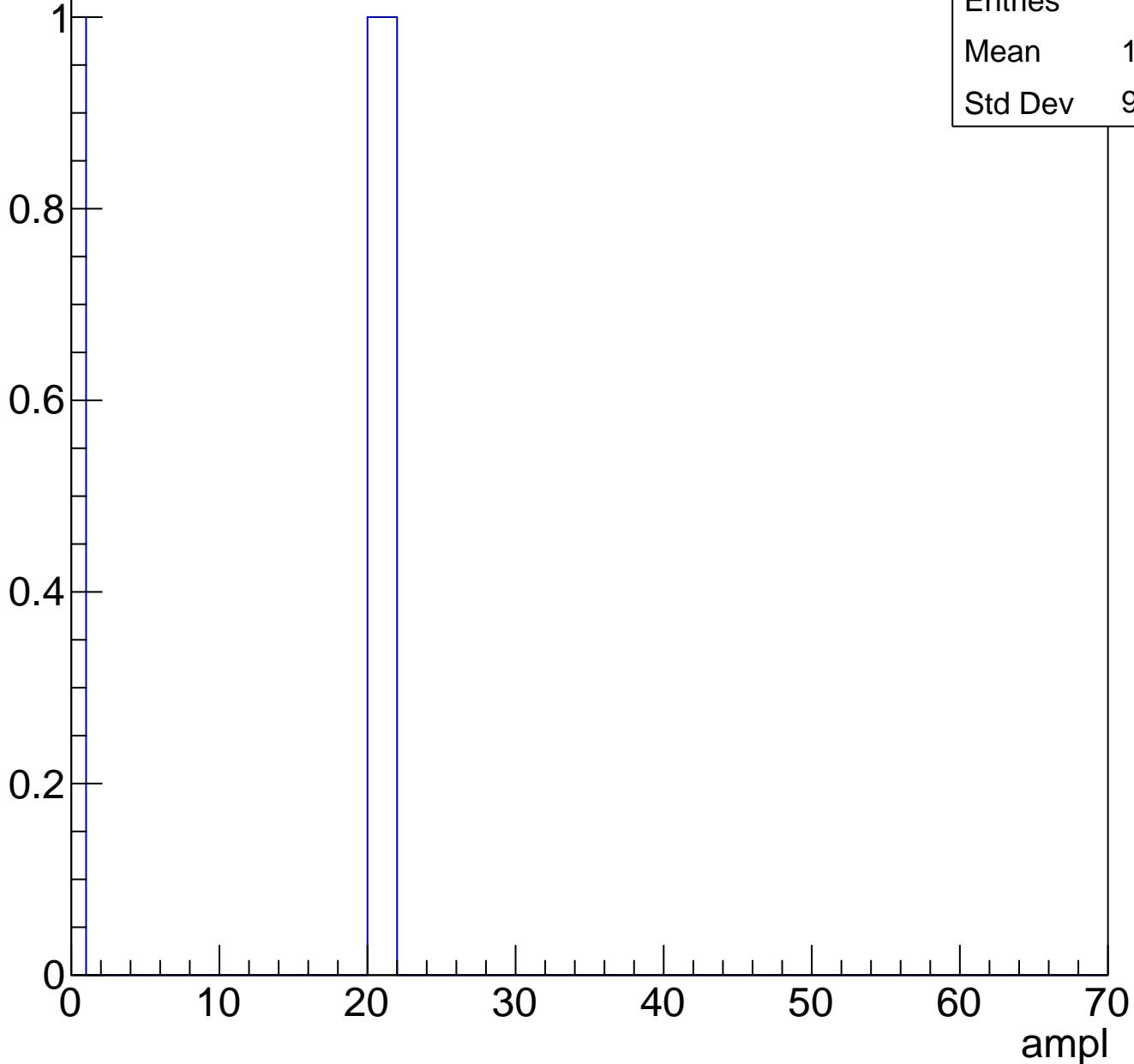
ampl

Entries	31
Mean	61.48
Std Dev	1.434

B0L101S, U20-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	13.67
Std Dev	9.672

B0L101S, U20-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	30.37
Std Dev	3.594

Gaus mean : 30.8465

Gaus Width: 3.6765

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

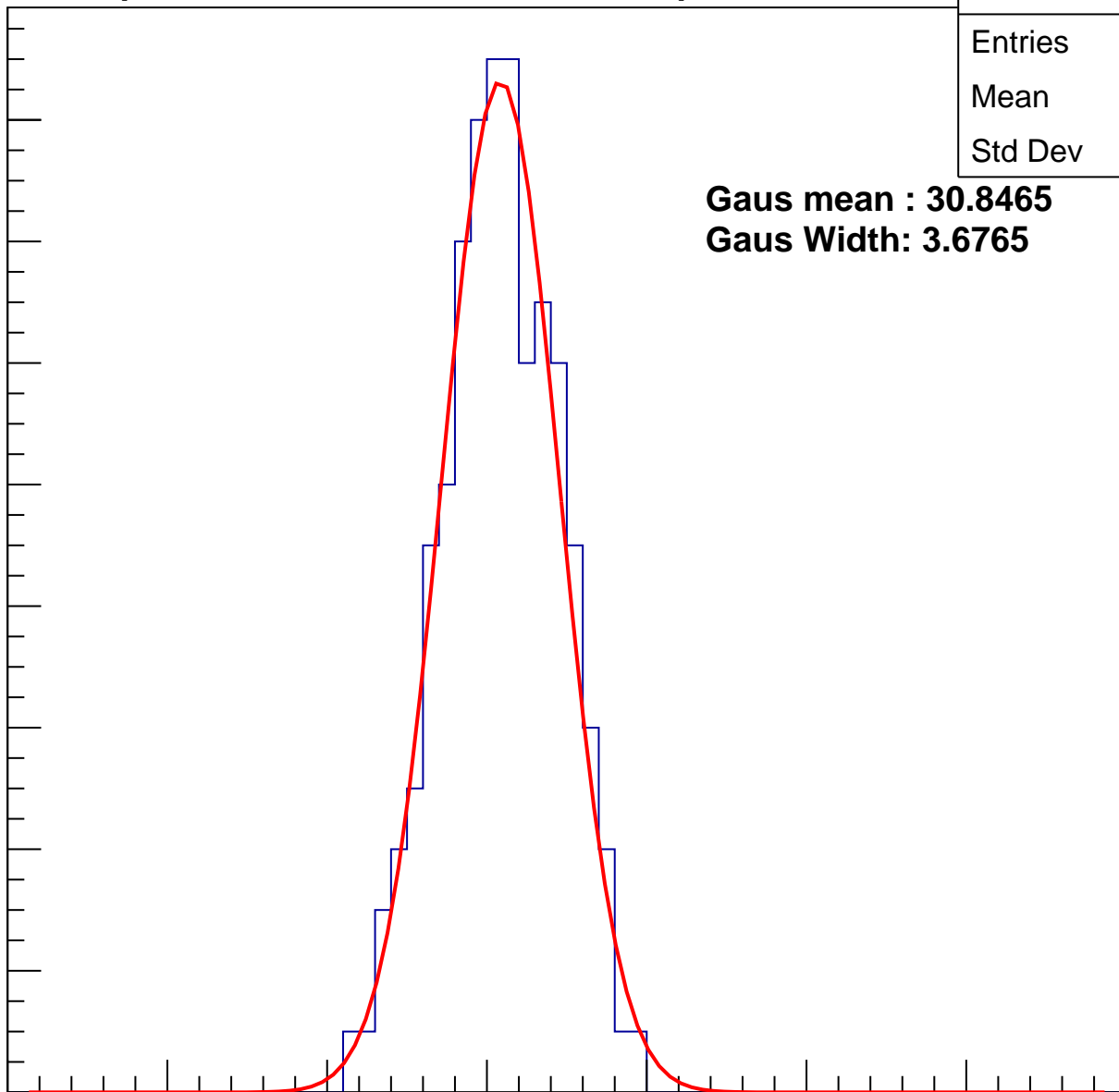
40

50

60

70

ampl



B0L101S, U20-ch41, adc1

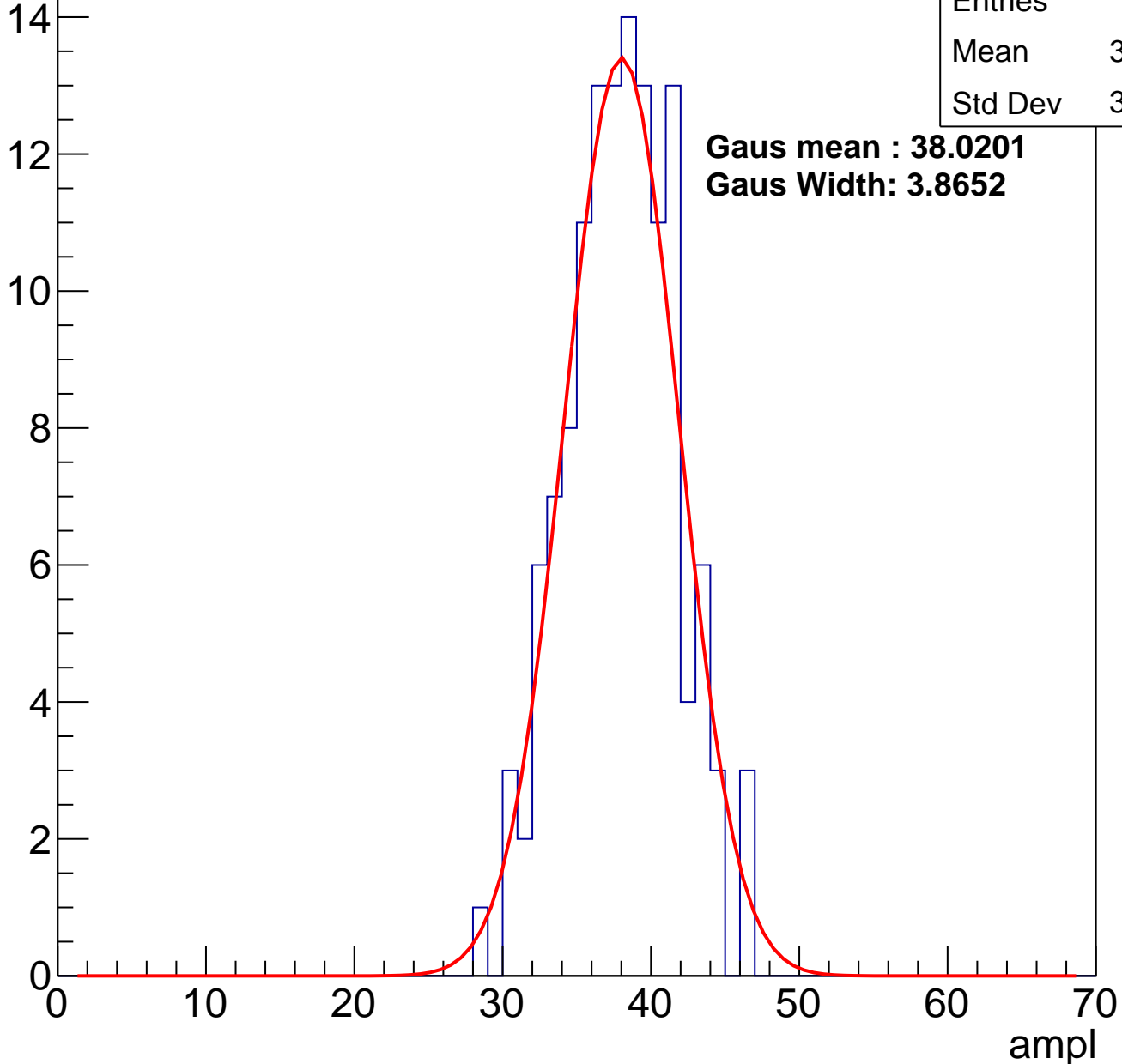
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	37.53
Std Dev	3.637

Gaus mean : 38.0201

Gaus Width: 3.8652

Entry



B0L101S, U20-ch41, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

140

Mean

44.33

Std Dev

3.733

Gaus mean : 44.7376

Gaus Width: 3.8555

0

10

20

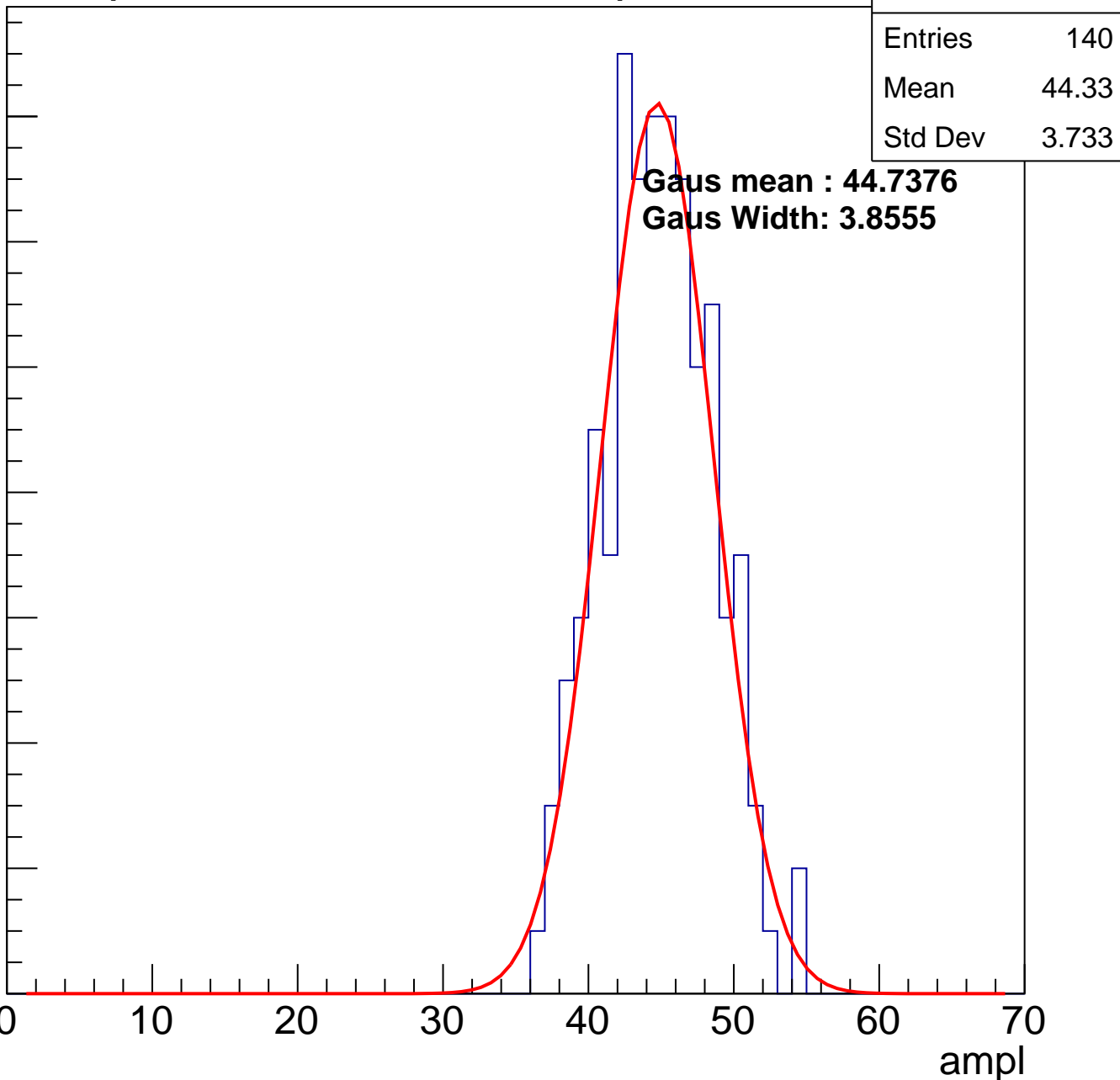
30

40

50

60

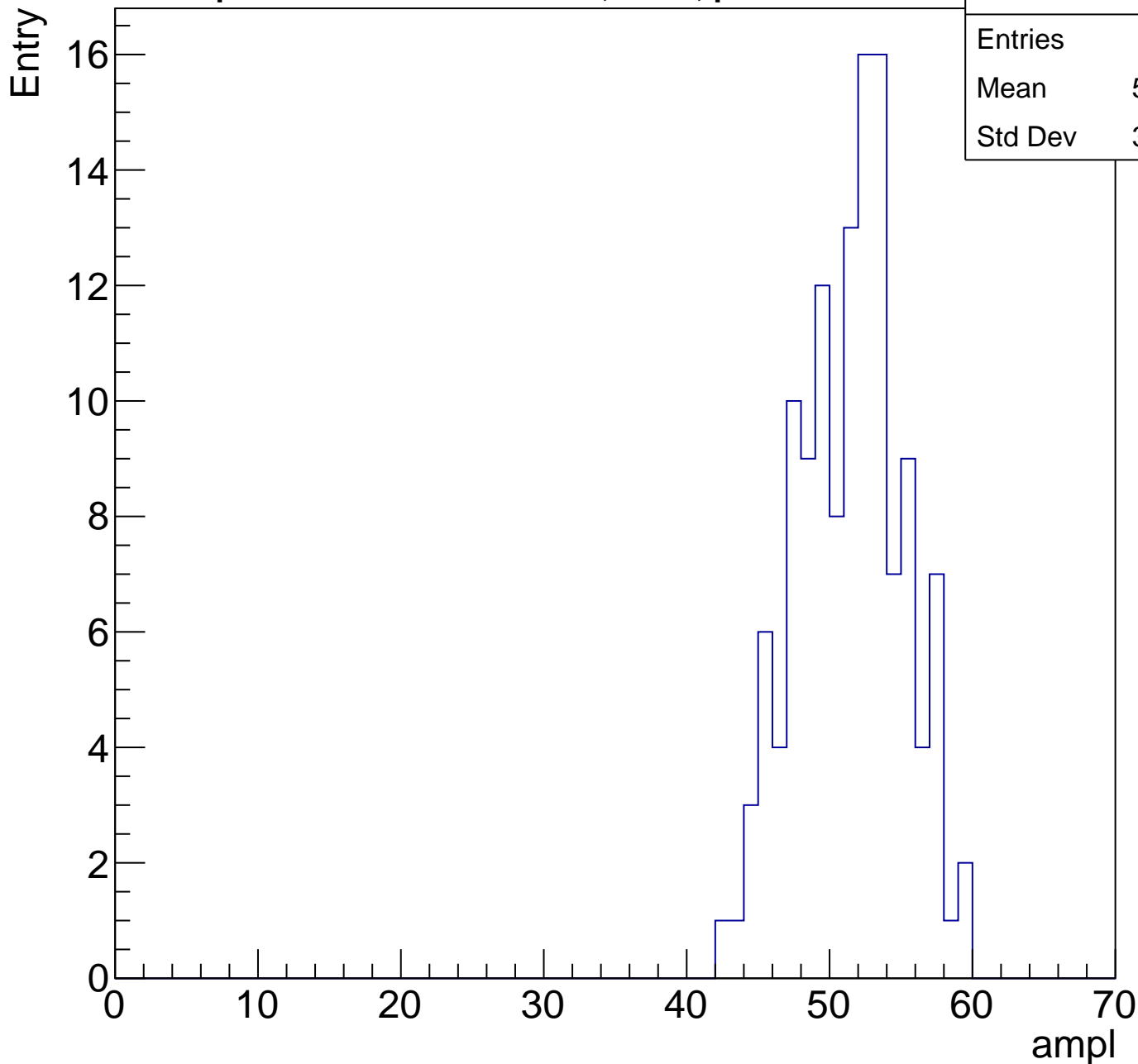
ampl



B0L101S, U20-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	50.98
Std Dev	3.652



B0L101S, U20-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

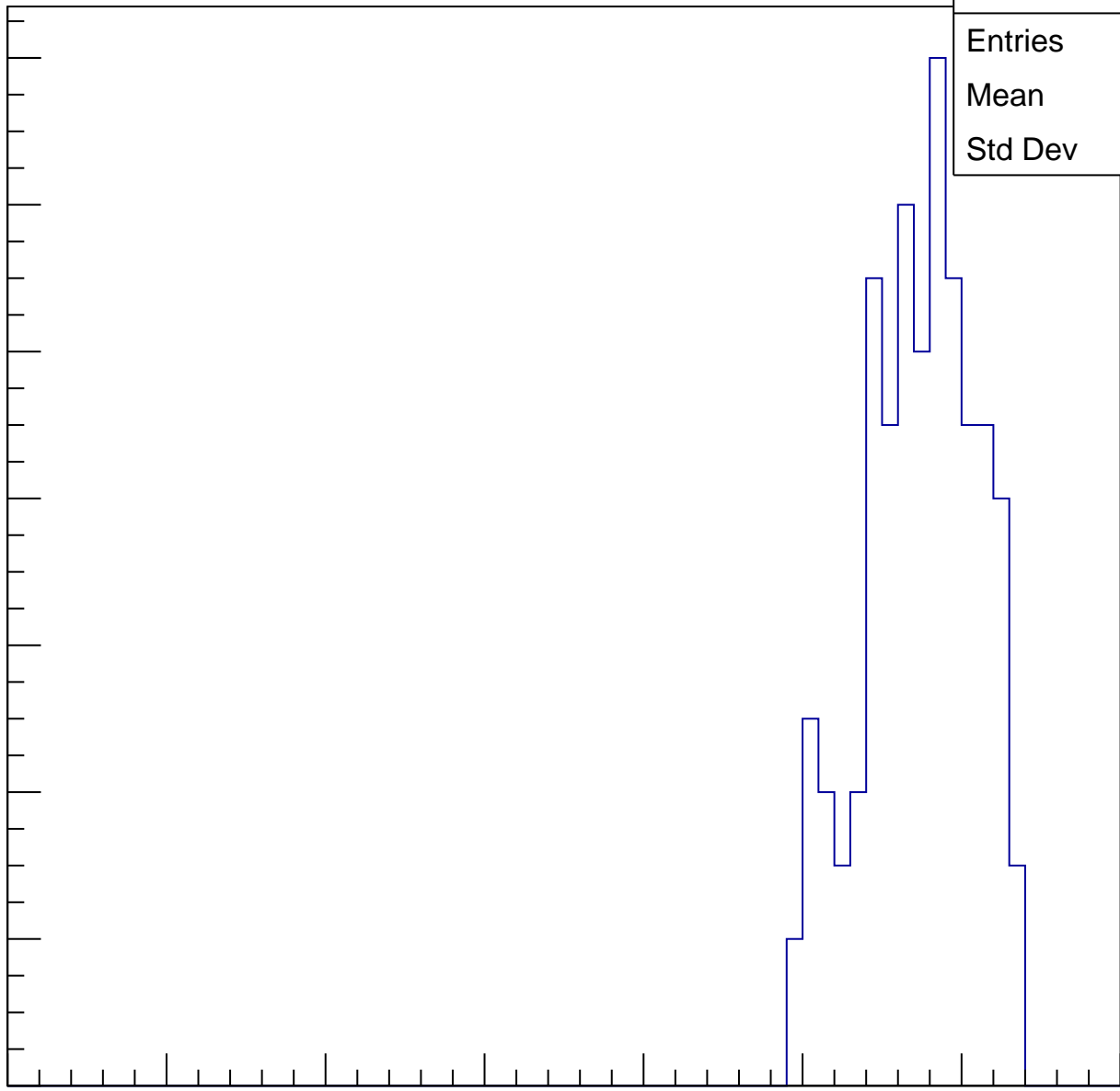
Entries	114
Mean	56.89
Std Dev	3.495

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch41, adc5

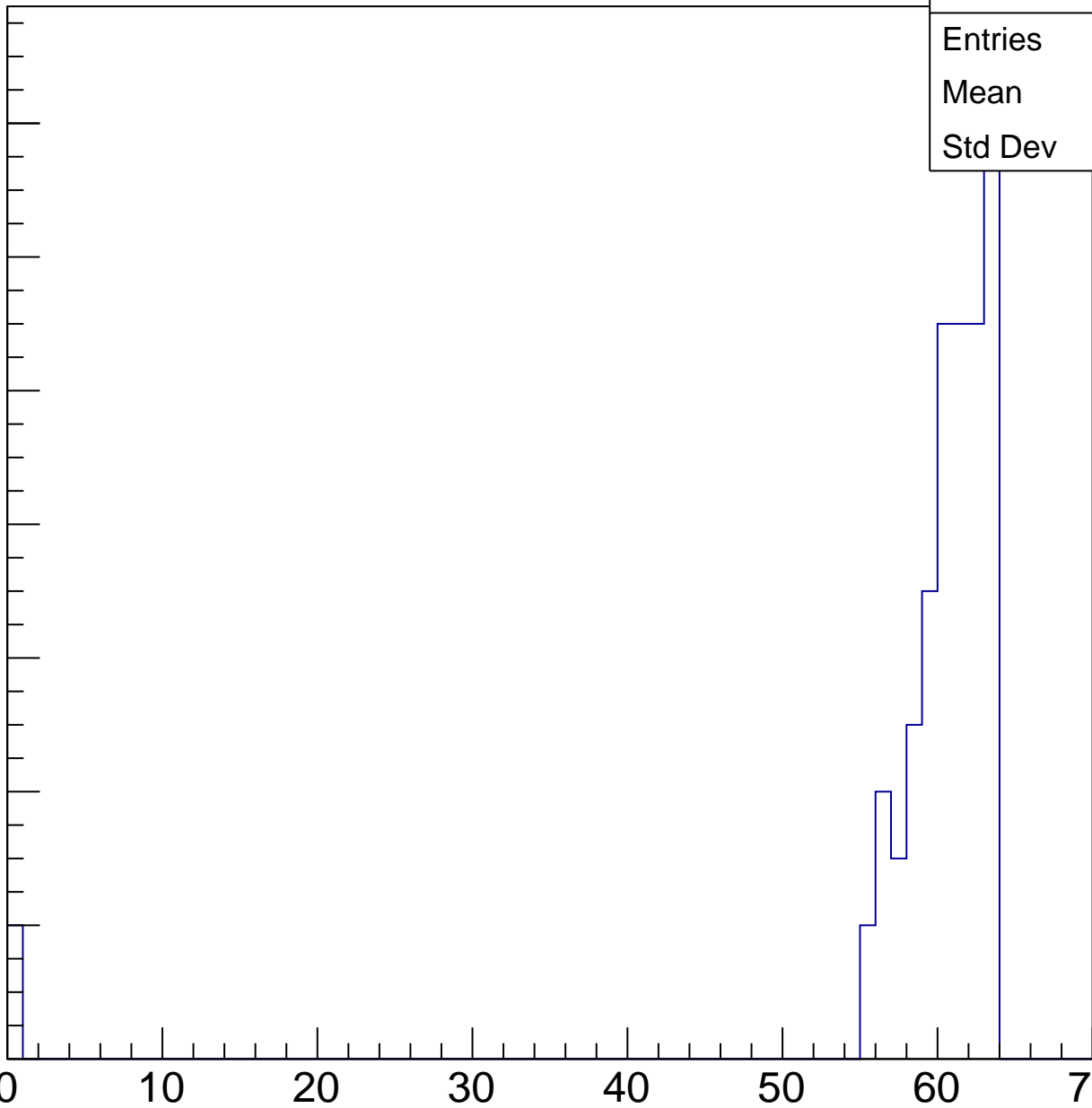
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.68
Std Dev	10.23

ampl



B0L101S, U20-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U20-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch42, adc0

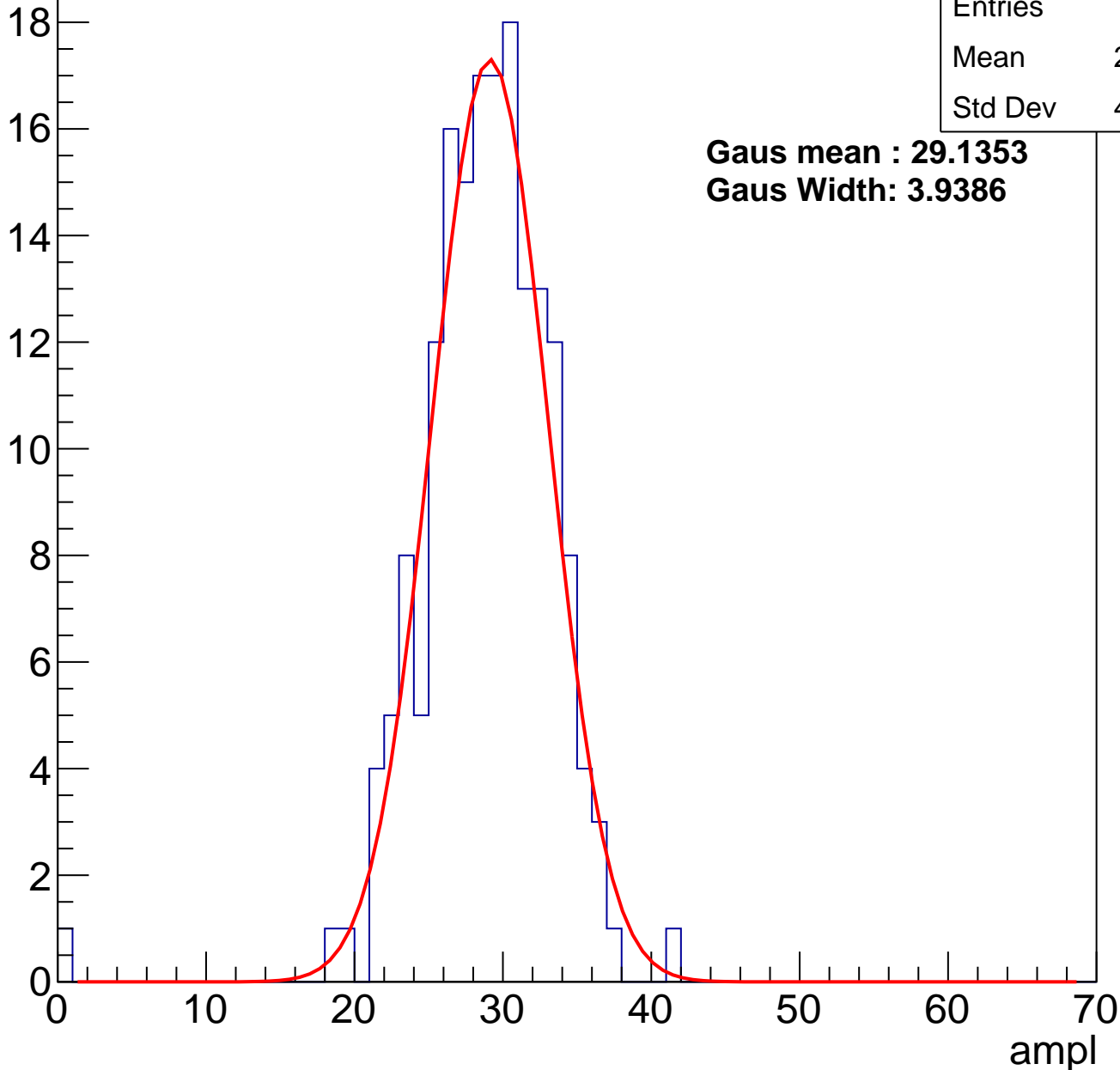
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	28.45
Std Dev	4.416

Gaus mean : 29.1353

Gaus Width: 3.9386

Entry



B0L101S, U20-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

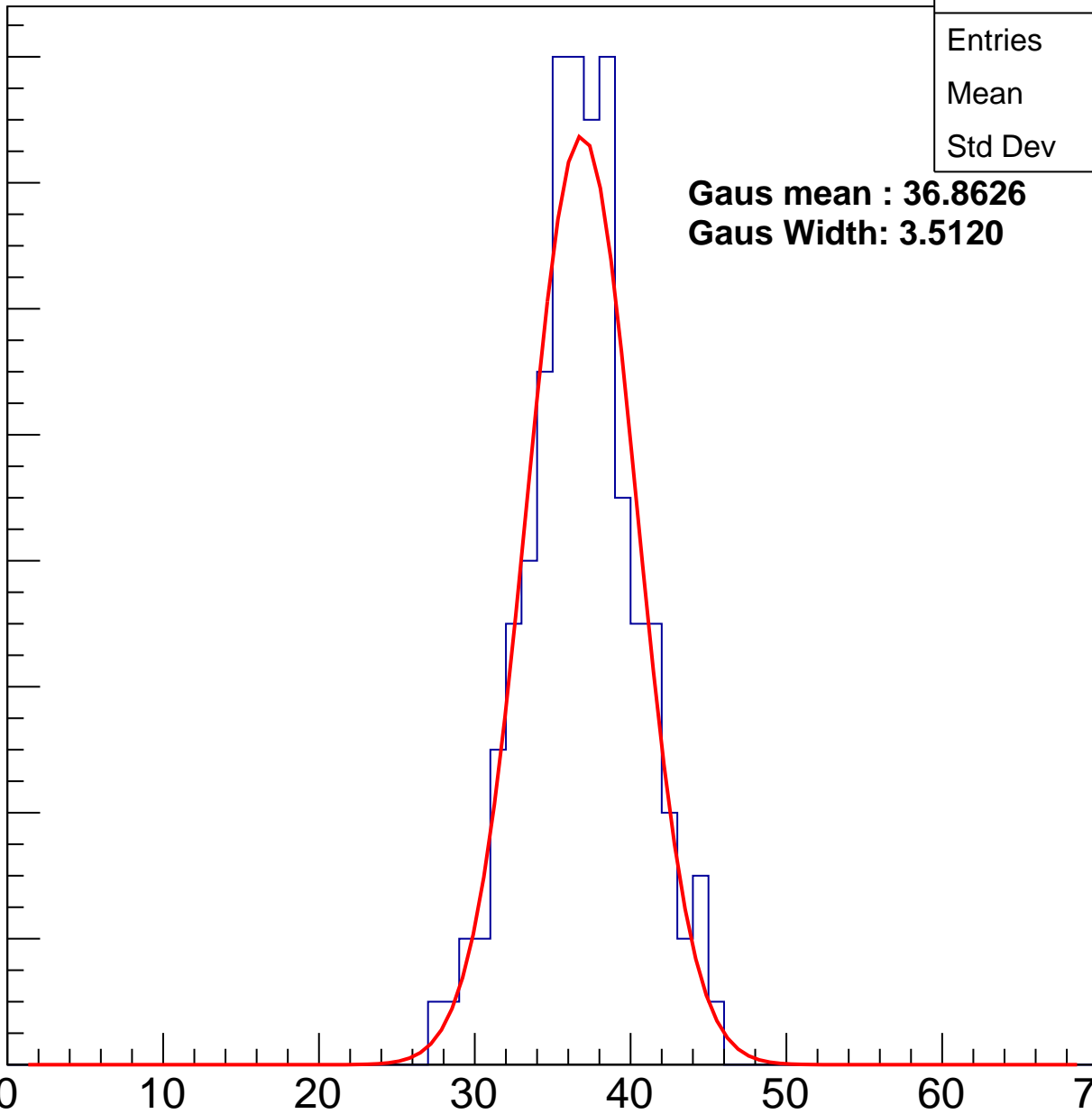
16
14
12
10
8
6
4
2
0

Entries	133
Mean	36.38
Std Dev	3.498

Gaus mean : 36.8626

Gaus Width: 3.5120

0 10 20 30 40 50 60 70
ampl



B0L101S, U20-ch42, adc2

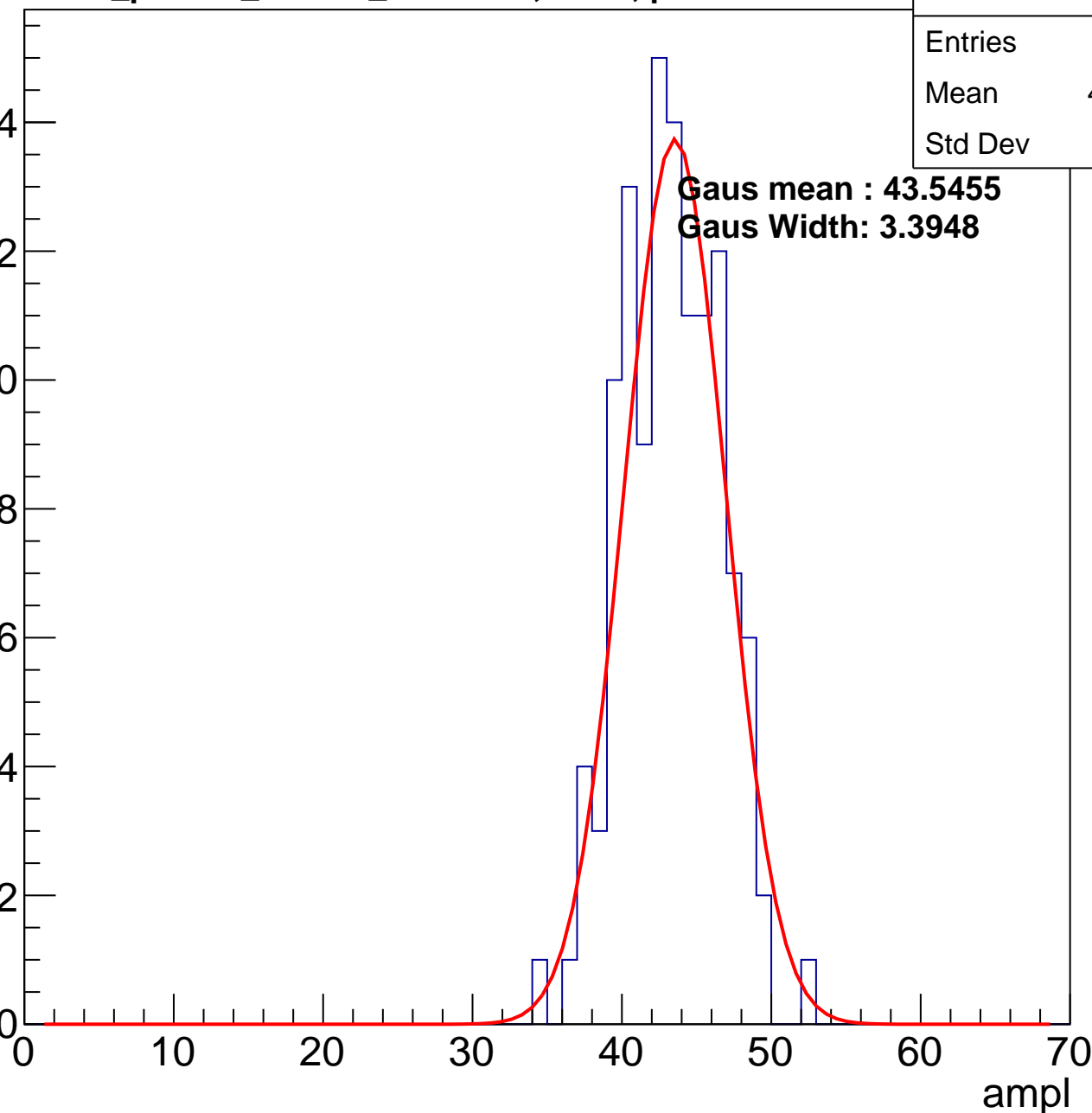
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	42.84
Std Dev	3.23

Gaus mean : 43.5455
Gaus Width: 3.3948



B0L101S, U20-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	48.93
Std Dev	3.641

Entry

16

14

12

10

8

6

4

2

0

0

10

20

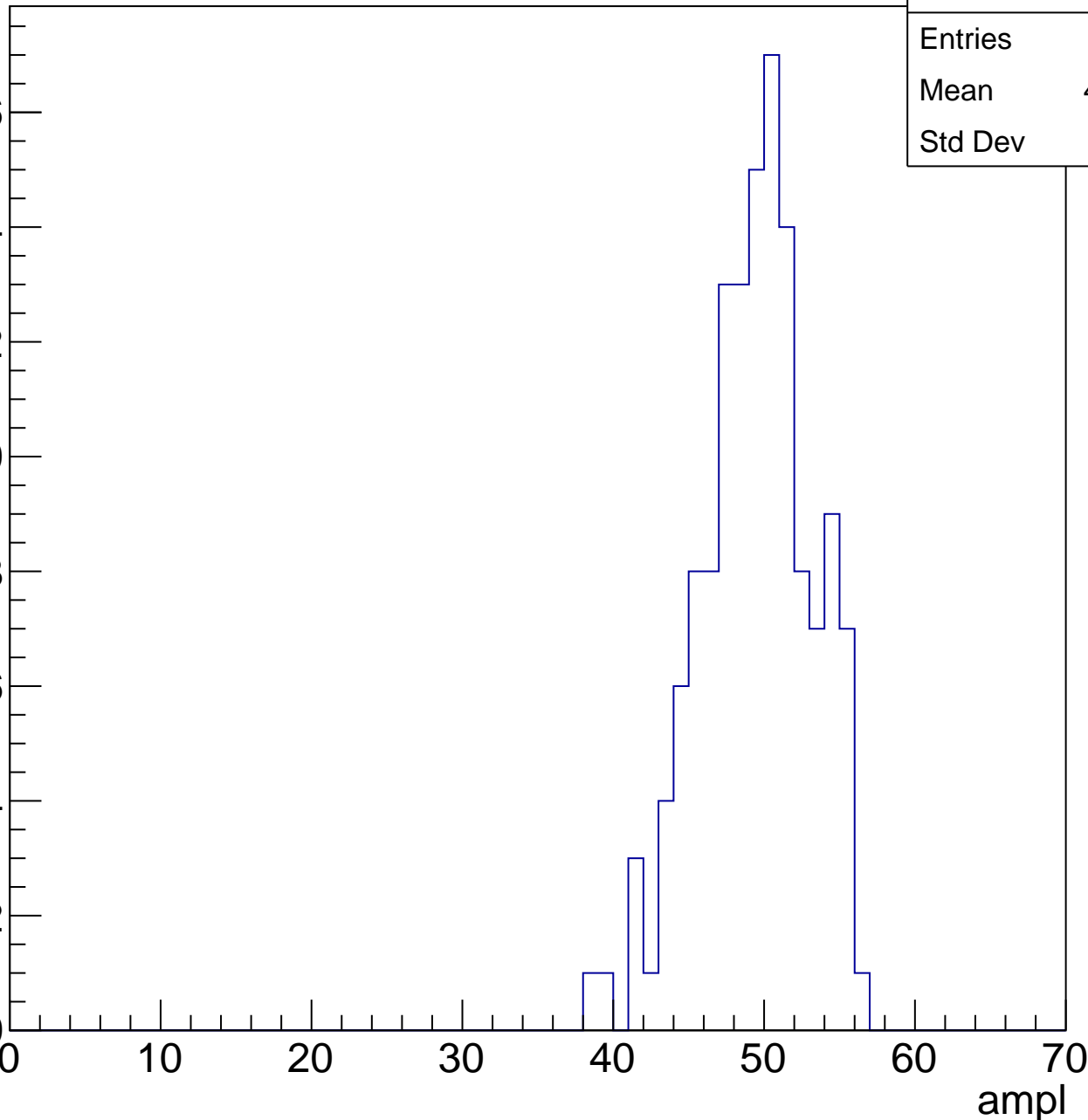
30

40

50

60

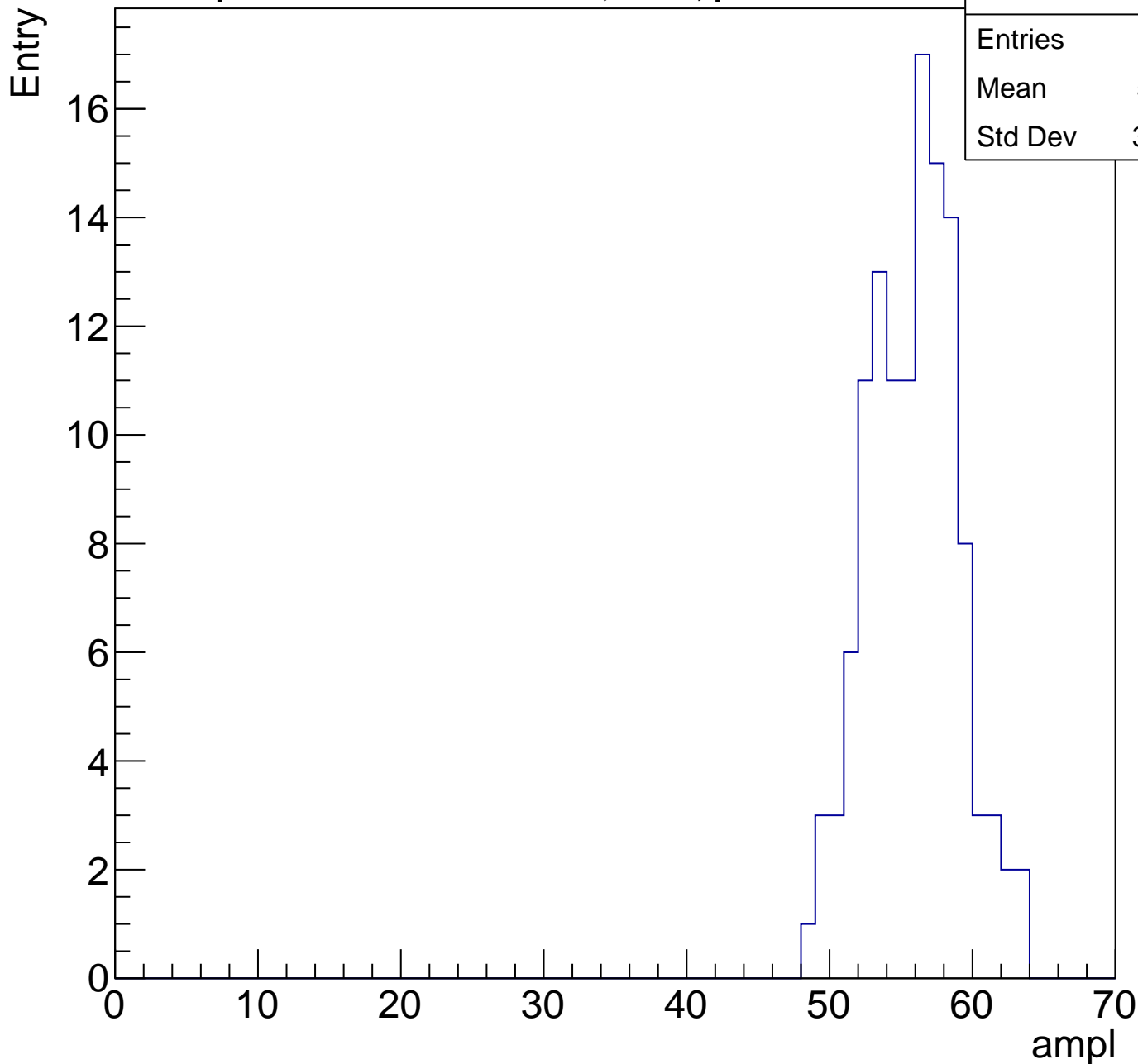
ampl



B0L101S, U20-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	55.41
Std Dev	3.136



B0L101S, U20-ch42, adc5

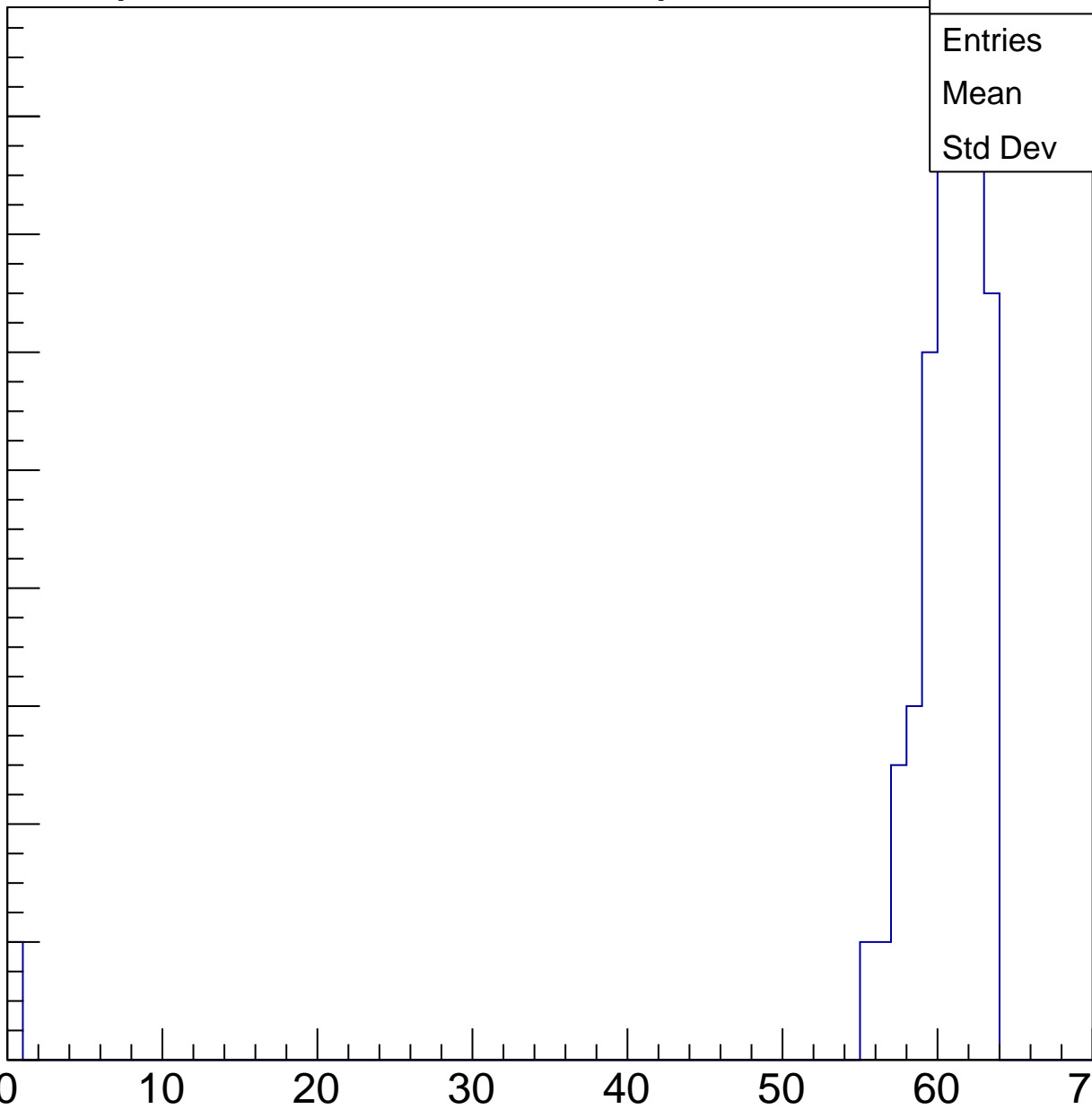
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.03
Std Dev	9.014

ampl

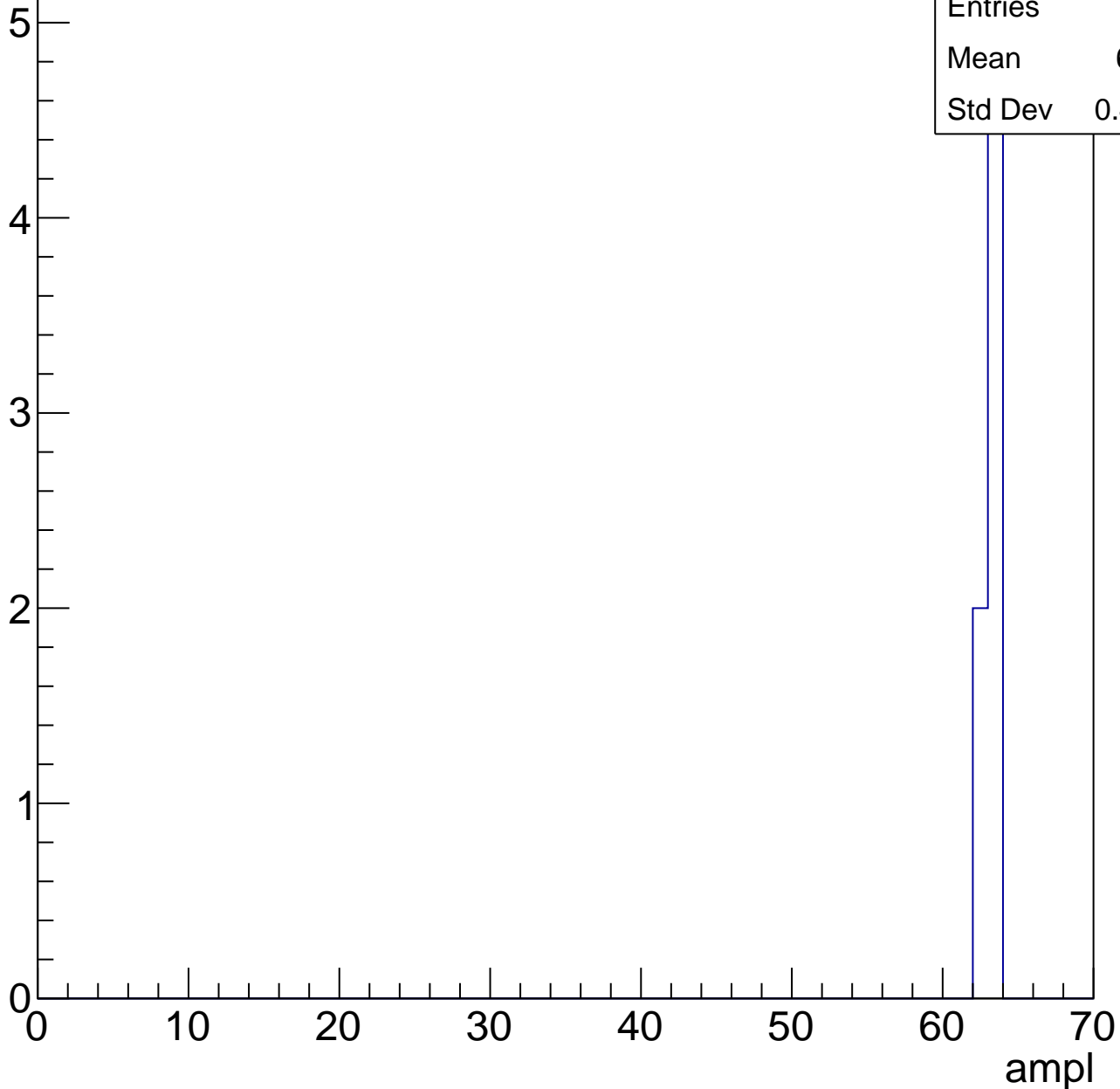


B0L101S, U20-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B0L101S, U20-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.12
Std Dev	4.168

Gaus mean : 30.1596

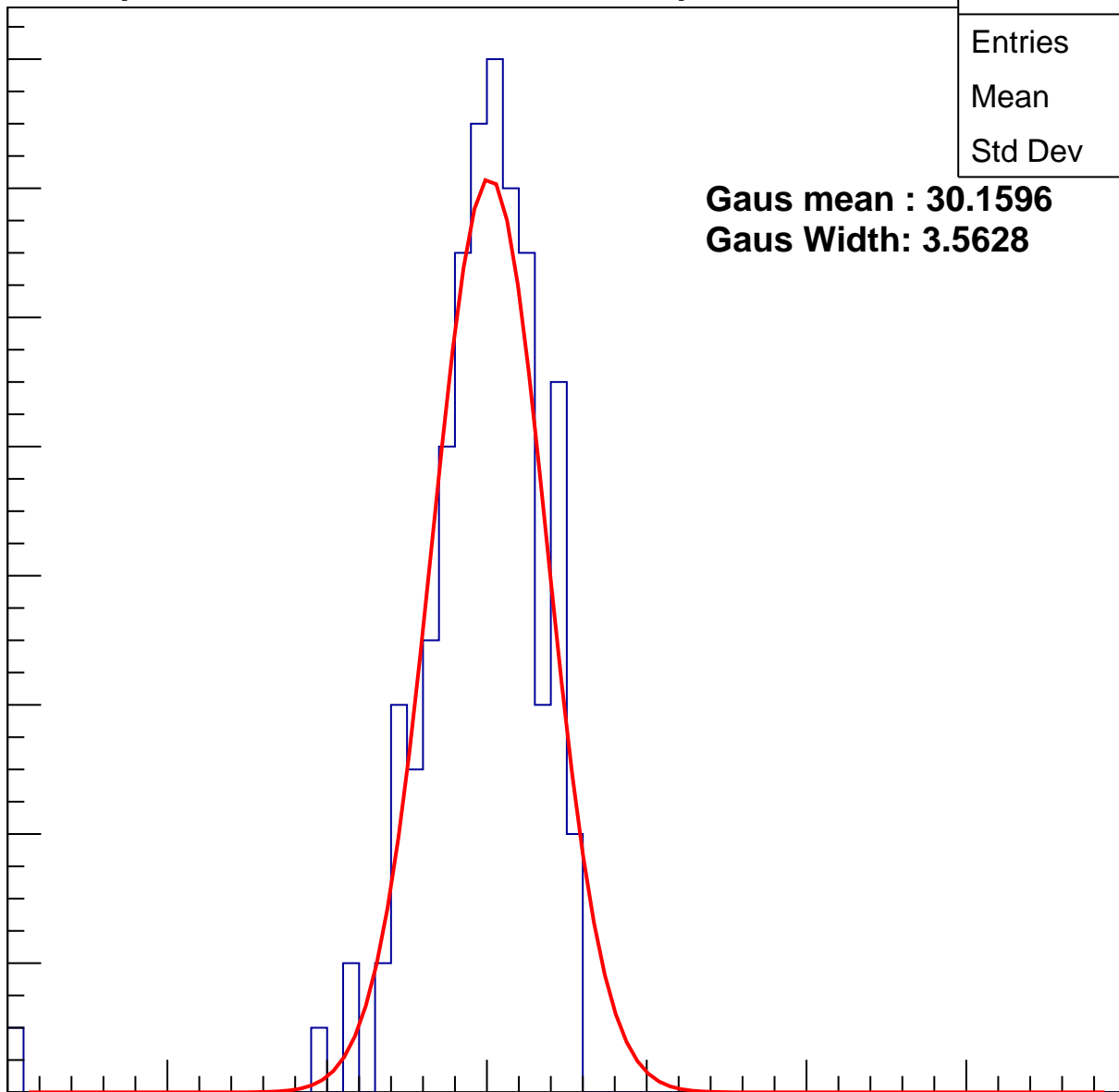
Gaus Width: 3.5628

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch43, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.37
Std Dev	3.455

Gaus mean : 36.9048

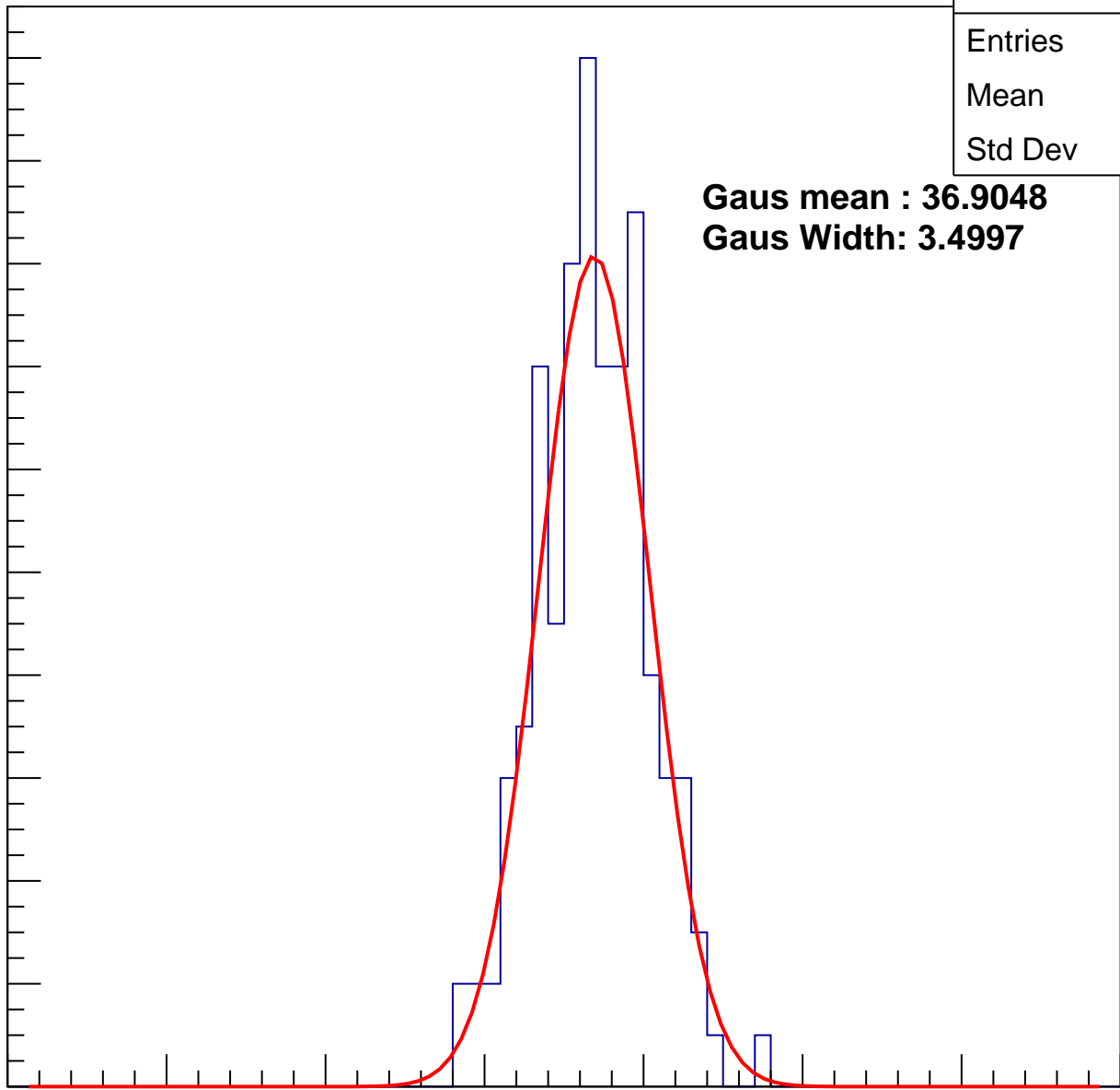
Gaus Width: 3.4997

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

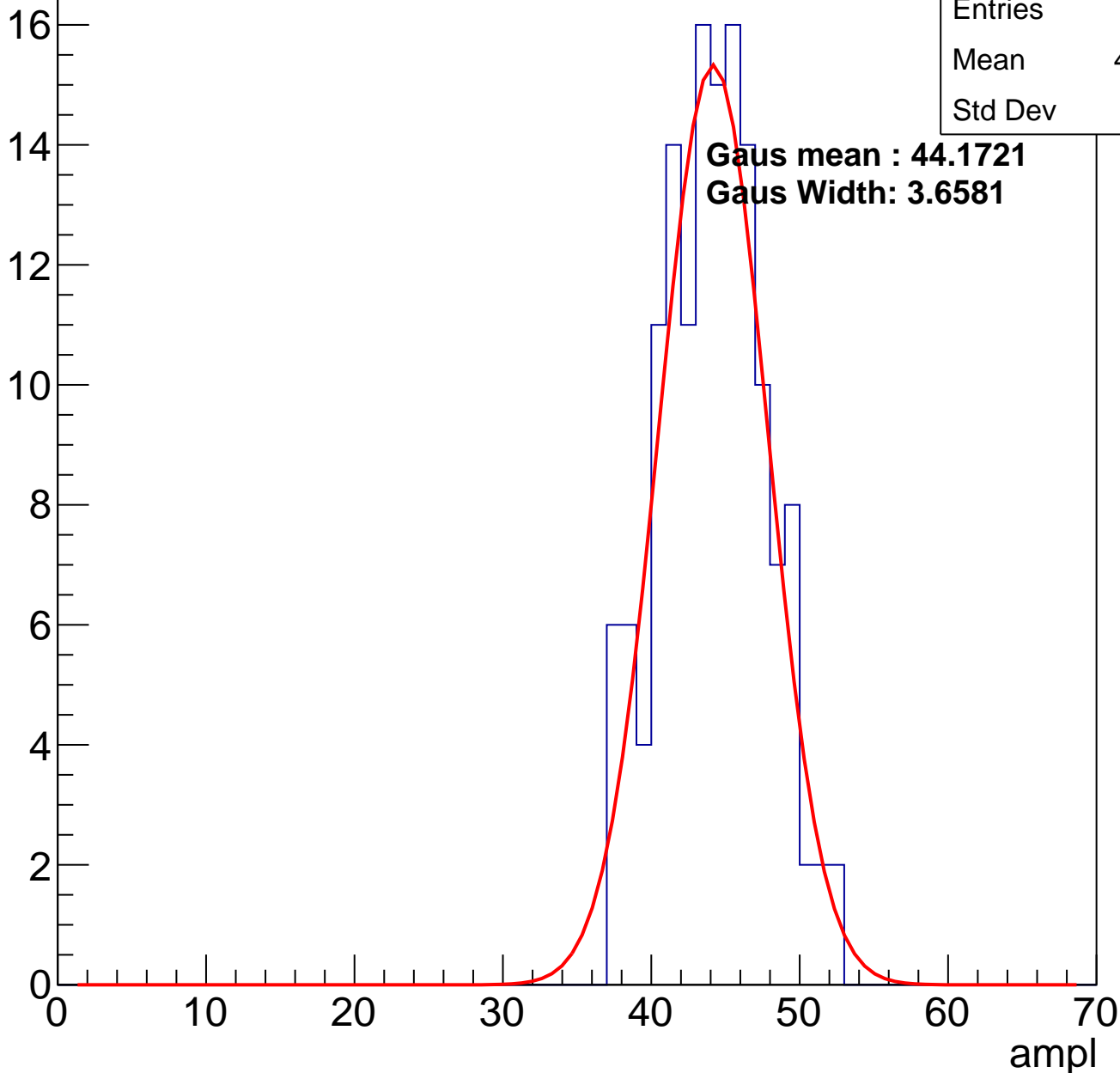
0 10 20 30 40 50 60 70



B0L101S, U20-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

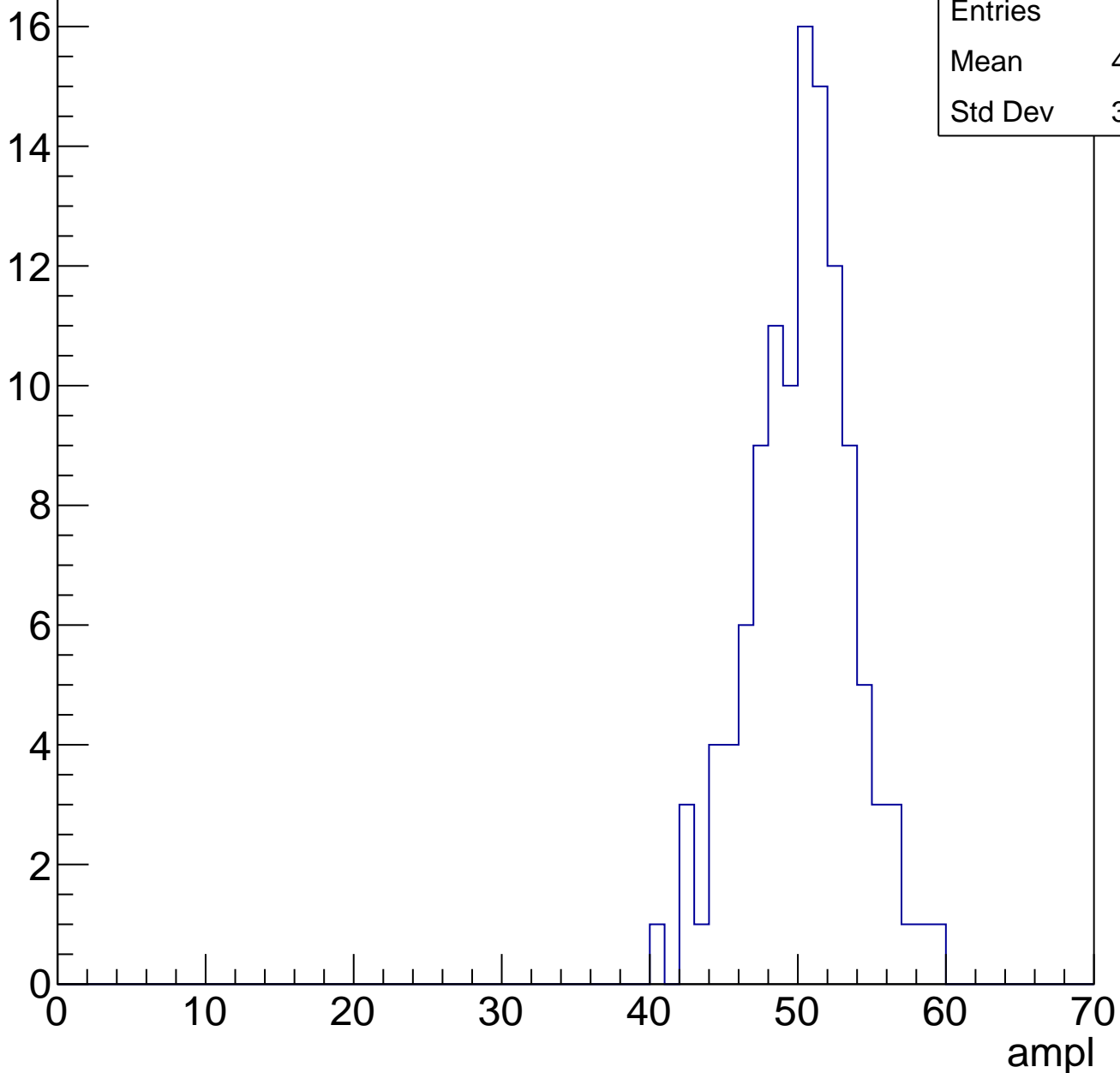
Entry



B0L101S, U20-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch43, adc4

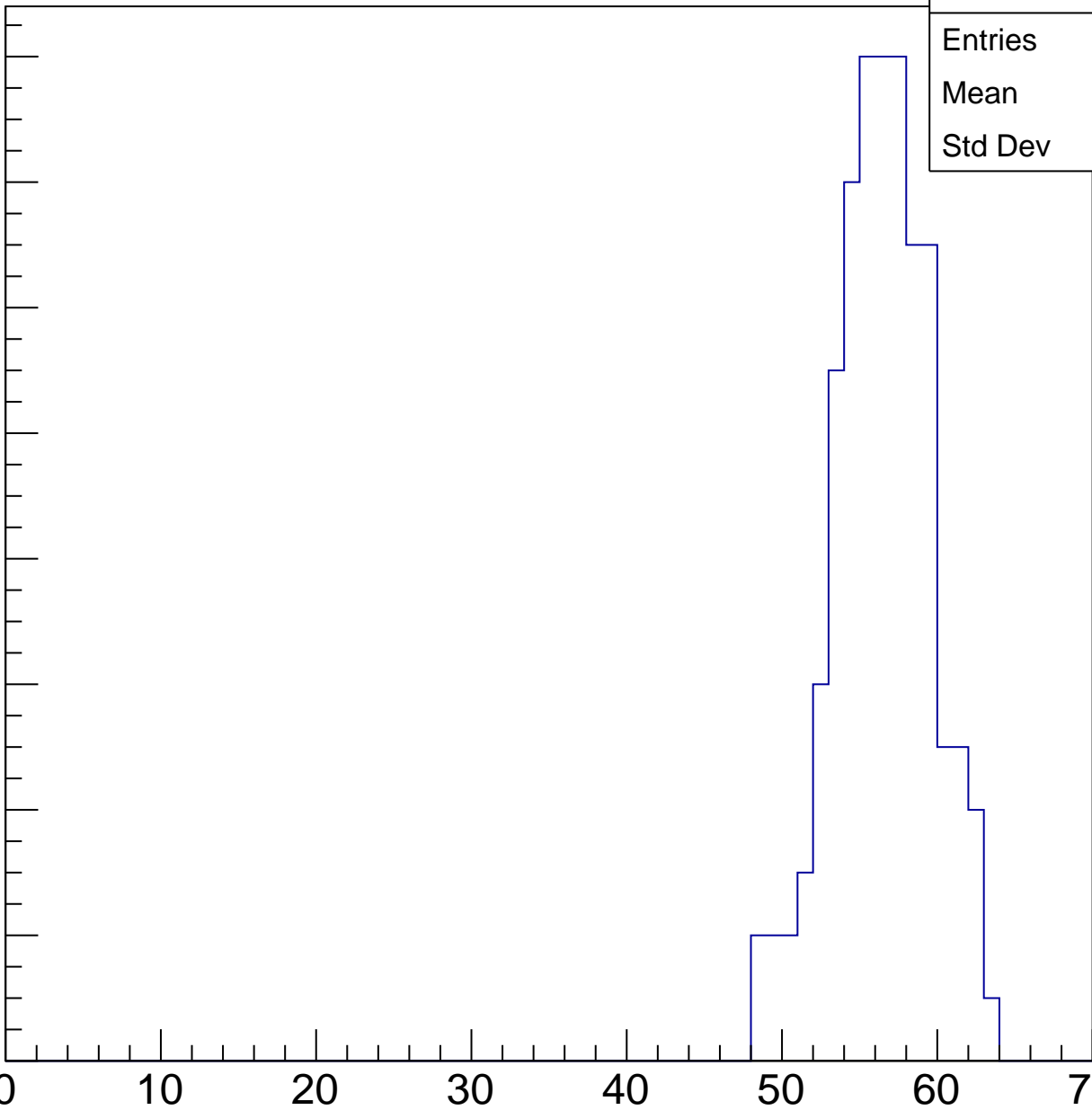
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	55.99
Std Dev	3.079

ampl



B0L101S, U20-ch43, adc5

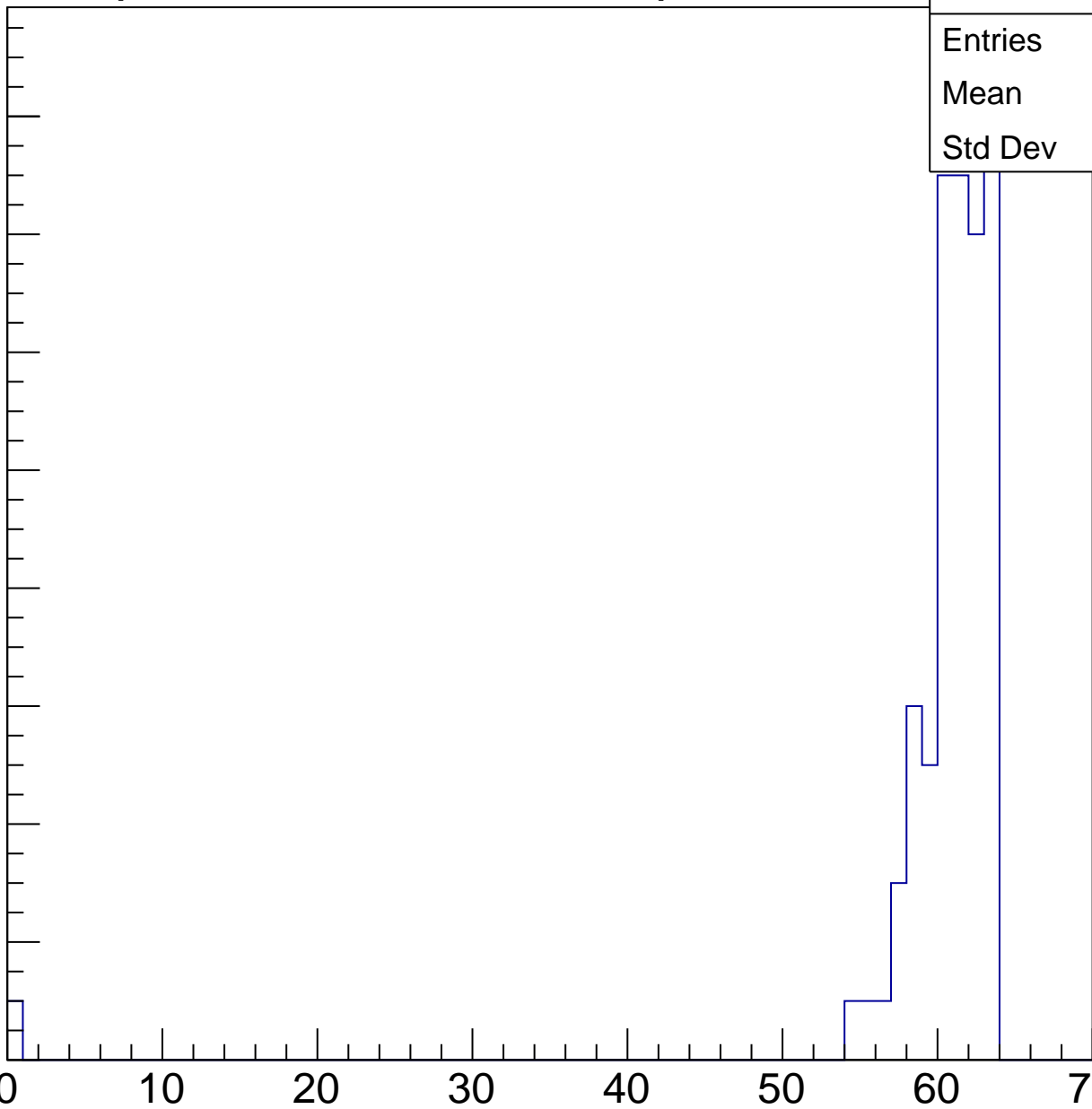
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.91
Std Dev	7.077

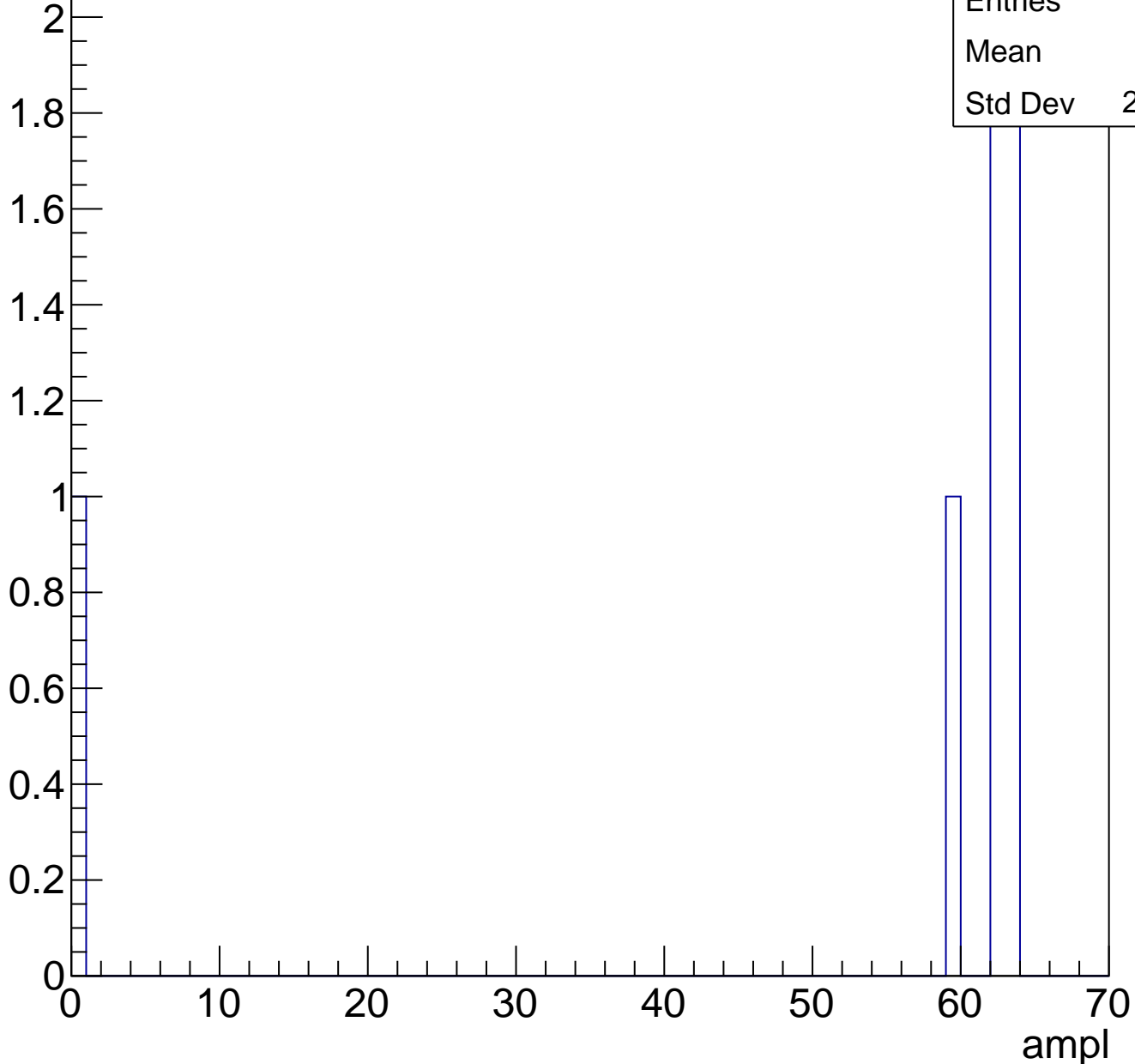
ampl



B0L101S, U20-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch44, adc0

calib_packv5_042523_0143.root, FC#1, port C1

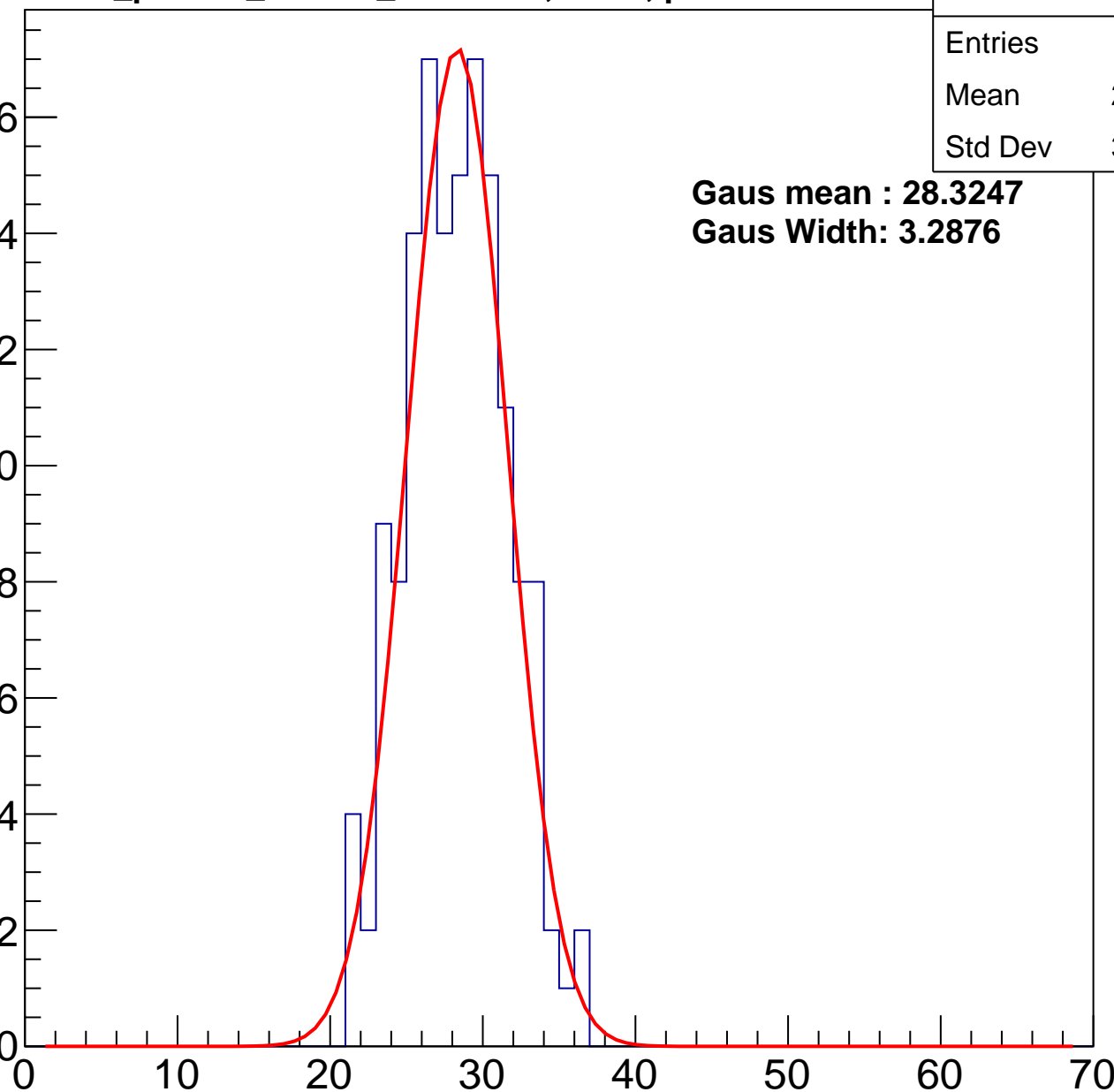
Entry

16
14
12
10
8
6
4
2
0

Entries	147
Mean	27.86
Std Dev	3.278

Gaus mean : 28.3247

Gaus Width: 3.2876



ampl

B0L101S, U20-ch44, adc1

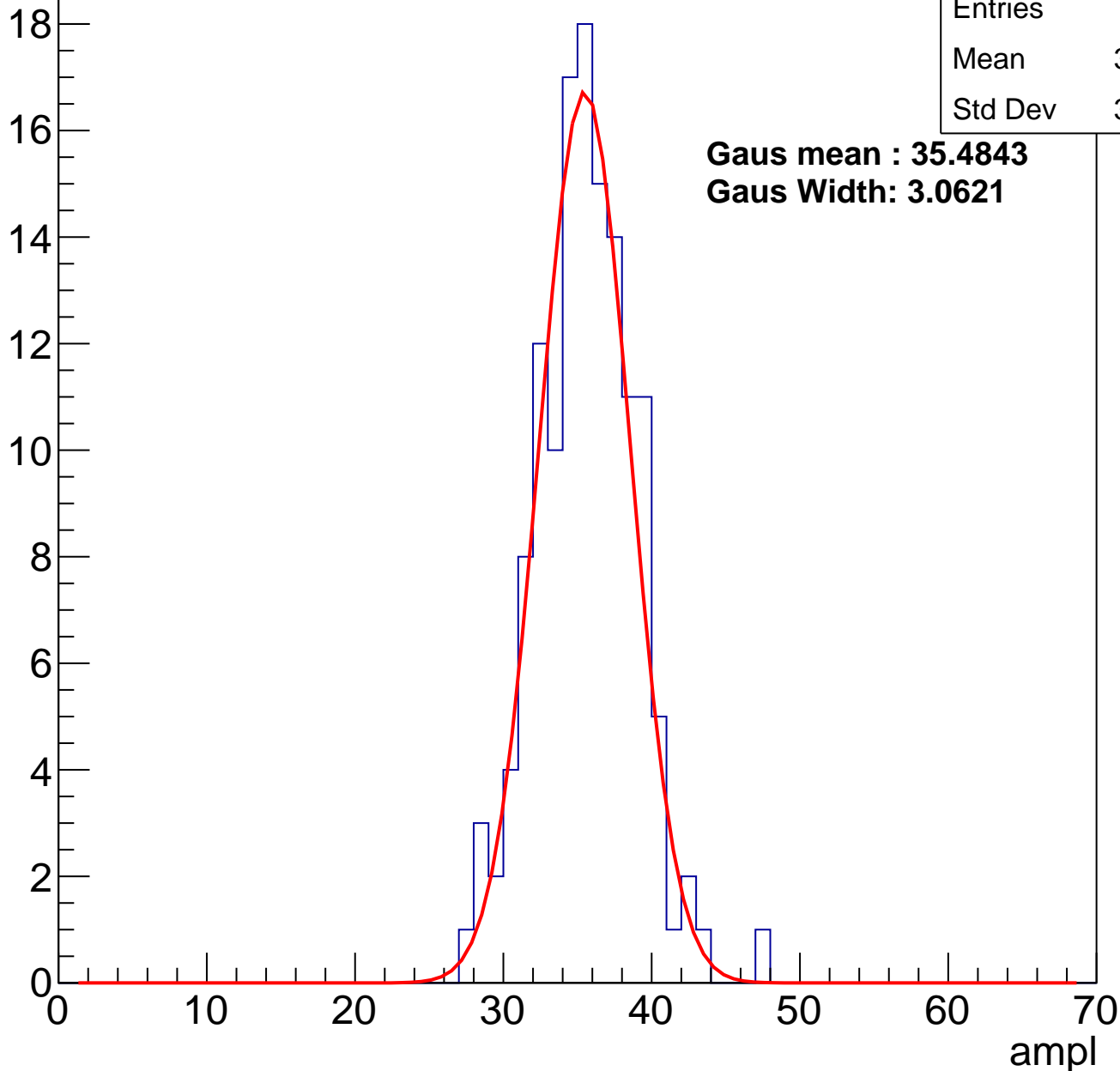
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	35.14
Std Dev	3.306

Gaus mean : 35.4843

Gaus Width: 3.0621

Entry



B0L101S, U20-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

106

Mean

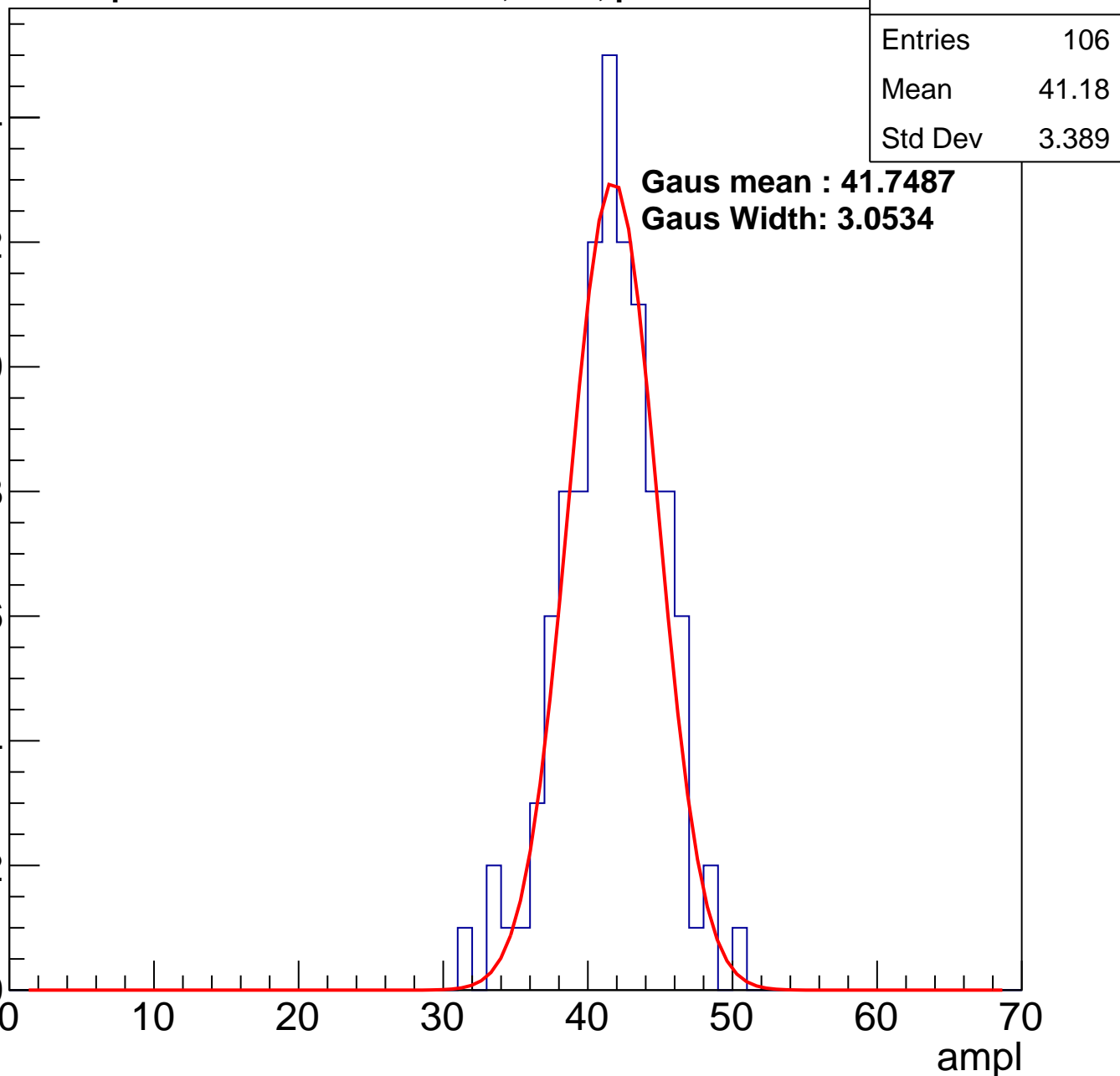
41.18

Std Dev

3.389

Gaus mean : 41.7487

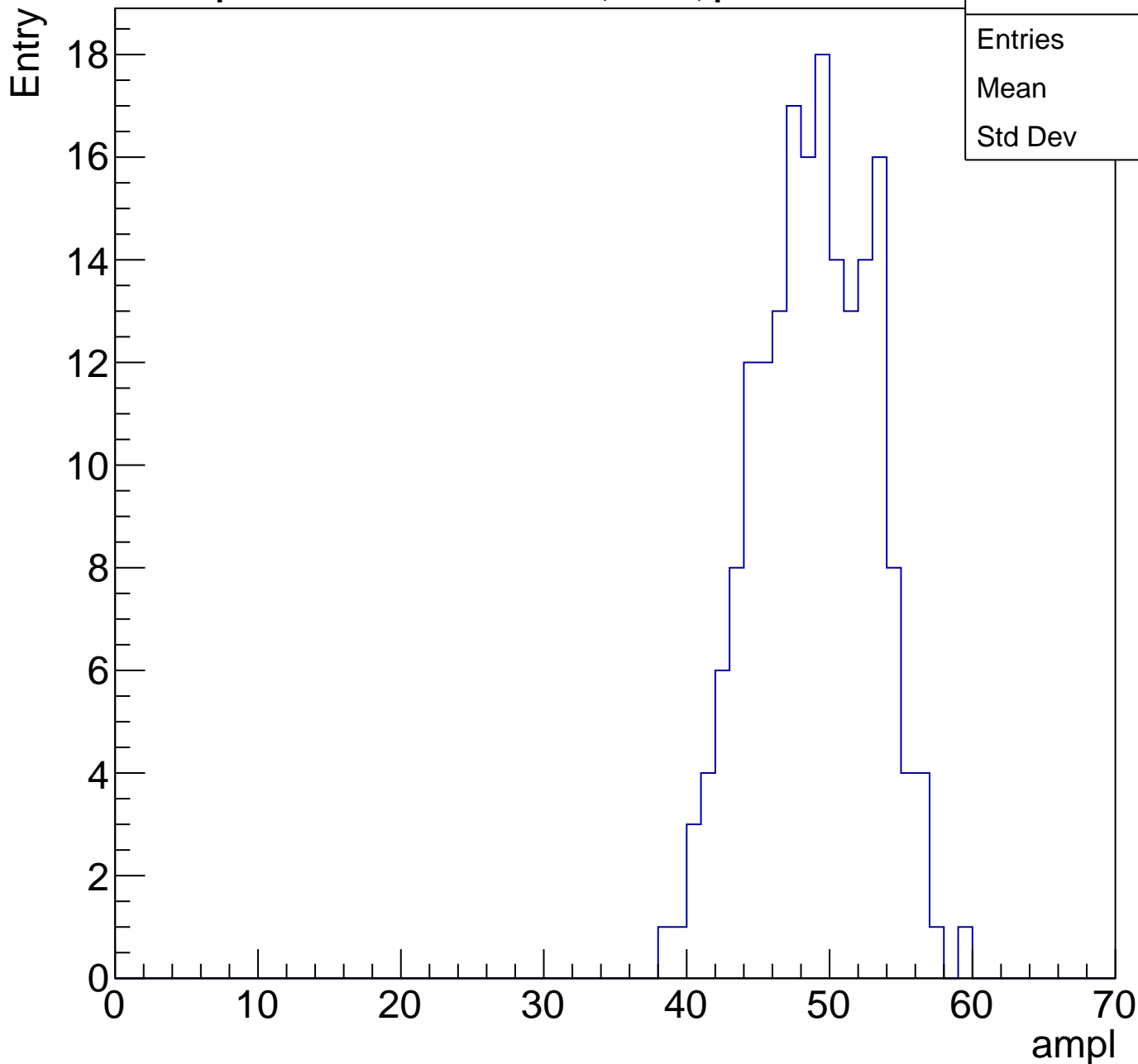
Gaus Width: 3.0534



B0L101S, U20-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	48.4
Std Dev	4.07



B0L101S, U20-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	77
Mean	54.57
Std Dev	3.477

Entry

10

8

6

4

2

0

0

10

20

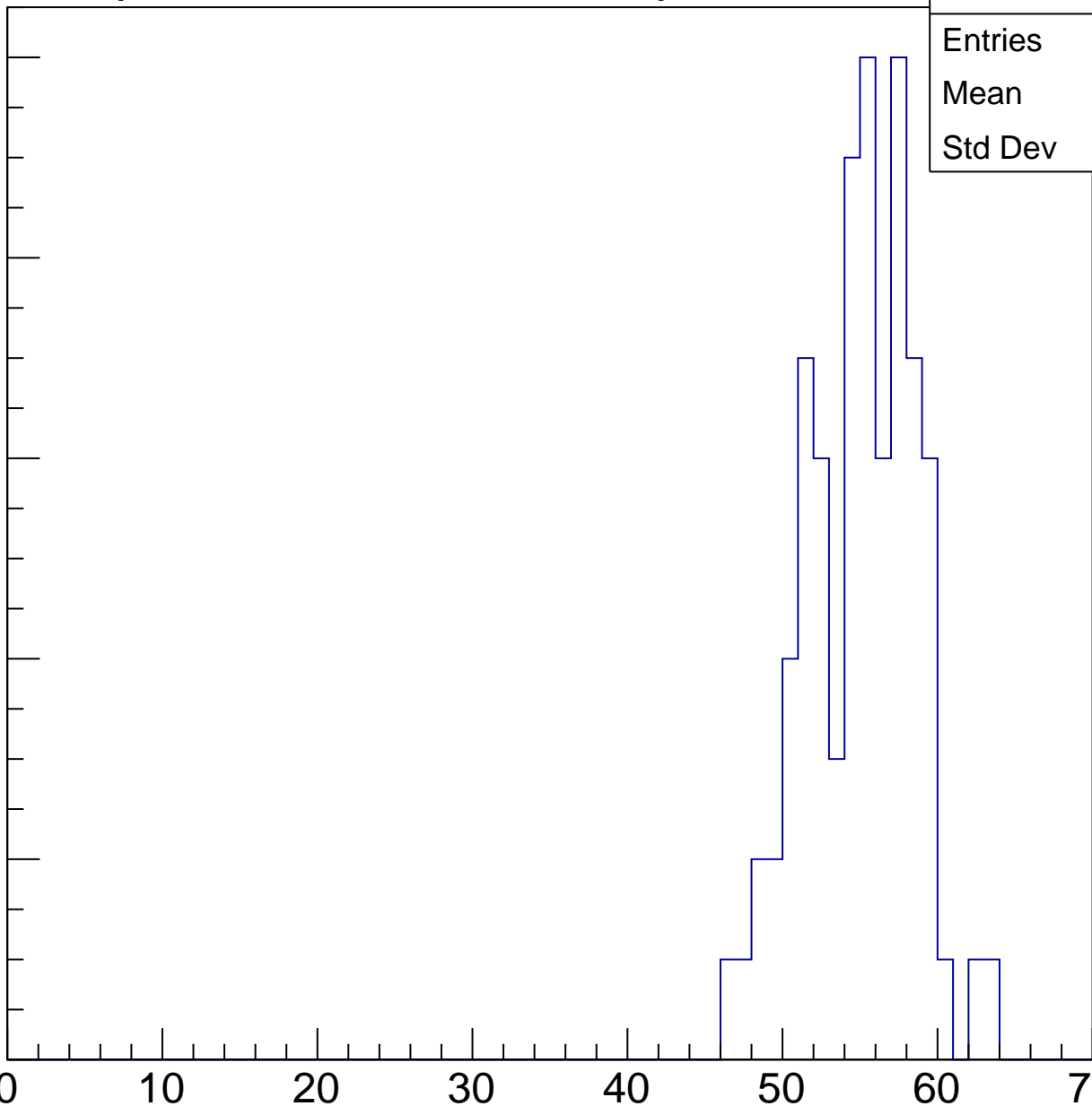
30

40

50

60

ampl



B0L101S, U20-ch44, adc5

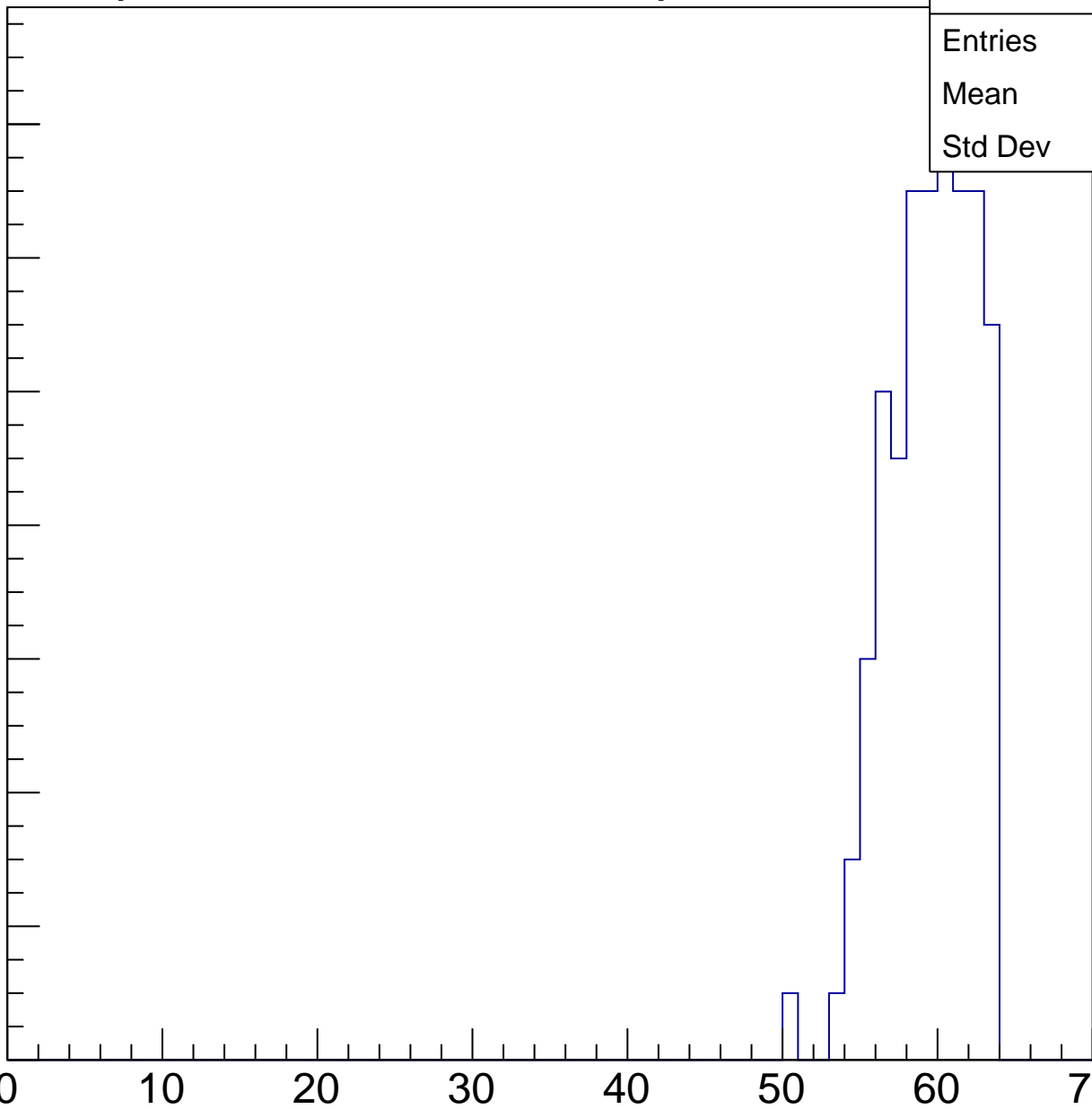
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	59.08
Std Dev	2.698

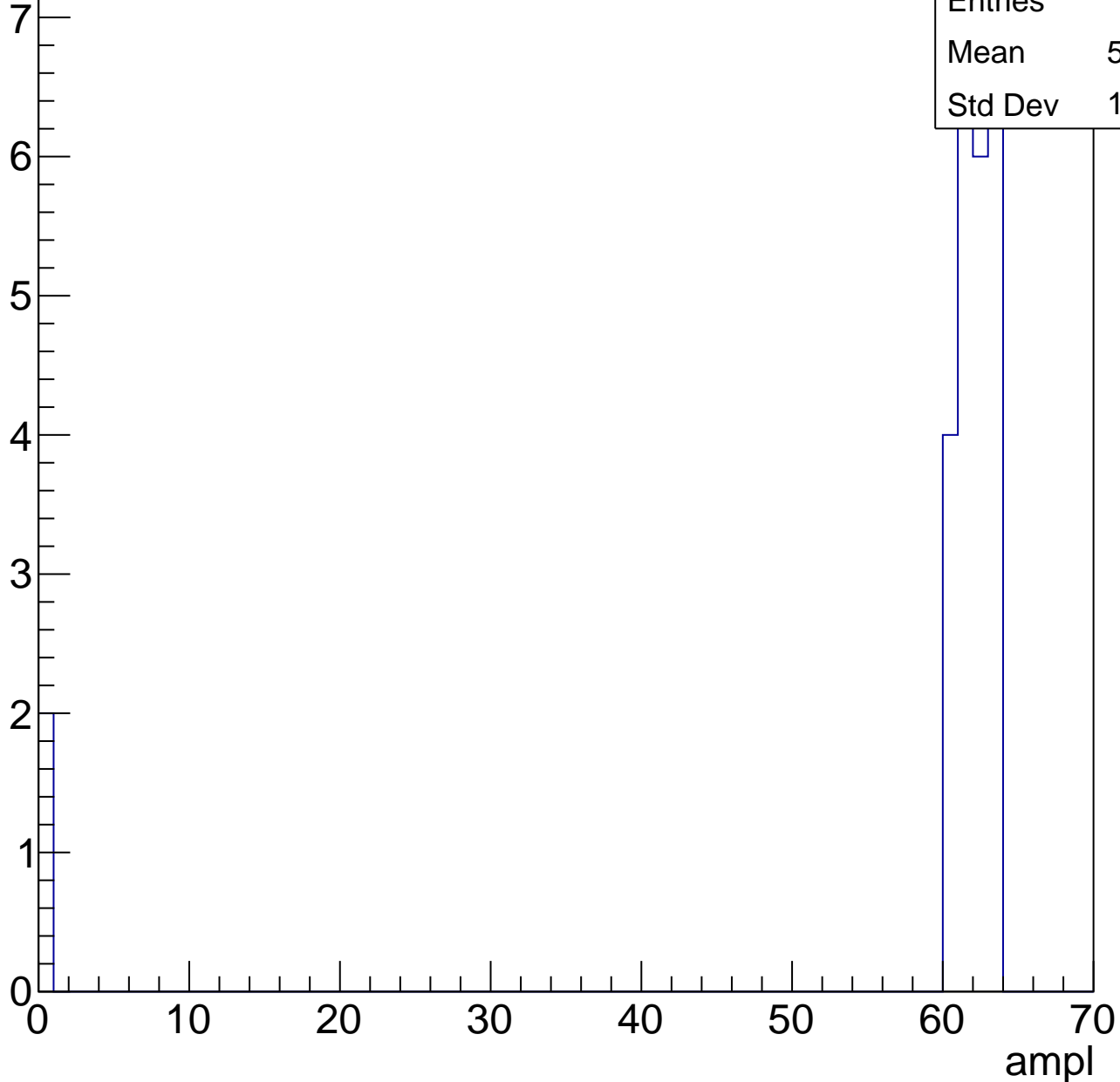
ampl



B0L101S, U20-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

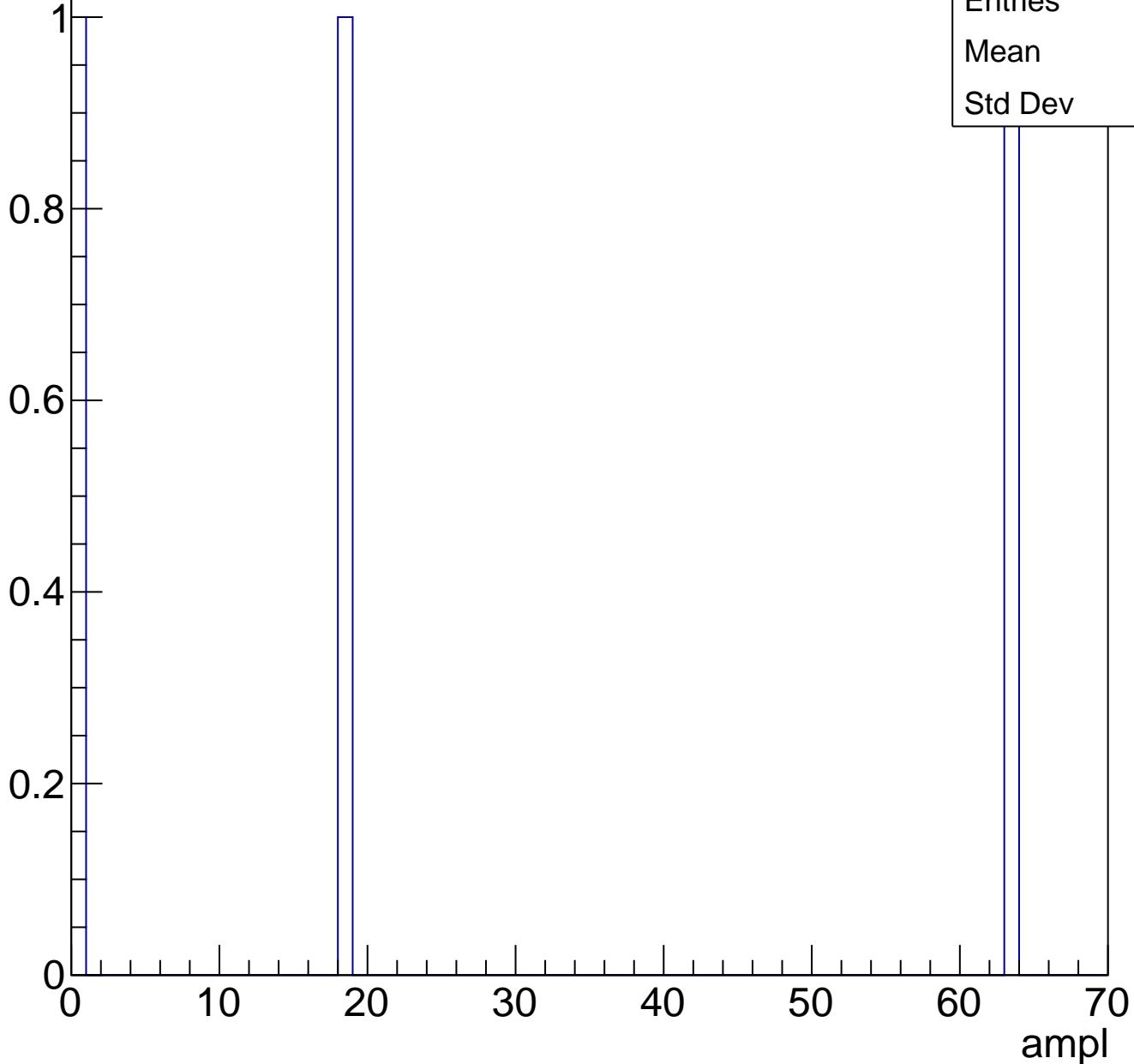
Entry



B0L101S, U20-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch45, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	28.61
Std Dev	3.354

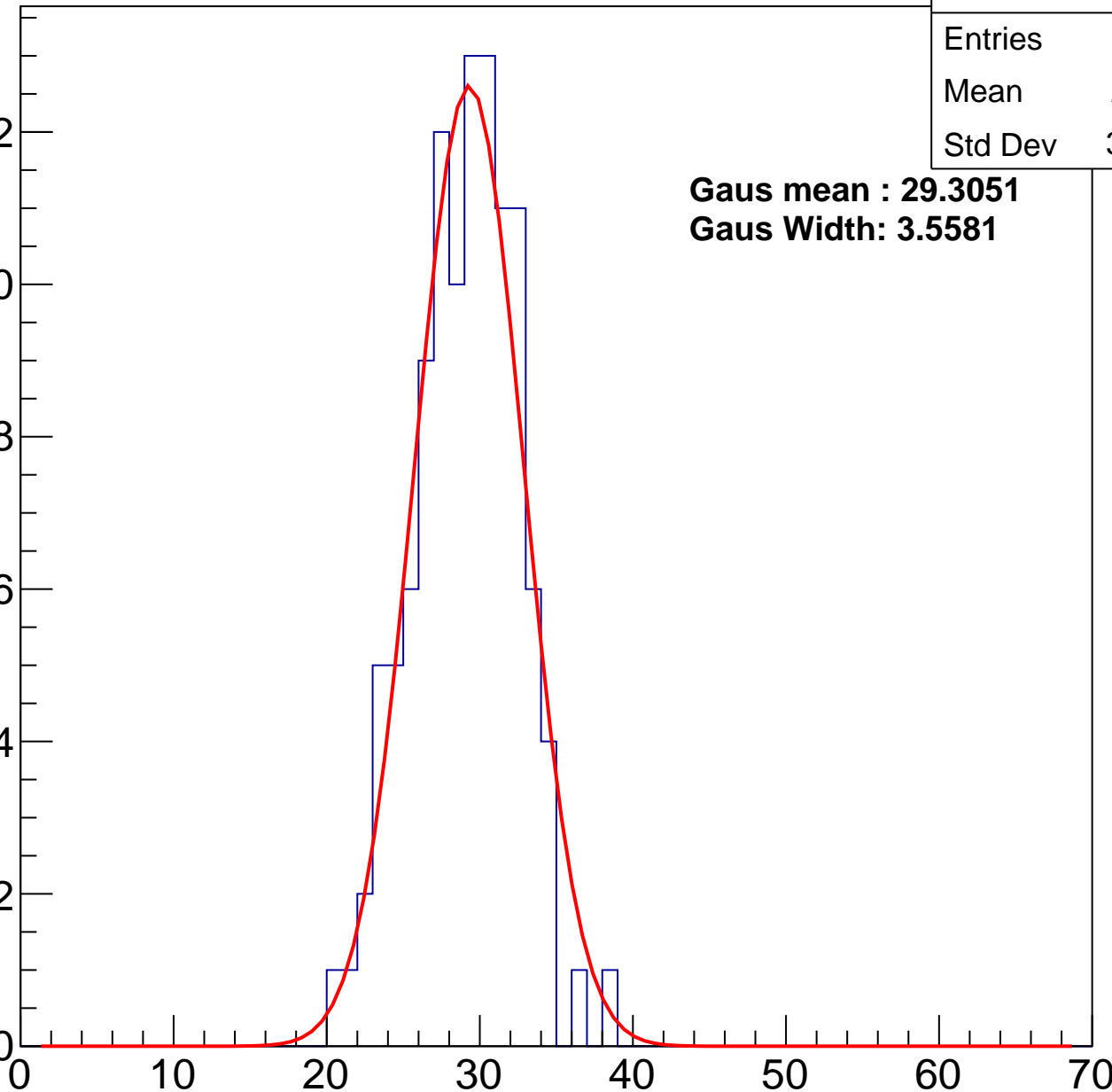
Gaus mean : 29.3051

Gaus Width: 3.5581

Entry

12
10
8
6
4
2
0

ampl



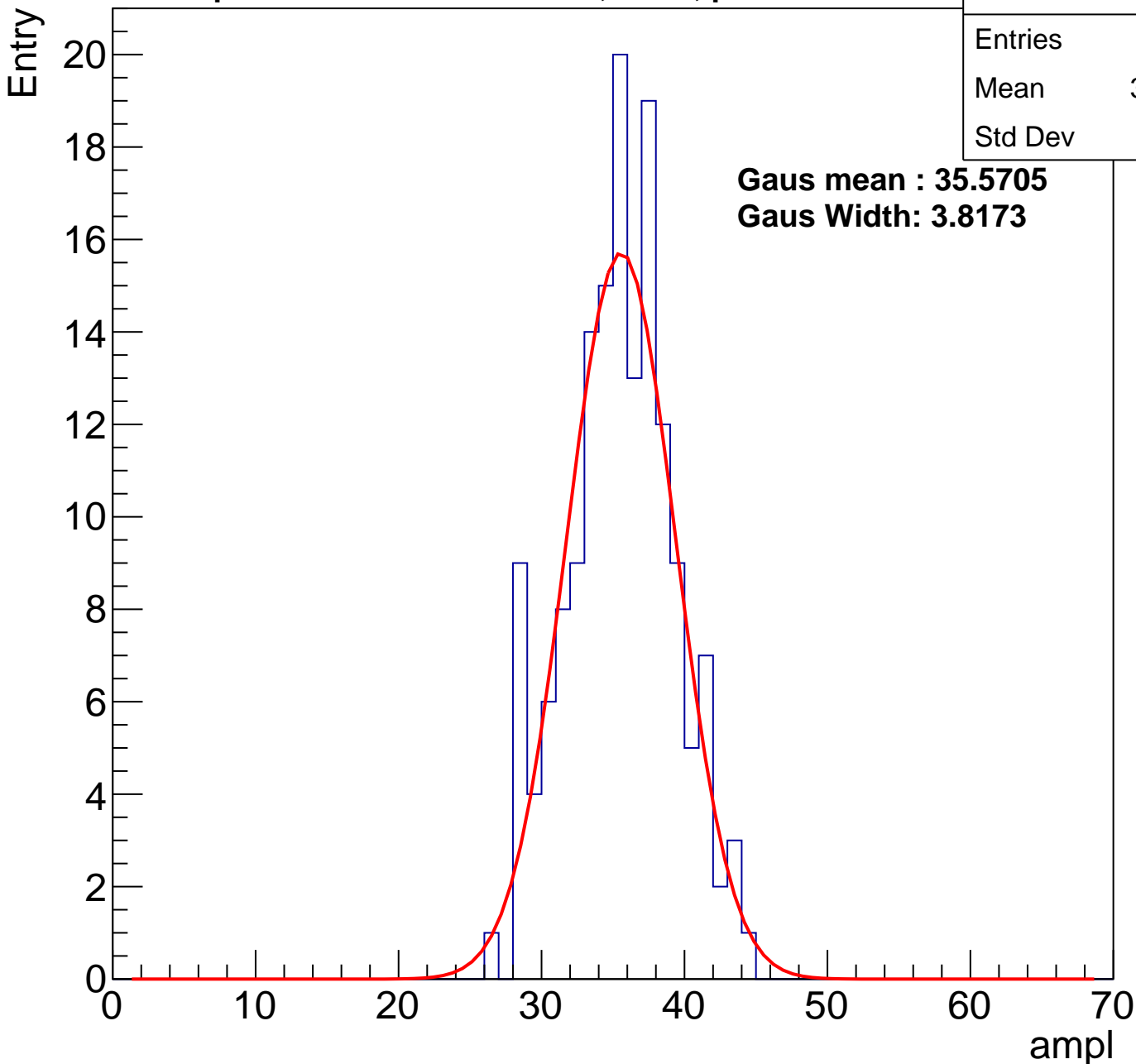
B0L101S, U20-ch45, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	35.06
Std Dev	3.73

Gaus mean : 35.5705

Gaus Width: 3.8173



B0L101S, U20-ch45, adc2

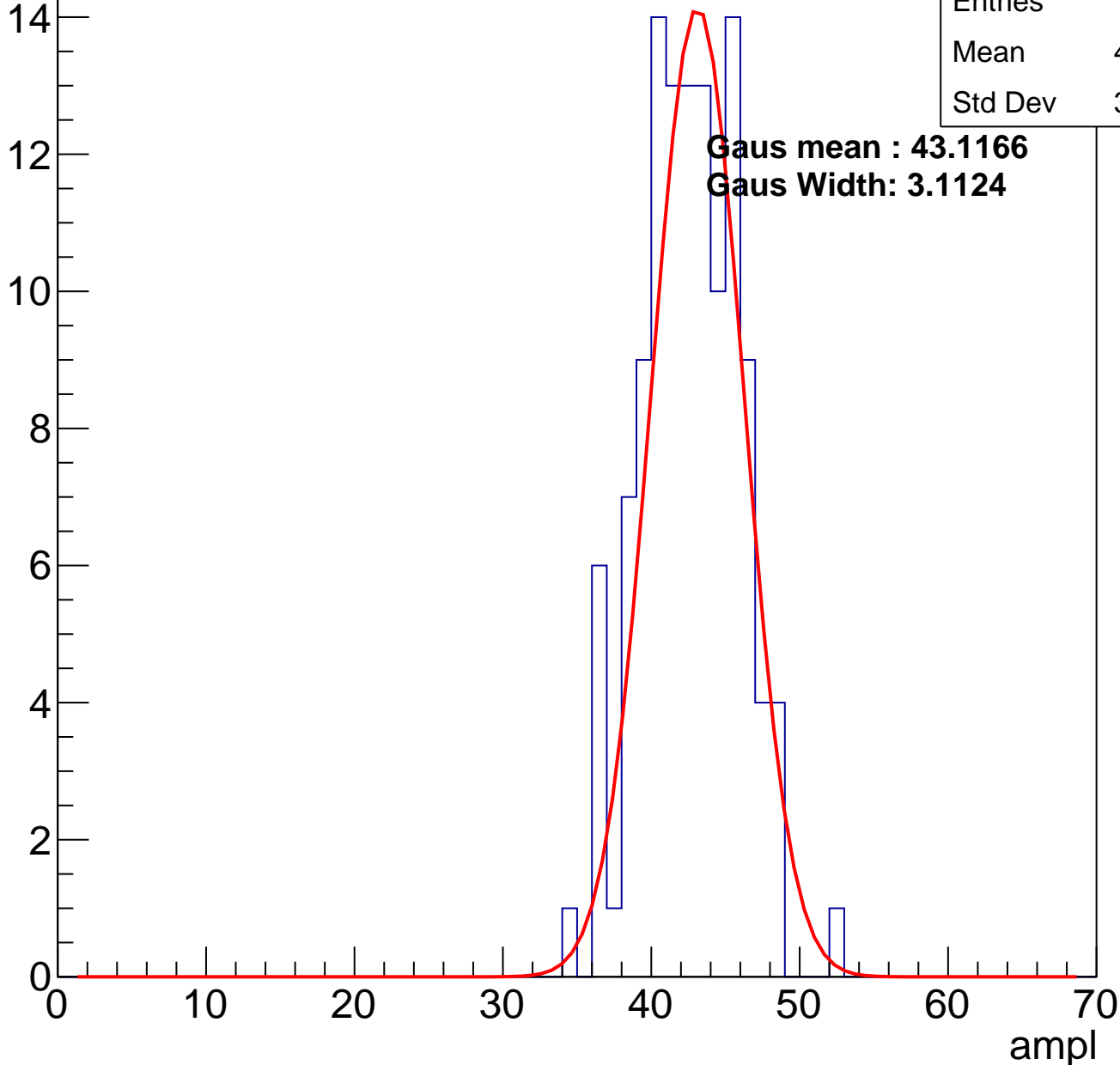
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	42.17
Std Dev	3.216

Gaus mean : 43.1166

Gaus Width: 3.1124

Entry

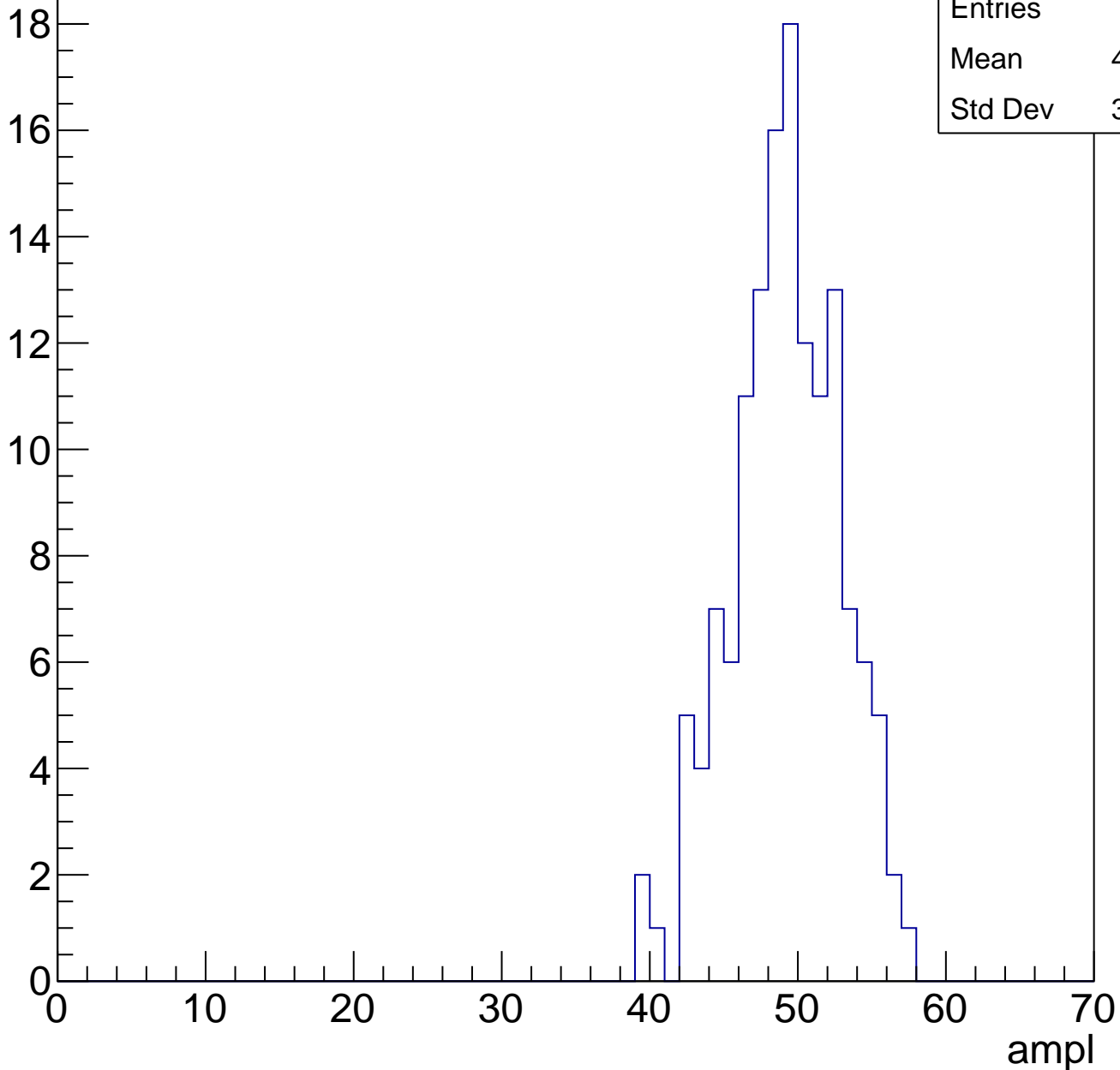


B0L101S, U20-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	48.72
Std Dev	3.659

Entry



B0L101S, U20-ch45, adc4

calib_packv5_042523_0143.root, FC#1, port C1

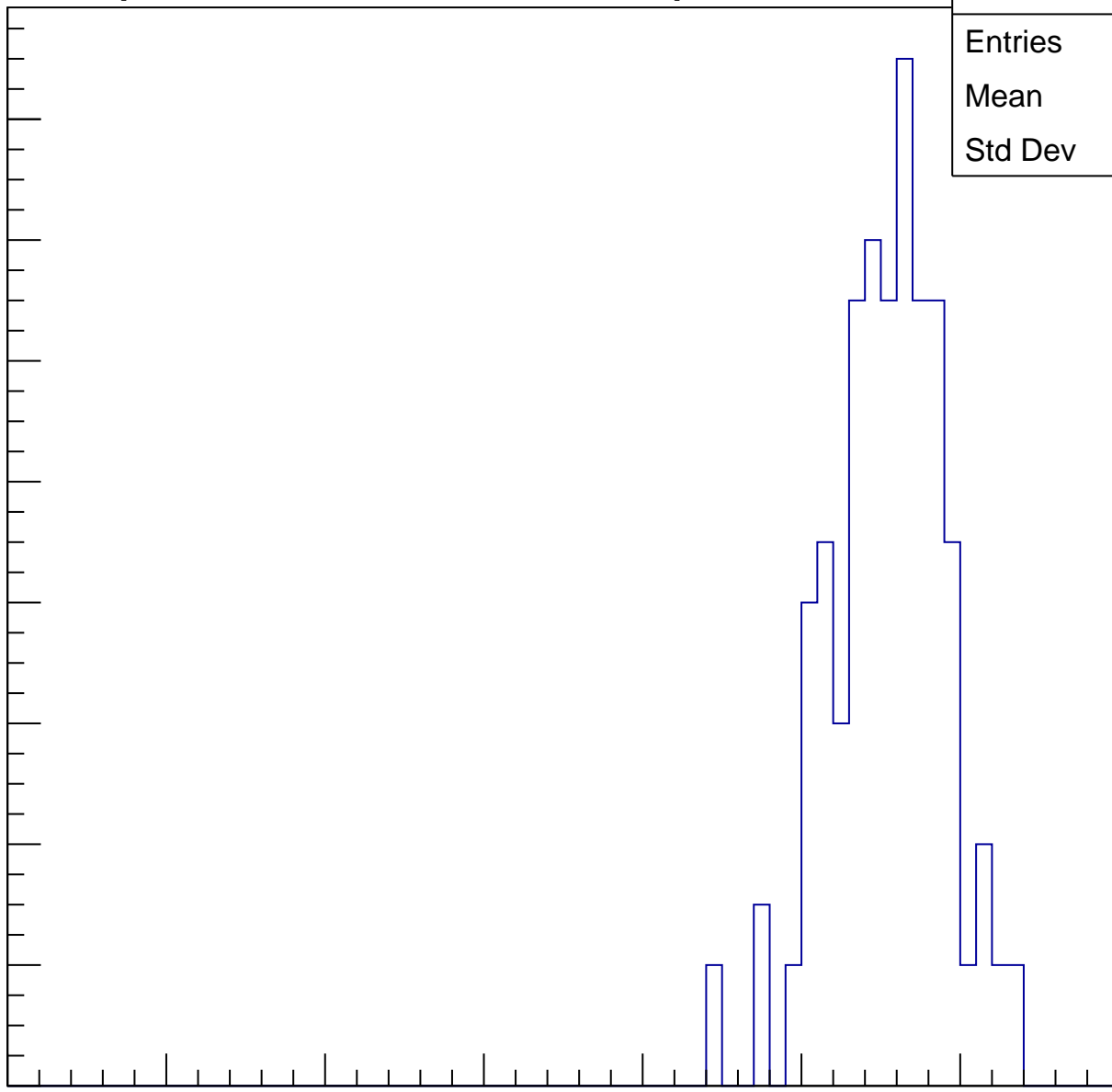
Entries	132
Mean	54.92
Std Dev	3.609

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch45, adc5

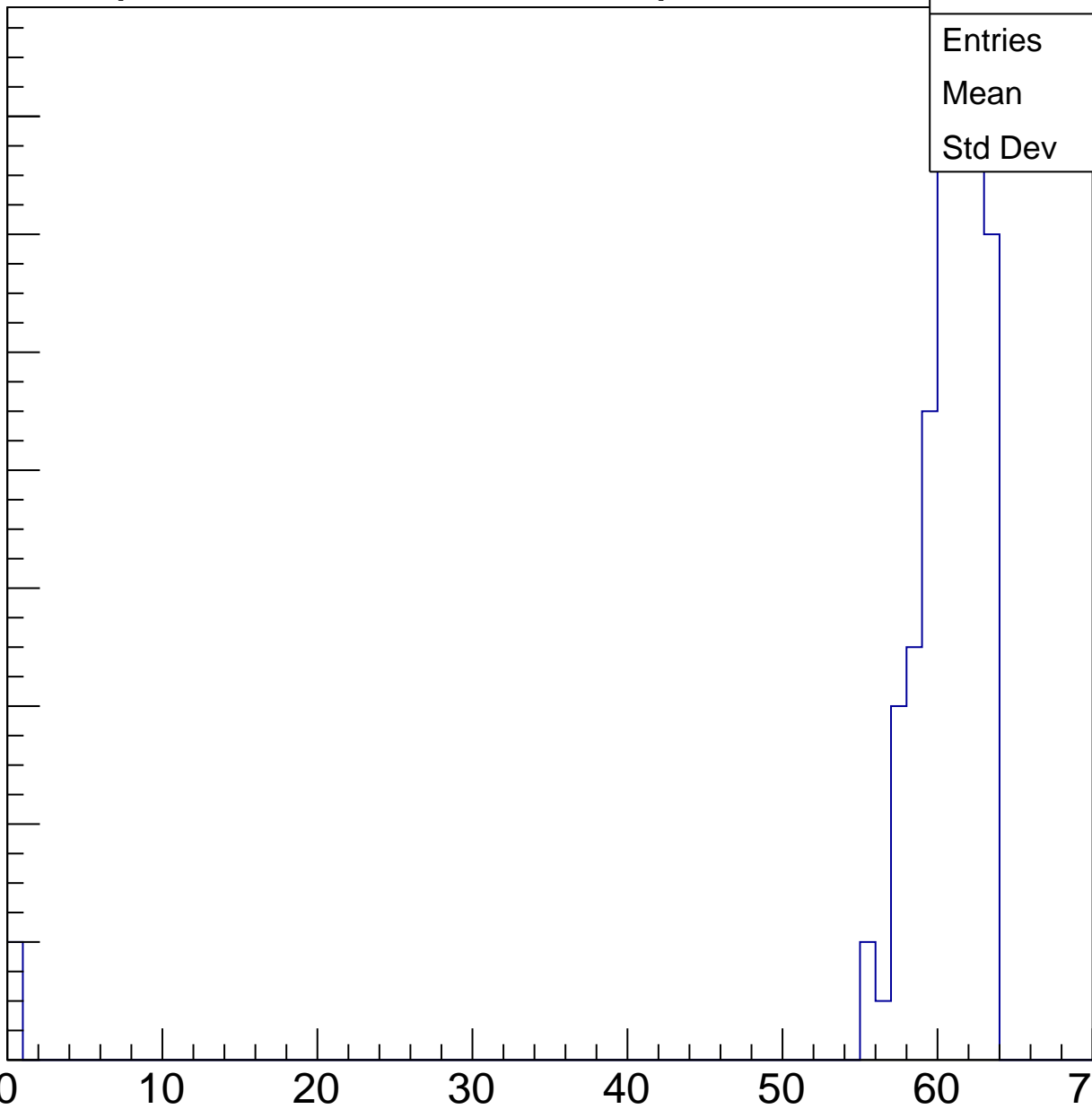
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.08
Std Dev	9.023

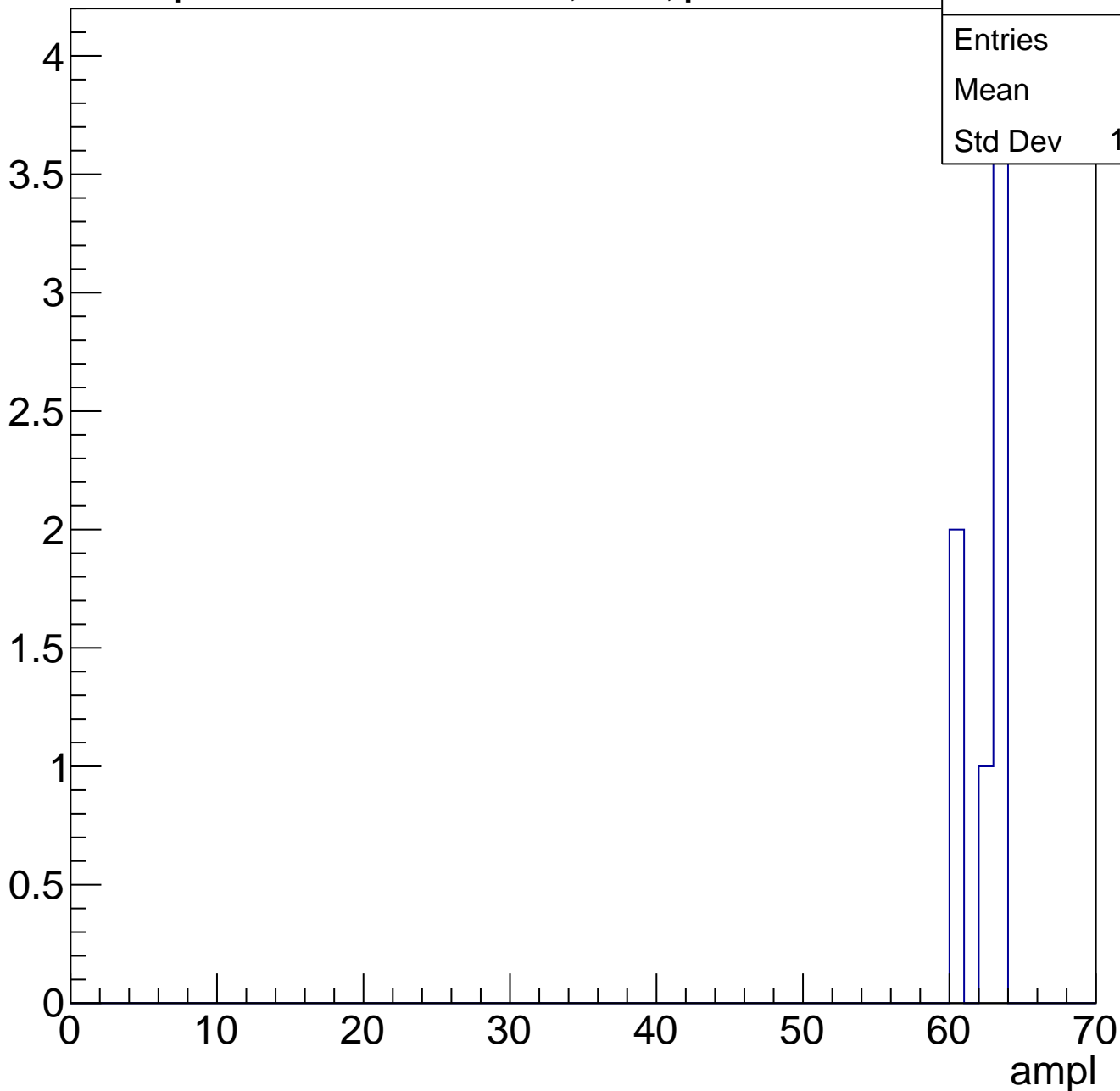
ampl



B0L101S, U20-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch46, adc0

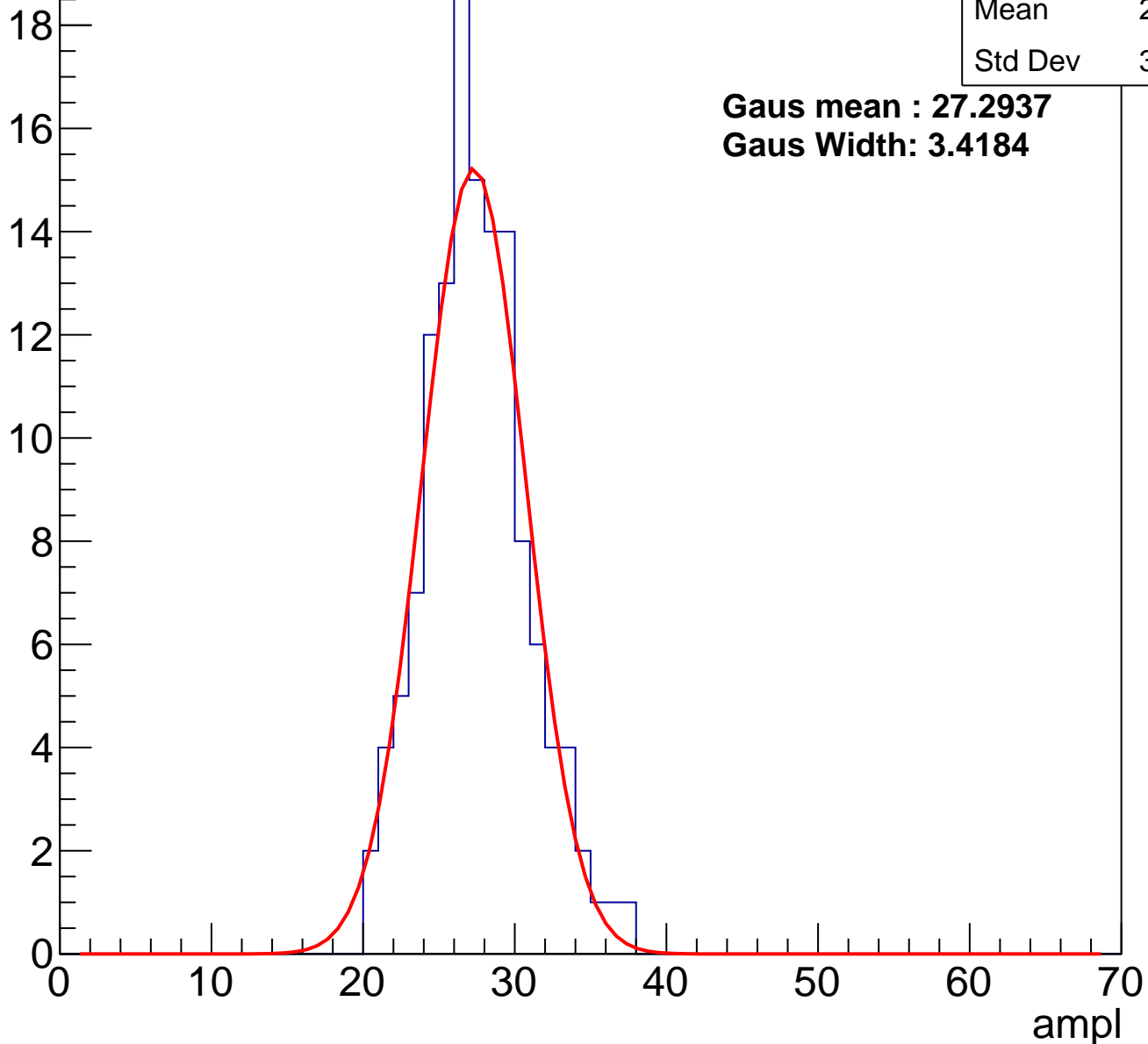
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	27.02
Std Dev	3.352

Gaus mean : 27.2937

Gaus Width: 3.4184

Entry

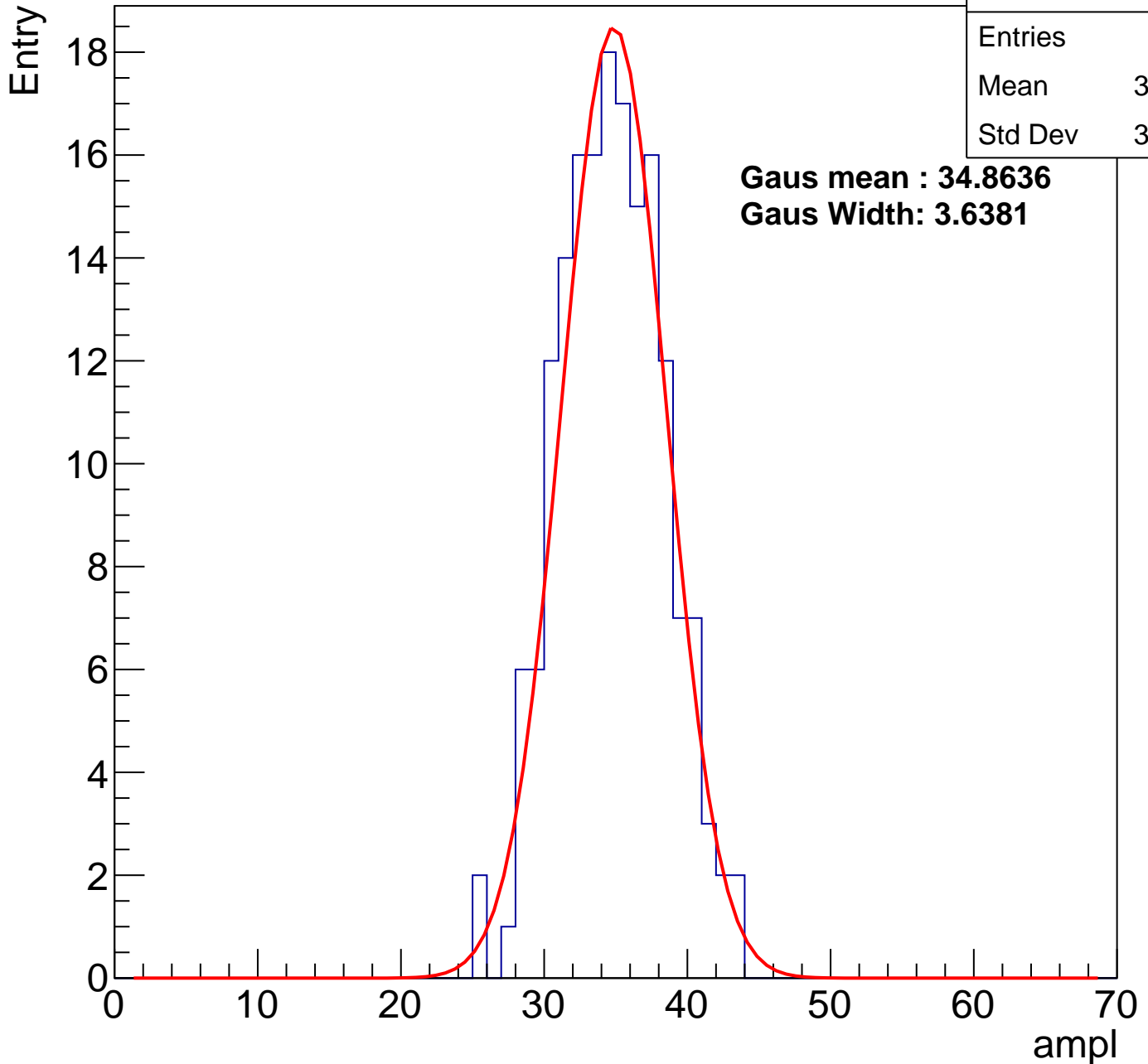


B0L101S, U20-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	34.27
Std Dev	3.609

Gaus mean : 34.8636
Gaus Width: 3.6381



B0L101S, U20-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	41.91
Std Dev	5.104

Gaus mean : 42.6387

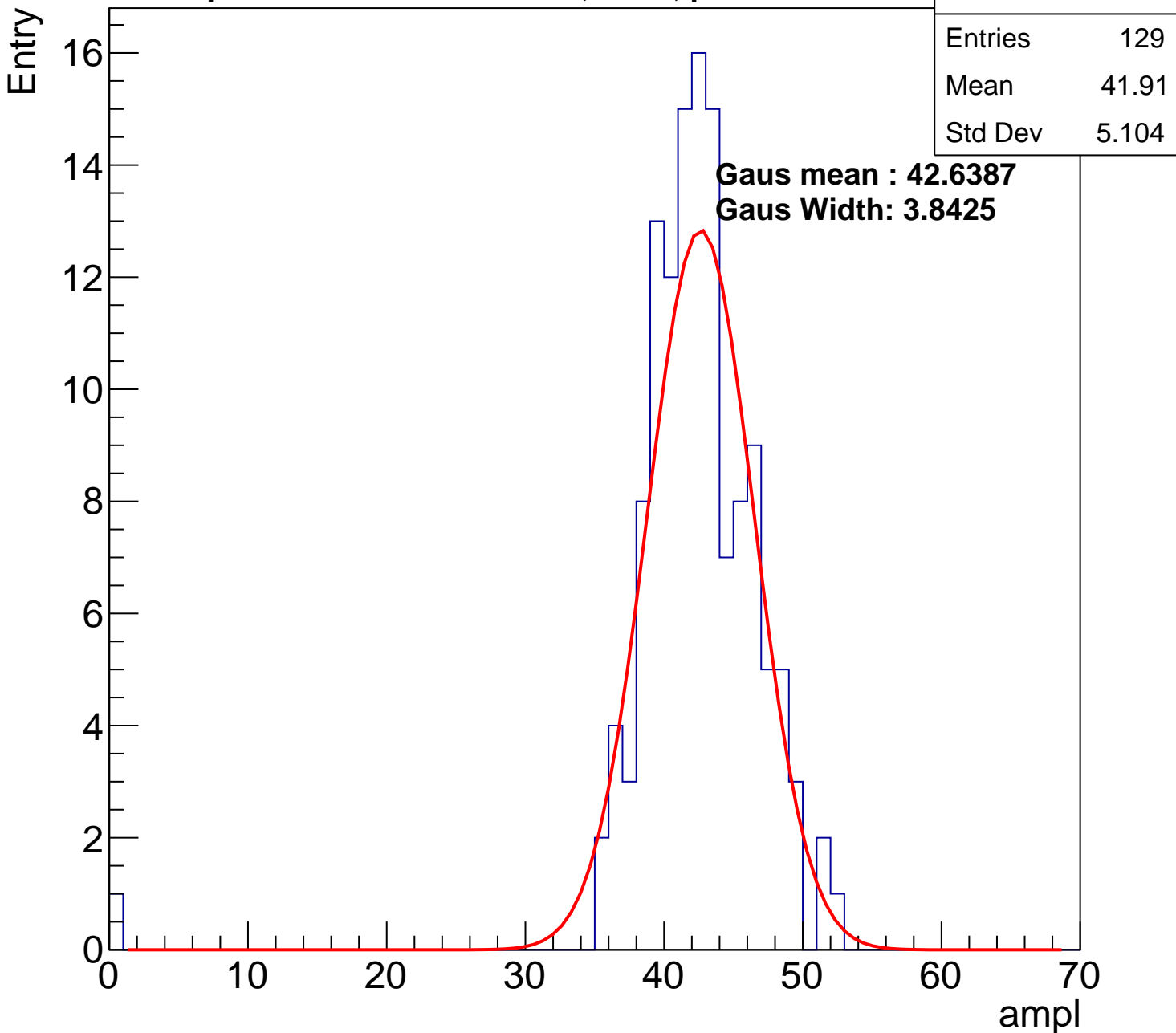
Gaus Width: 3.8425

Entry

16
14
12
10
8
6
4
2
0

ampl

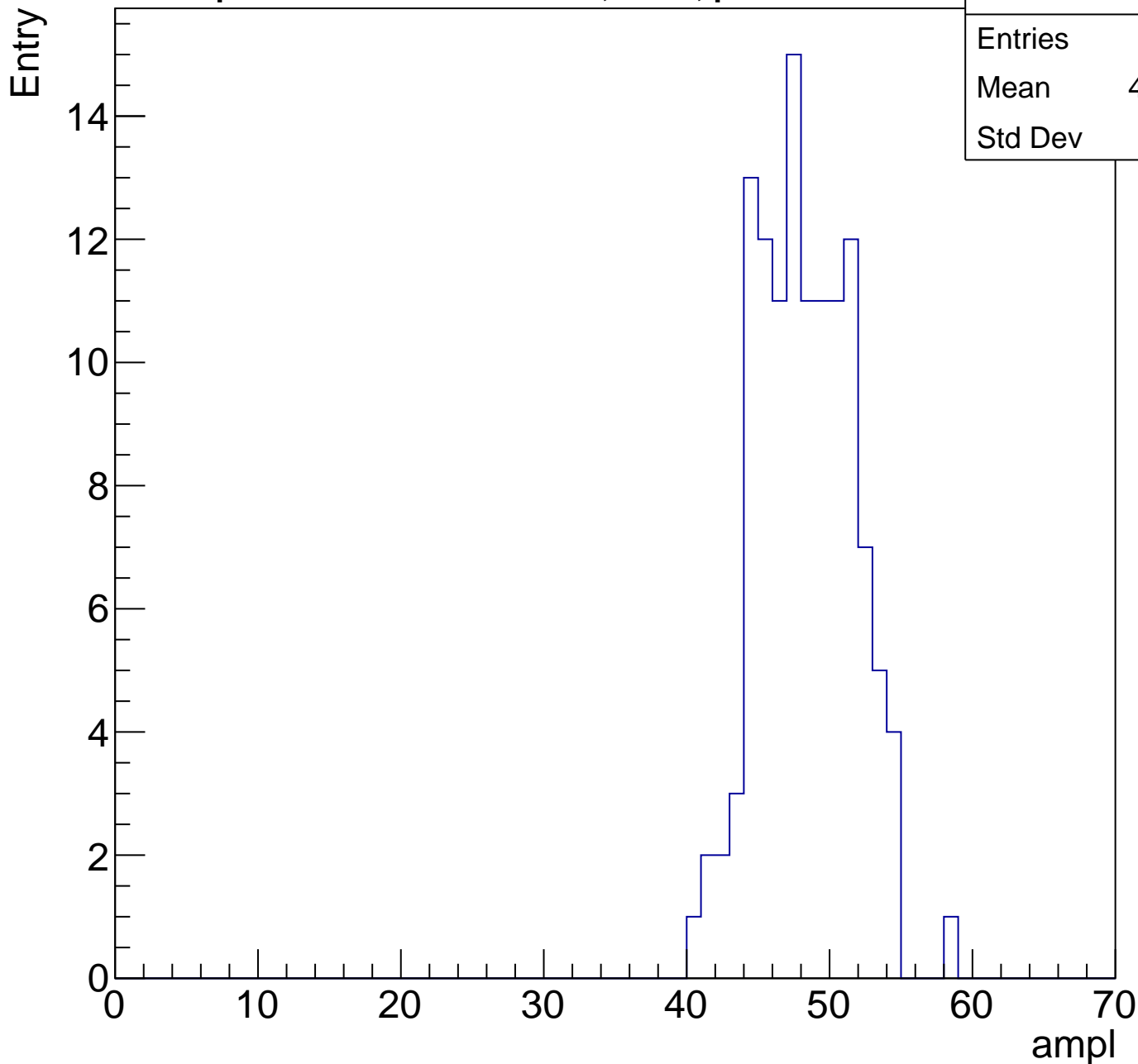
0 10 20 30 40 50 60 70



B0L101S, U20-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	47.85
Std Dev	3.31

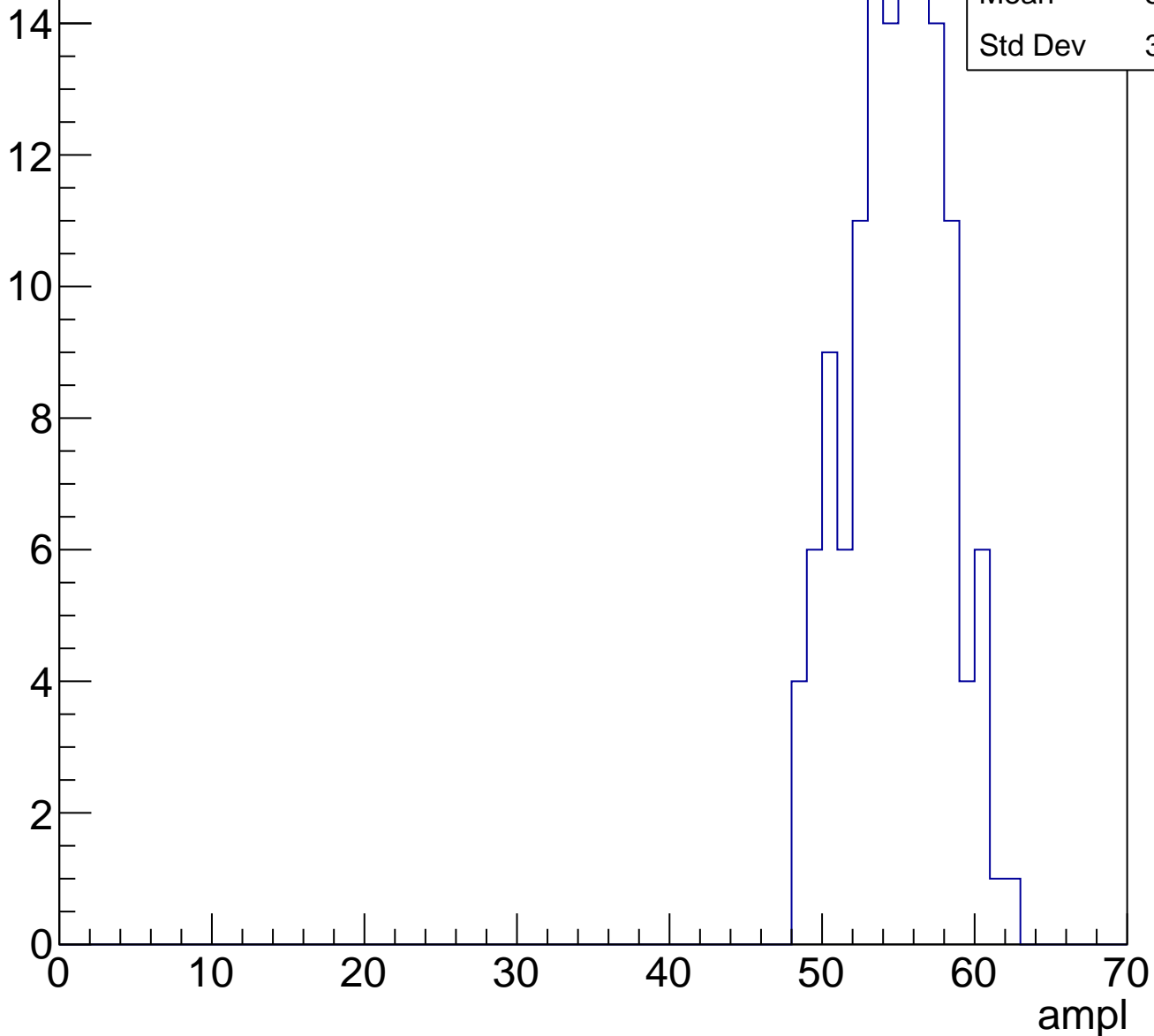


B0L101S, U20-ch46, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	54.43
Std Dev	3.177

Entry



B0L101S, U20-ch46, adc5

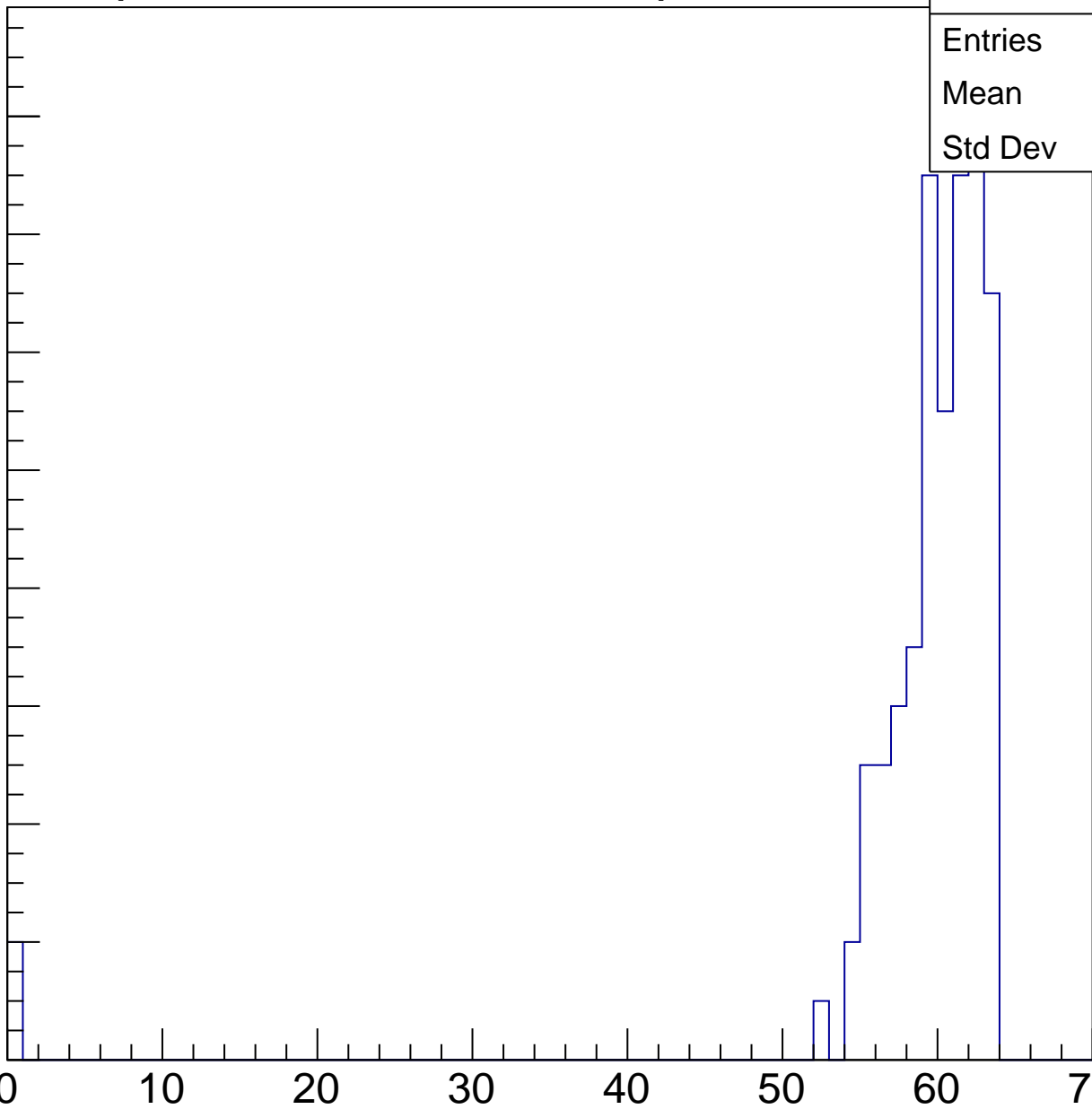
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.55
Std Dev	8.778

ampl

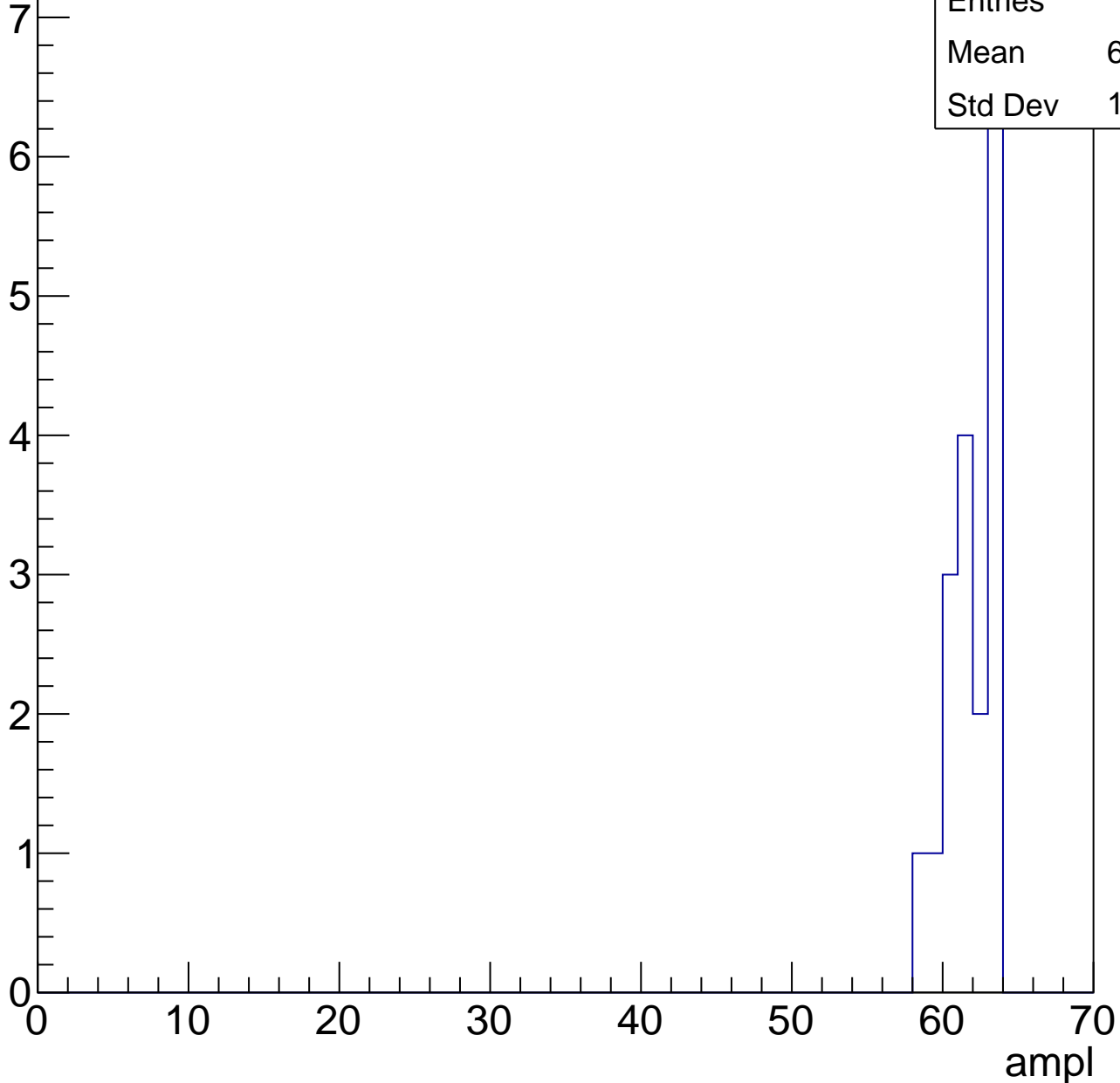


B0L101S, U20-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.44
Std Dev	1.536



B0L101S, U20-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch47, adc0

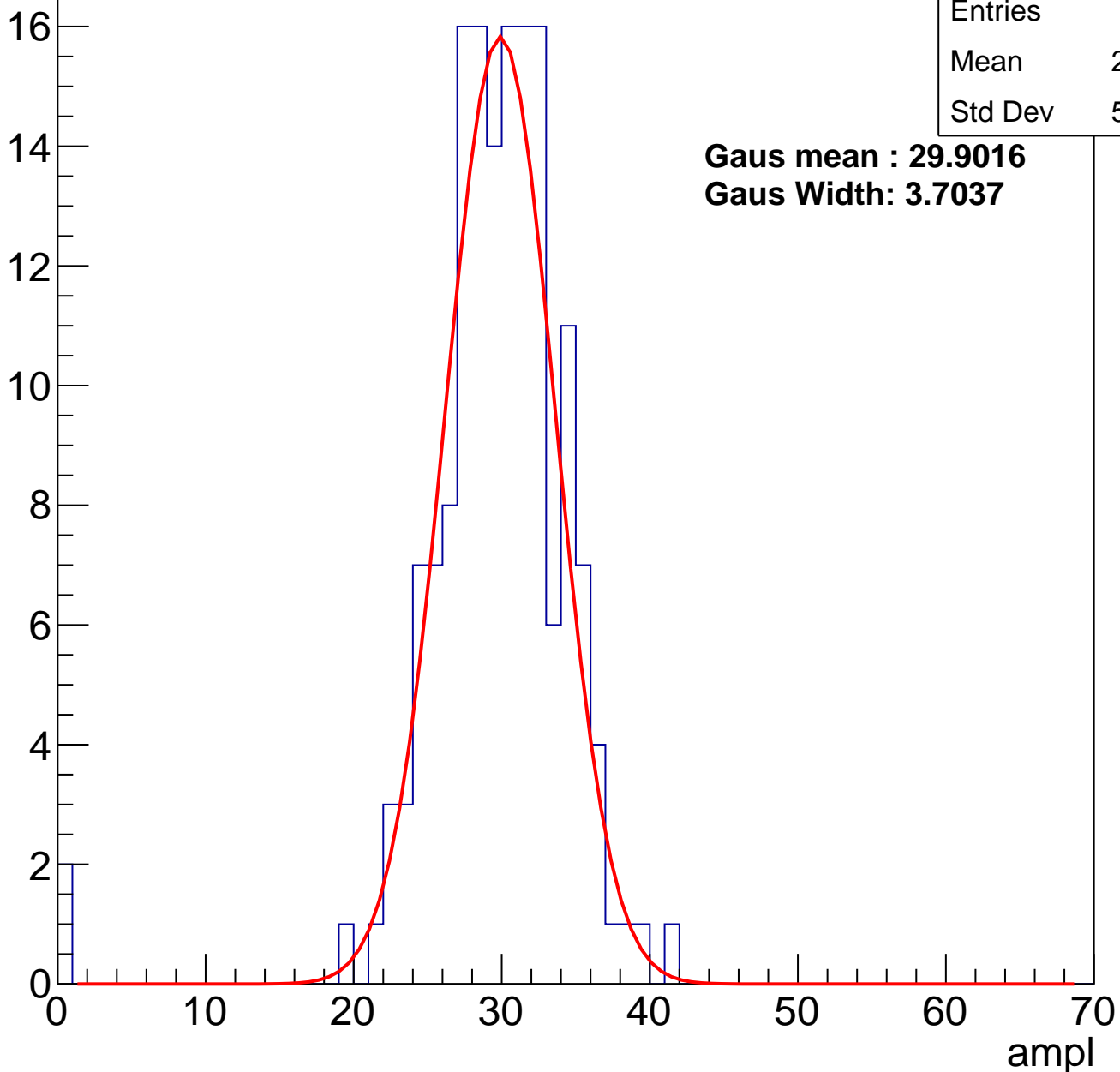
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	29.22
Std Dev	5.009

Gaus mean : 29.9016

Gaus Width: 3.7037

Entry



B0L101S, U20-ch47, adc1

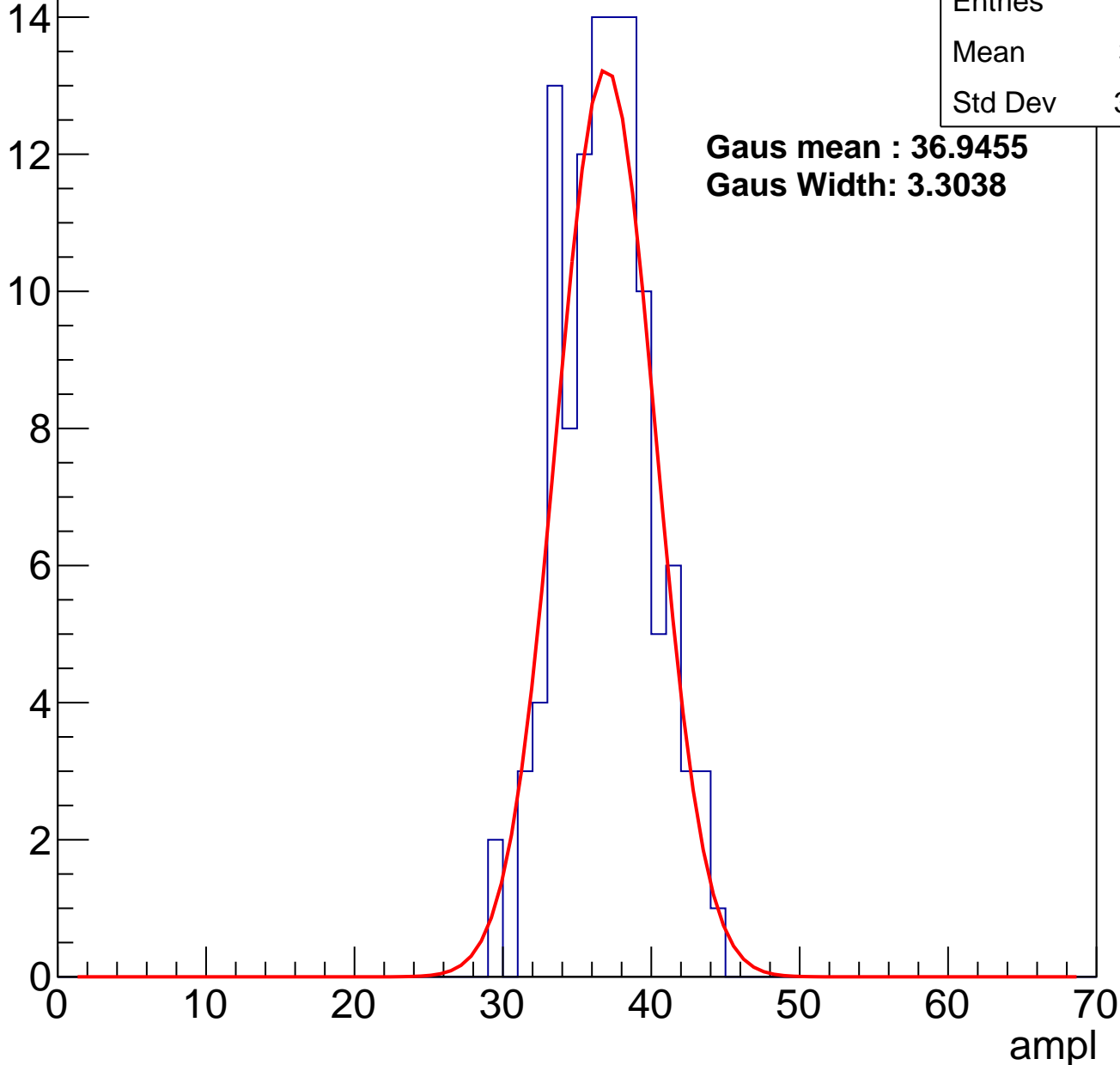
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	36.51
Std Dev	3.102

Gaus mean : 36.9455

Gaus Width: 3.3038

Entry



B0L101S, U20-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	42.43
Std Dev	3.439

Gaus mean : 42.7995

Gaus Width: 3.2777

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

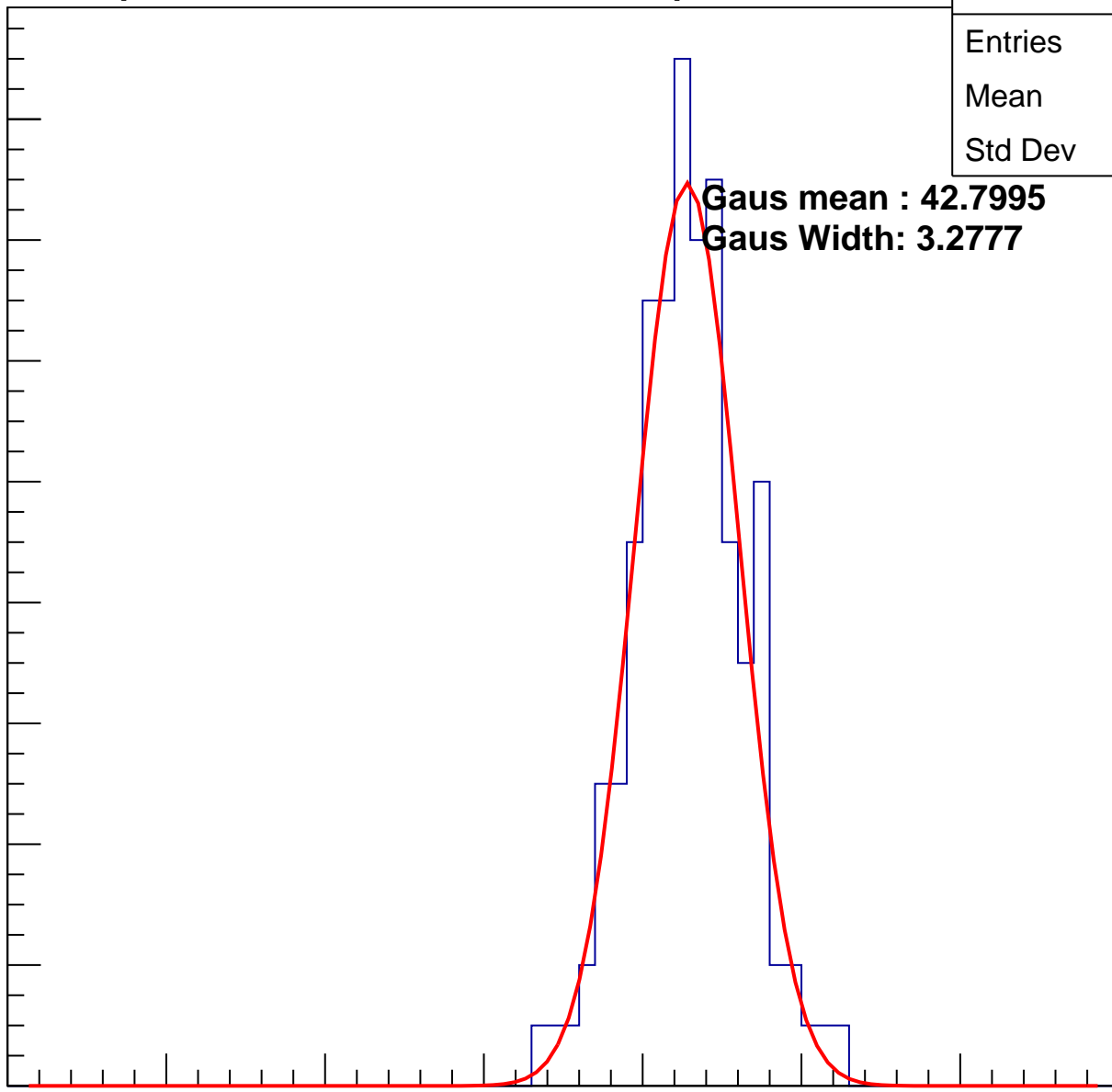
40

50

60

70

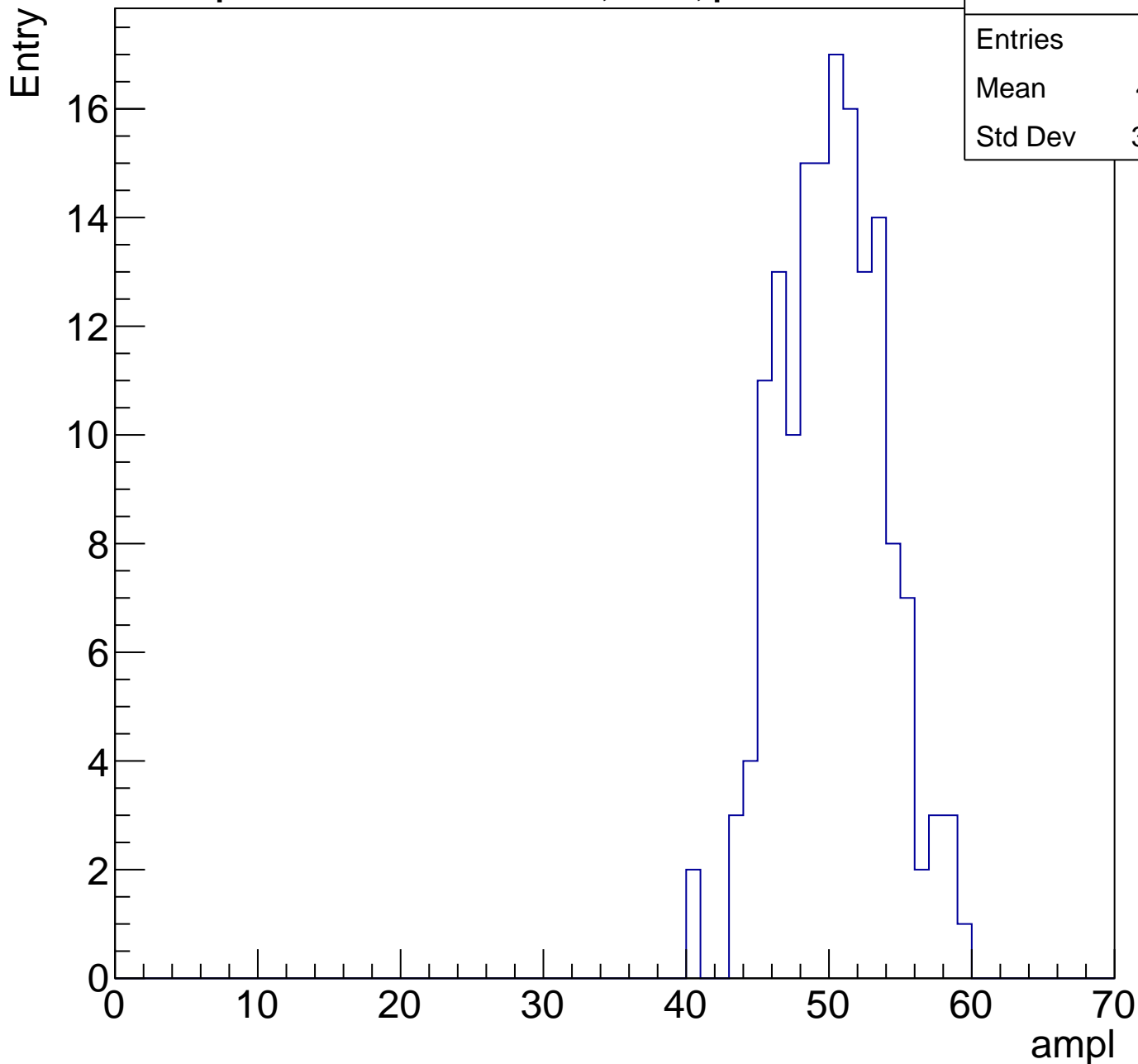
ampl



B0L101S, U20-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	49.81
Std Dev	3.677



B0L101S, U20-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

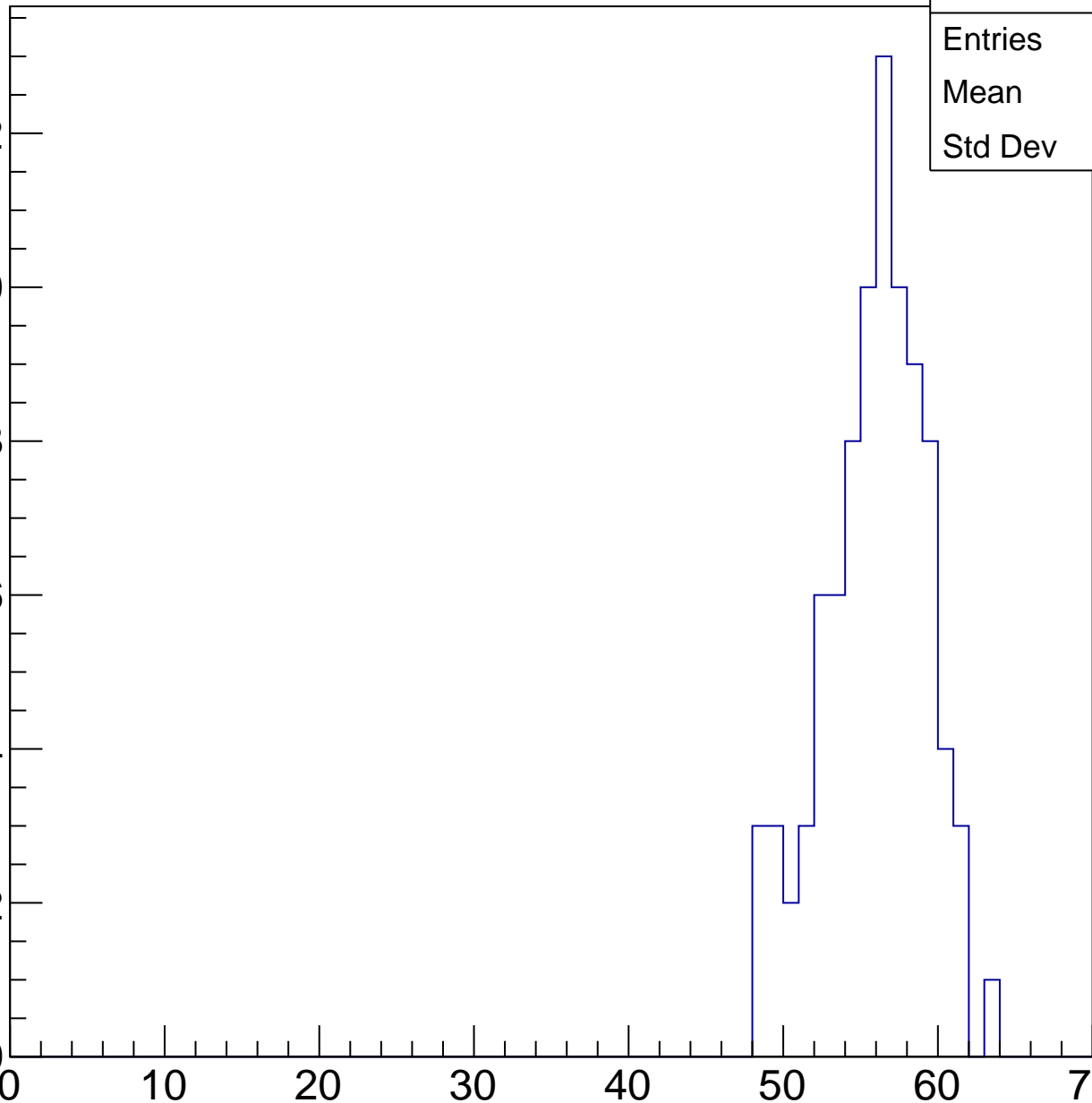
40

50

60

ampl

Entries	89
Mean	55.44
Std Dev	3.277



B0L101S, U20-ch47, adc5

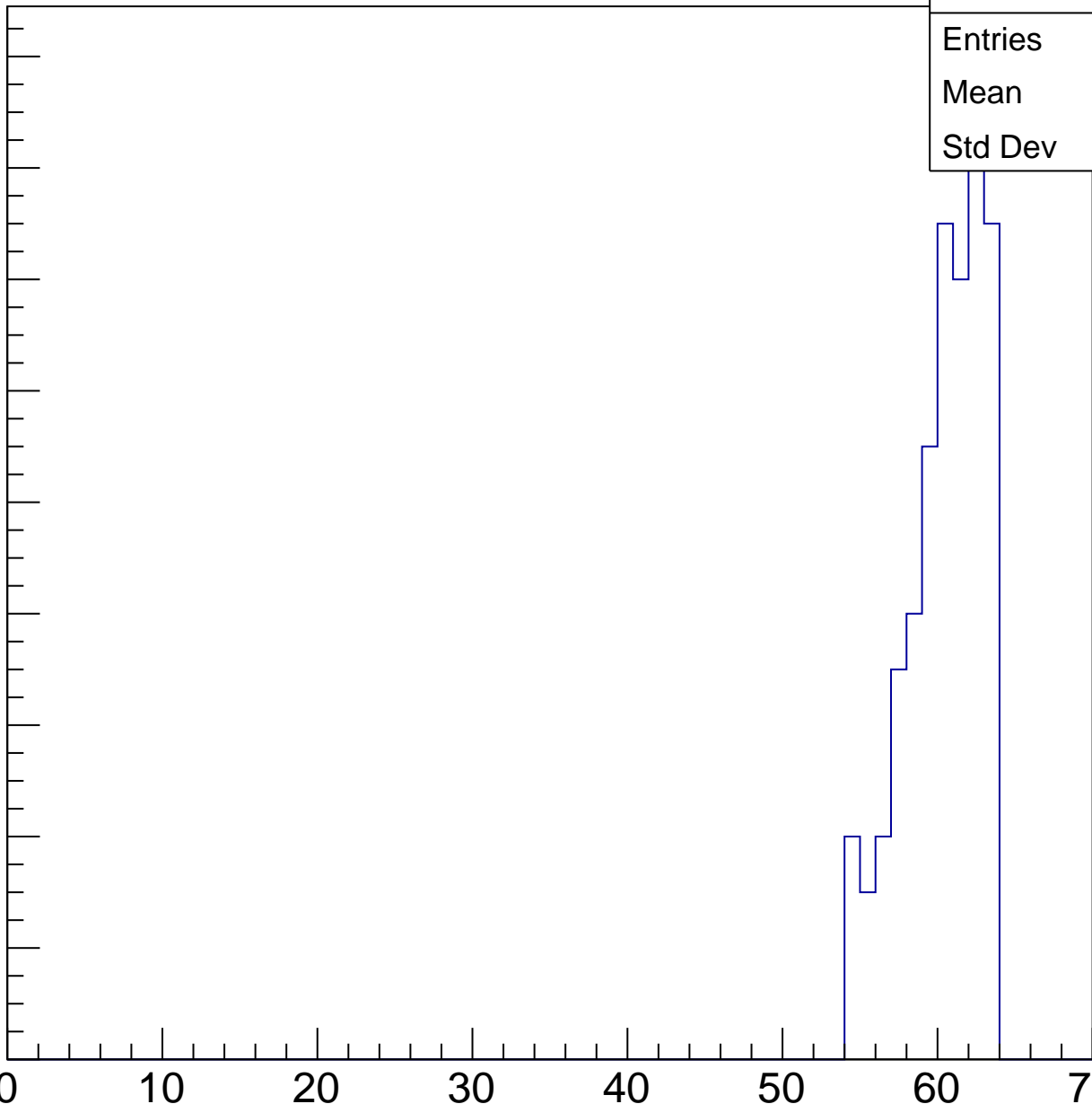
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.92
Std Dev	2.481

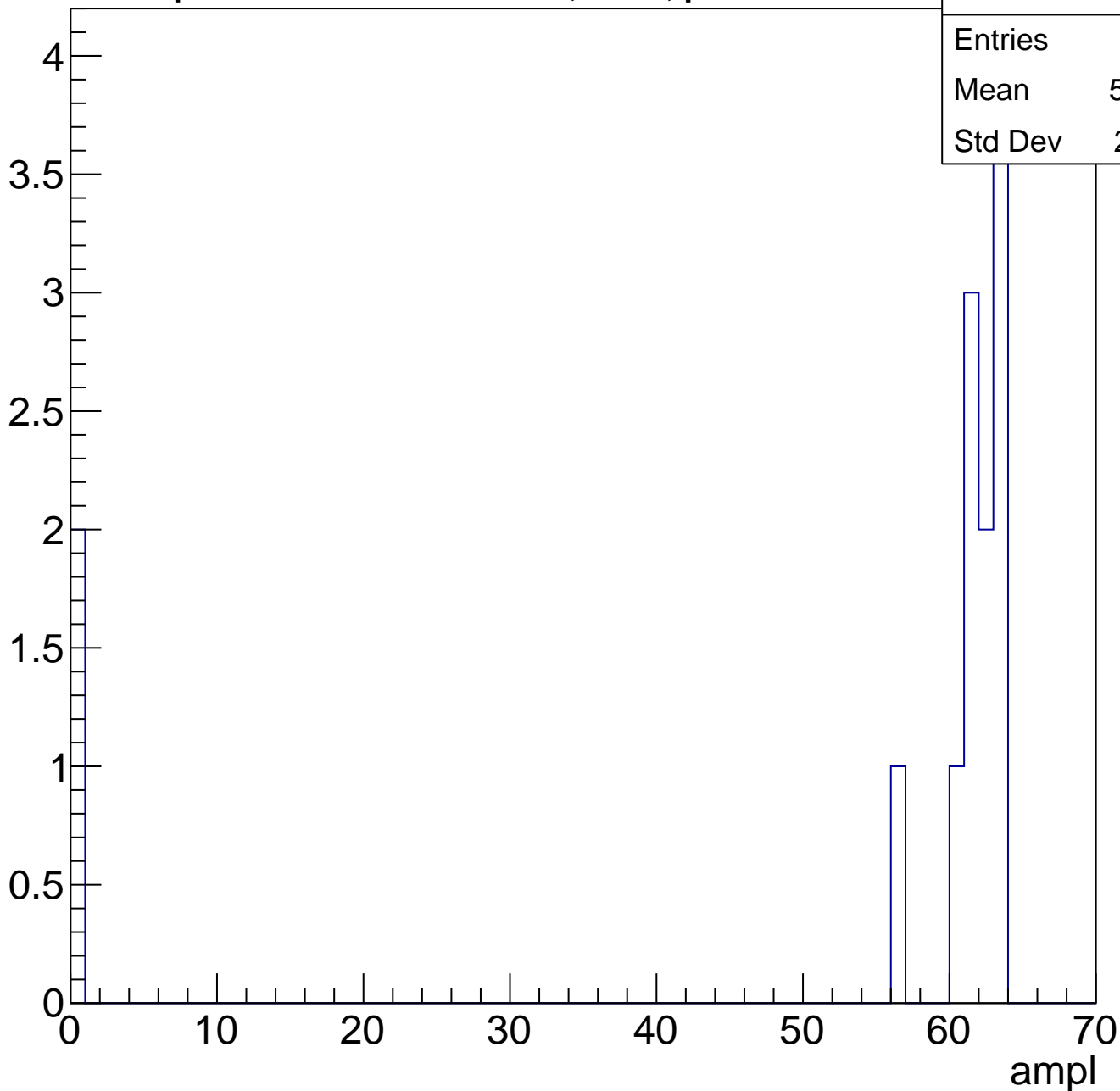
ampl



B0L101S, U20-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch48, adc0

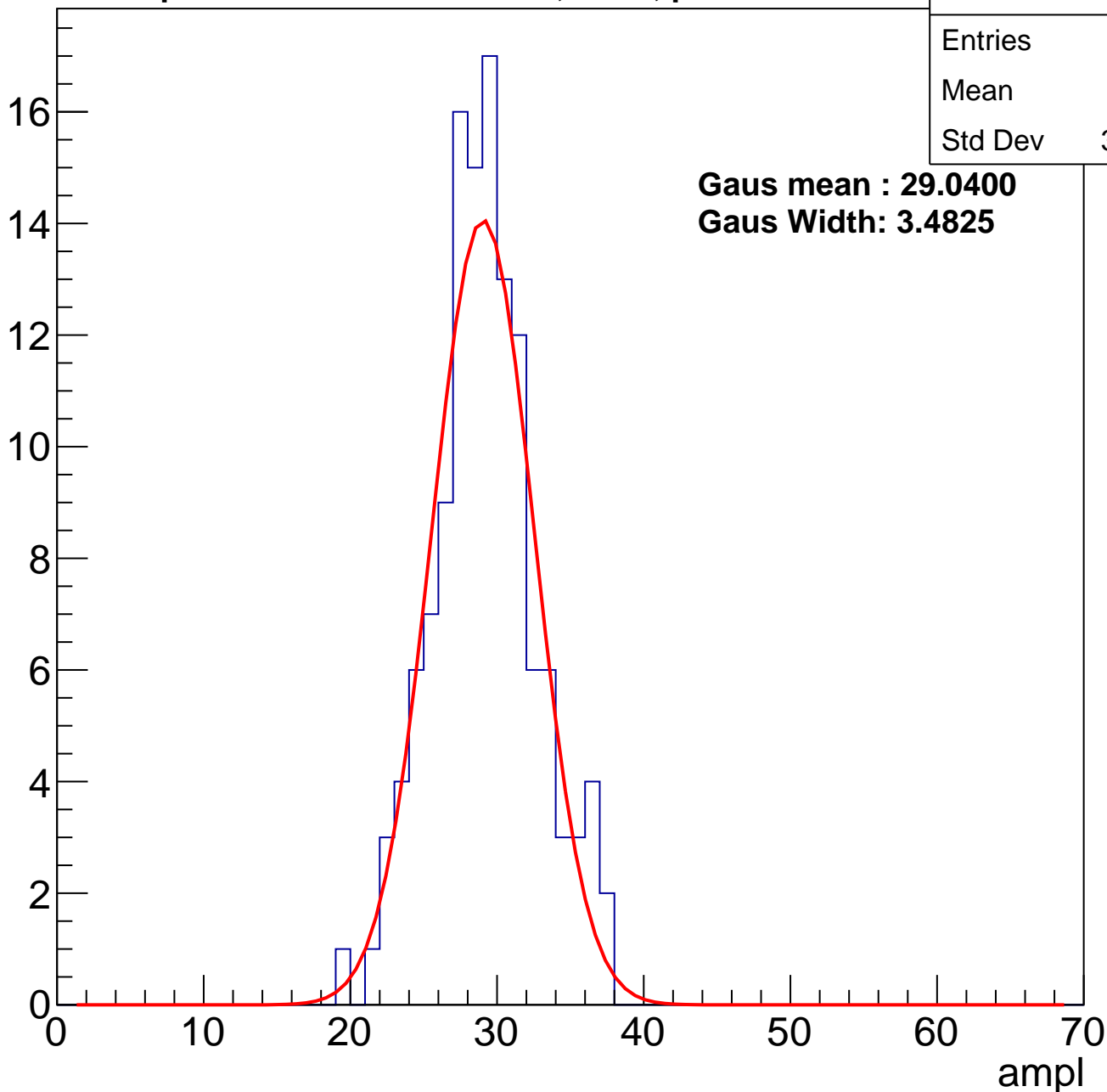
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	28.7
Std Dev	3.517

Gaus mean : 29.0400

Gaus Width: 3.4825

Entry



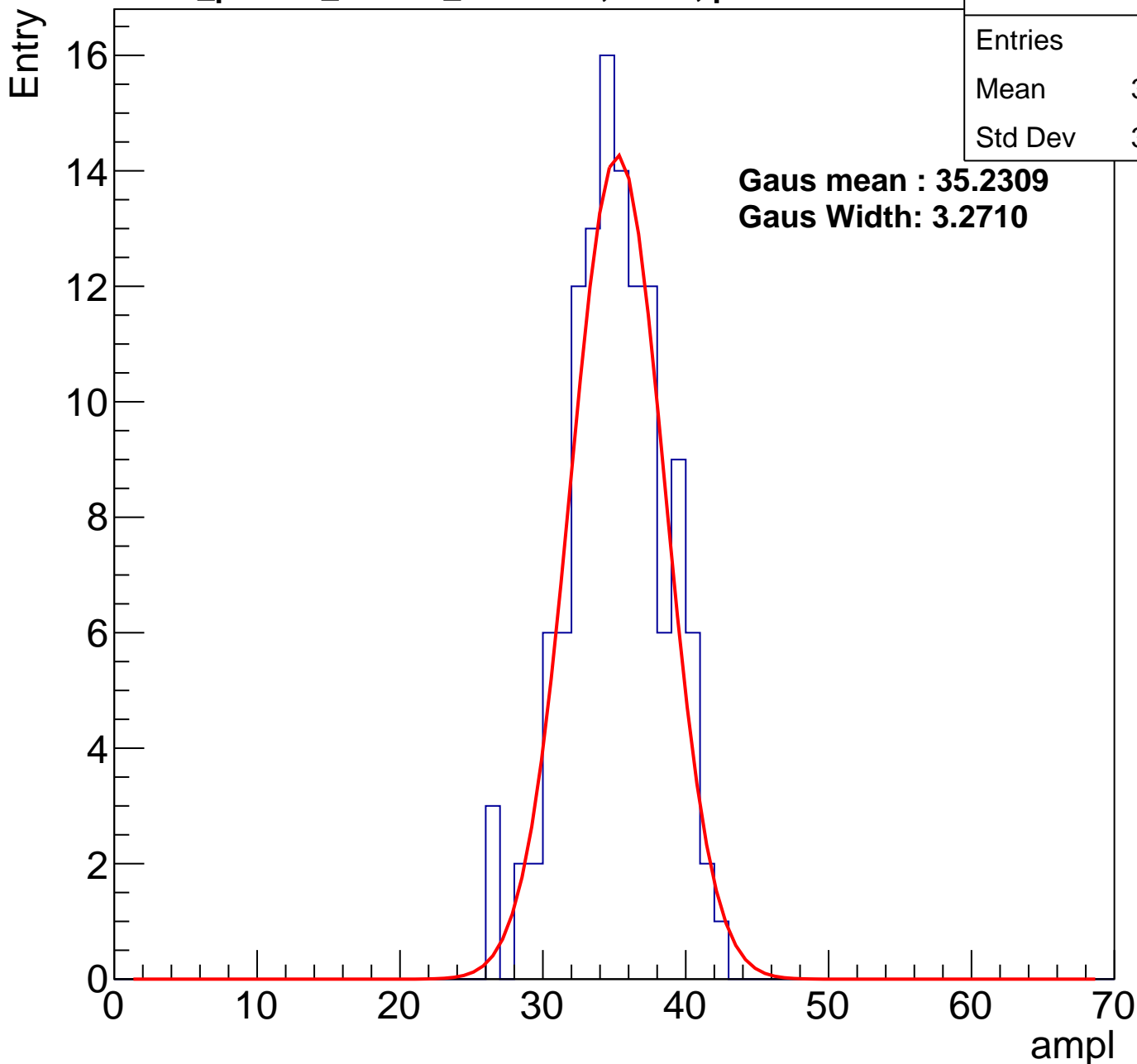
B0L101S, U20-ch48, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	34.62
Std Dev	3.322

Gaus mean : 35.2309

Gaus Width: 3.2710



B0L101S, U20-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

138

Mean

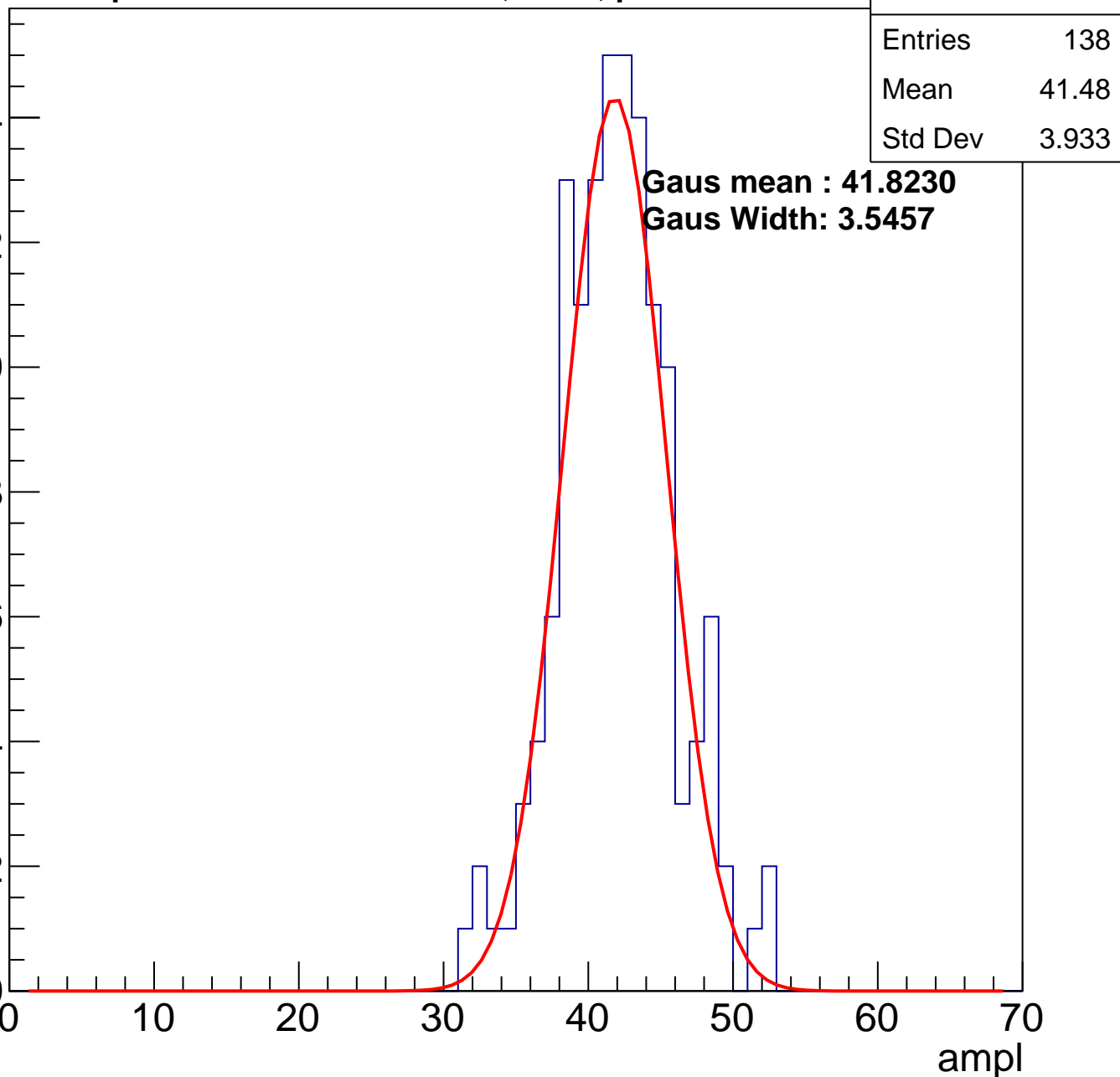
41.48

Std Dev

3.933

Gaus mean : 41.8230

Gaus Width: 3.5457



B0L101S, U20-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

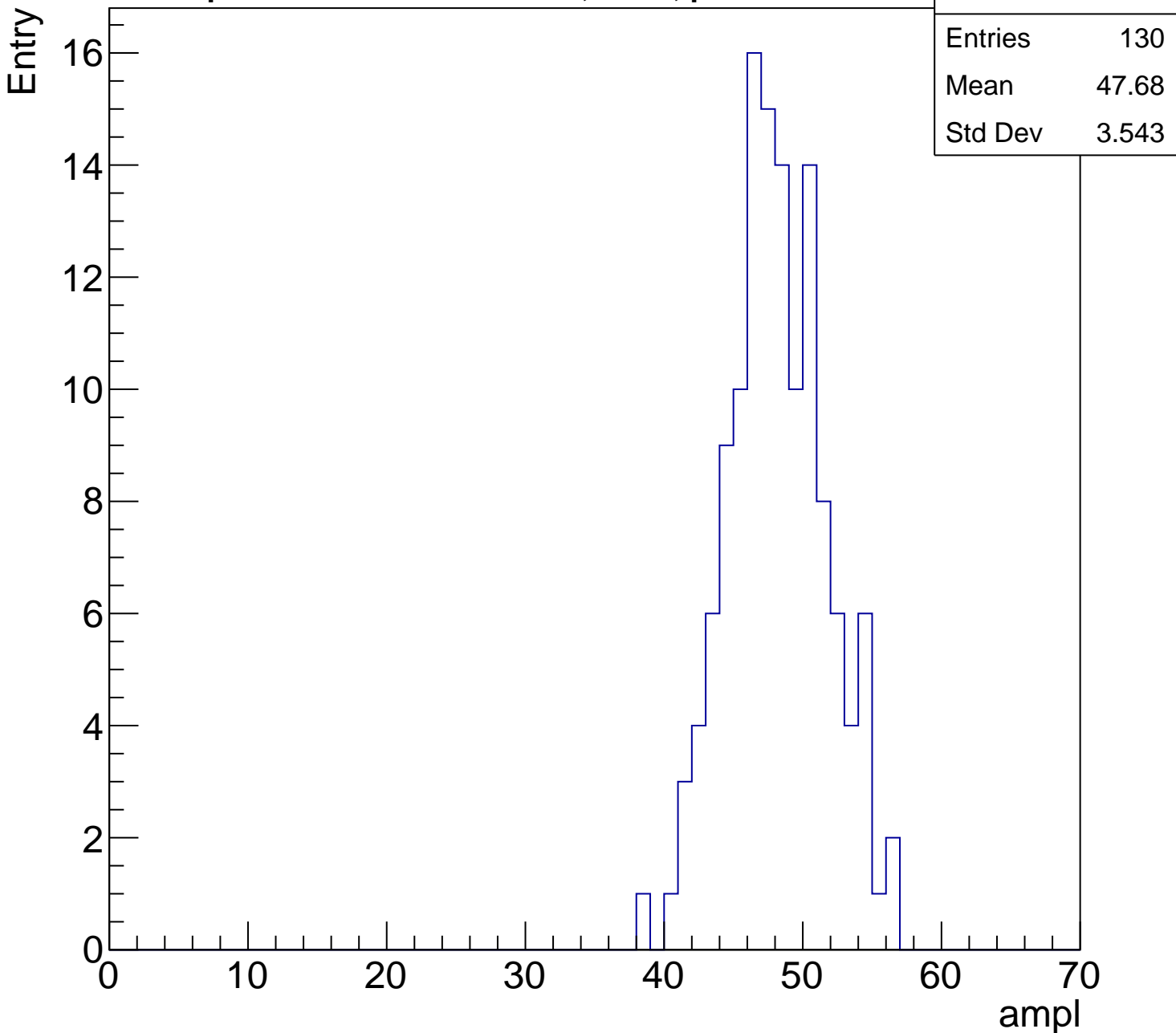
Entries	130
Mean	47.68
Std Dev	3.543

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

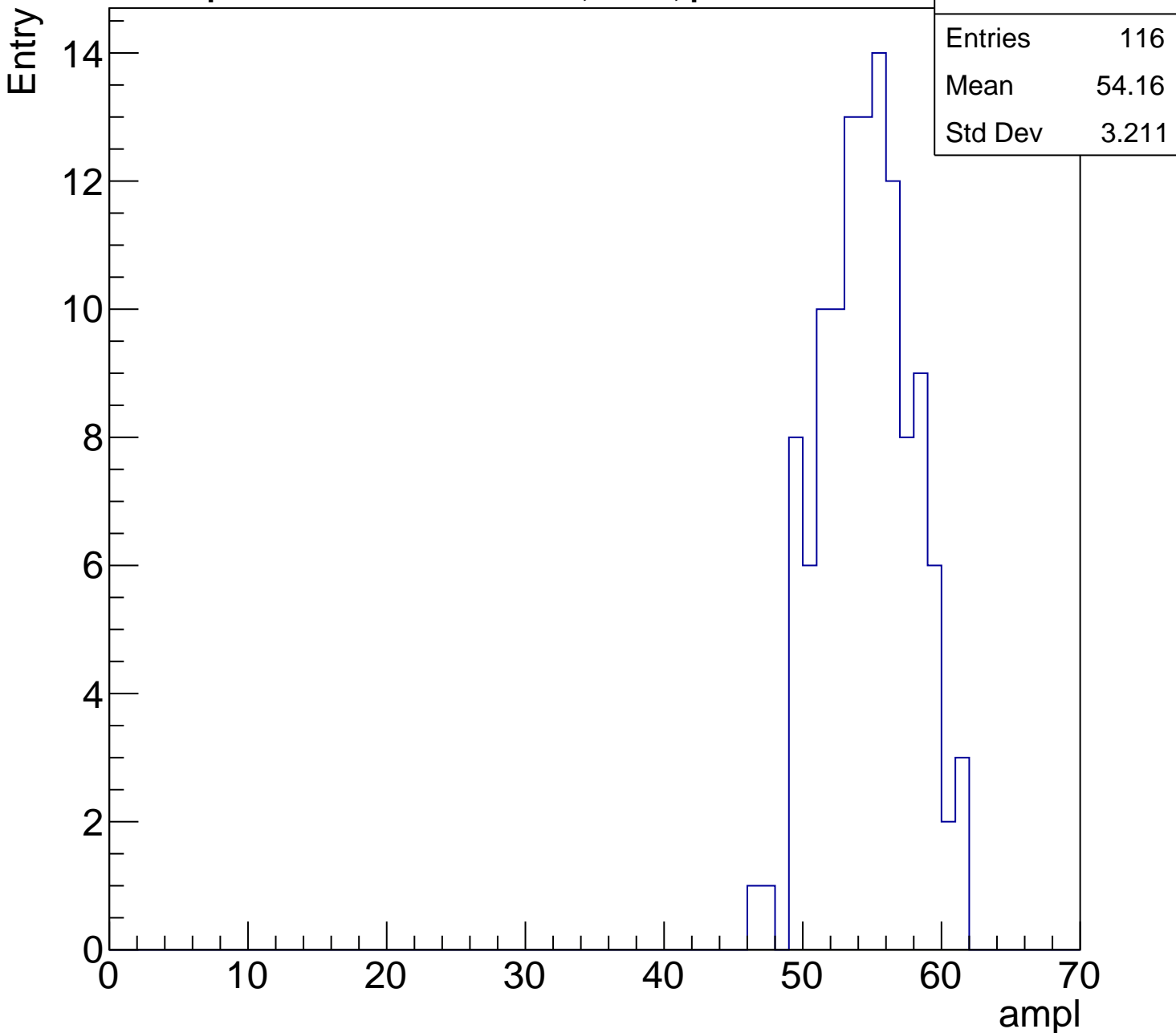
Entries	116
Mean	54.16
Std Dev	3.211

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

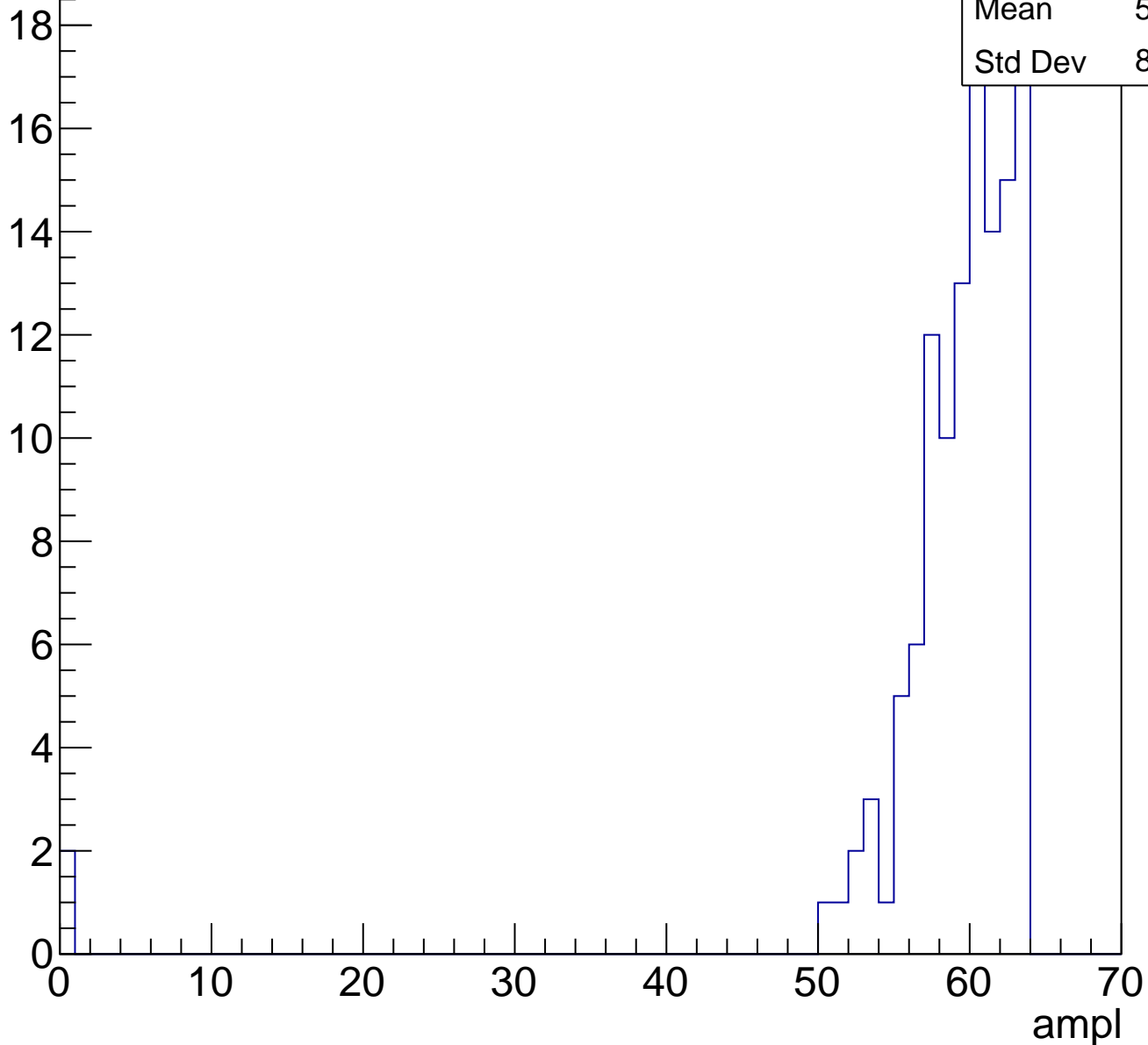
ampl



B0L101S, U20-ch48, adc5

calib_packv5_042523_0143.root, FC#1, port C1

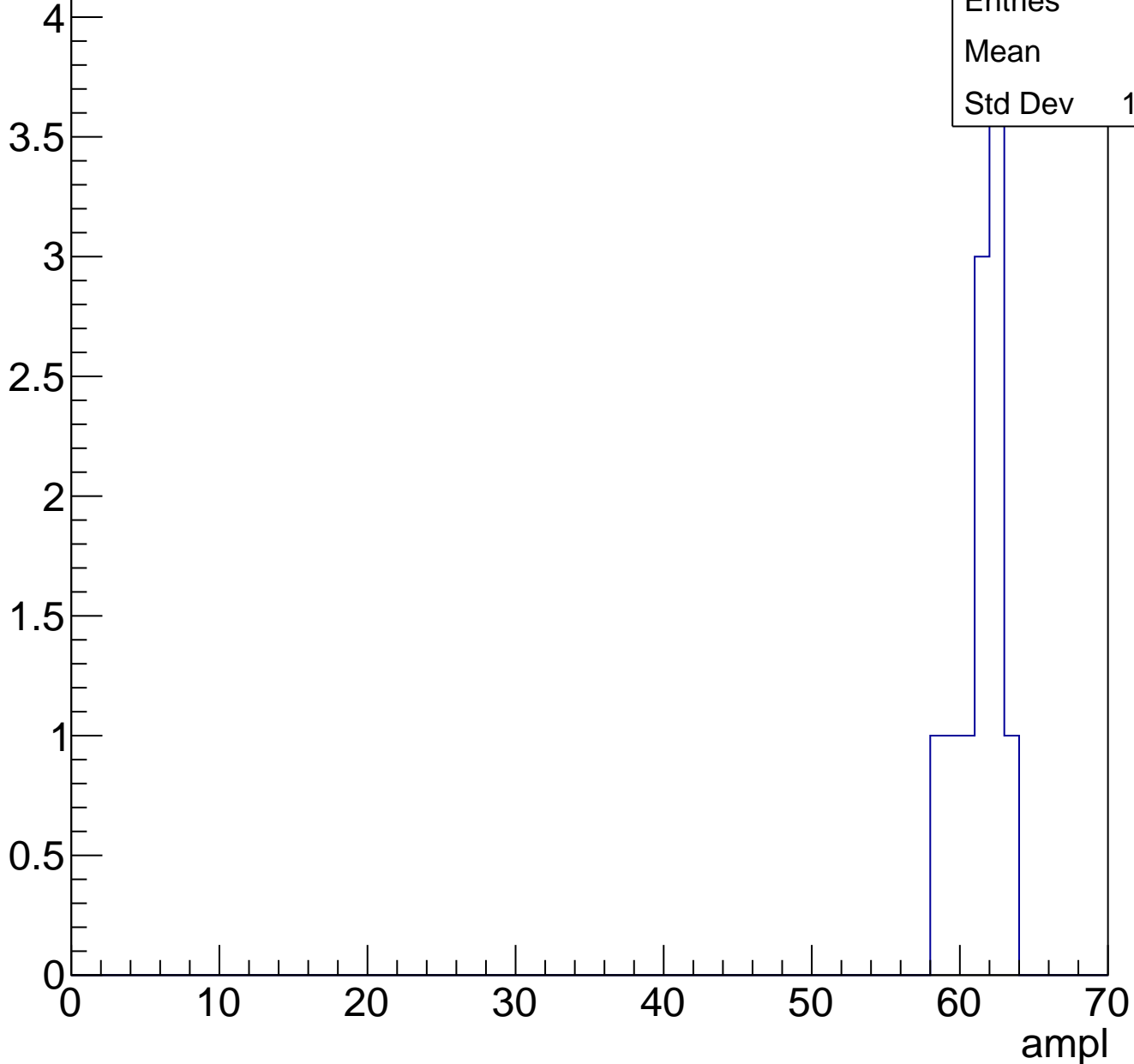
Entry



B0L101S, U20-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch49, adc0

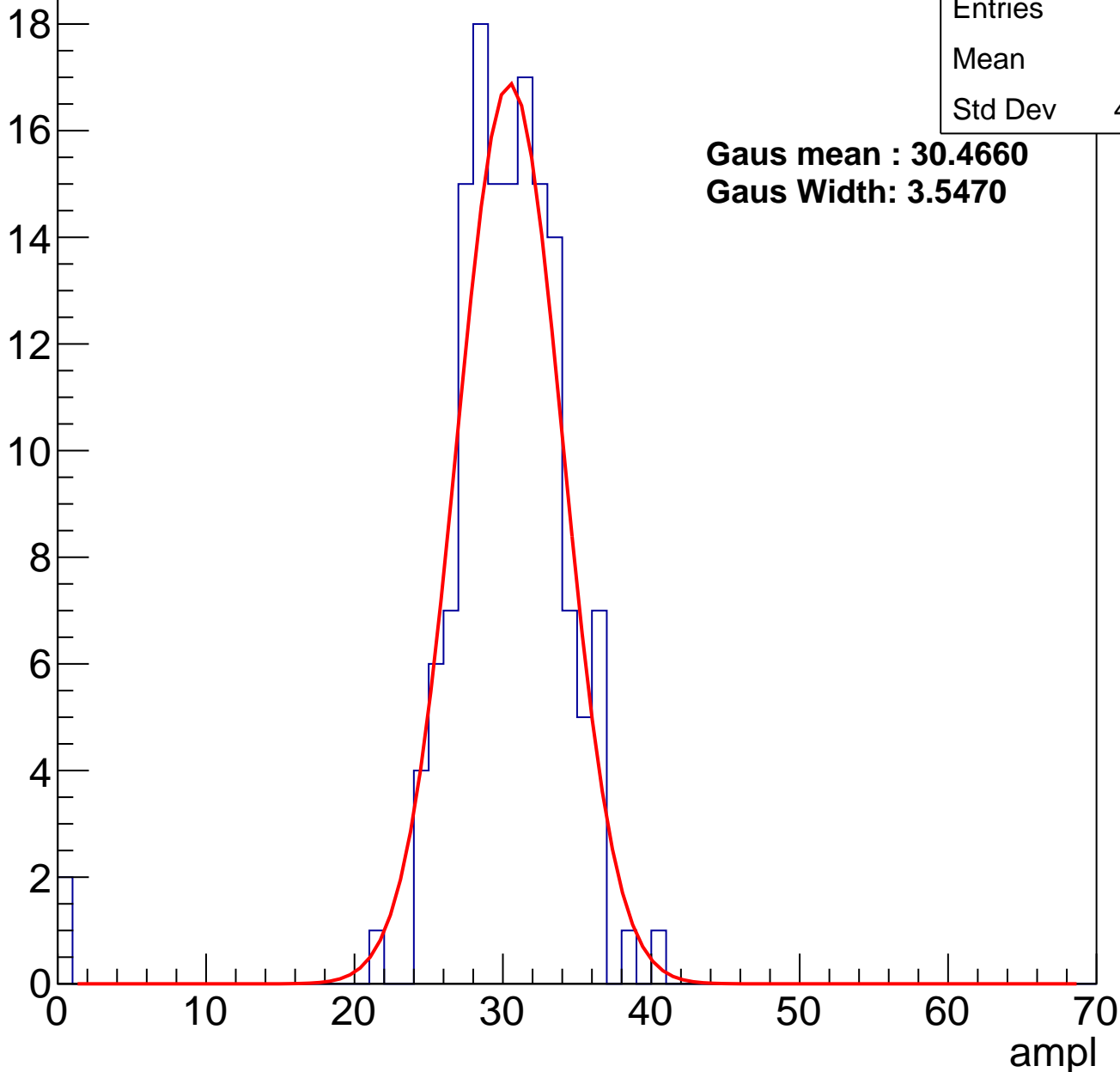
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.7
Std Dev	4.727

Gaus mean : 30.4660

Gaus Width: 3.5470

Entry



B0L101S, U20-ch49, adc1

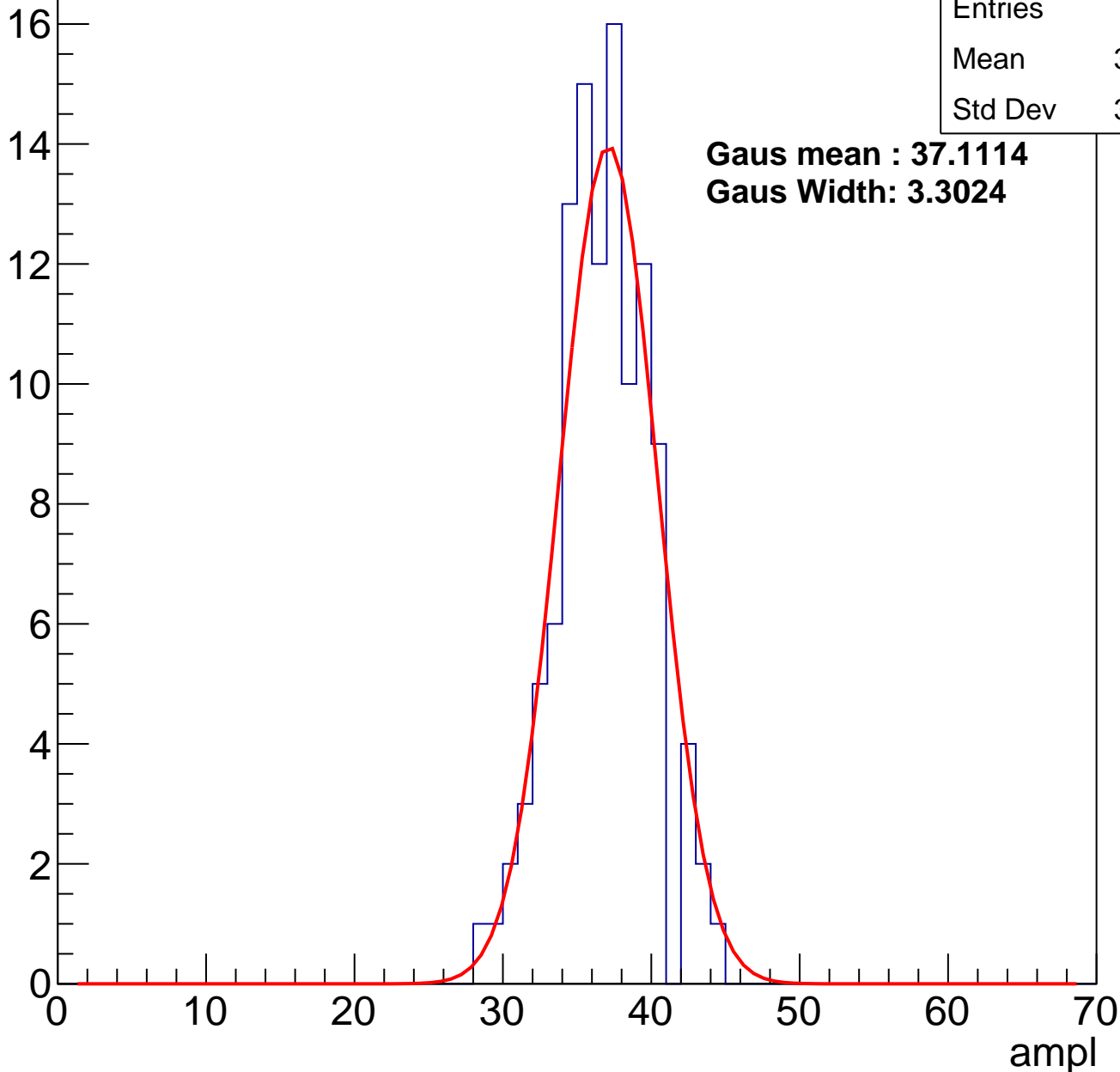
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	36.29
Std Dev	3.084

Gaus mean : 37.1114

Gaus Width: 3.3024

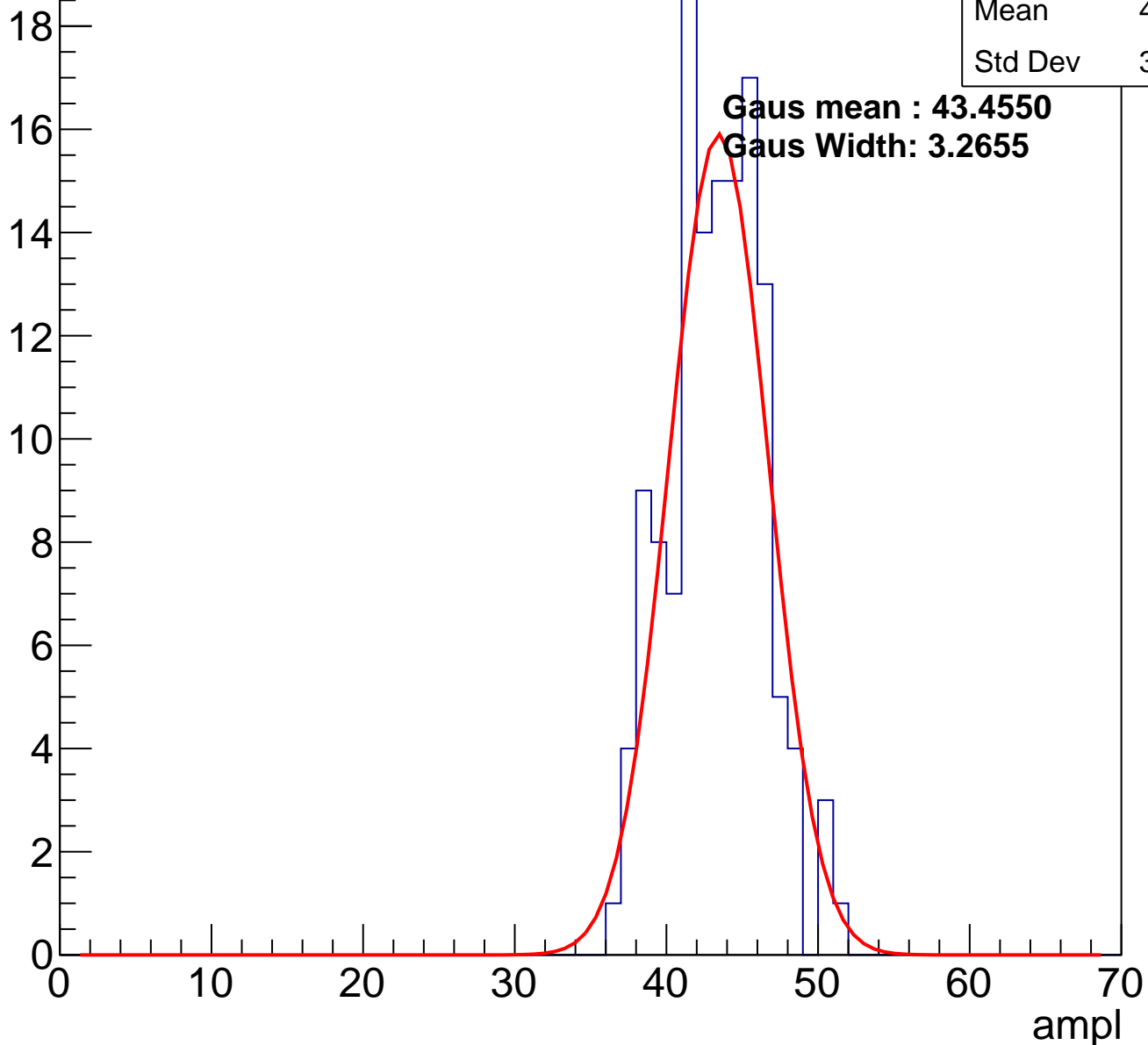
Entry



B0L101S, U20-ch49, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	49.65
Std Dev	3.828

Entry

16

14

12

10

8

6

4

2

0

0

10

20

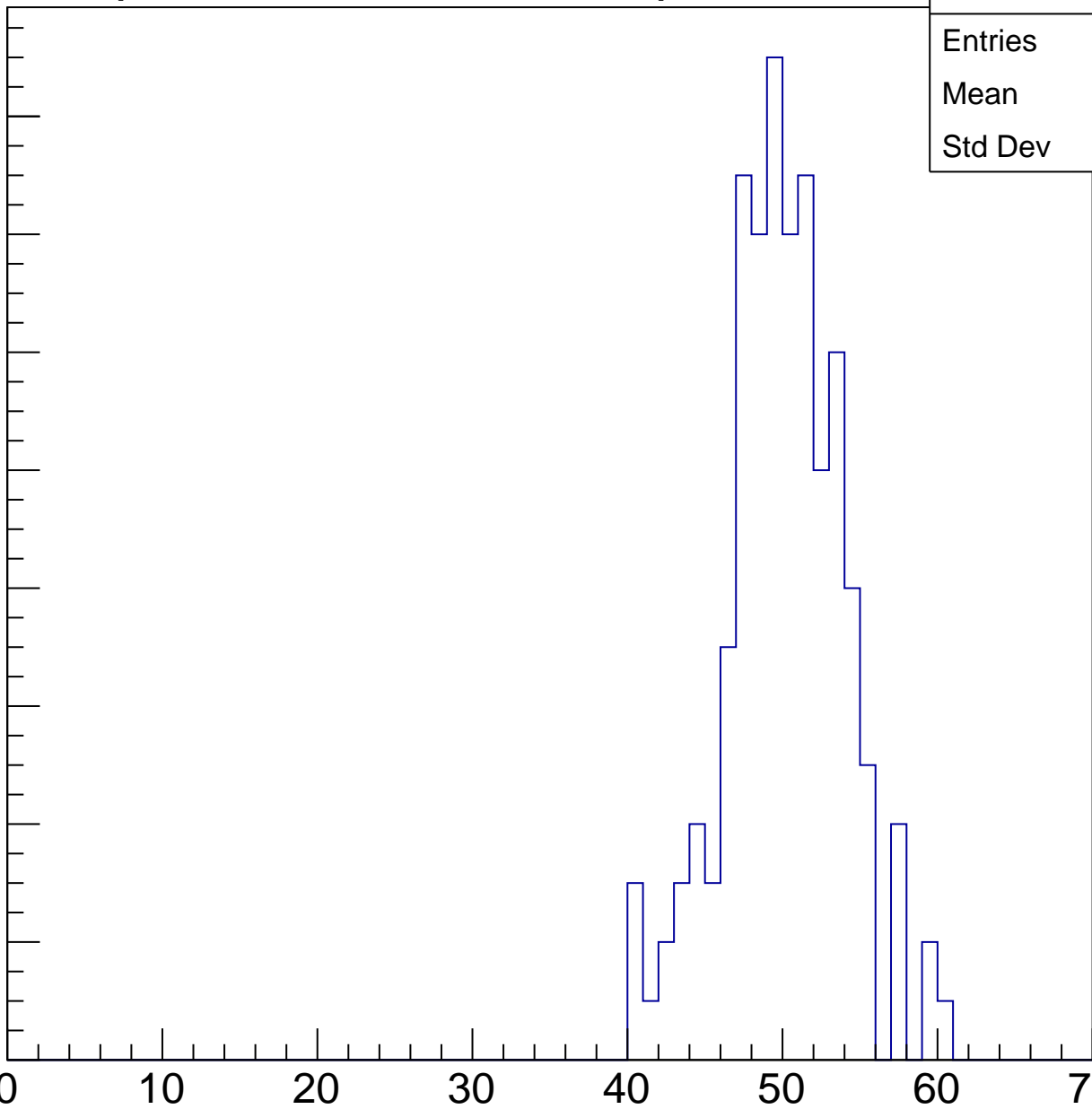
30

40

50

60

ampl



B0L101S, U20-ch49, adc4

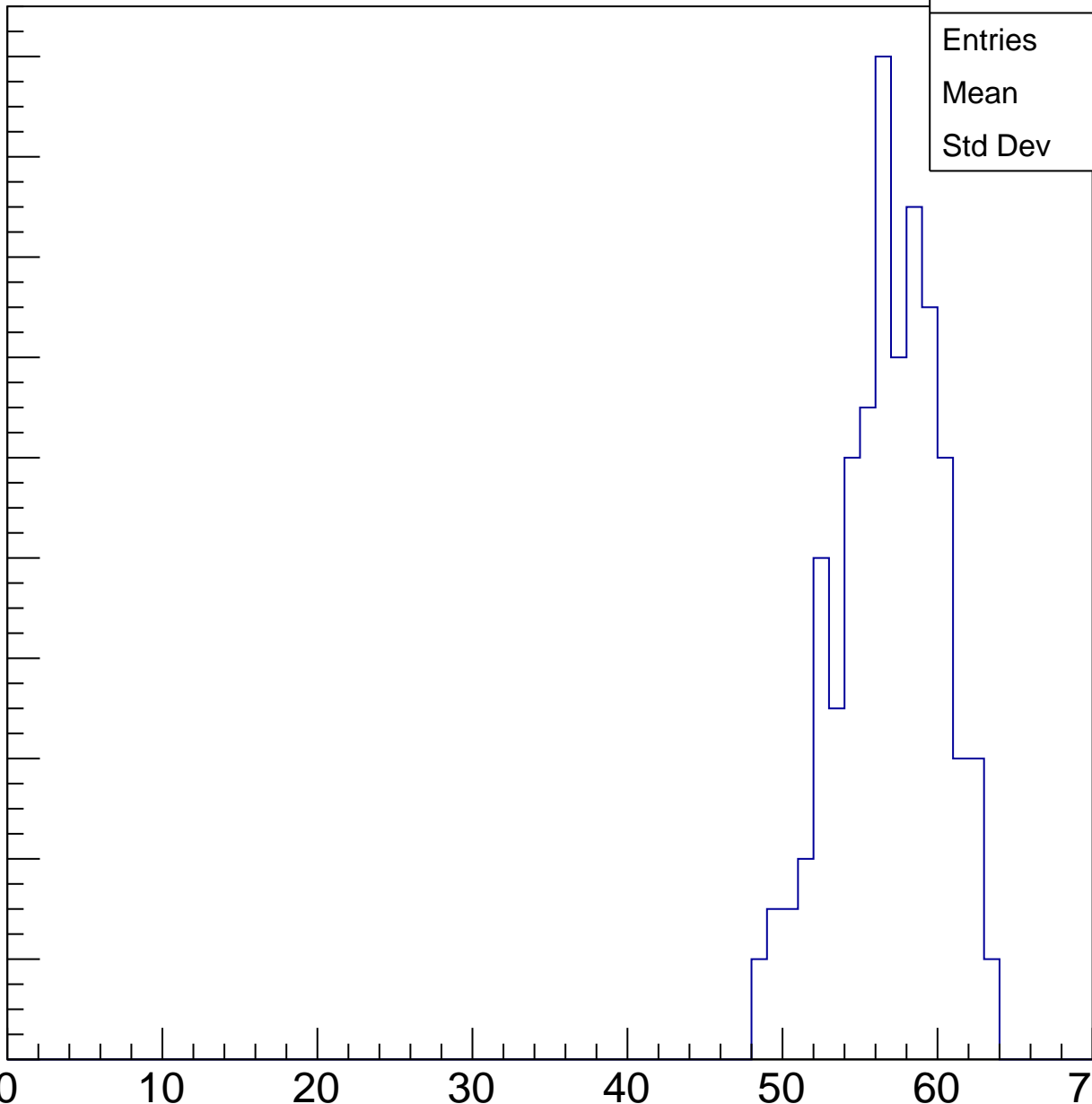
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	146
Mean	56.33
Std Dev	3.335

ampl



B0L101S, U20-ch49, adc5

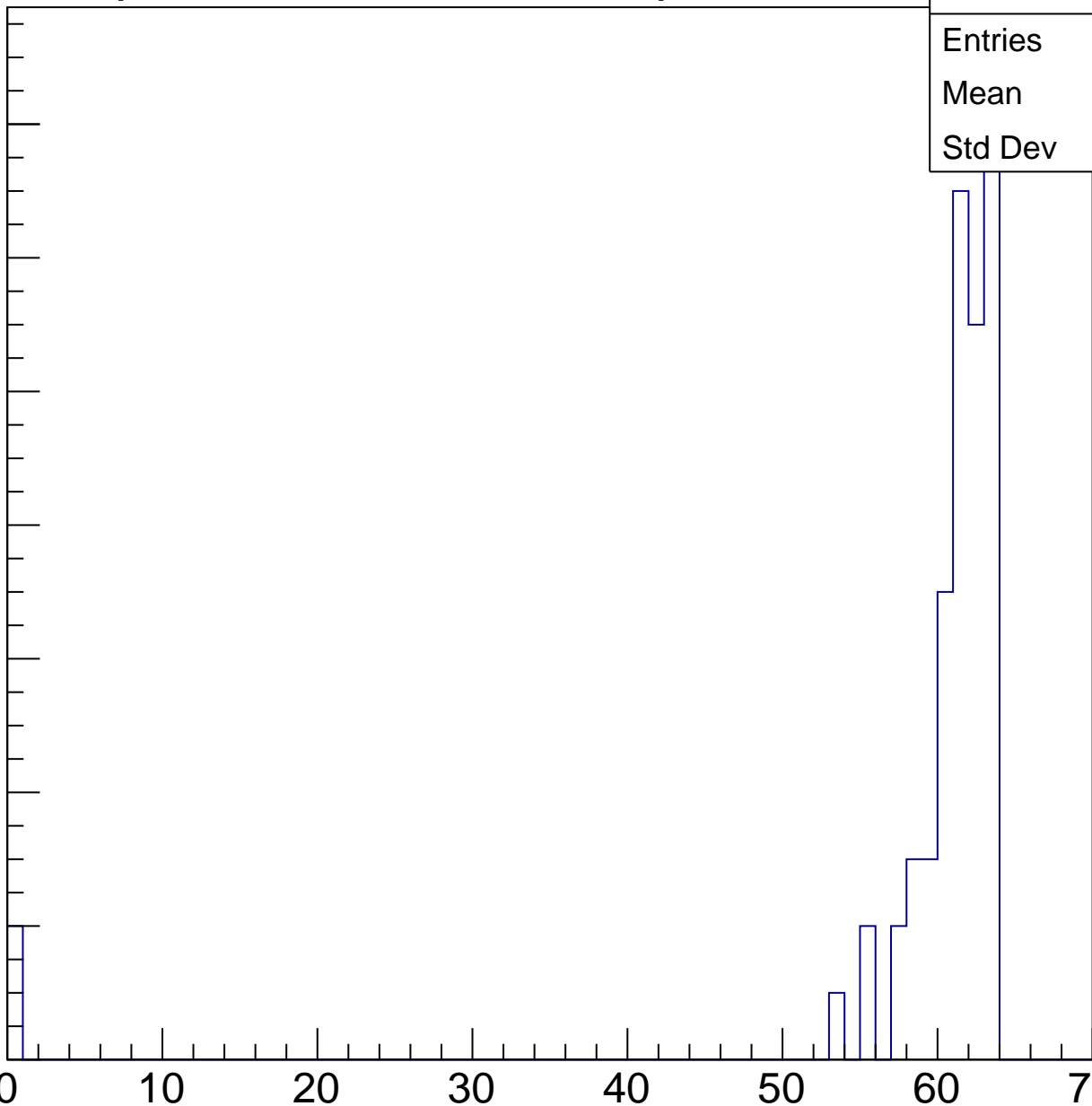
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	58.78
Std Dev	11.23

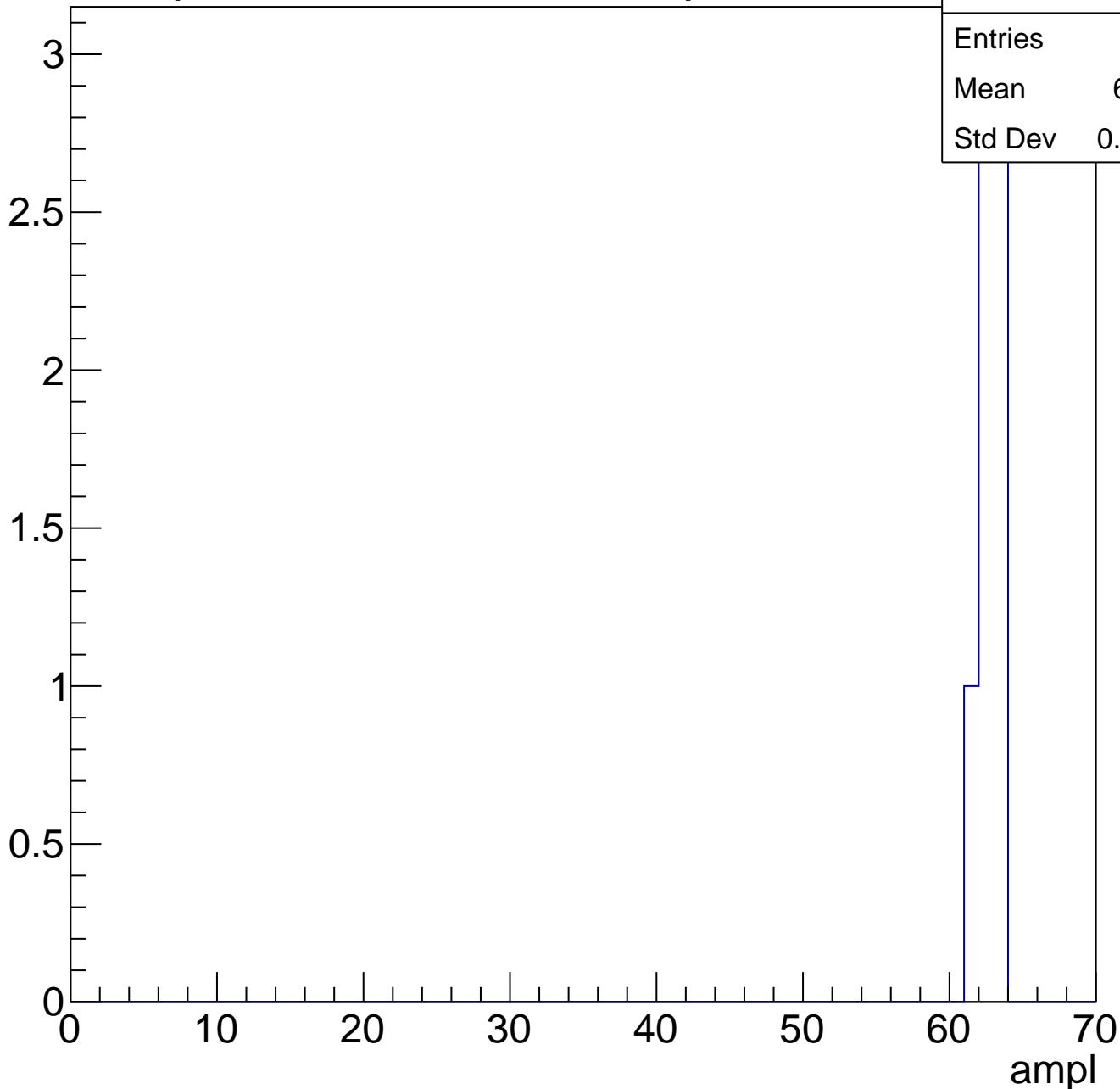
ampl



B0L101S, U20-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

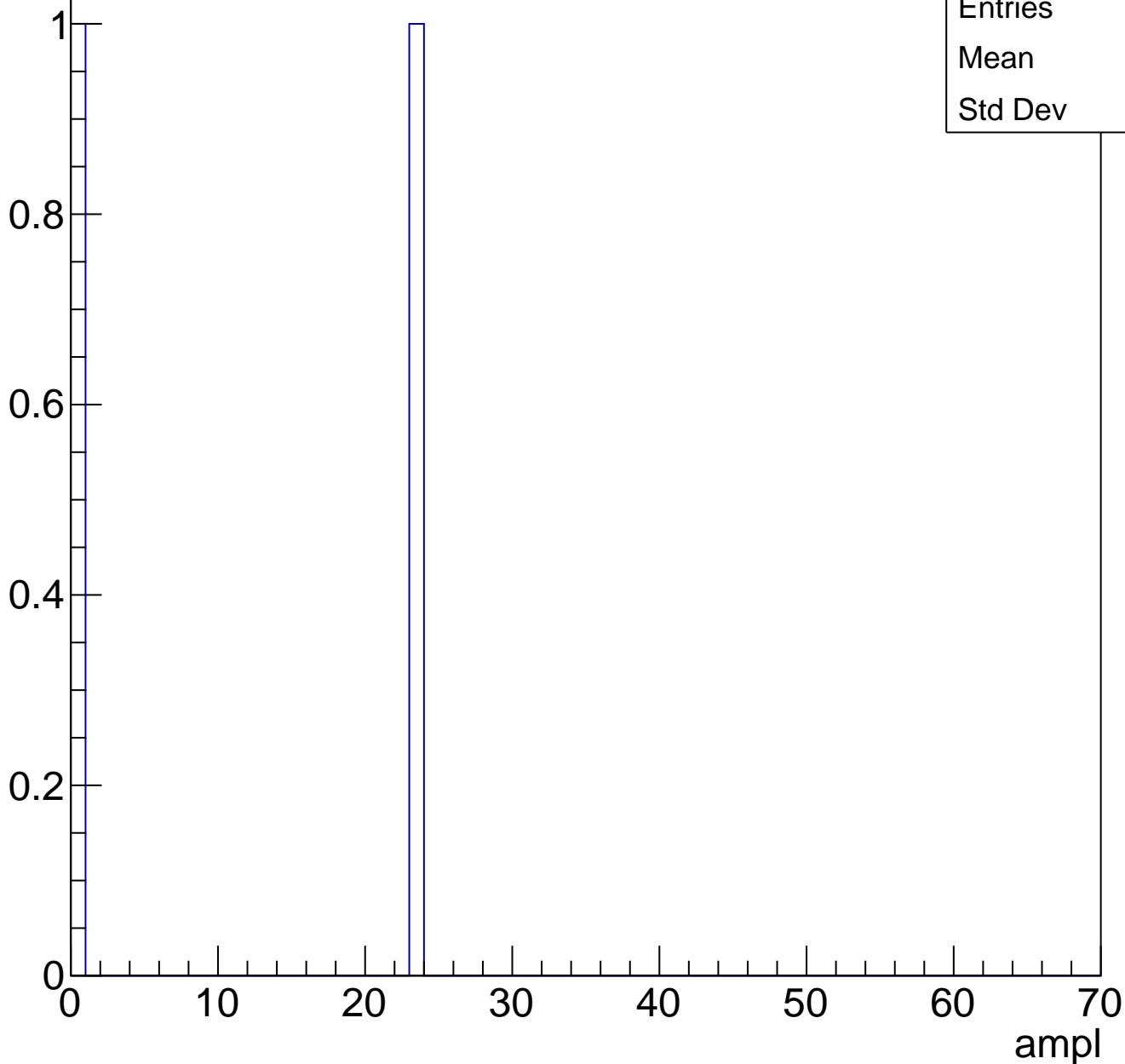
Entry



B0L101S, U20-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch50, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	27.26
Std Dev	4.246

Gaus mean : 28.1908

Gaus Width: 3.2920

Entry

12

10

8

6

4

2

0

0

10

20

30

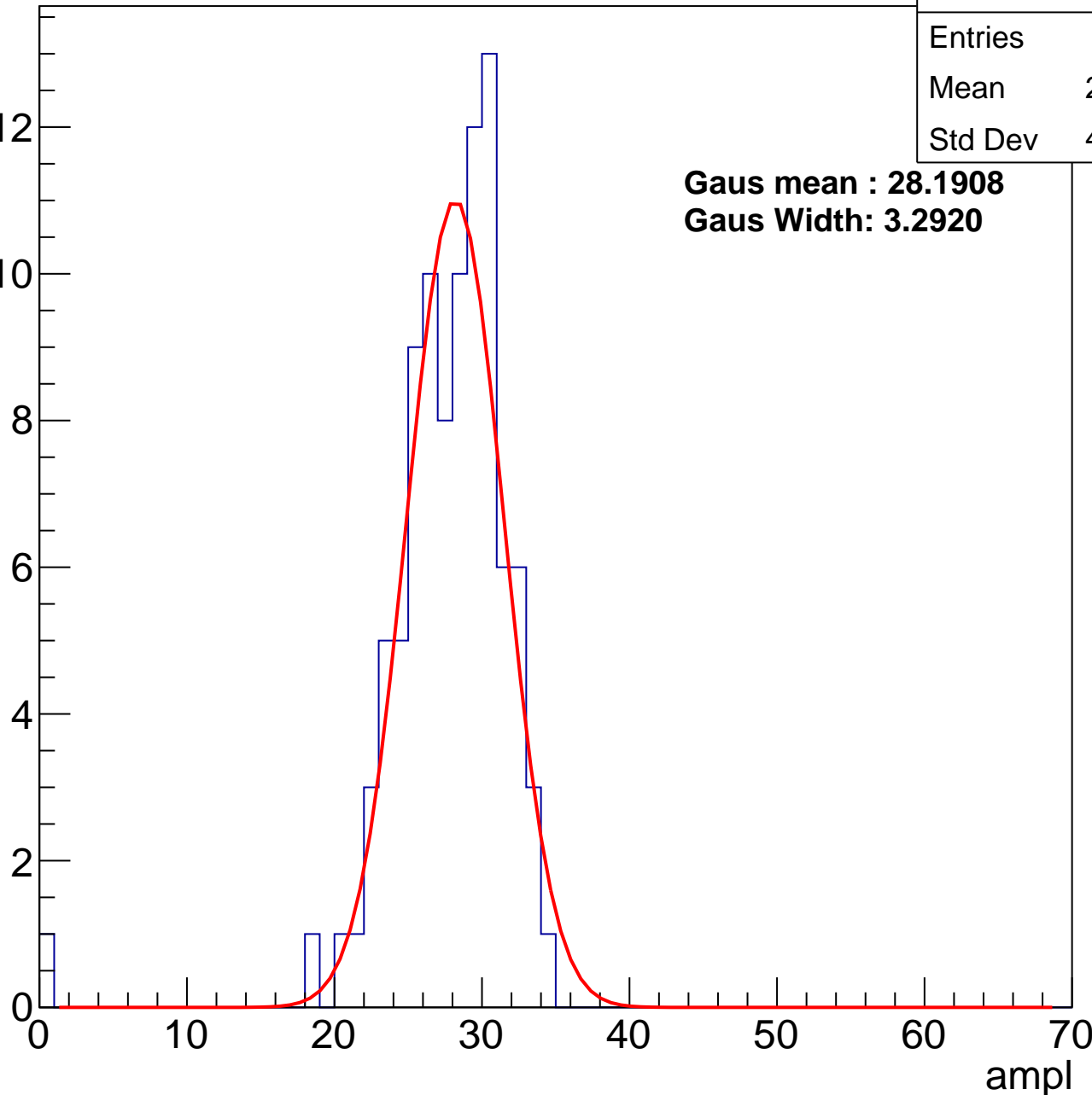
40

50

60

70

ampl



B0L101S, U20-ch50, adc1

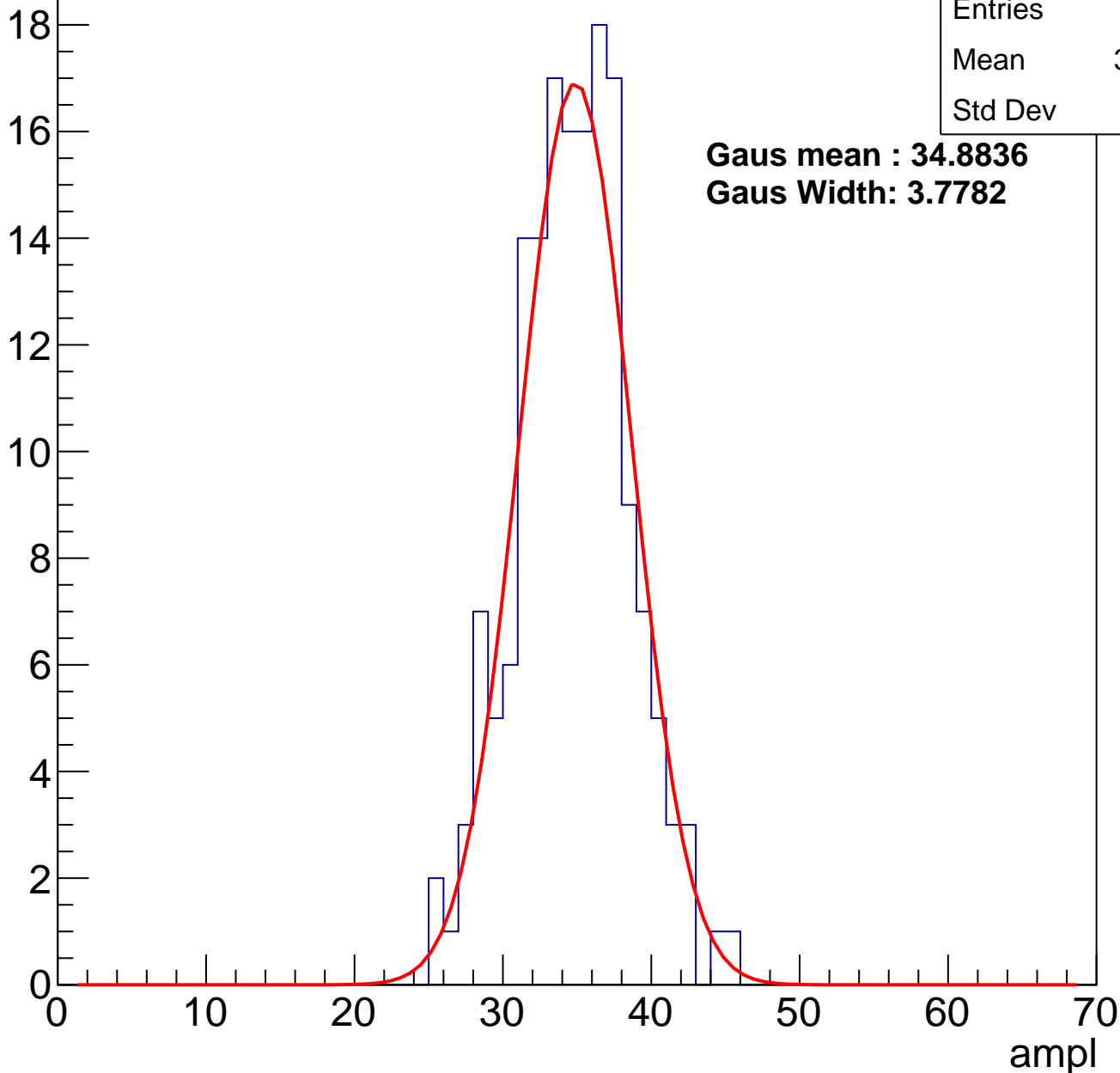
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	34.27
Std Dev	3.76

Gaus mean : 34.8836

Gaus Width: 3.7782

Entry



B0L101S, U20-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

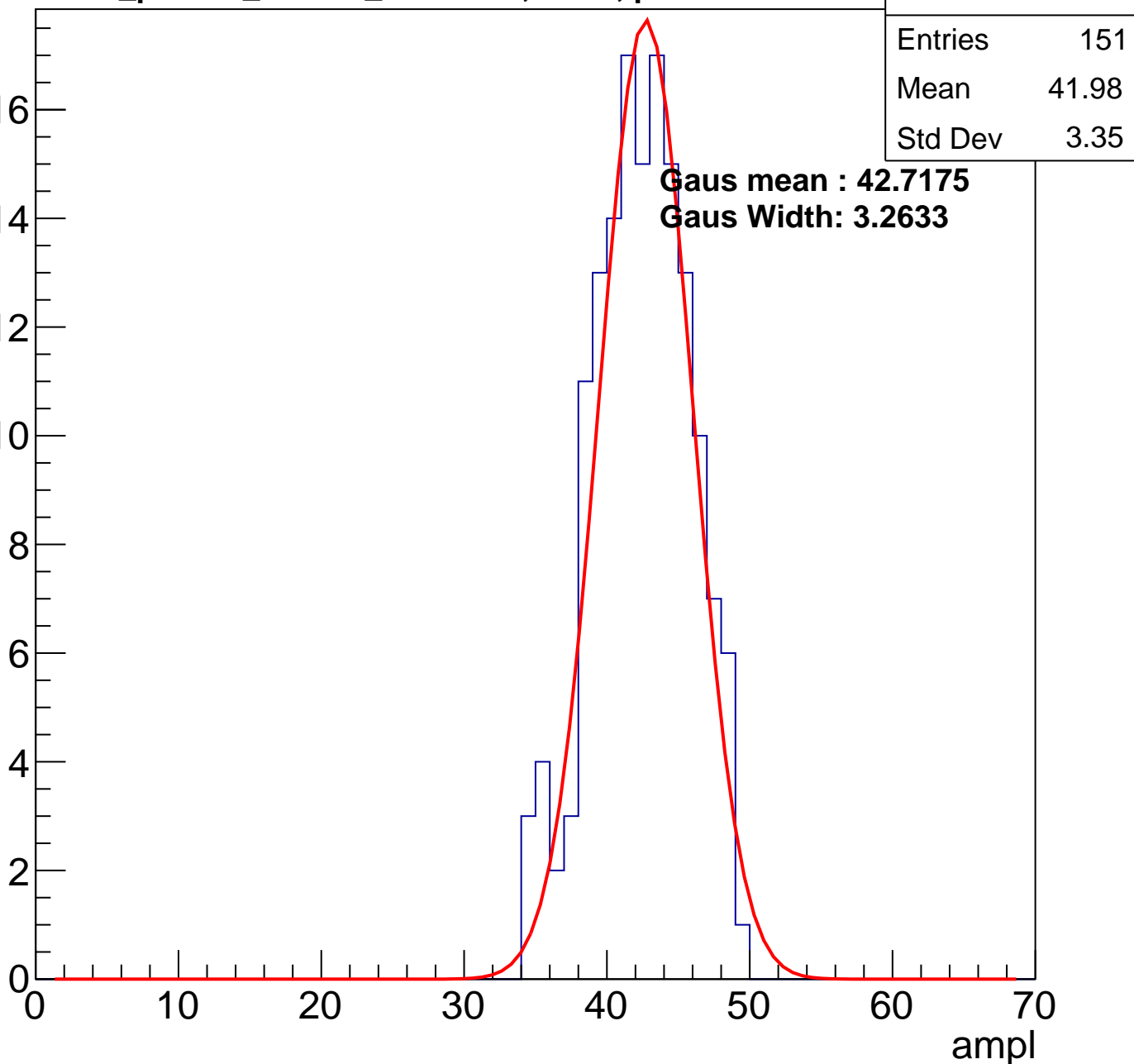
70

ampl

Entries	151
Mean	41.98
Std Dev	3.35

Gaus mean : 42.7175

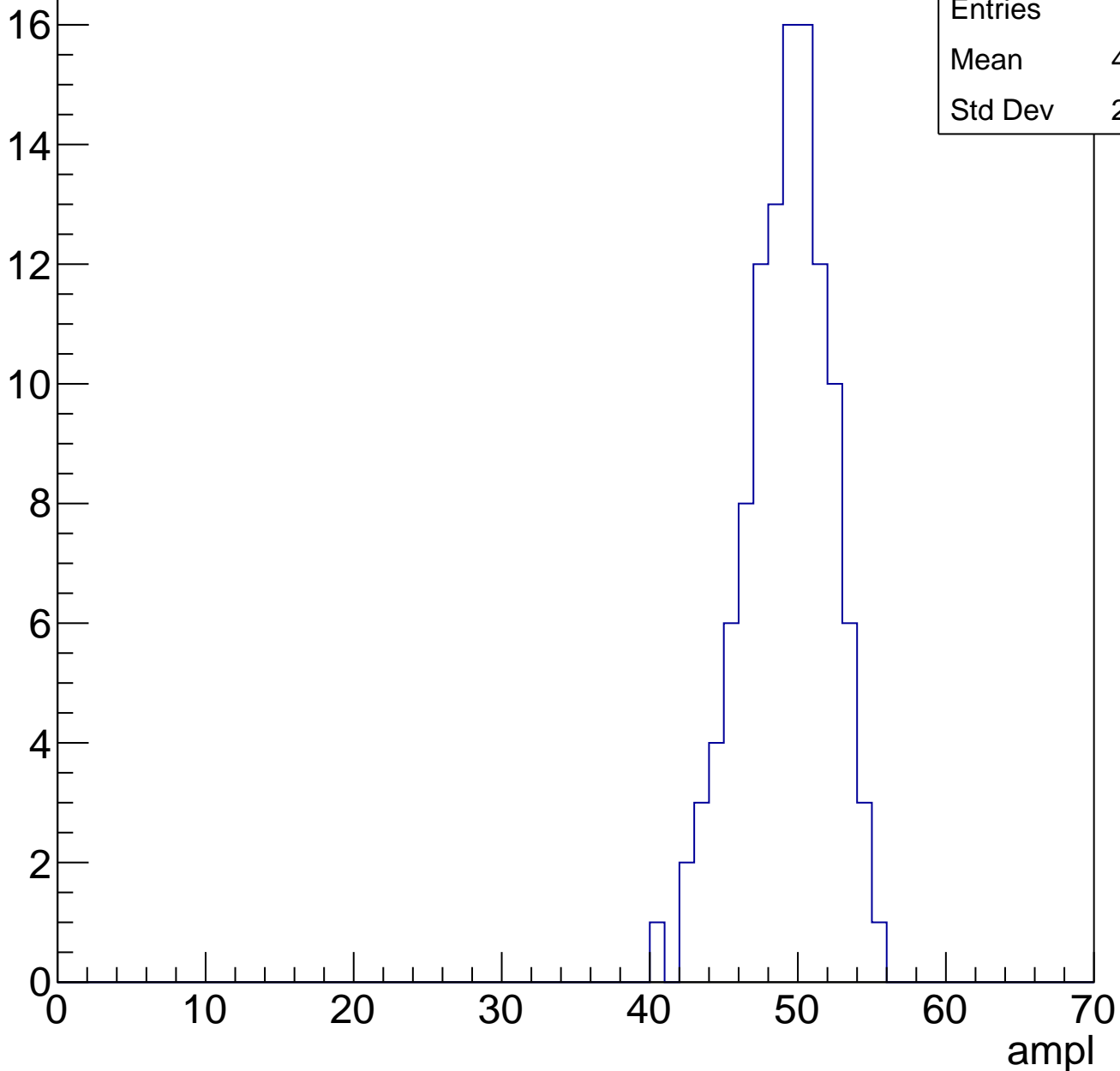
Gaus Width: 3.2633



B0L101S, U20-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

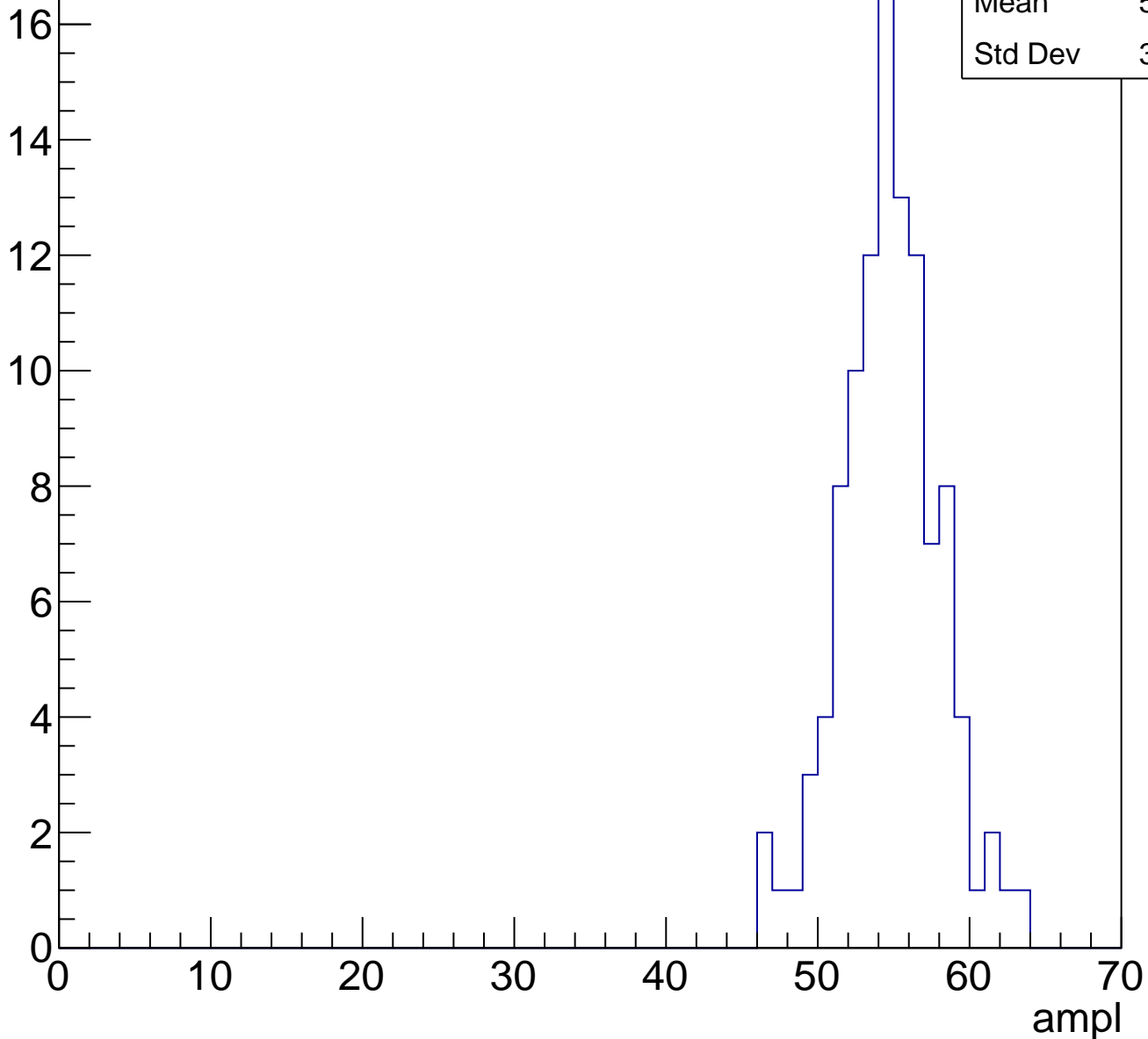


B0L101S, U20-ch50, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	107
Mean	54.29
Std Dev	3.203



B0L101S, U20-ch50, adc5

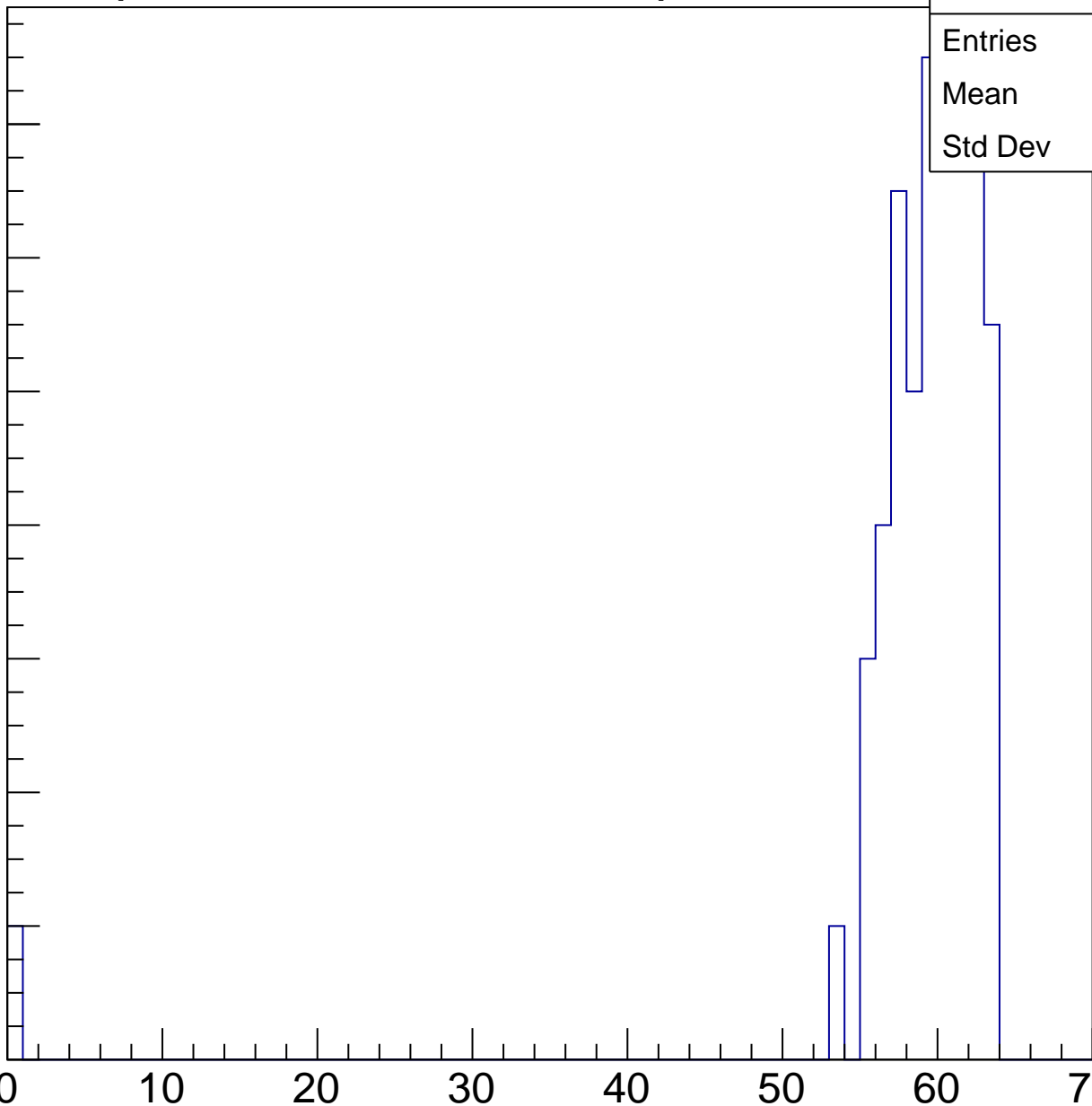
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	58.23
Std Dev	8.296

ampl



B0L101S, U20-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

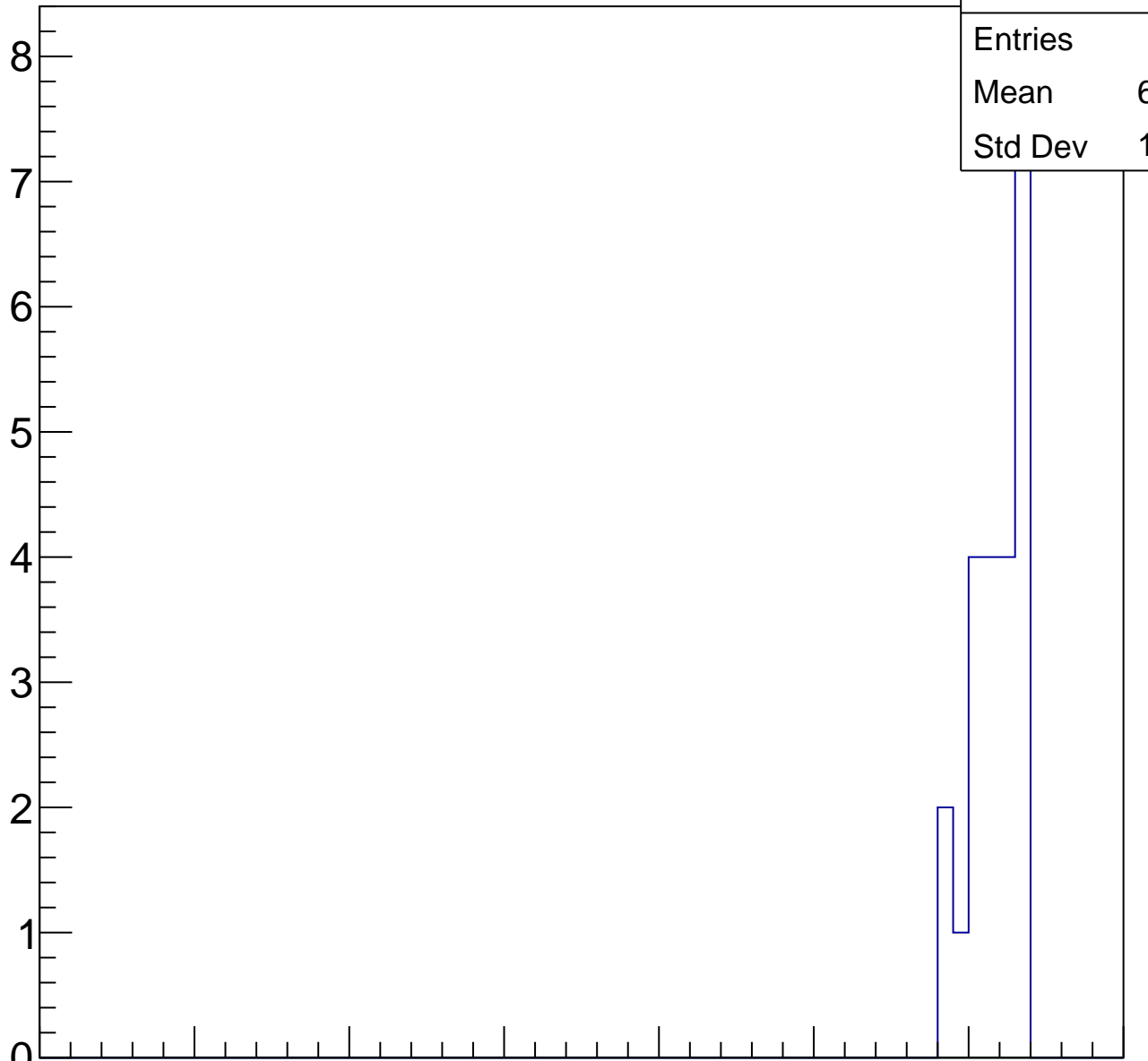
Entry

8
7
6
5
4
3
2
1
0

Entries	23
Mean	61.35
Std Dev	1.605

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch51, adc0

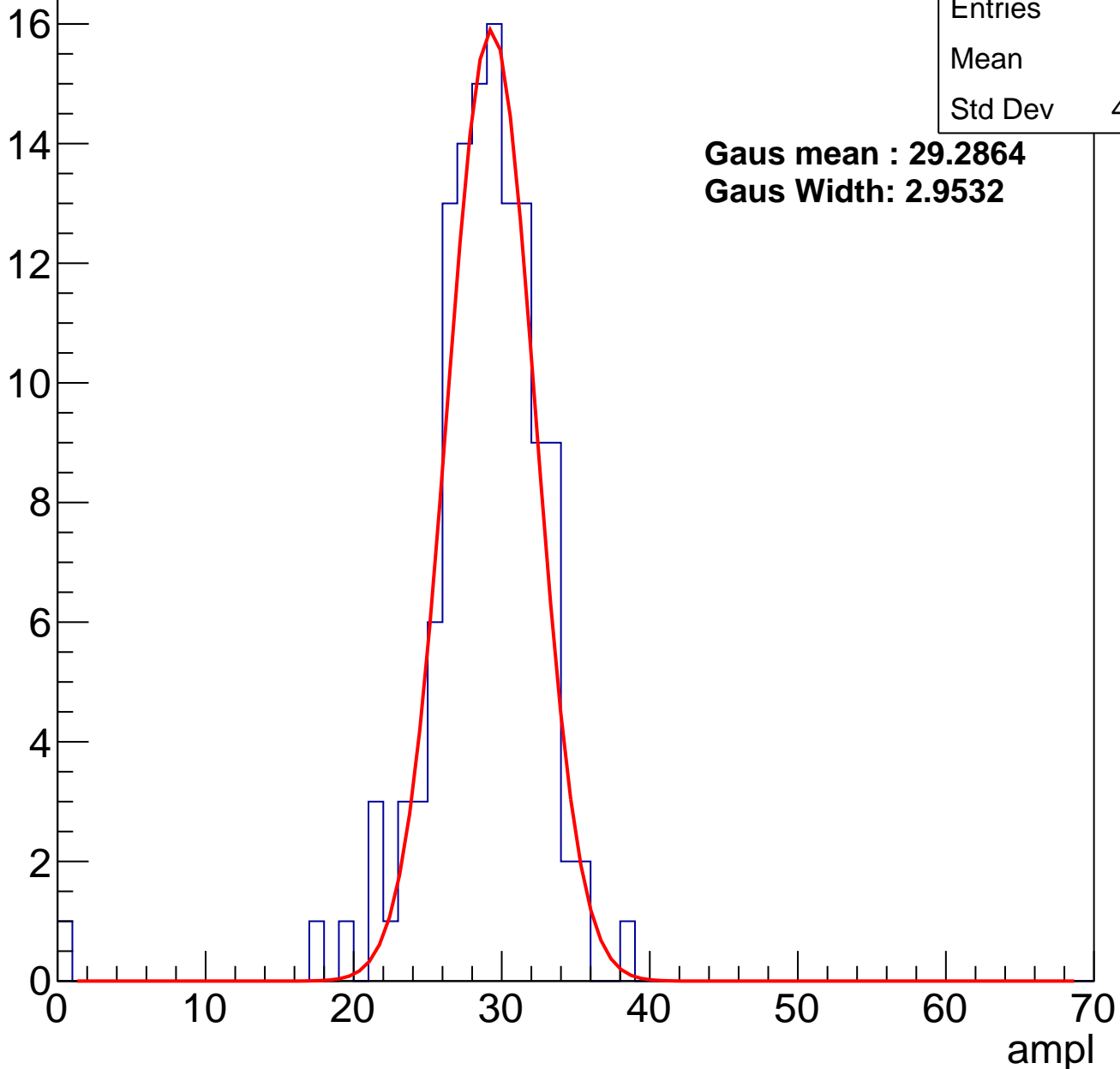
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	28.3
Std Dev	4.202

Gaus mean : 29.2864

Gaus Width: 2.9532

Entry



B0L101S, U20-ch51, adc1

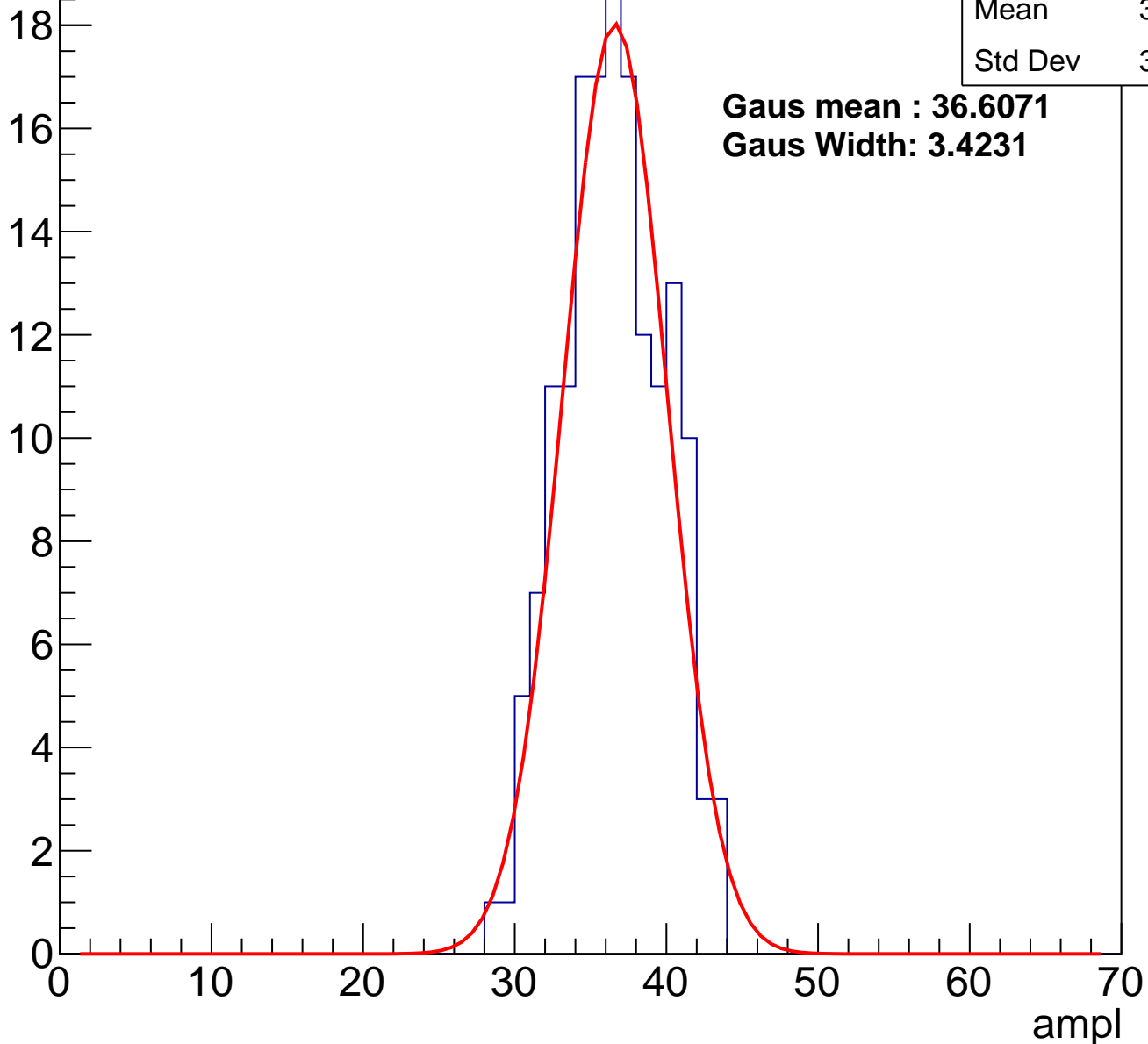
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.04
Std Dev	3.285

Gaus mean : 36.6071

Gaus Width: 3.4231

Entry



B0L101S, U20-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

105

Mean

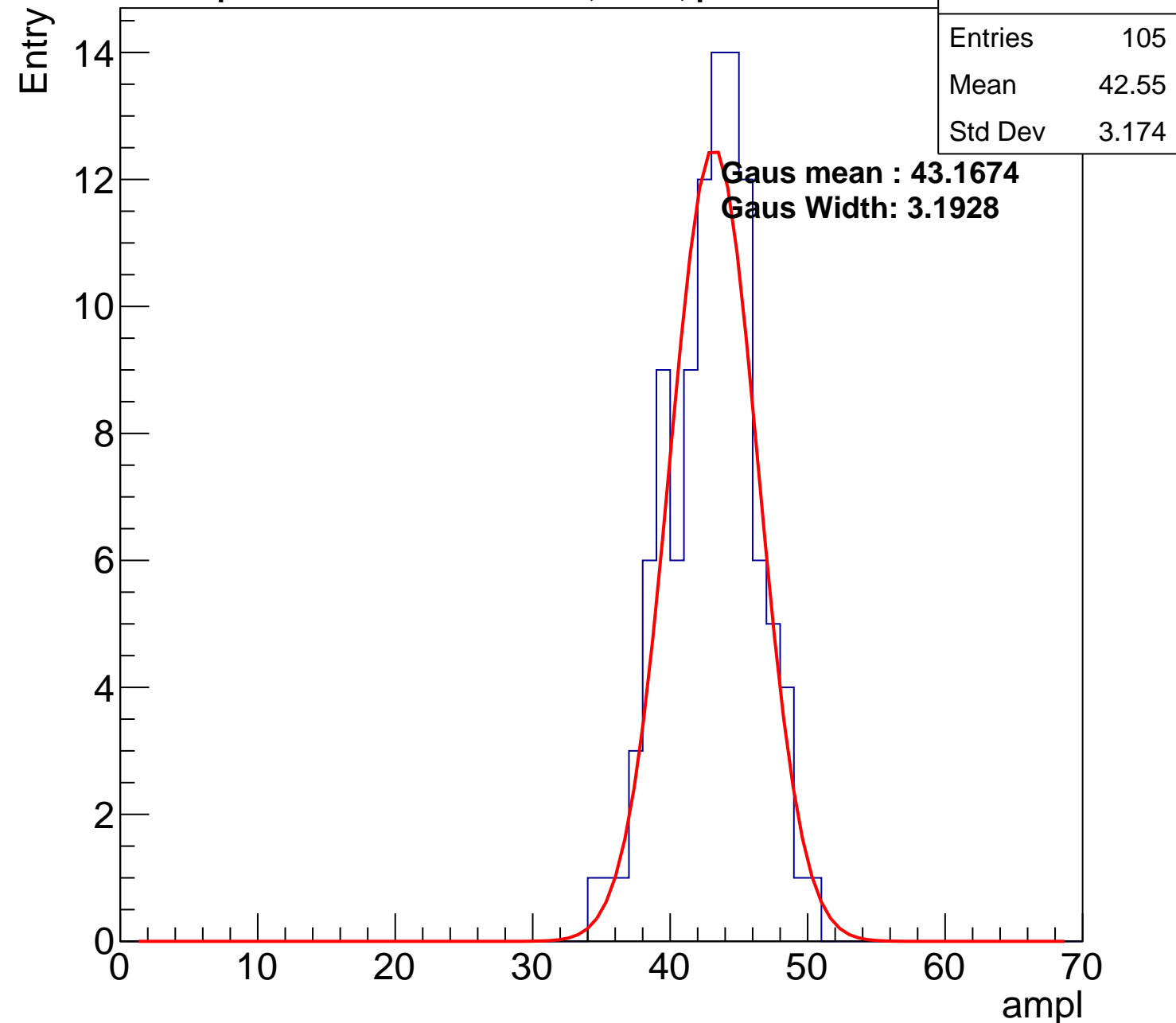
42.55

Std Dev

3.174

Gaus mean : 43.1674

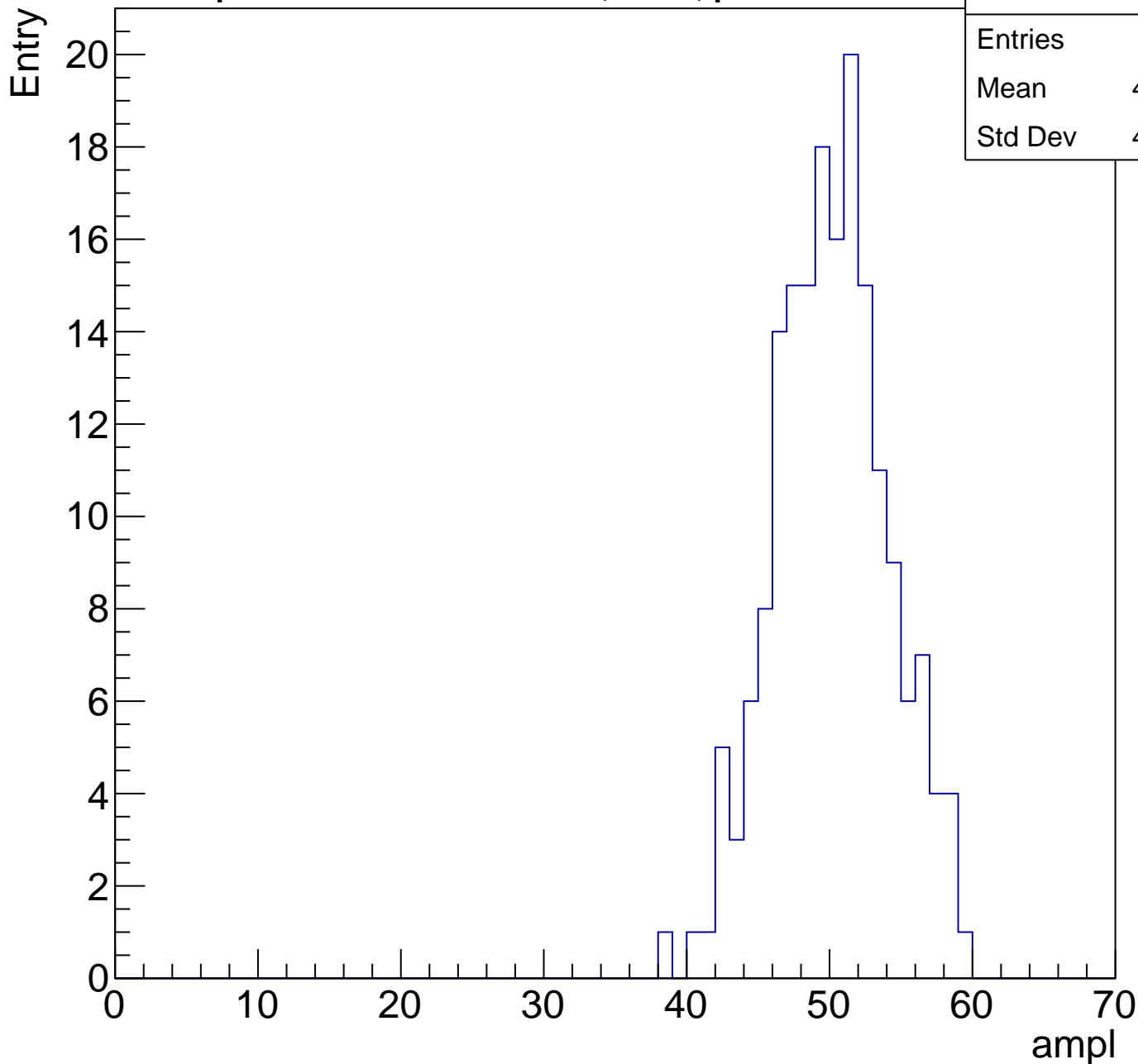
Gaus Width: 3.1928



B0L101S, U20-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	49.68
Std Dev	4.003



B0L101S, U20-ch51, adc4

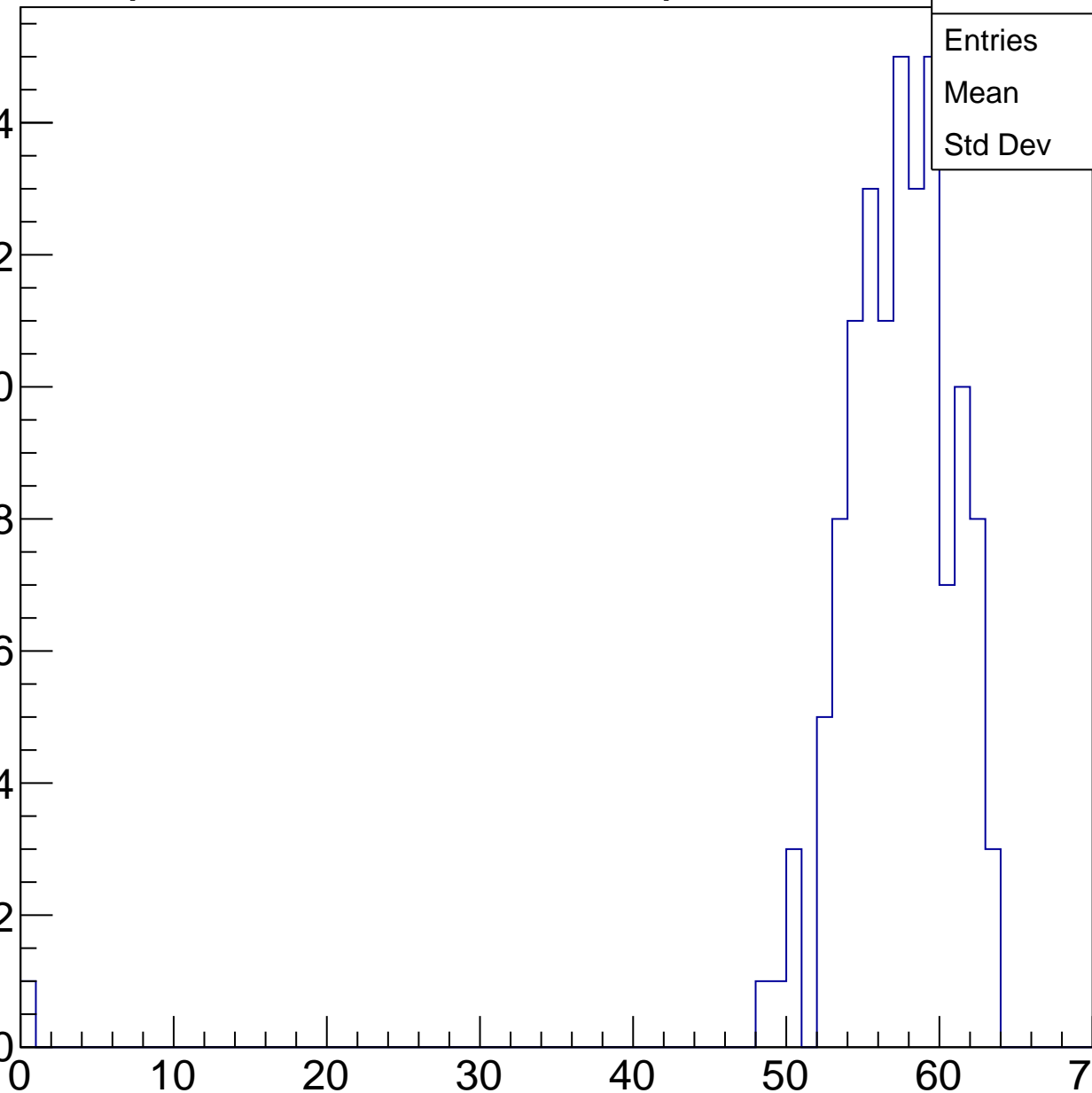
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	125
Mean	56.52
Std Dev	6.021

ampl



B0L101S, U20-ch51, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

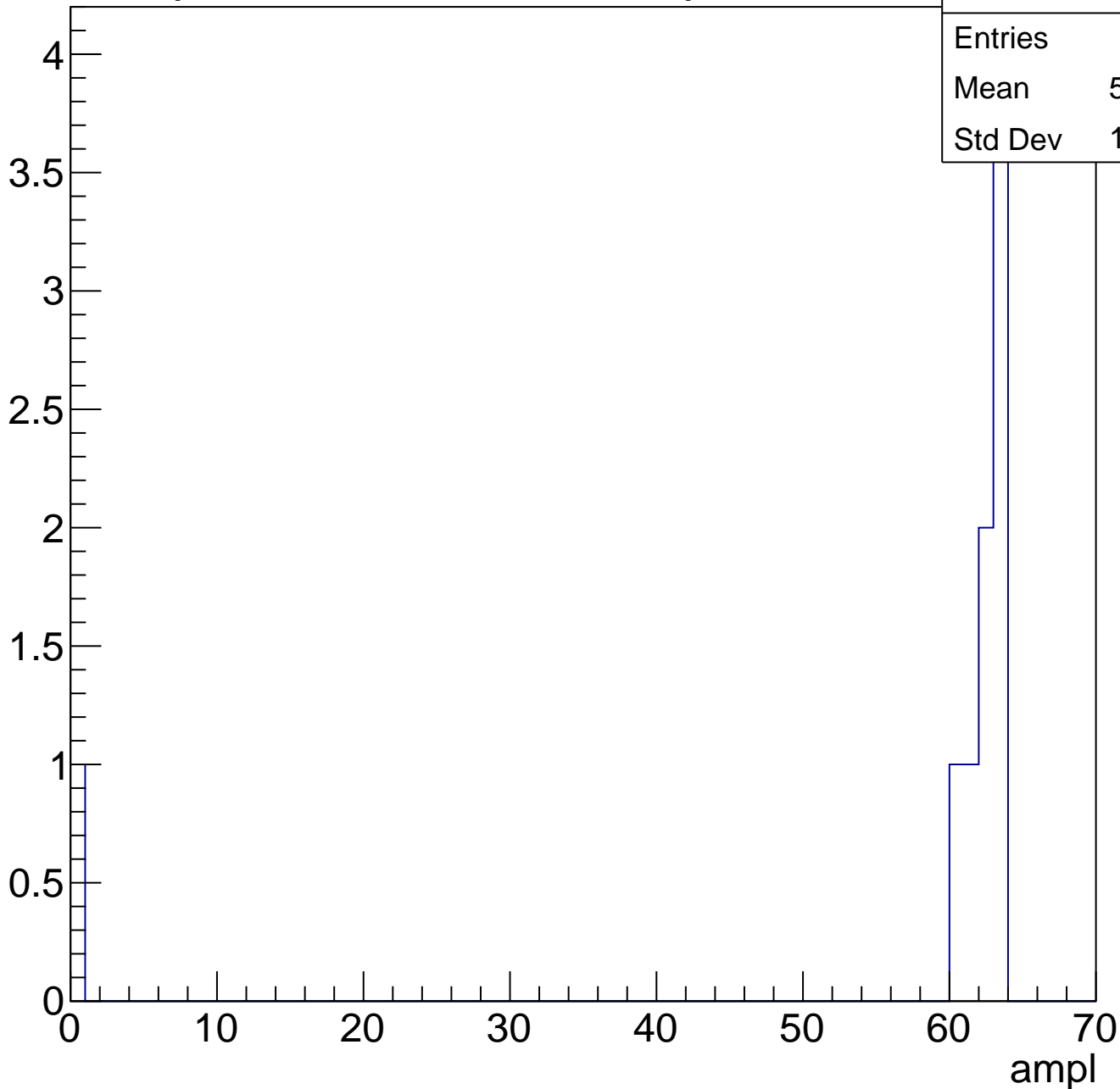
ampl

Entries	56
Mean	60.62
Std Dev	2.217

B0L101S, U20-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch52, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

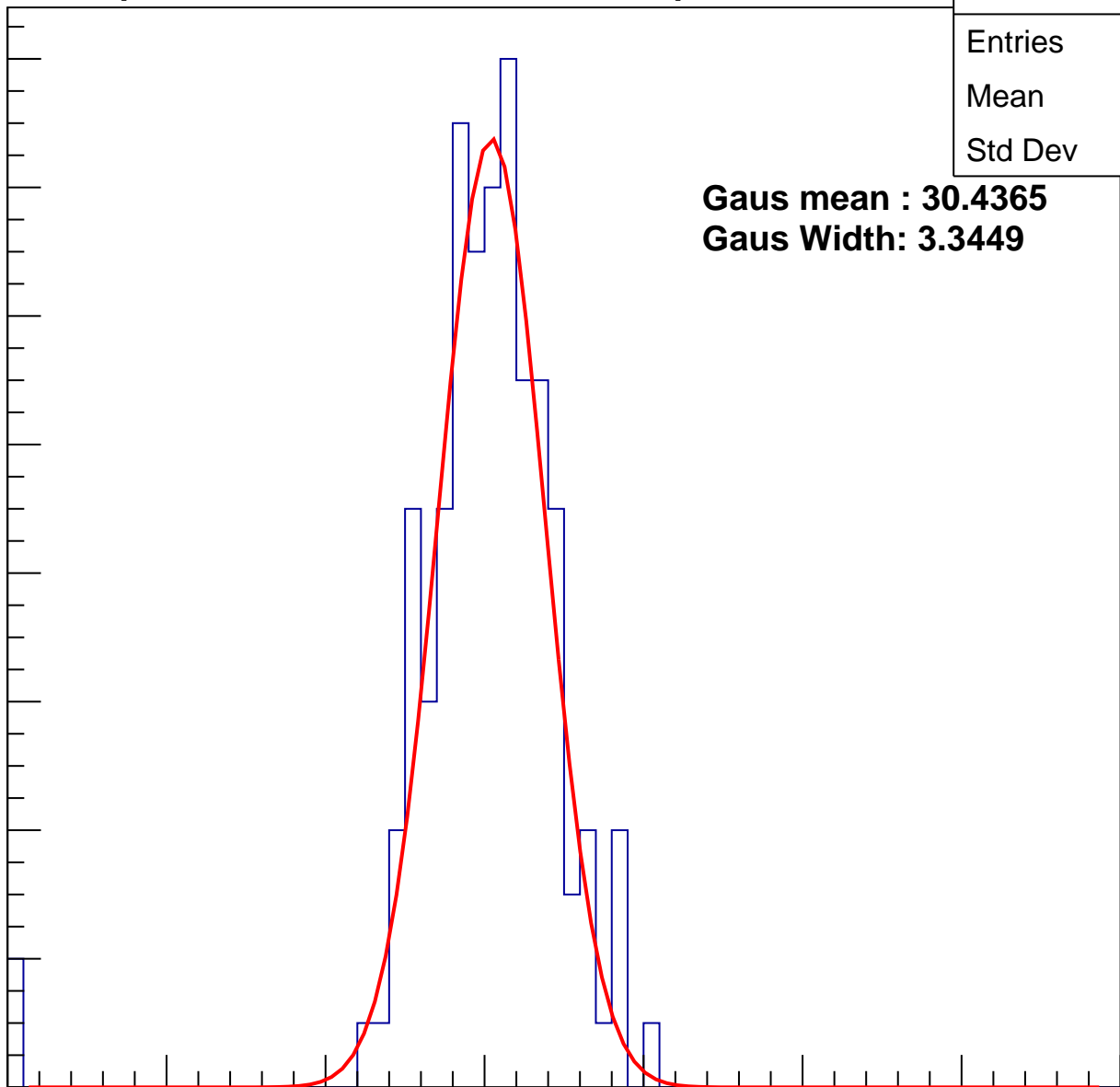
Entries	134
Mean	29.68
Std Dev	5.071

Gaus mean : 30.4365

Gaus Width: 3.3449

0 10 20 30 40 50 60 70

ampl



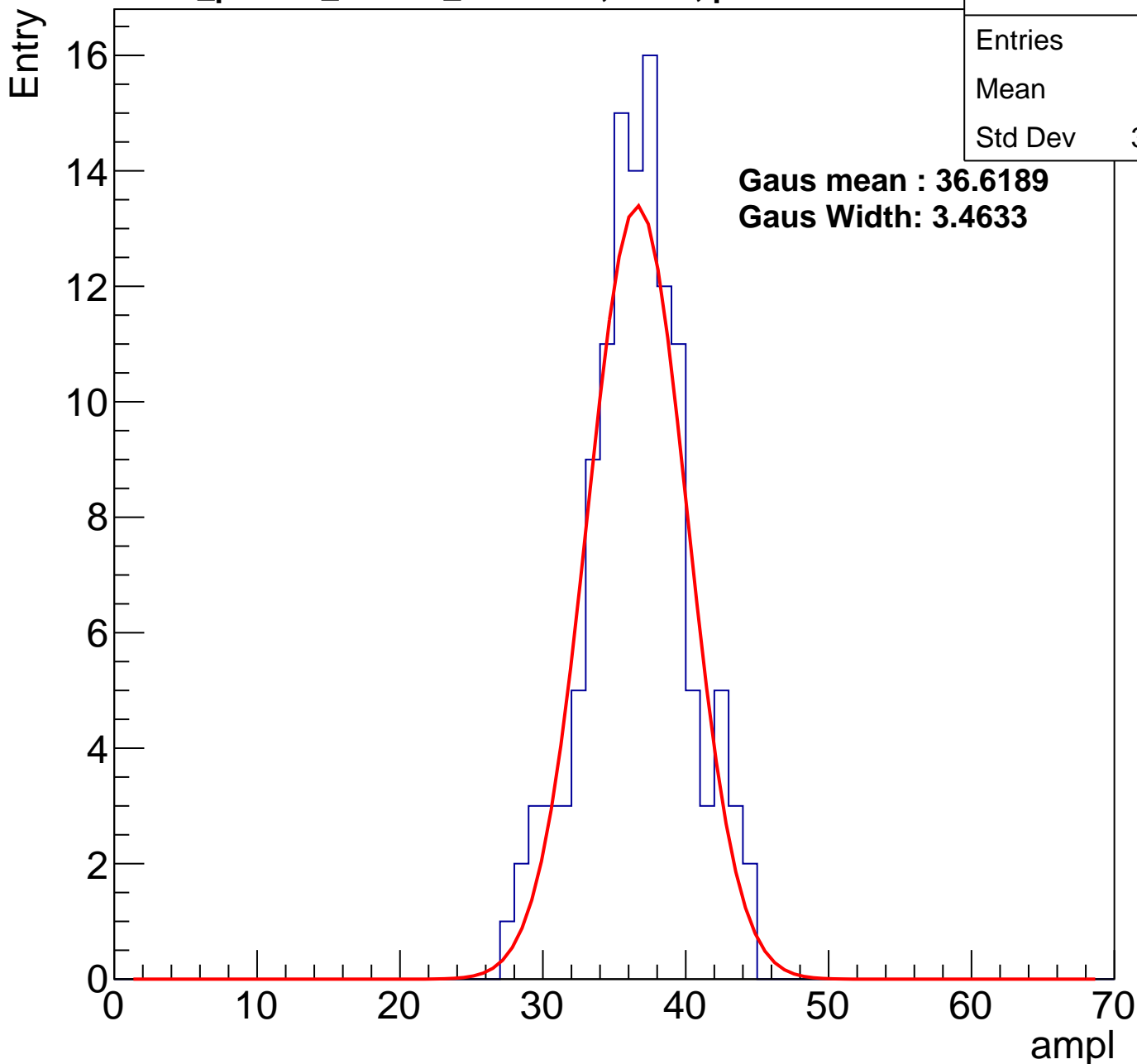
B0L101S, U20-ch52, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.1
Std Dev	3.528

Gaus mean : 36.6189

Gaus Width: 3.4633



B0L101S, U20-ch52, adc2

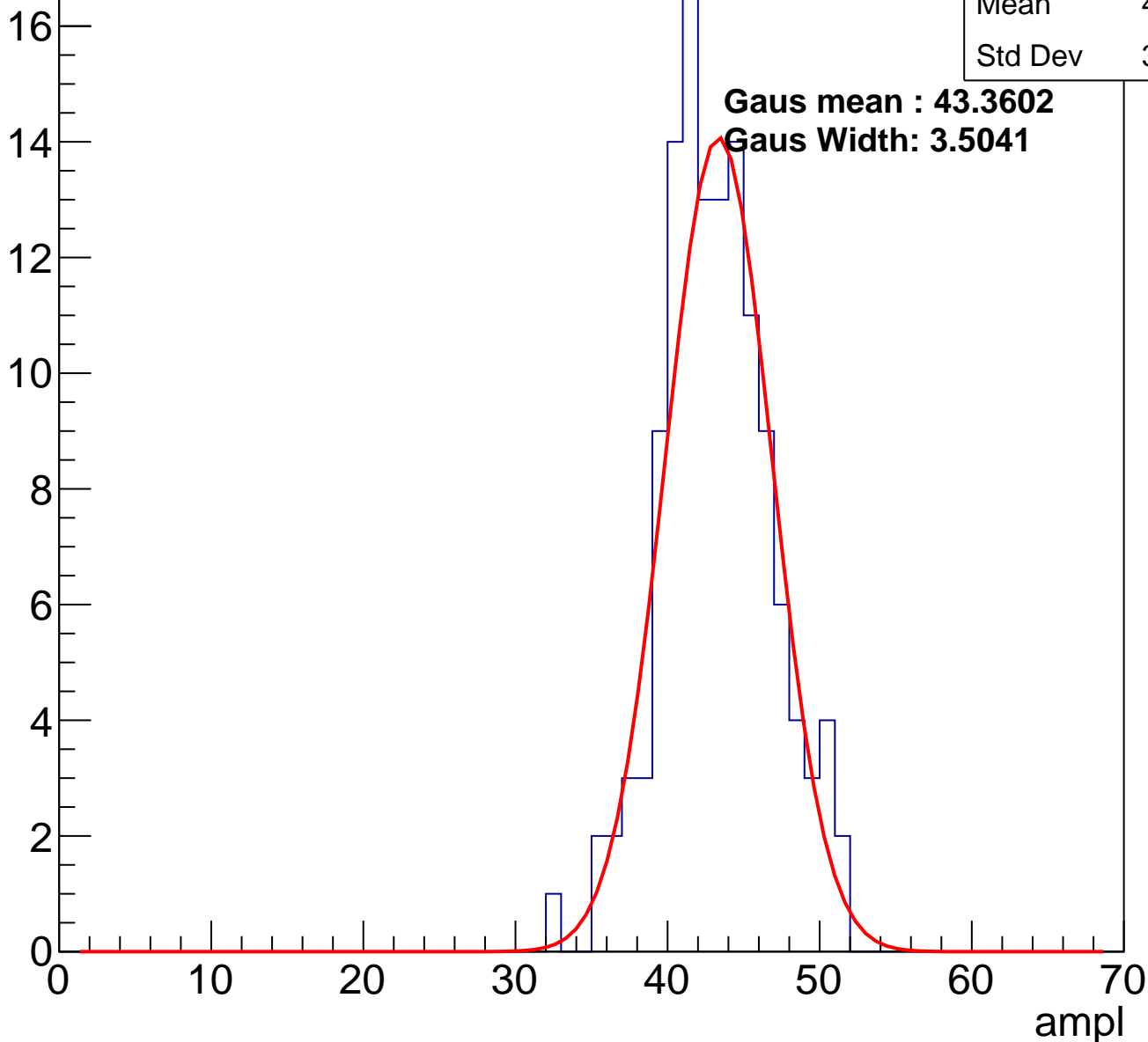
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	130
Mean	42.77
Std Dev	3.553

Gaus mean : 43.3602

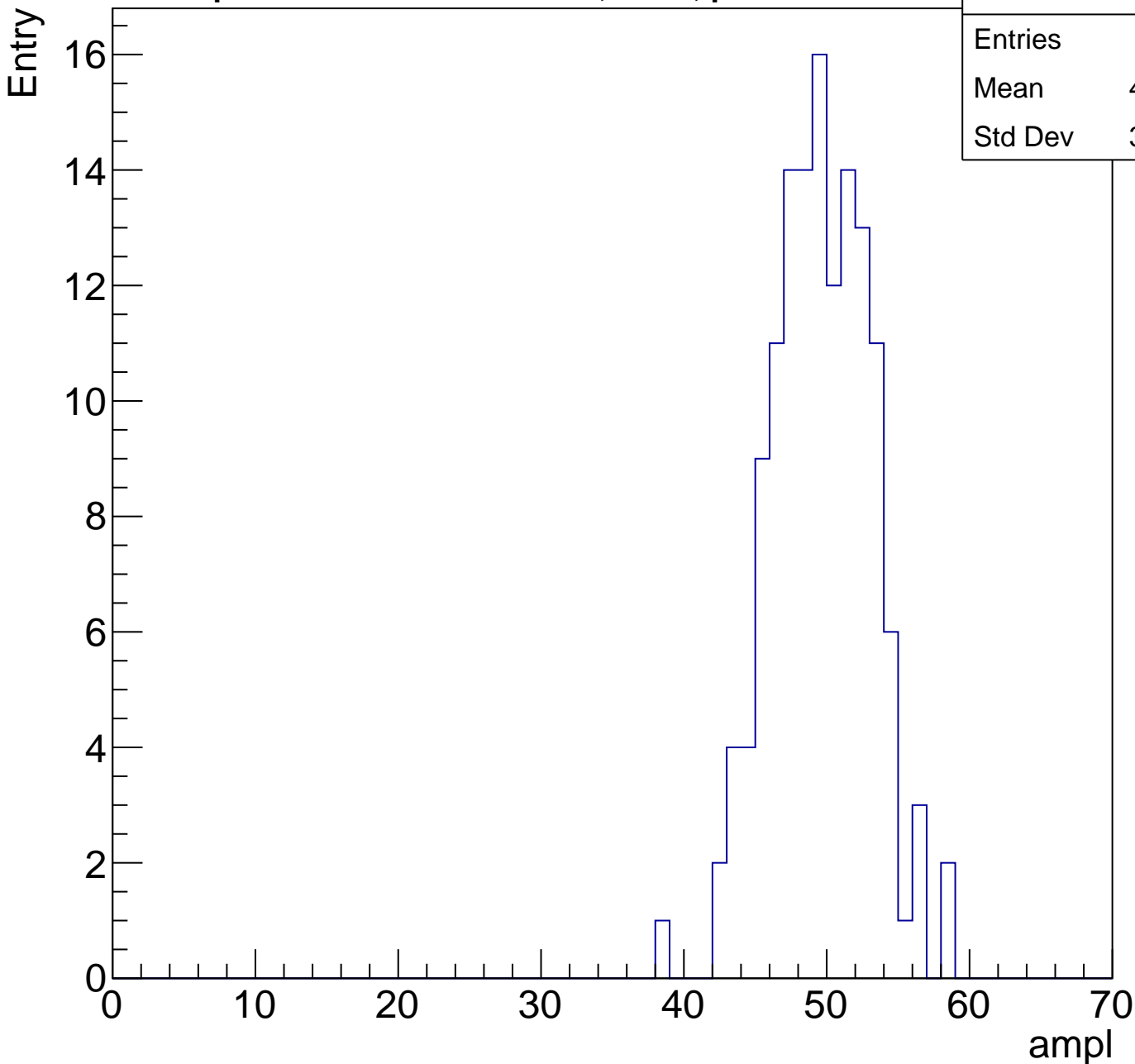
Gaus Width: 3.5041



B0L101S, U20-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

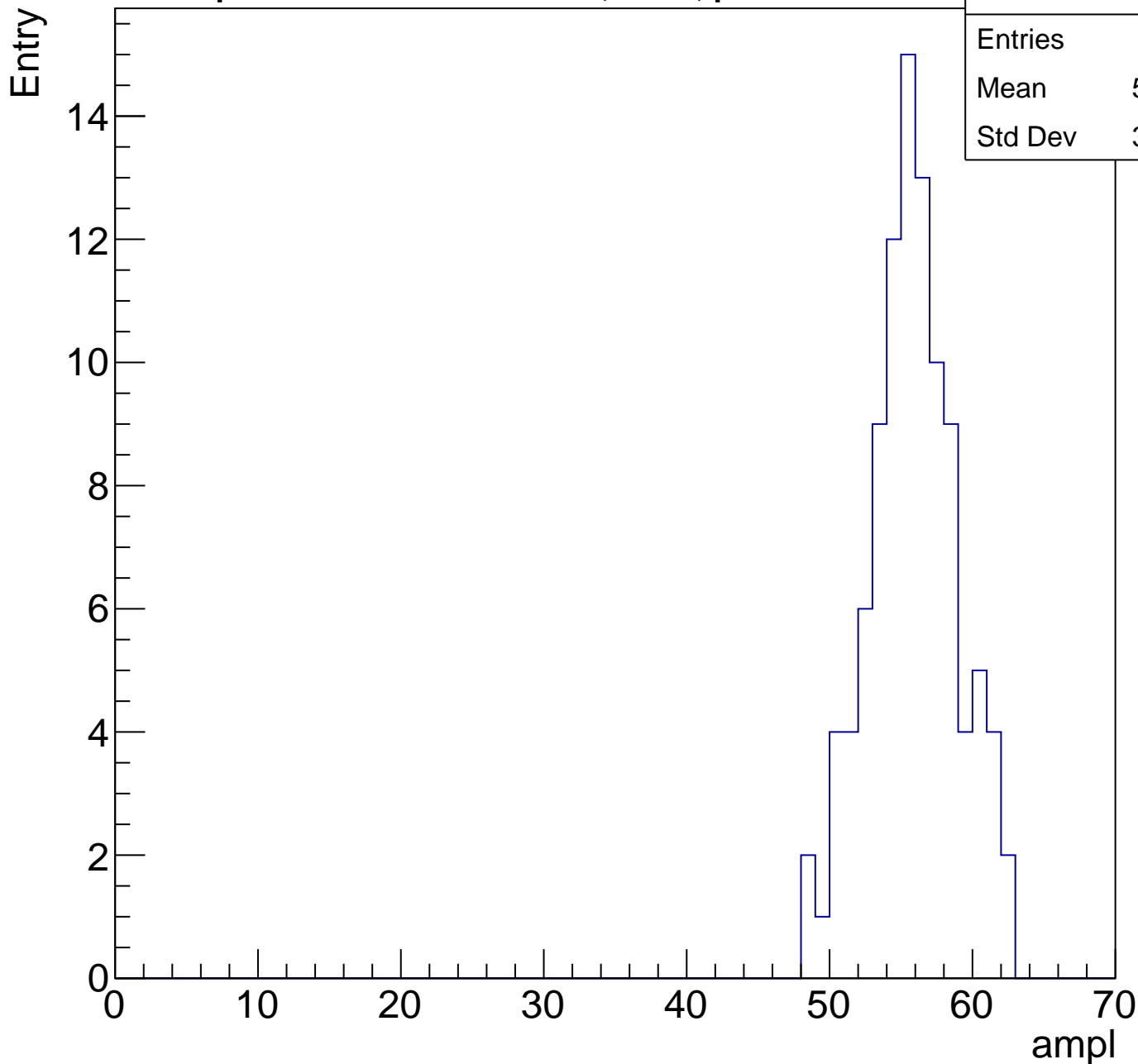
Entries	137
Mean	49.13
Std Dev	3.449



B0L101S, U20-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

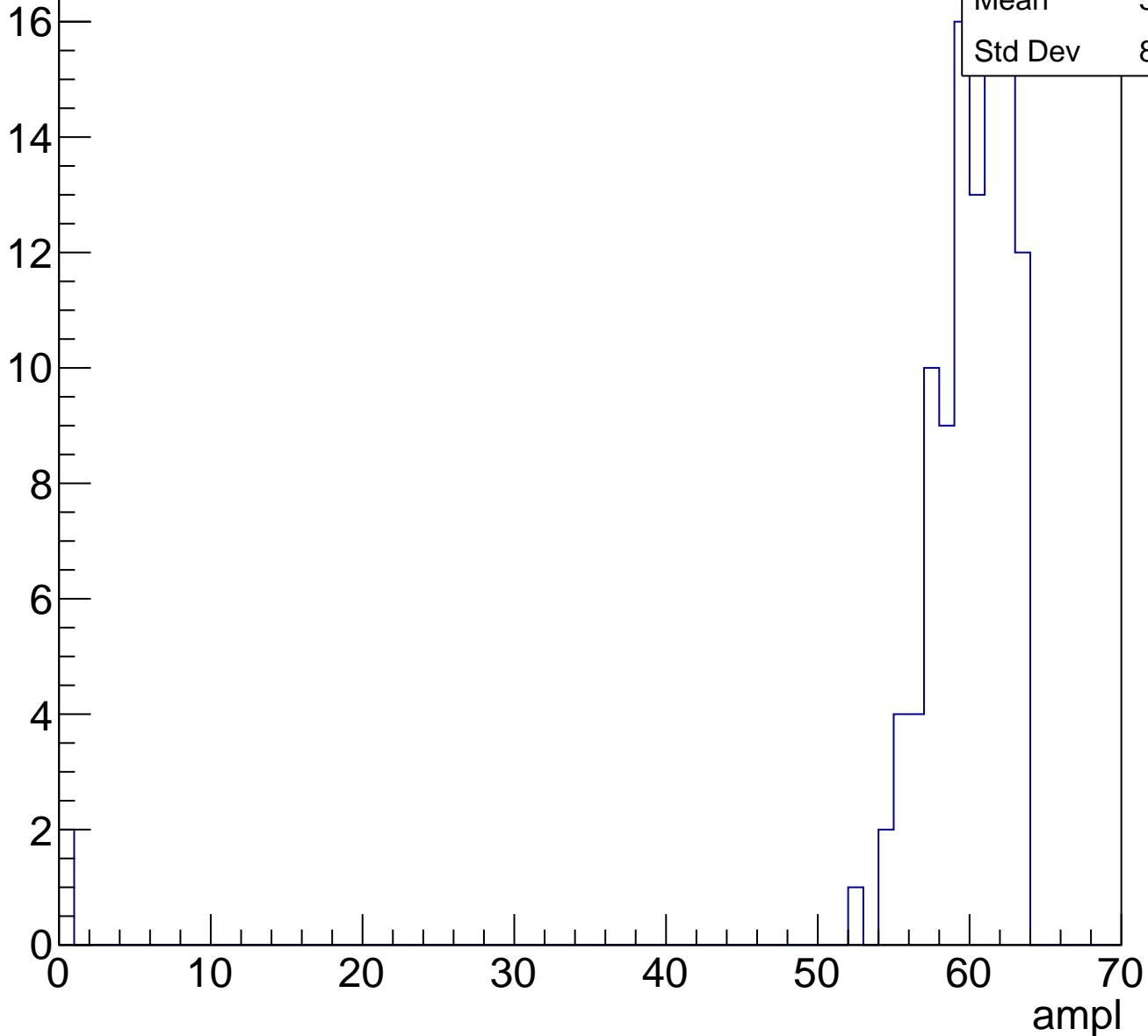
Entries	100
Mean	55.35
Std Dev	3.087



B0L101S, U20-ch52, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

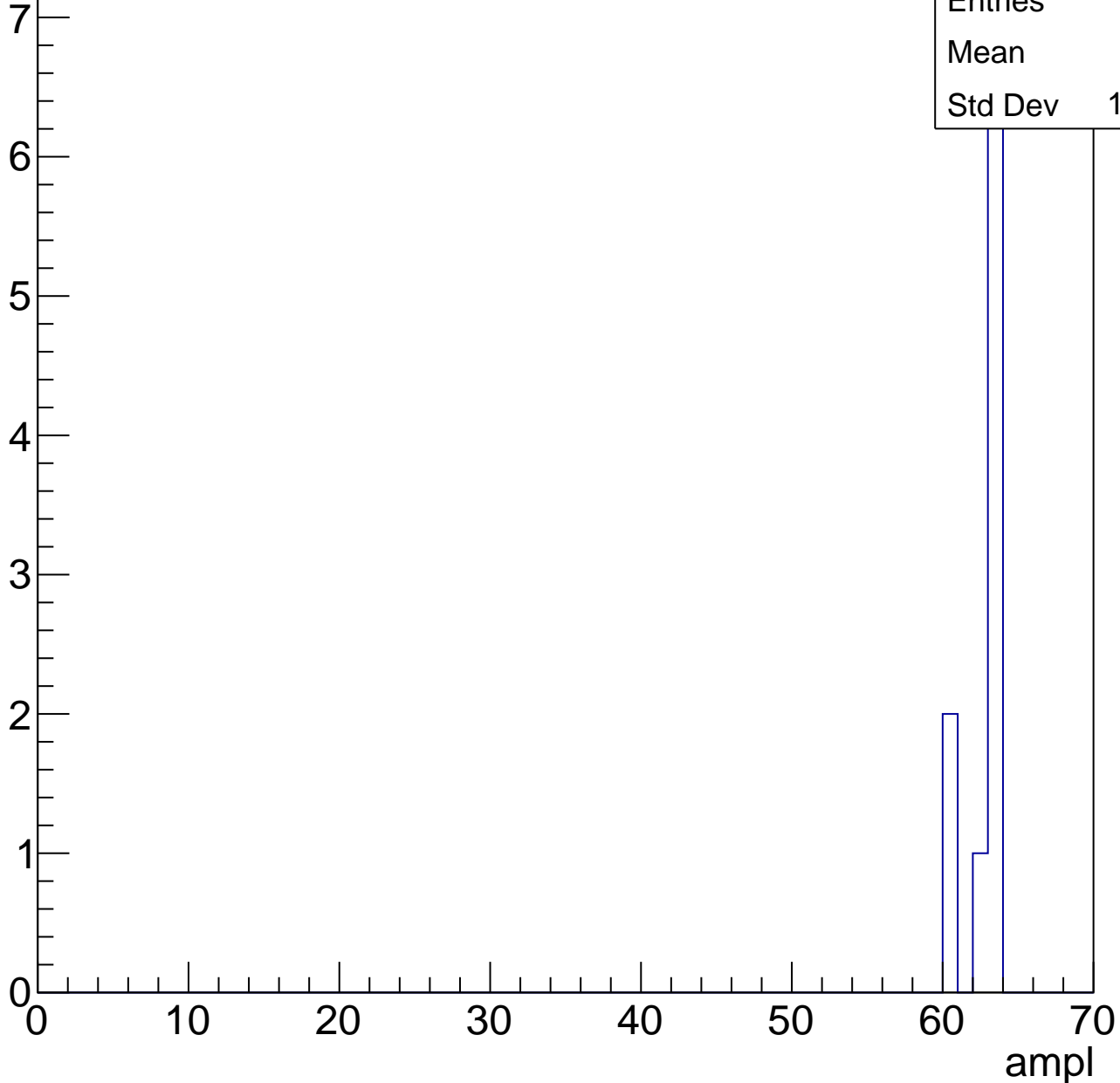


B0L101S, U20-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

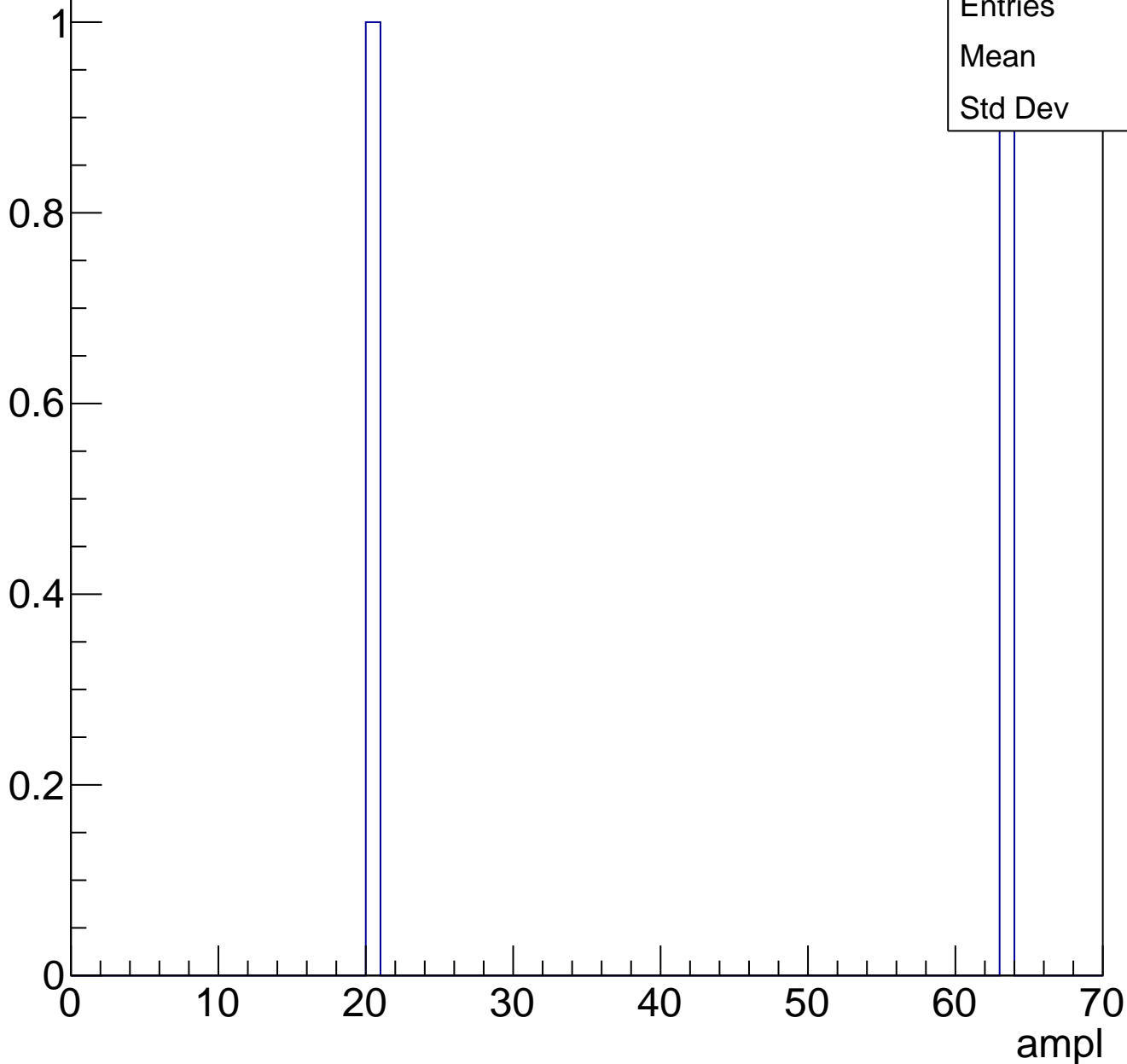
Entries	10
Mean	62.3
Std Dev	1.187



B0L101S, U20-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch53, adc0

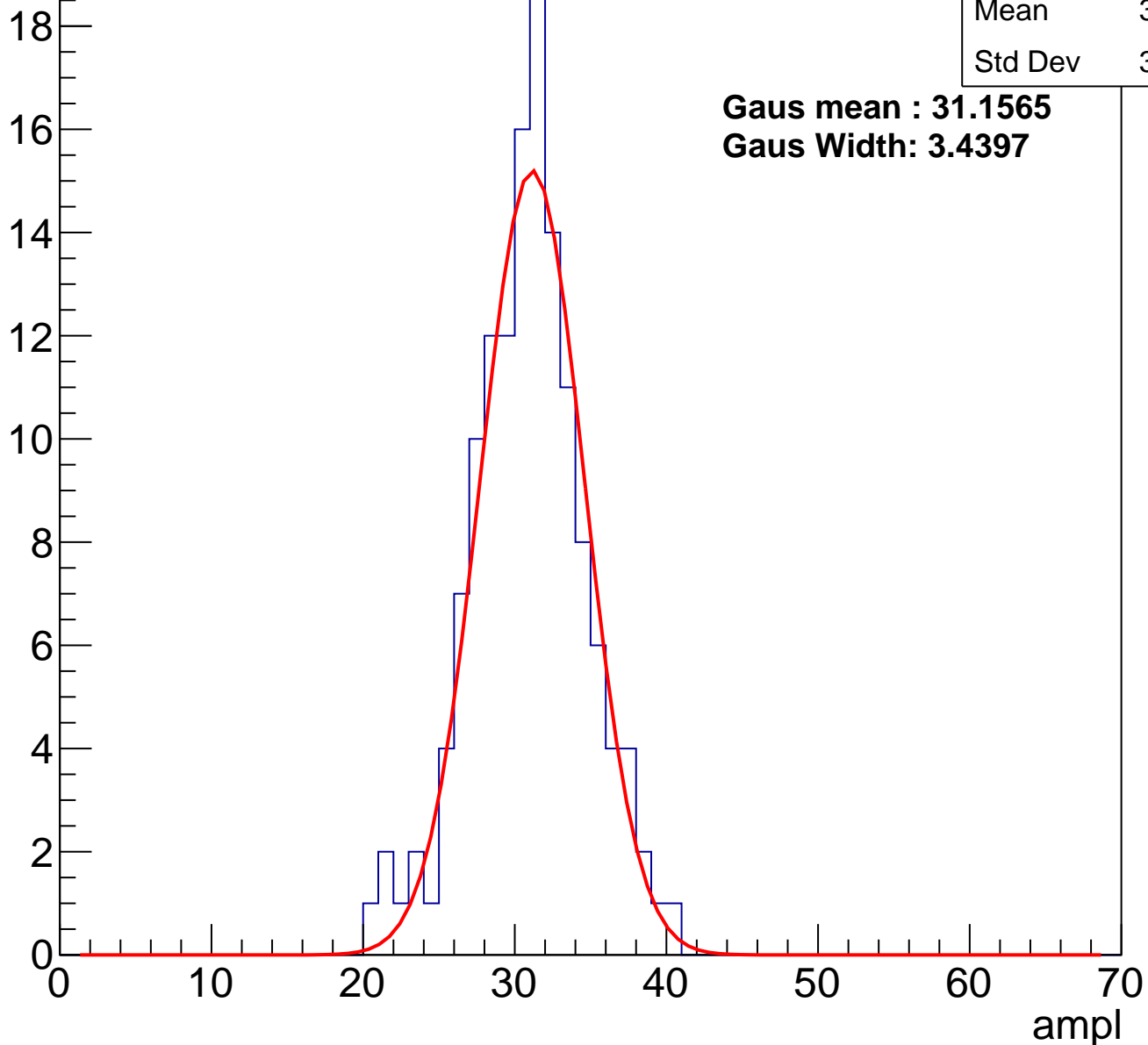
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	30.43
Std Dev	3.708

Gaus mean : 31.1565

Gaus Width: 3.4397

Entry

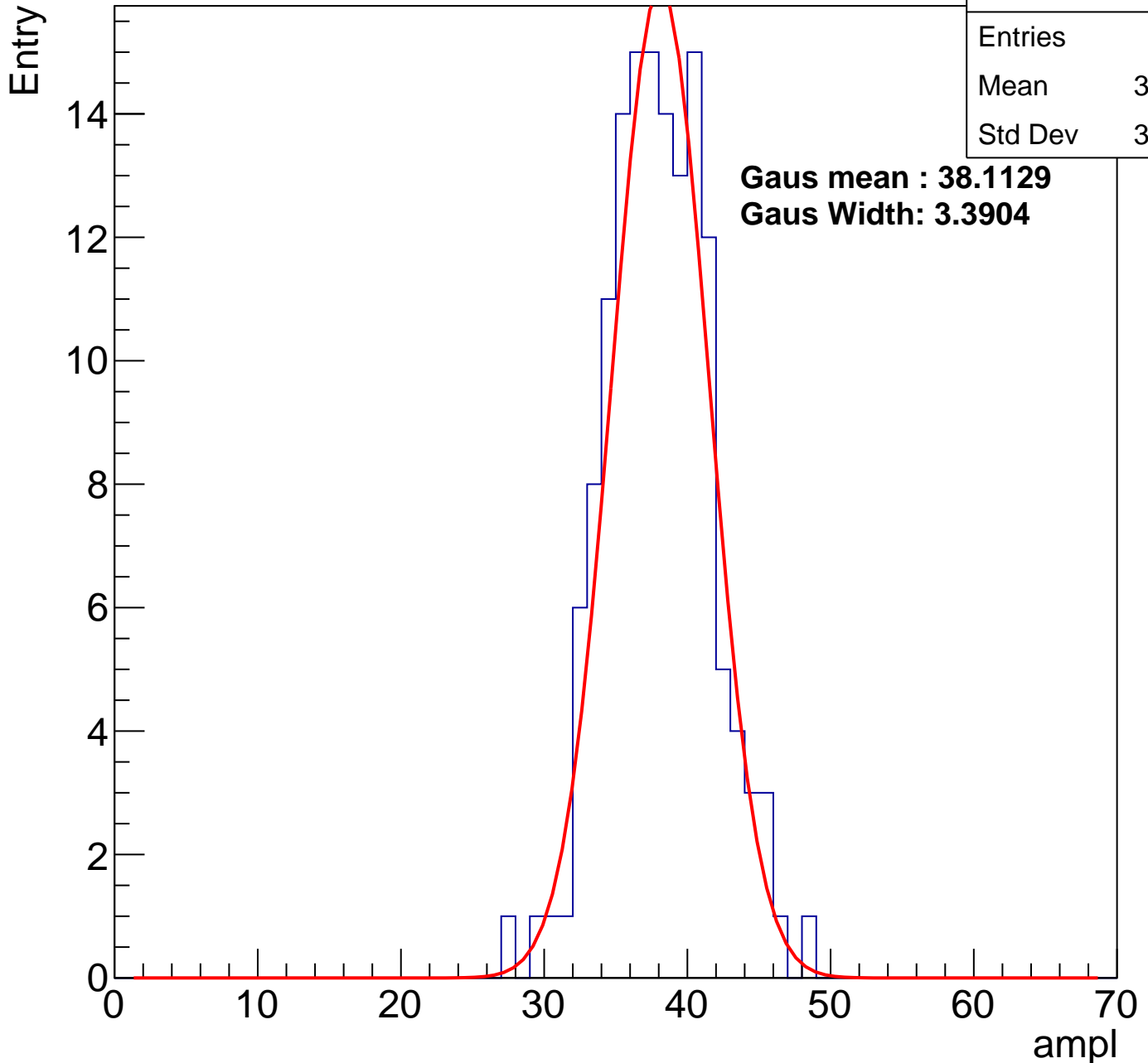


B0L101S, U20-ch53, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	37.54
Std Dev	3.592

Gaus mean : 38.1129
Gaus Width: 3.3904



B0L101S, U20-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

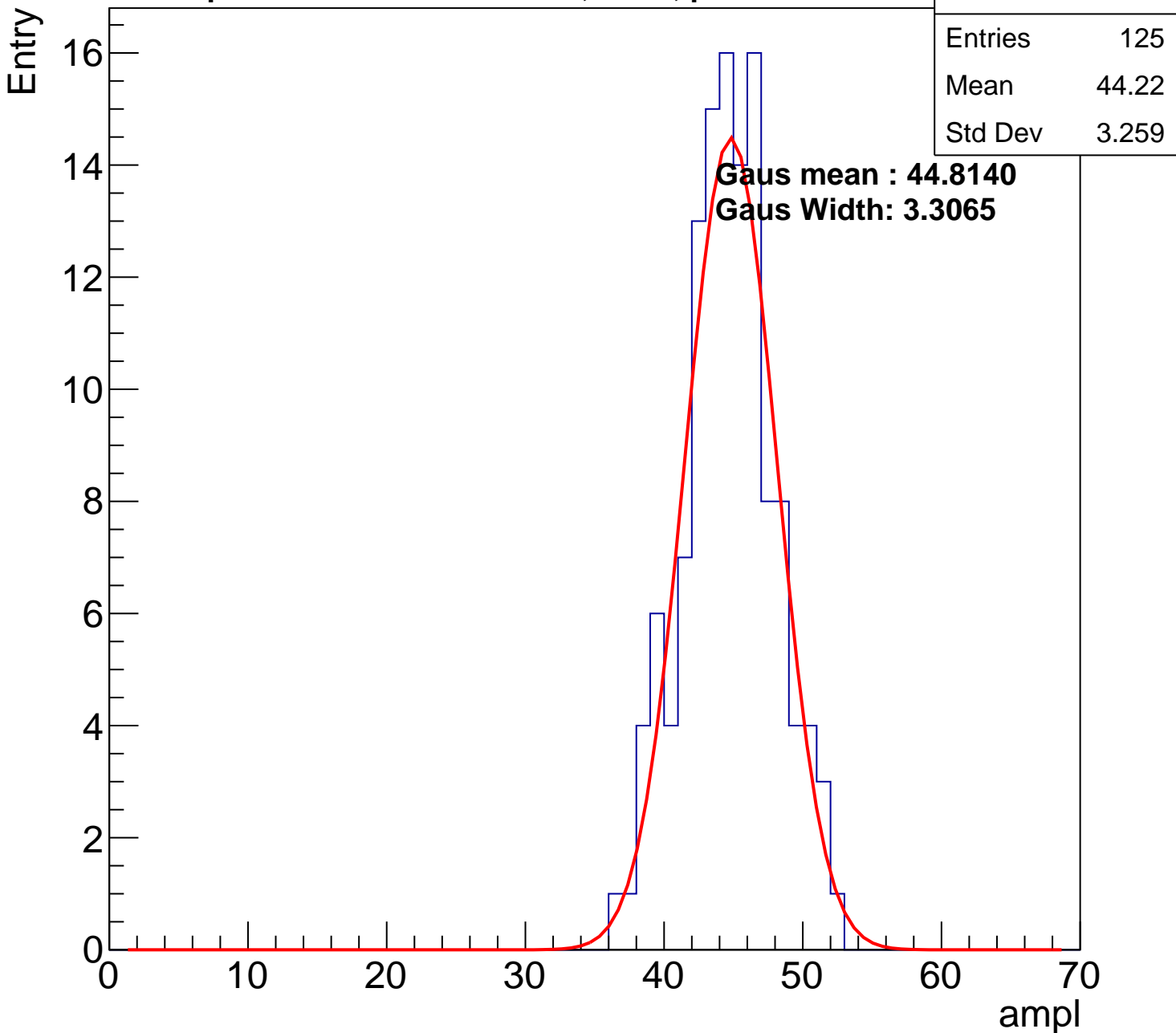
Entries	125
Mean	44.22
Std Dev	3.259

Gaus mean : 44.8140

Gaus Width: 3.3065

ampl

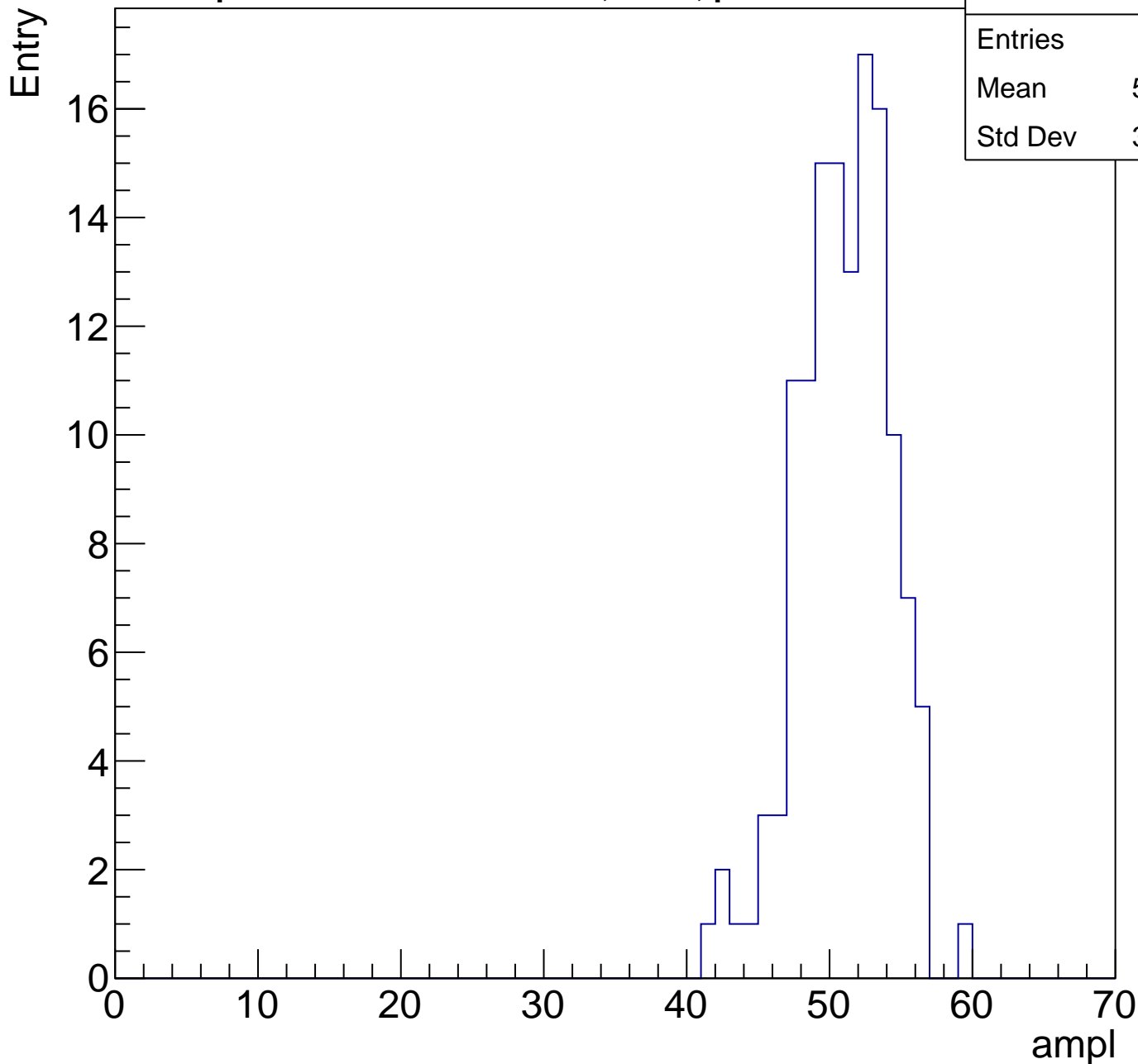
0 10 20 30 40 50 60 70



B0L101S, U20-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	50.56
Std Dev	3.203

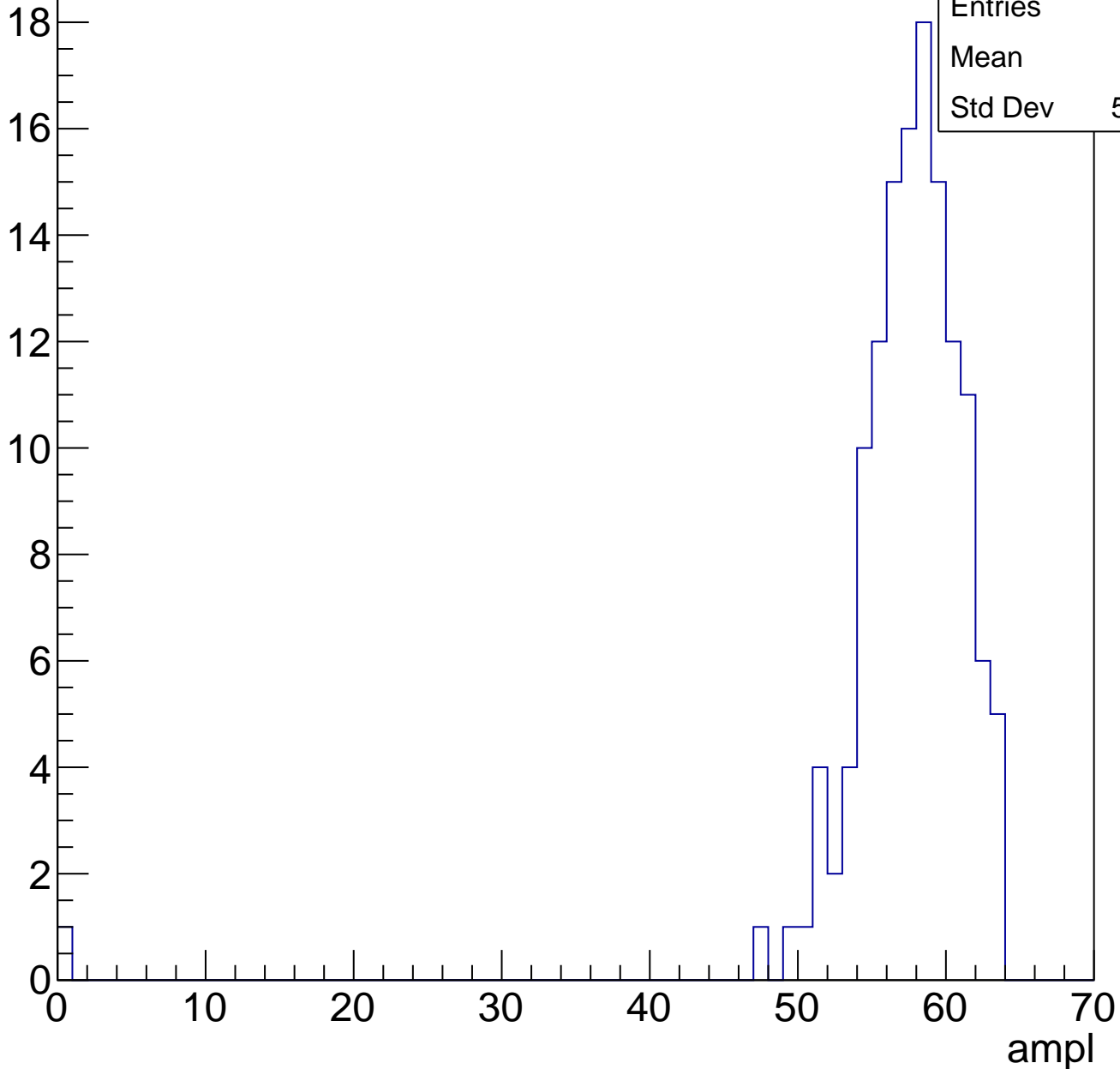


B0L101S, U20-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	56.9
Std Dev	5.836

Entry



B0L101S, U20-ch53, adc5

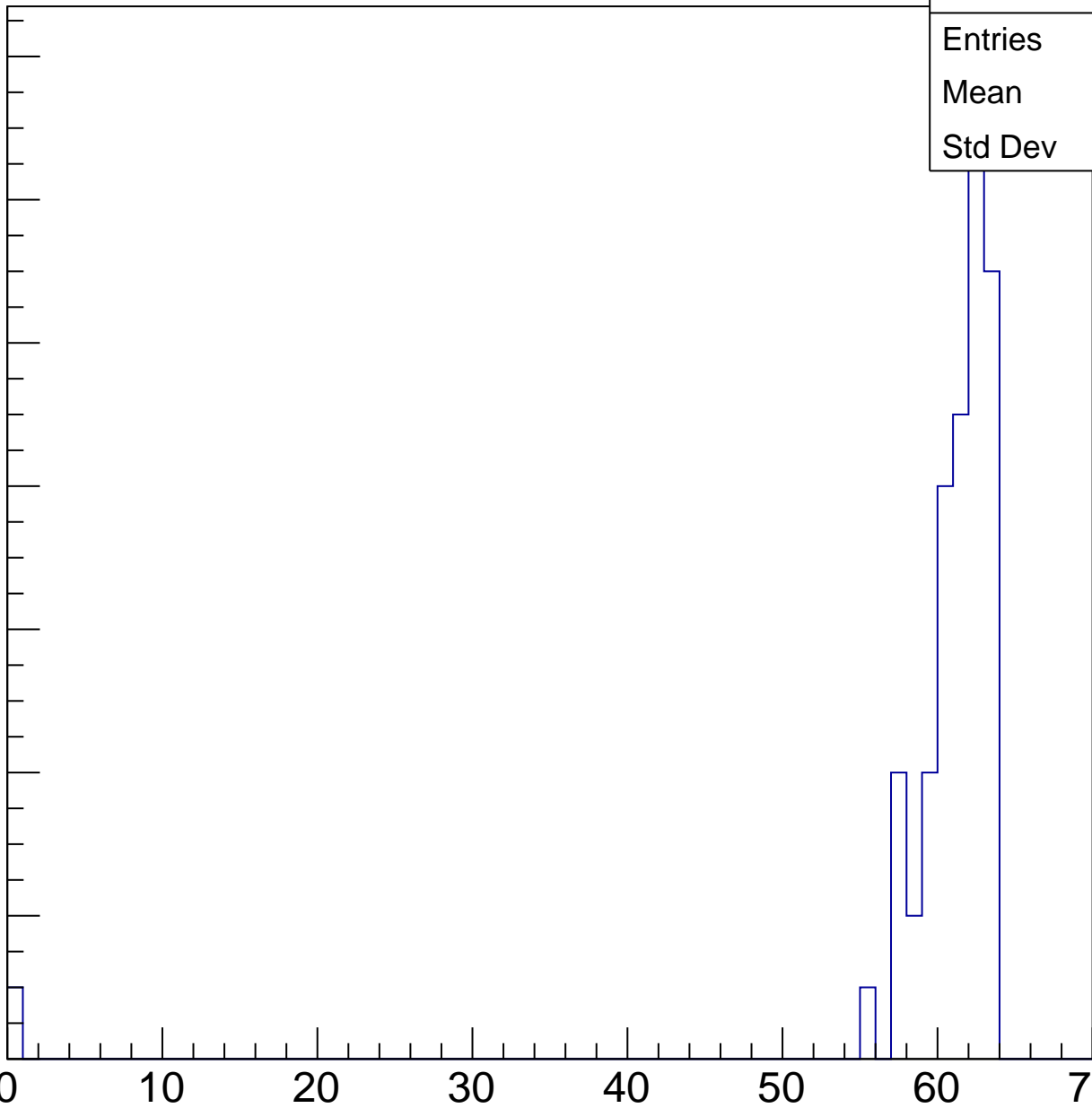
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	54
Mean	59.72
Std Dev	8.425

ampl



B0L101S, U20-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch54, adc0

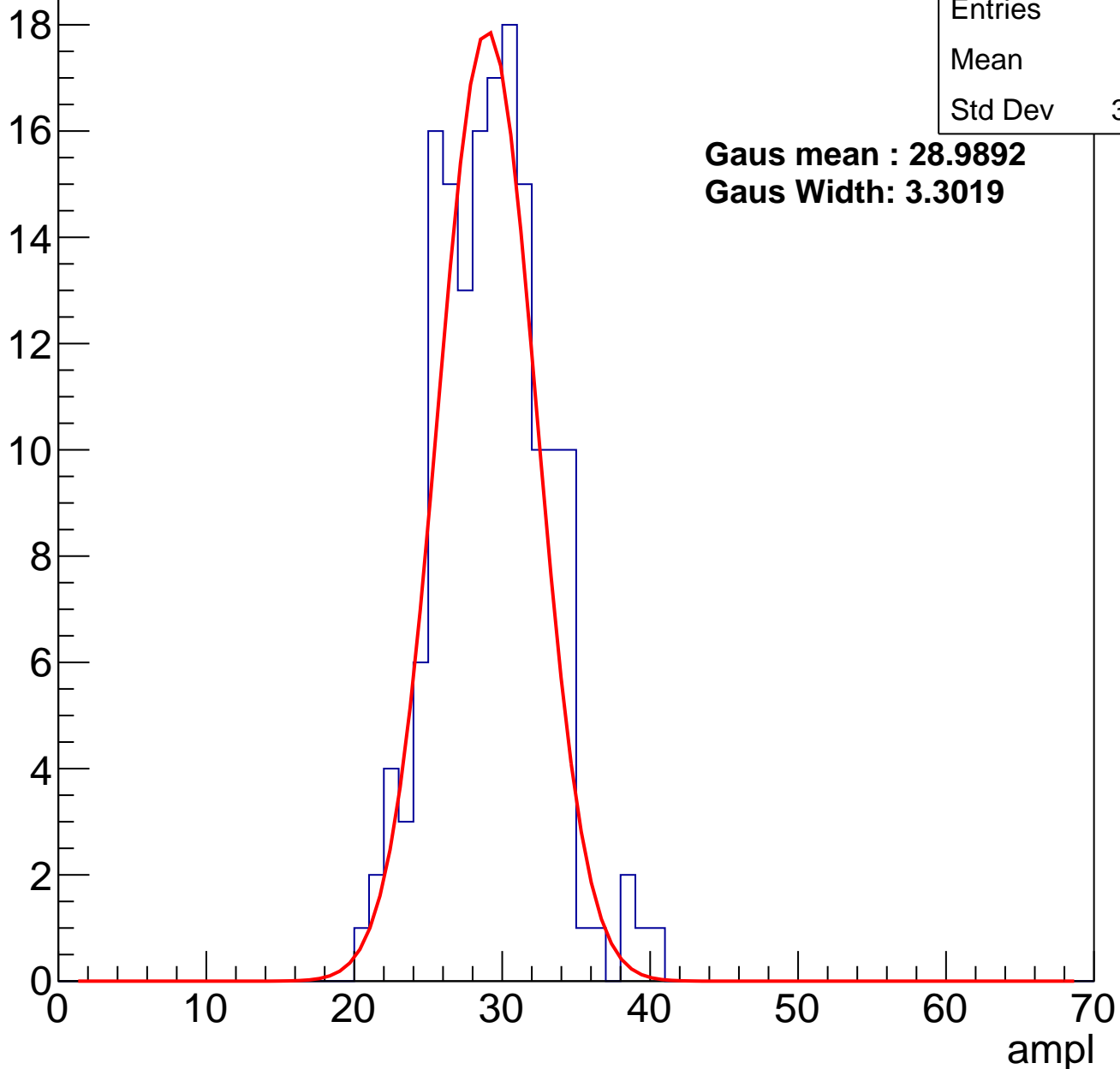
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	28.8
Std Dev	3.653

Gaus mean : 28.9892

Gaus Width: 3.3019

Entry



B0L101S, U20-ch54, adc1

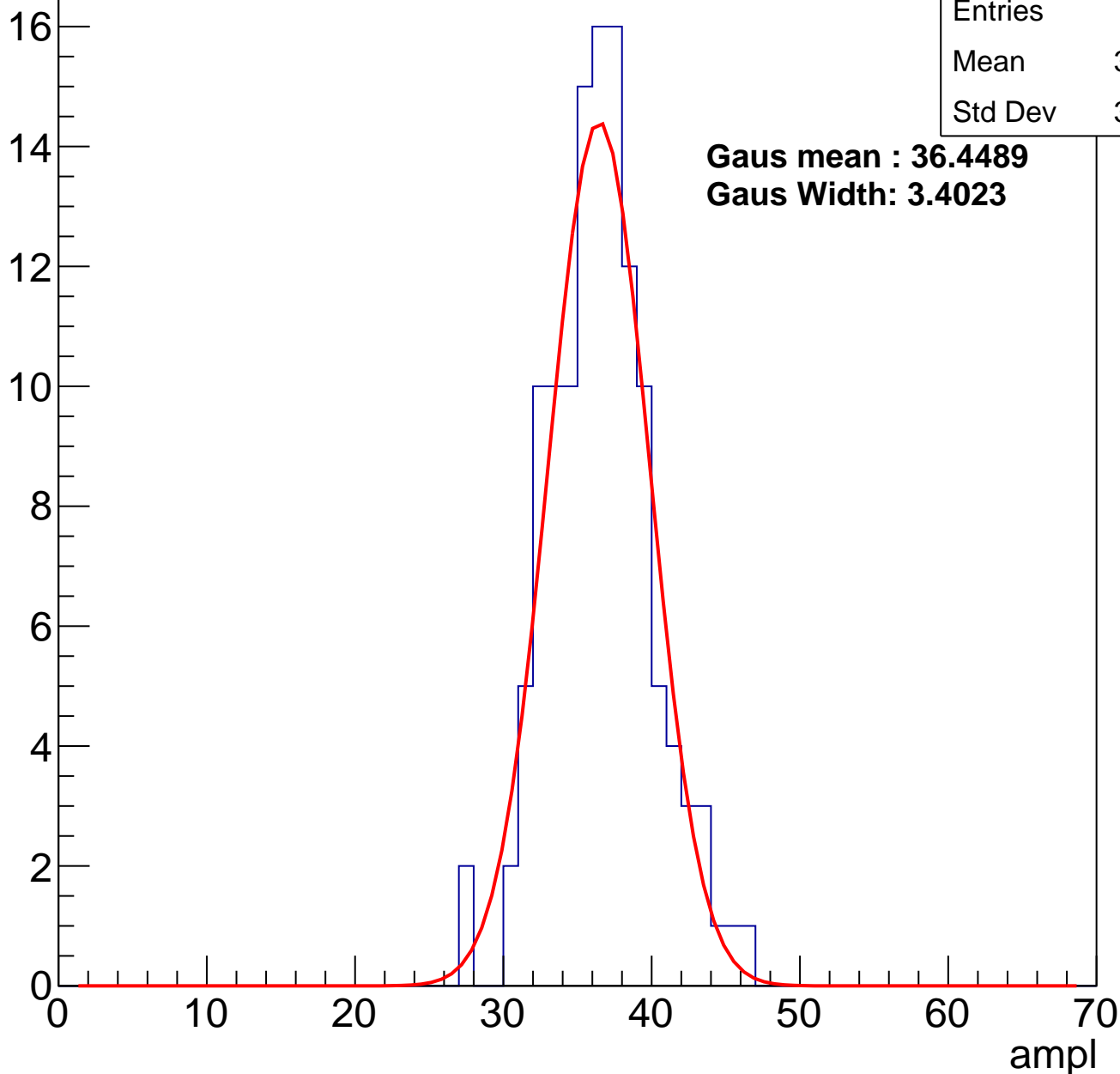
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	36.13
Std Dev	3.446

Gaus mean : 36.4489

Gaus Width: 3.4023

Entry



B0L101S, U20-ch54, adc2

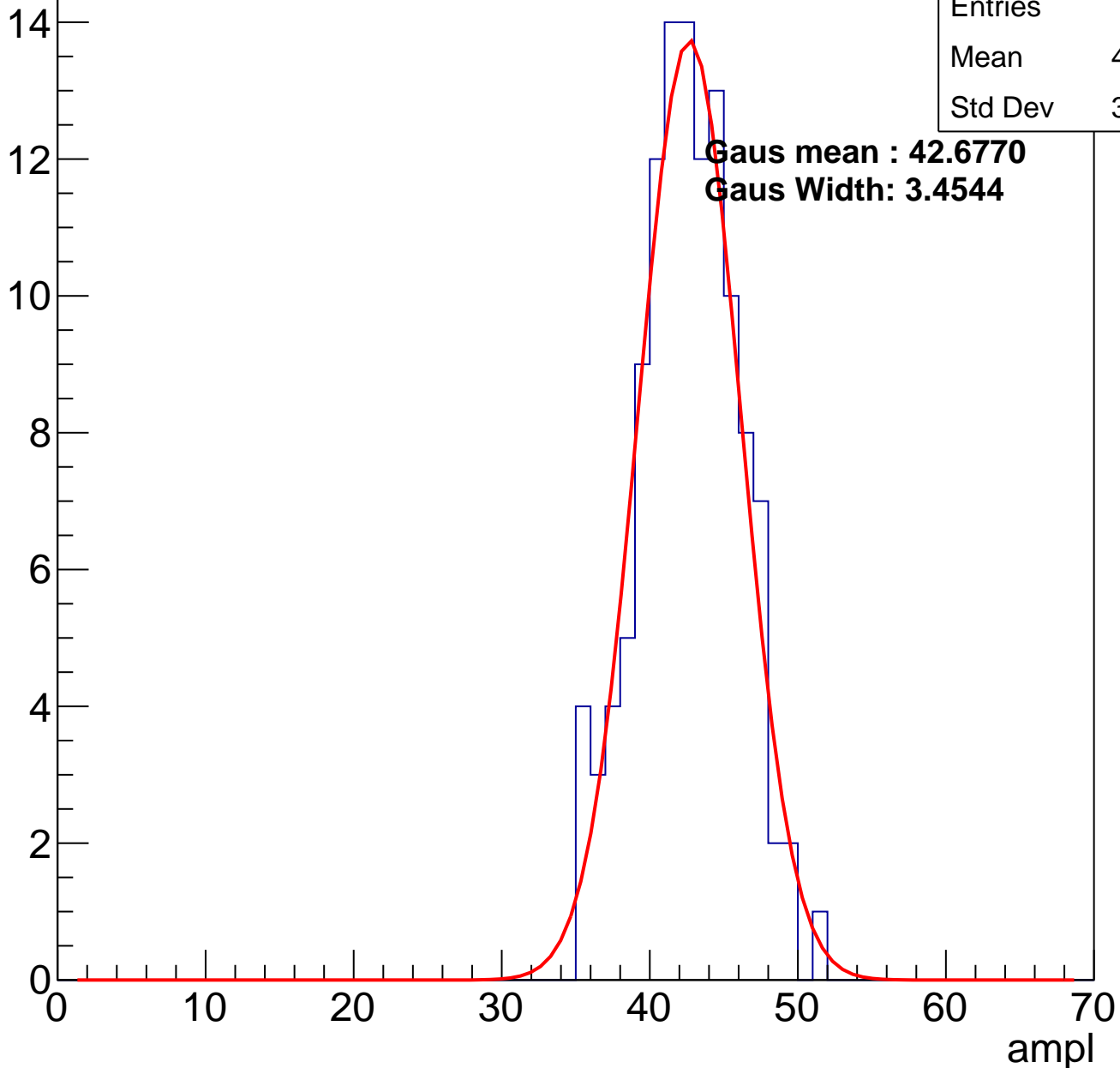
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	42.16
Std Dev	3.332

Gaus mean : 42.6770

Gaus Width: 3.4544

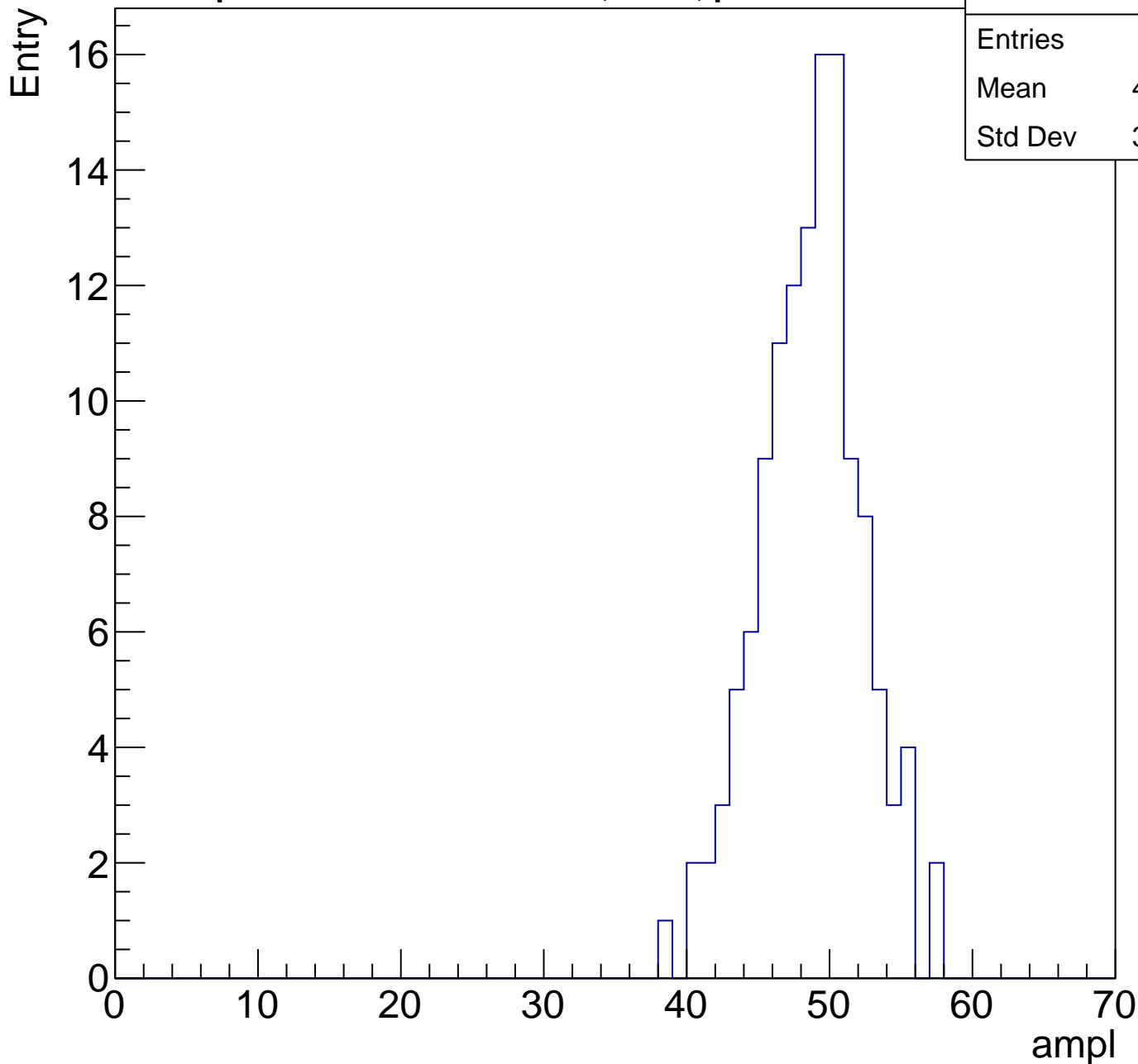
Entry



B0L101S, U20-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	48.22
Std Dev	3.622



B0L101S, U20-ch54, adc4

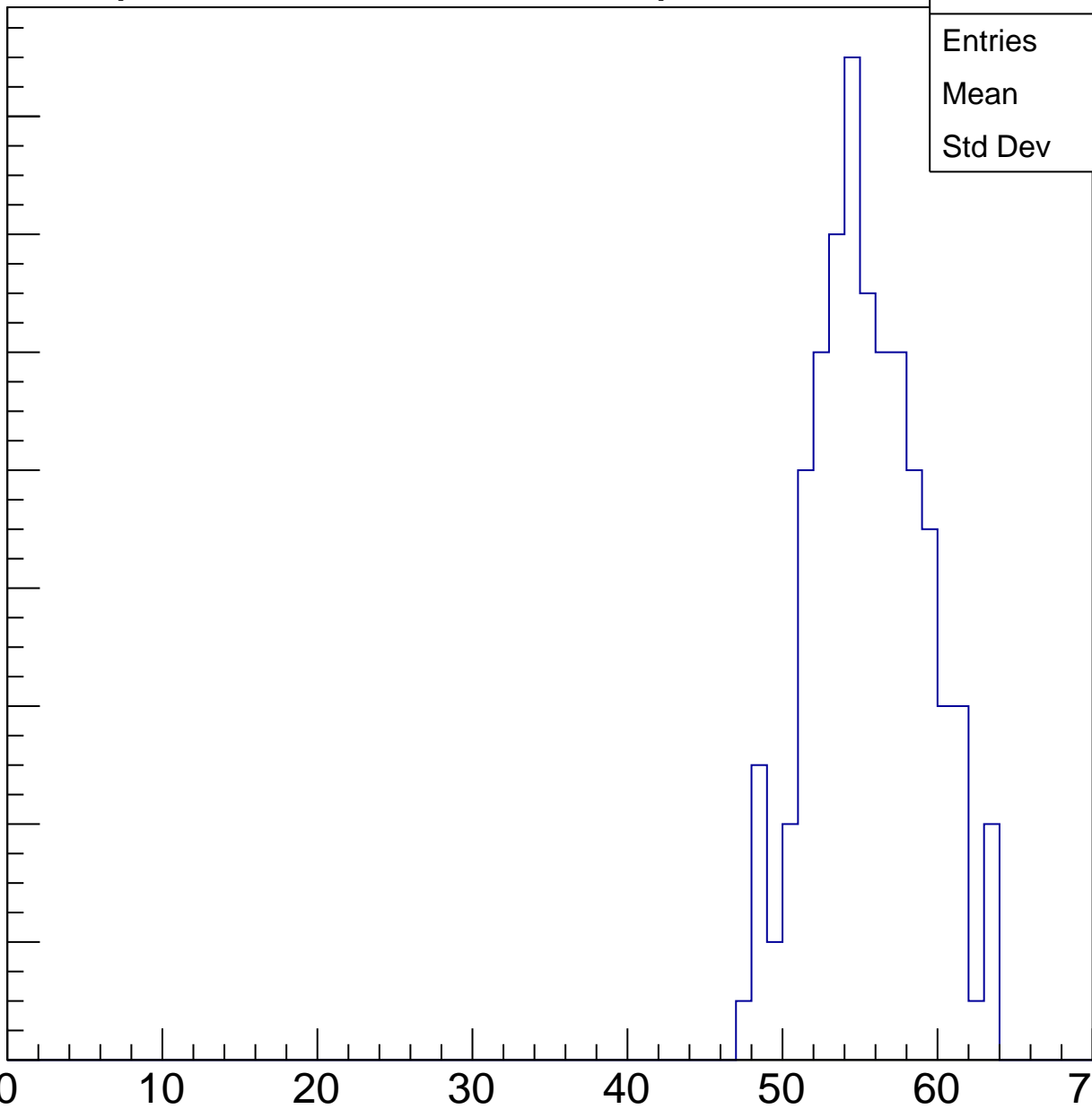
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	138
Mean	55.08
Std Dev	3.598

ampl



B0L101S, U20-ch54, adc5

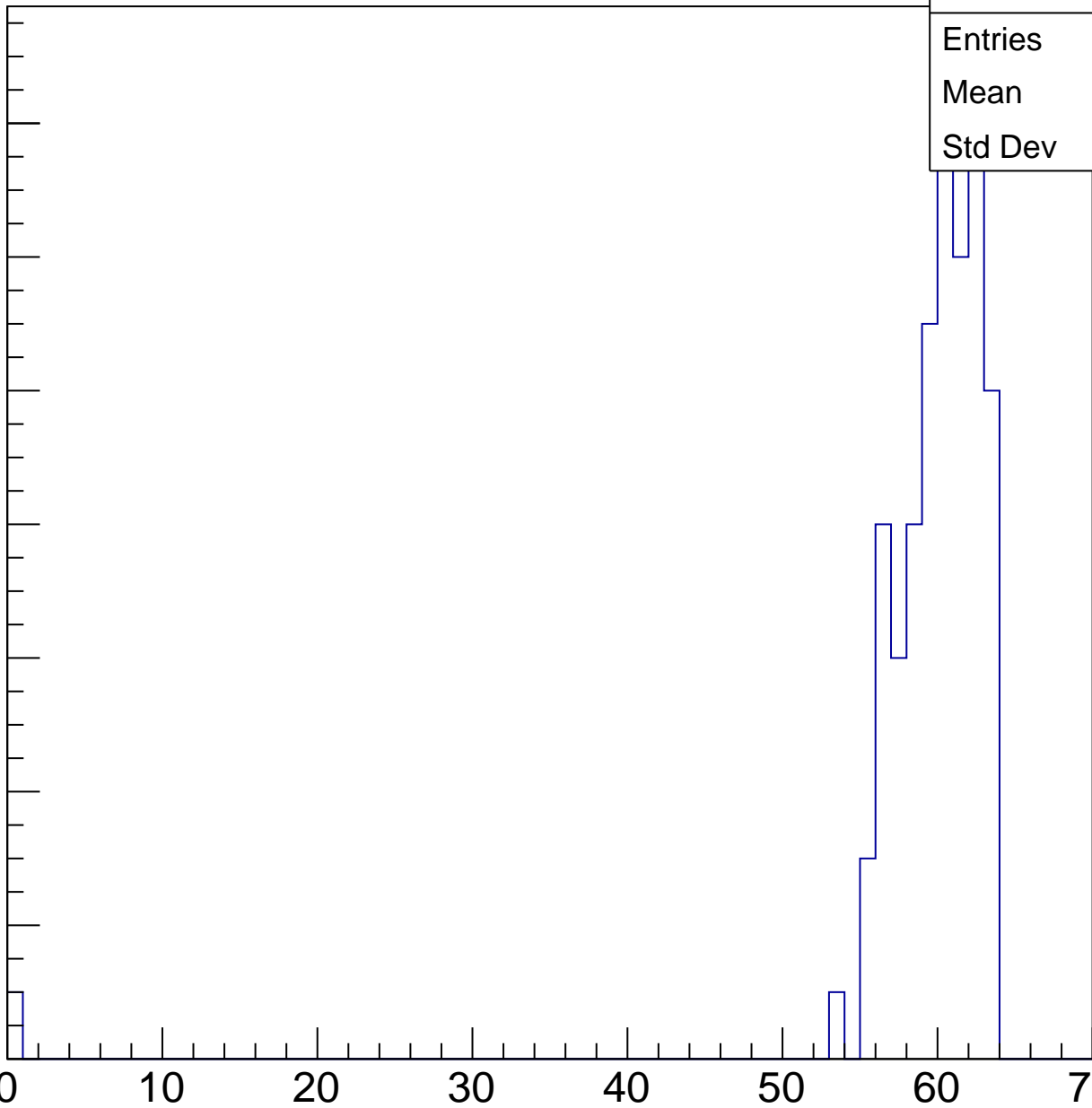
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	59.02
Std Dev	6.726

ampl

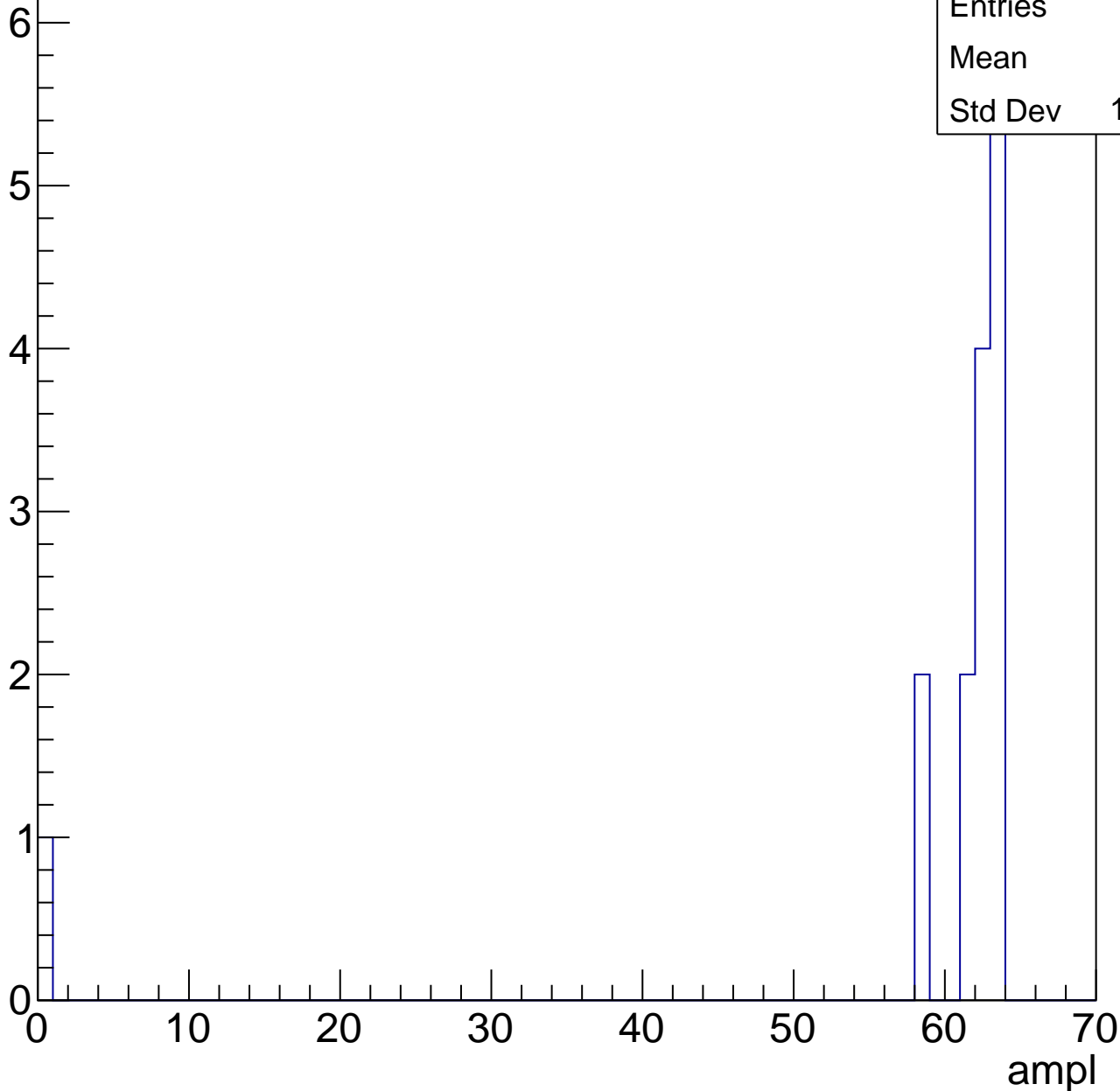


B0L101S, U20-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.6
Std Dev	15.48



B0L101S, U20-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch55, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	31
Std Dev	4.512

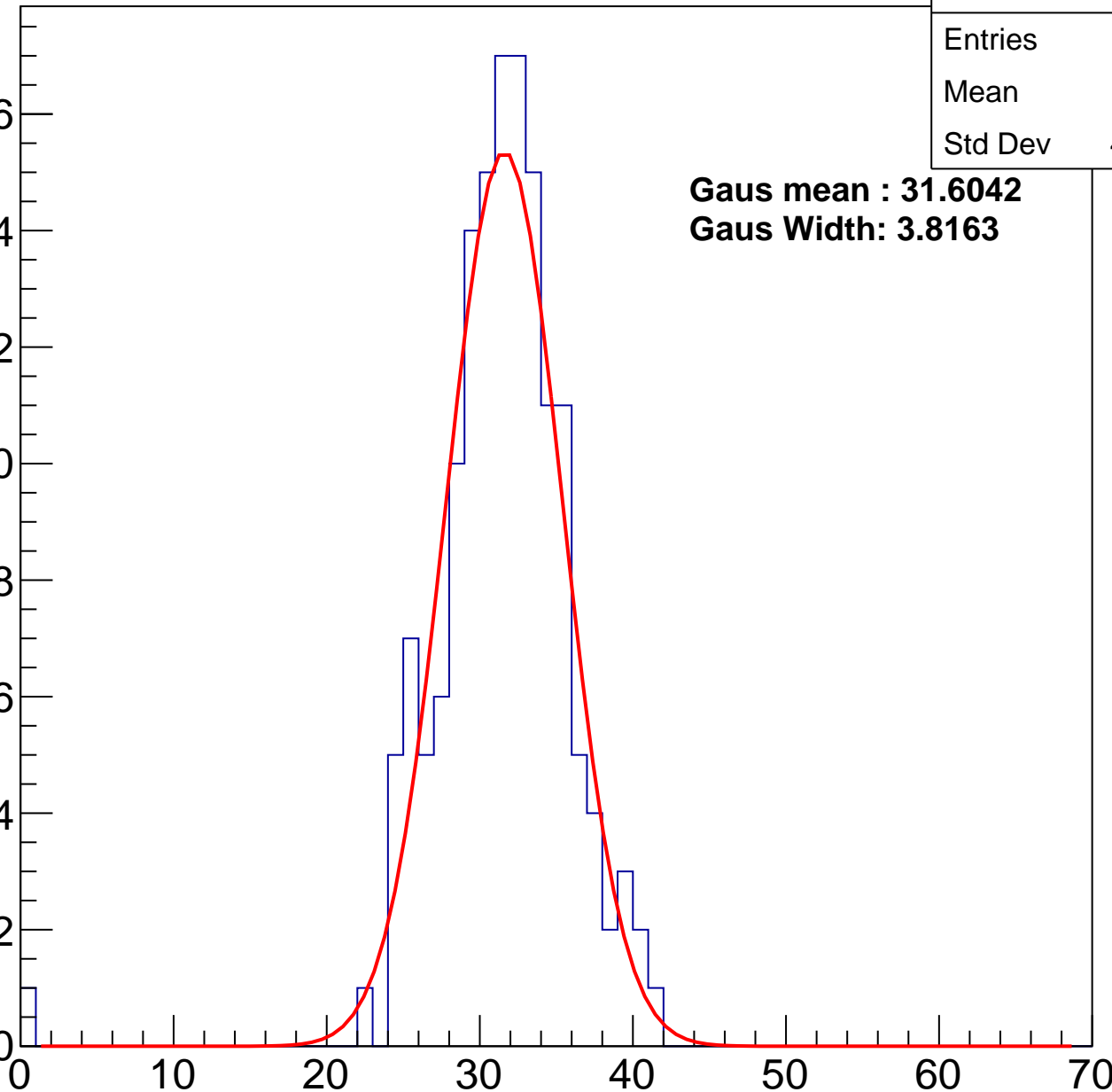
Gaus mean : 31.6042

Gaus Width: 3.8163

Entry

16
14
12
10
8
6
4
2
0

ampl



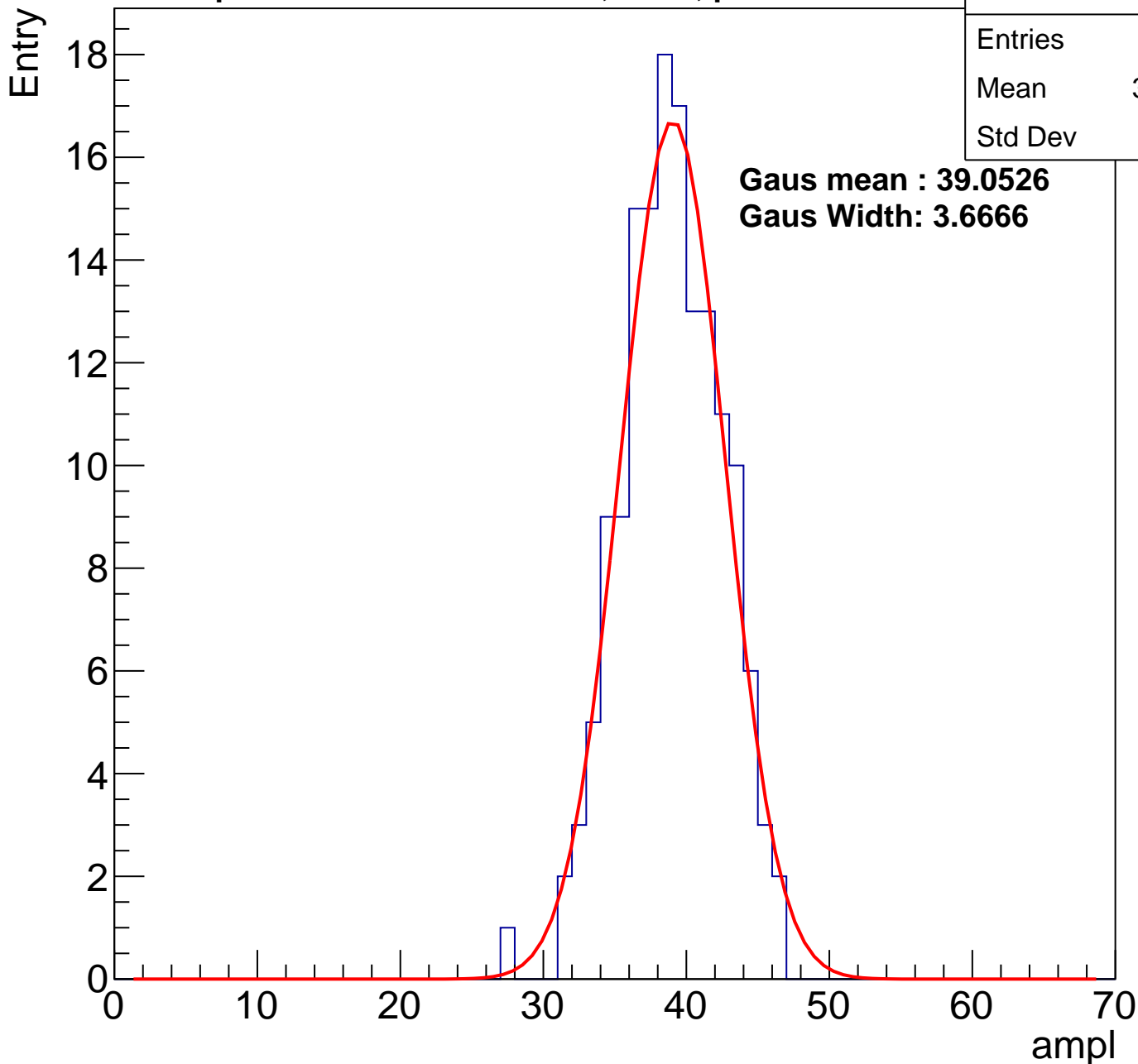
B0L101S, U20-ch55, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	38.48
Std Dev	3.46

Gaus mean : 39.0526

Gaus Width: 3.6666



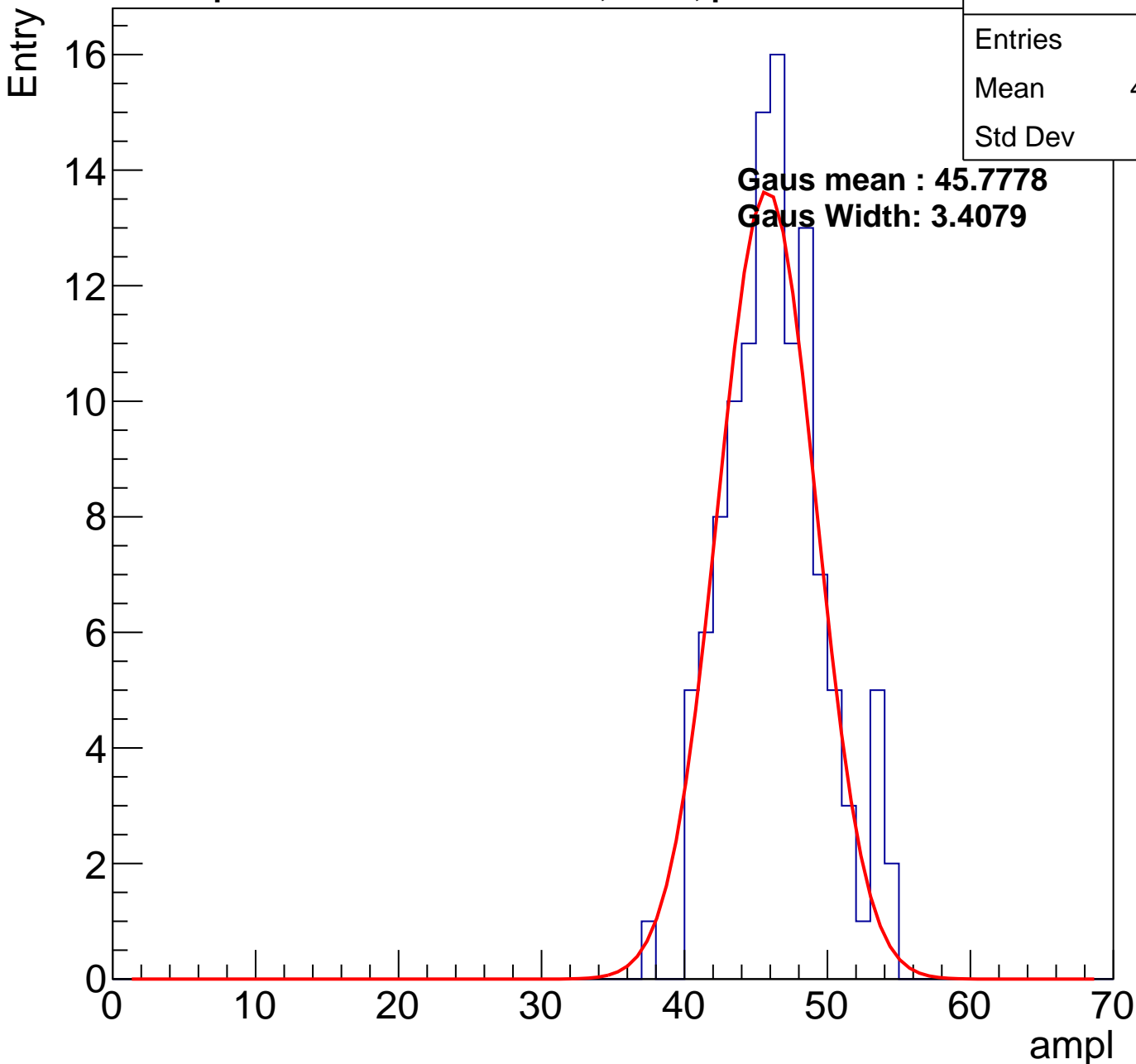
B0L101S, U20-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	45.85
Std Dev	3.4

Gaus mean : 45.7778

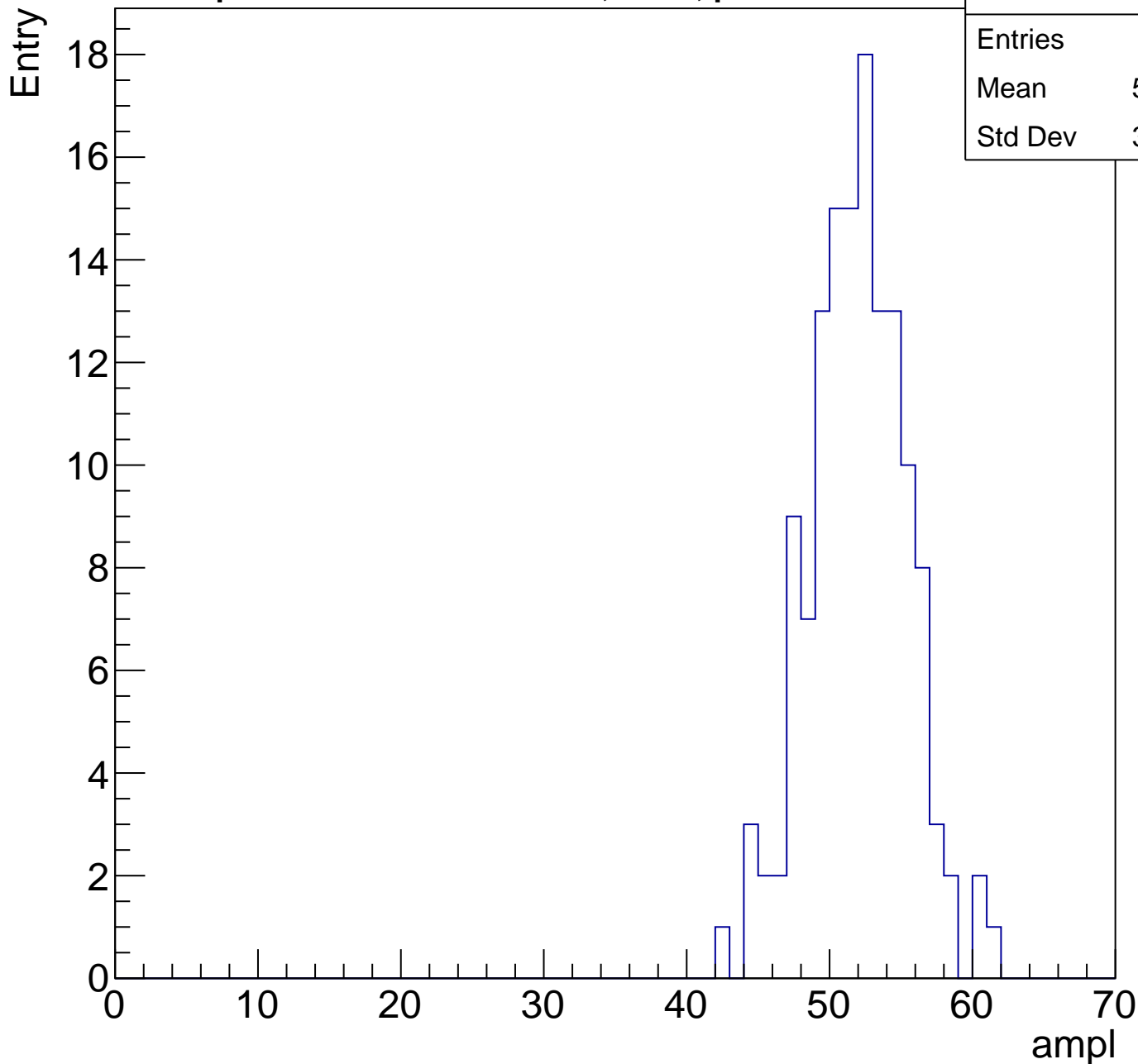
Gaus Width: 3.4079



B0L101S, U20-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

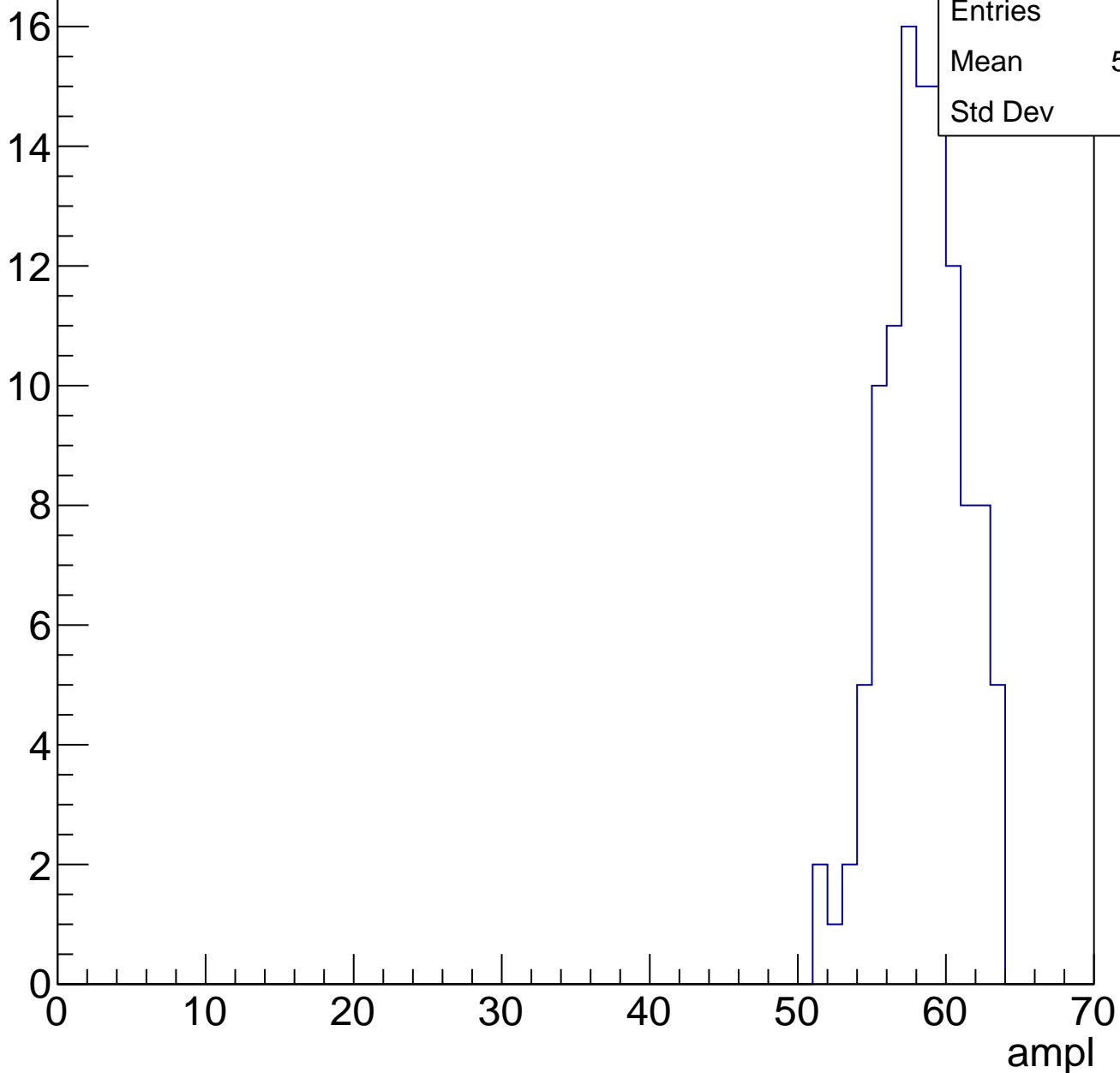
Entries	137
Mean	51.53
Std Dev	3.418



B0L101S, U20-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch55, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

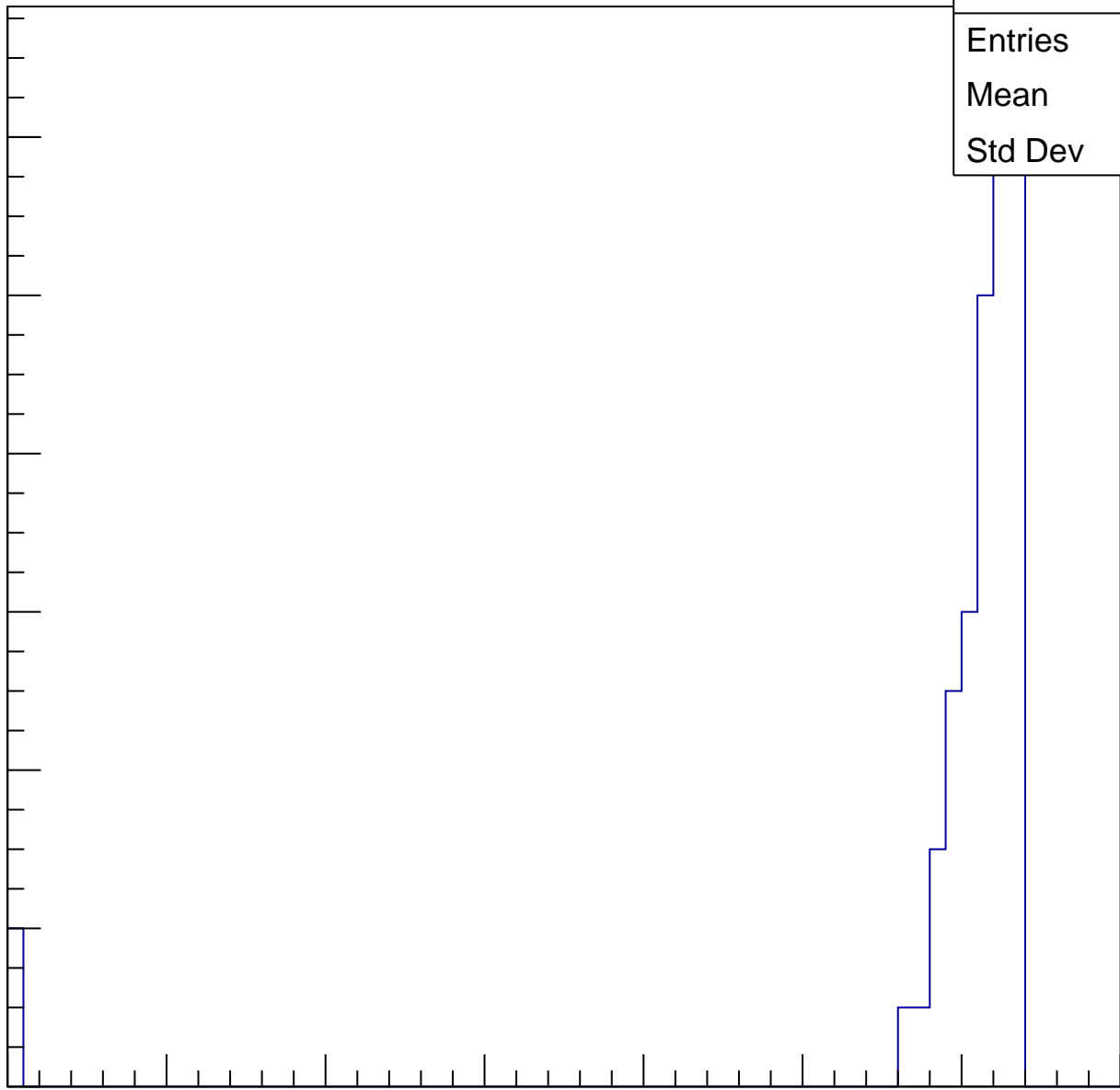
2

0

Entries	53
Mean	58.77
Std Dev	11.77

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch56, adc0

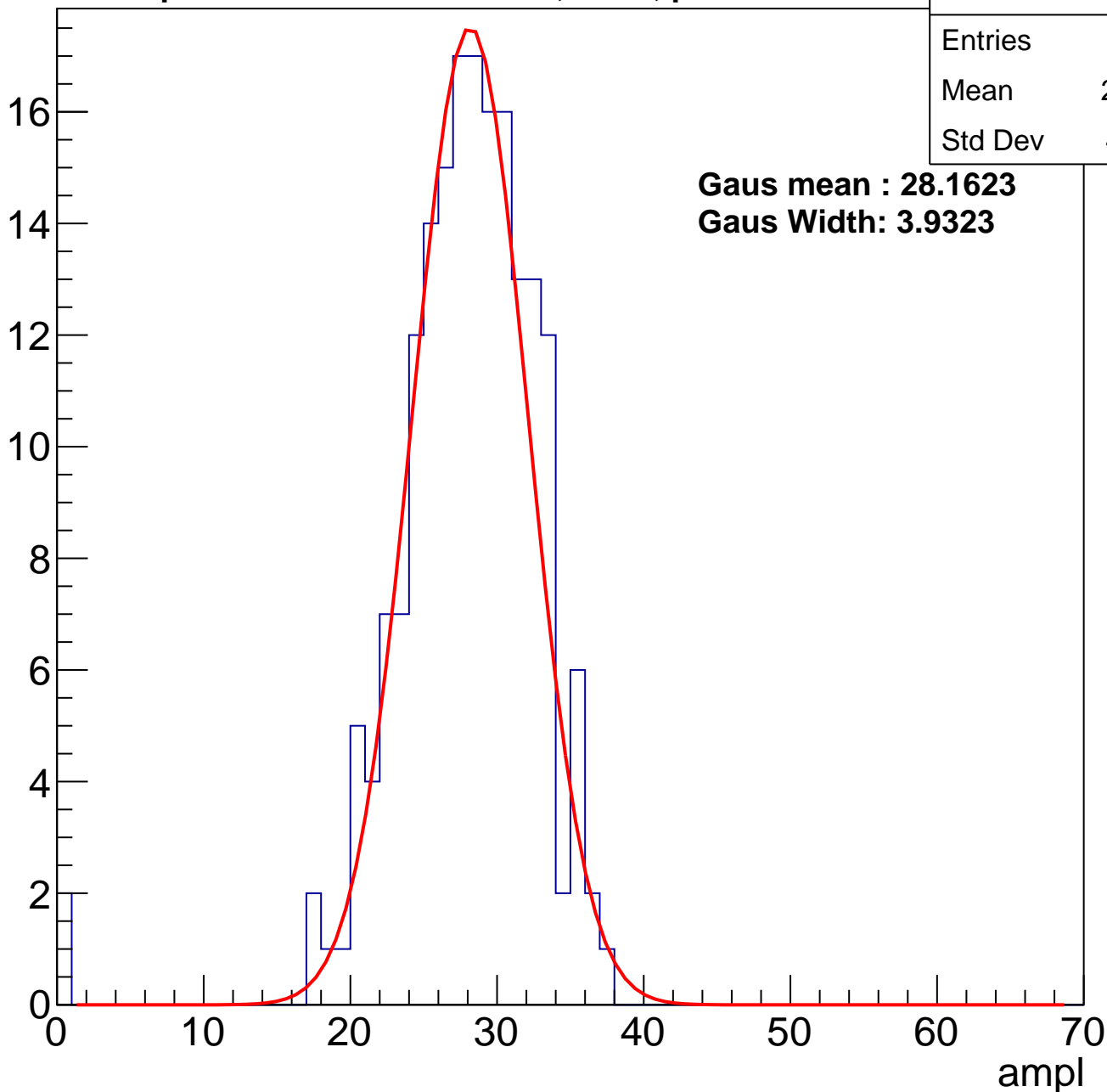
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	27.45
Std Dev	4.961

Gaus mean : 28.1623

Gaus Width: 3.9323

Entry



B0L101S, U20-ch56, adc1

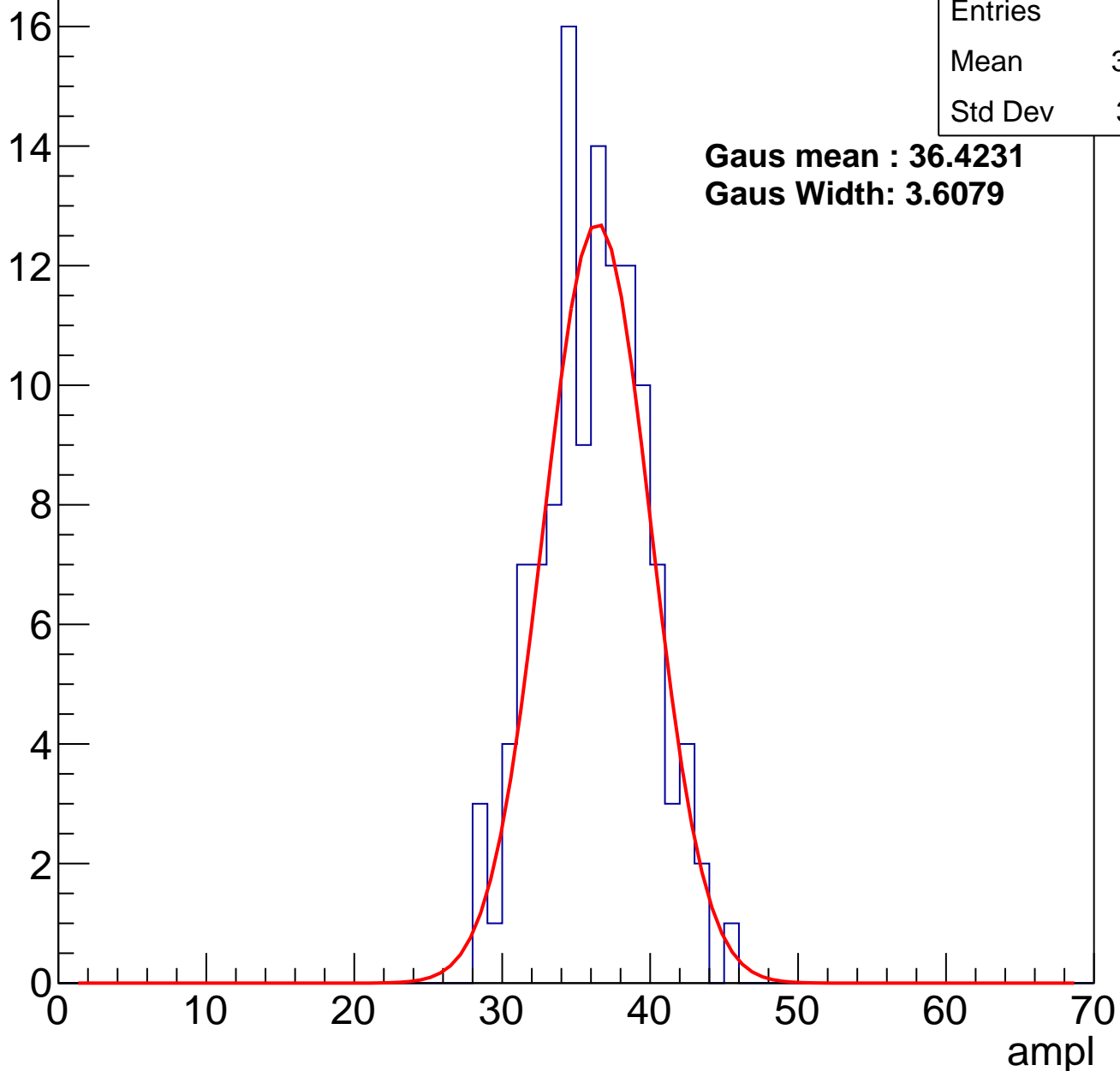
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	35.77
Std Dev	3.501

Gaus mean : 36.4231

Gaus Width: 3.6079

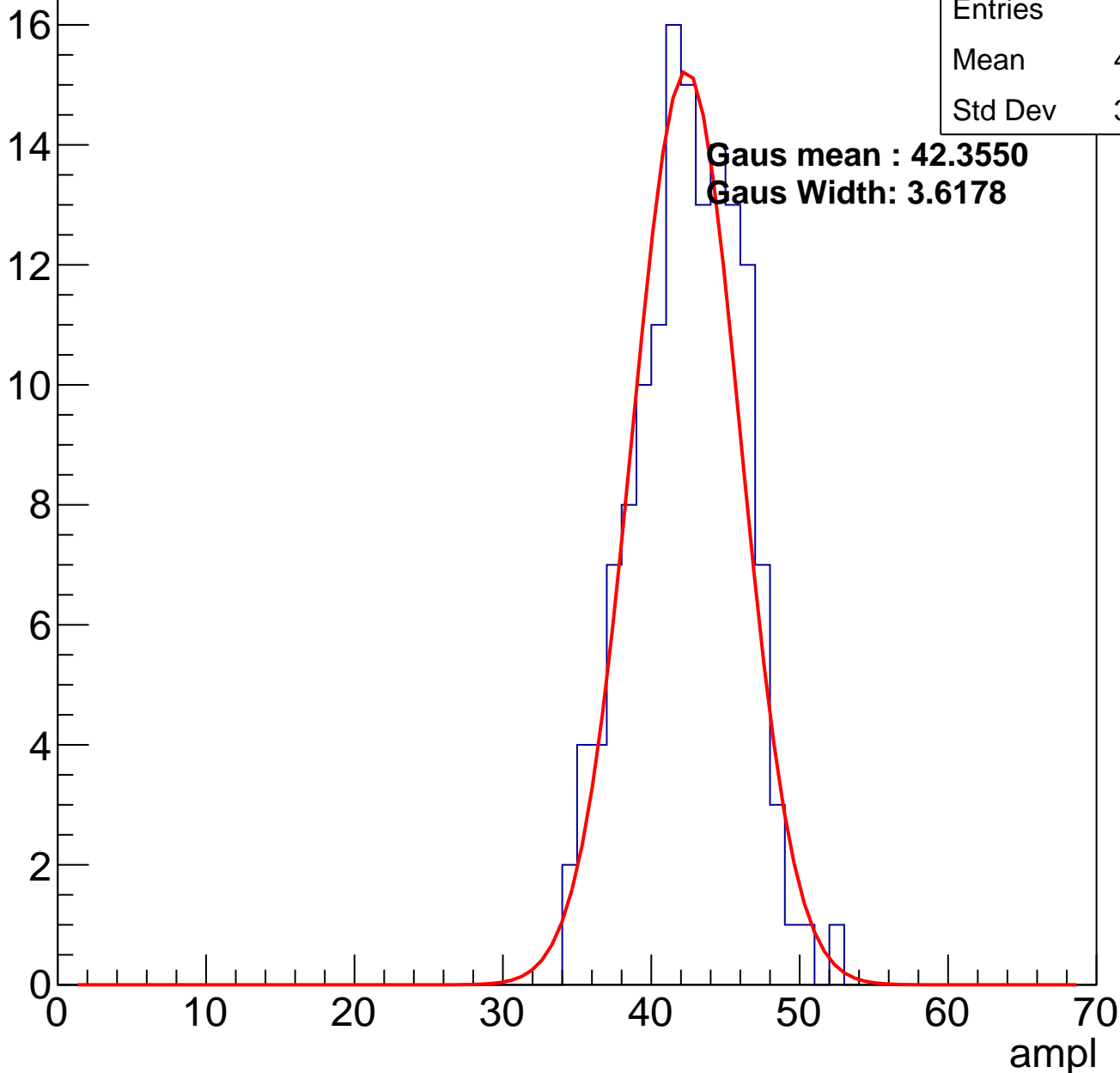
Entry



B0L101S, U20-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

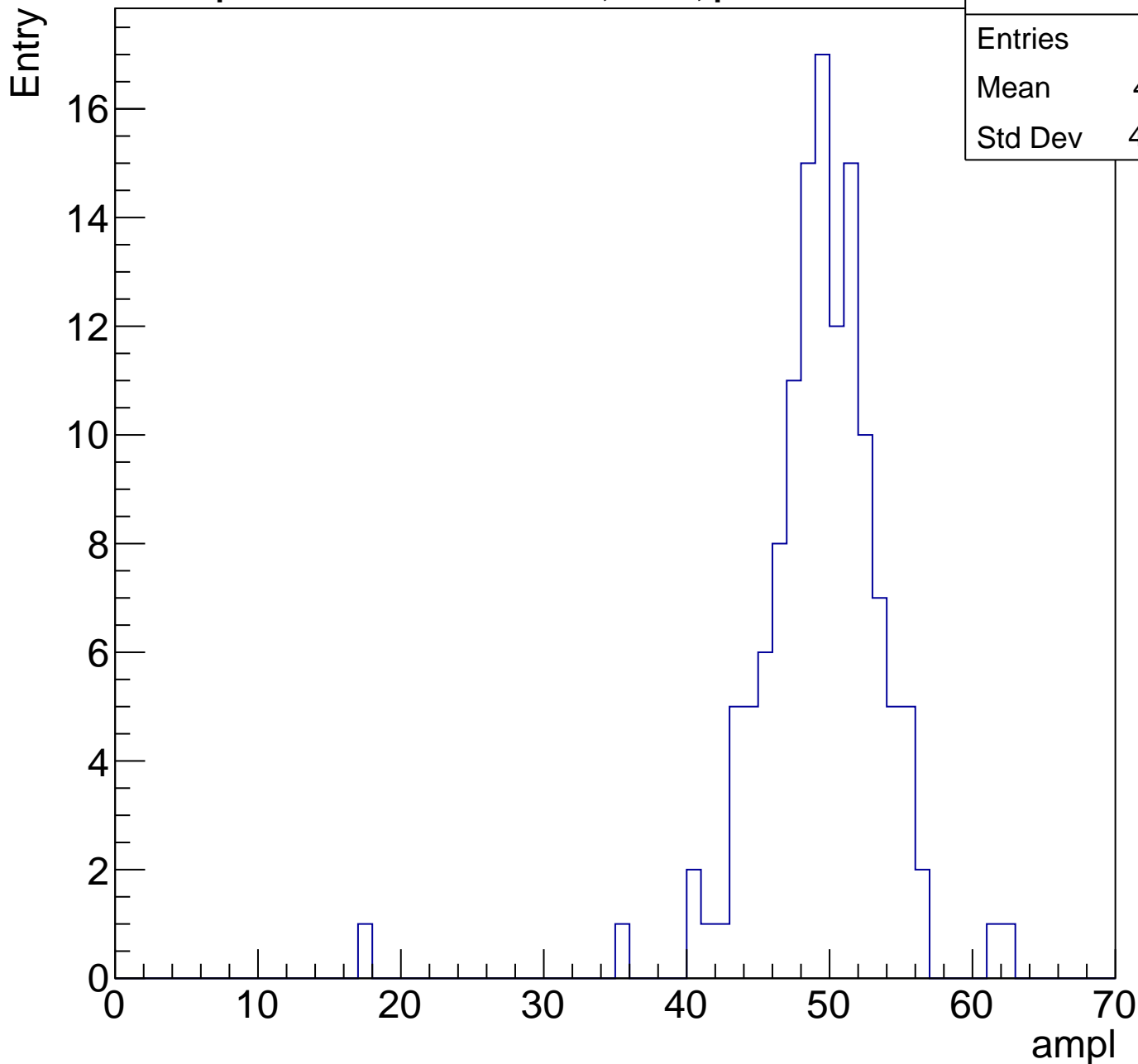


Entries	142
Mean	42.02
Std Dev	3.555

B0L101S, U20-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

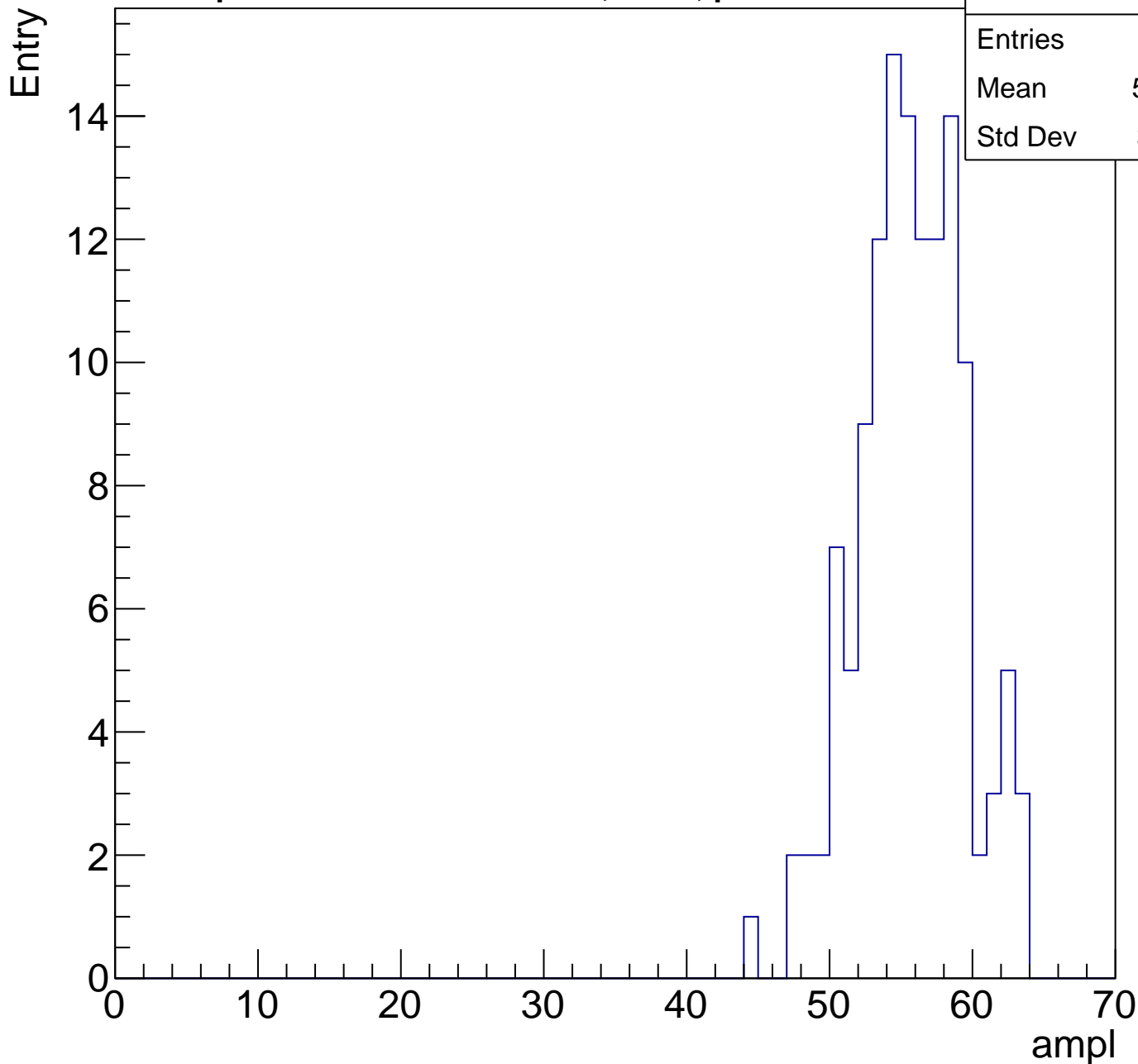
Entries	131
Mean	48.81
Std Dev	4.795



B0L101S, U20-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	55.24
Std Dev	3.671



B0L101S, U20-ch56, adc5

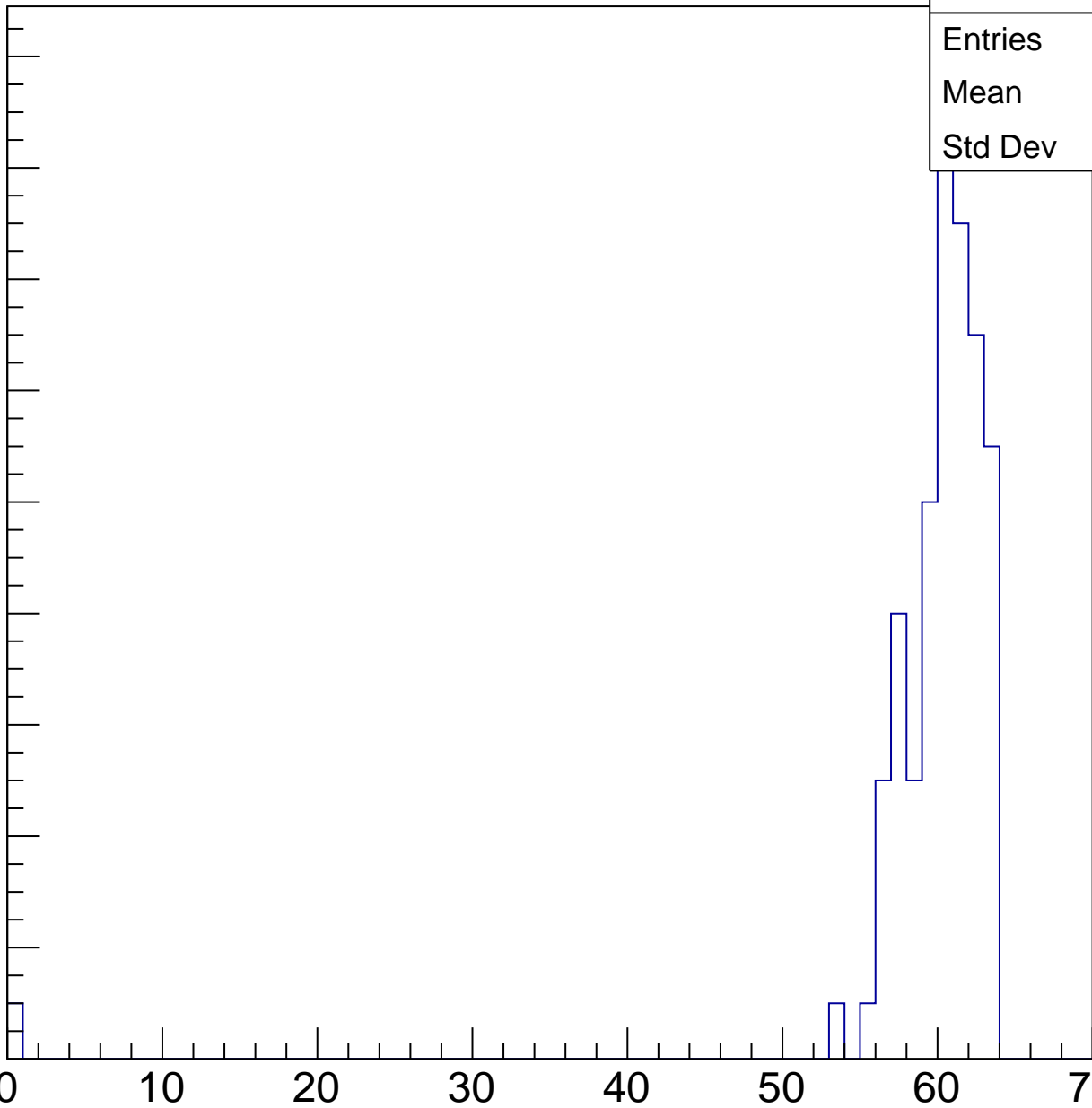
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.3
Std Dev	6.724

ampl

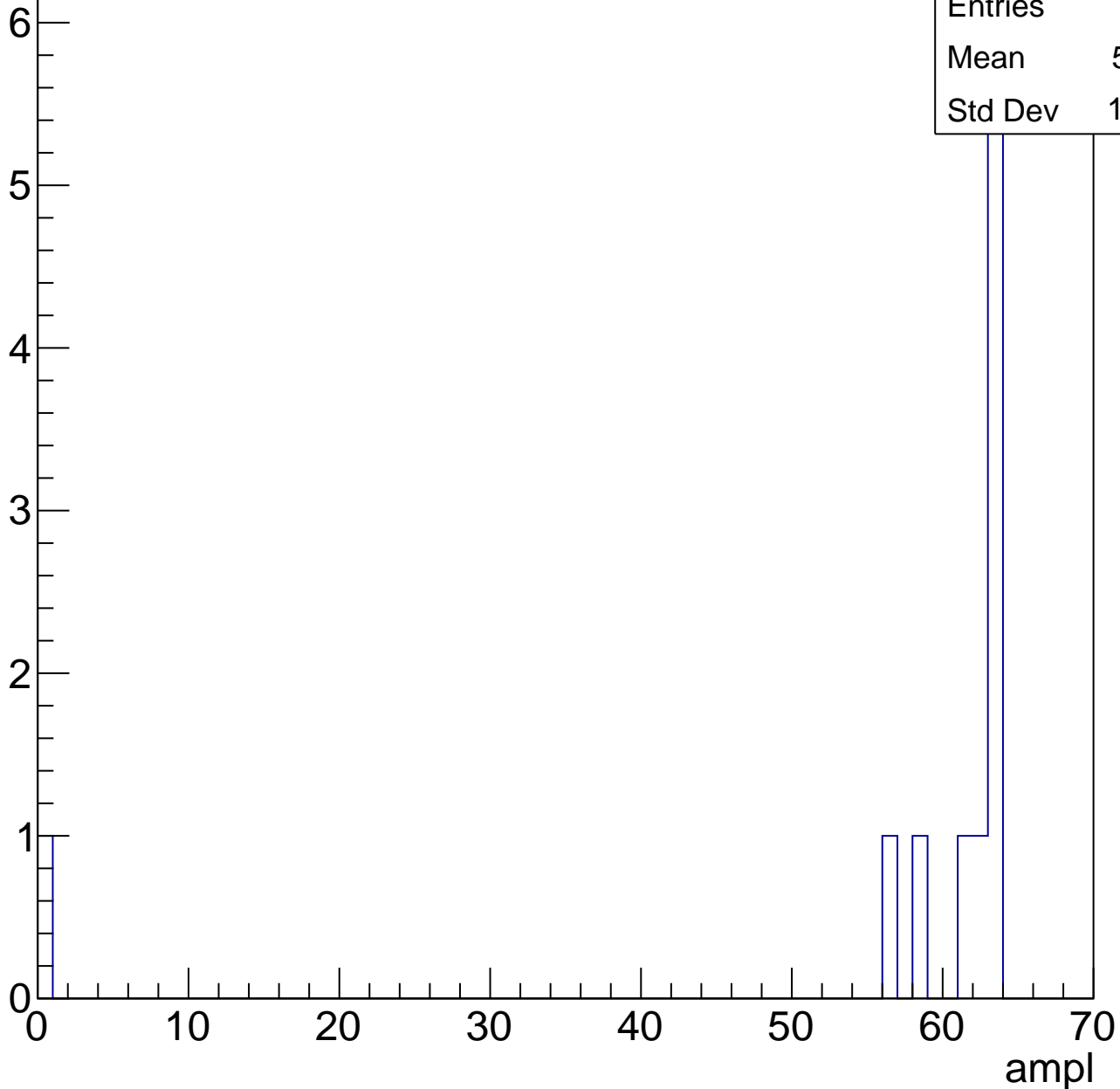


B0L101S, U20-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	55.91
Std Dev	17.82



B0L101S, U20-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



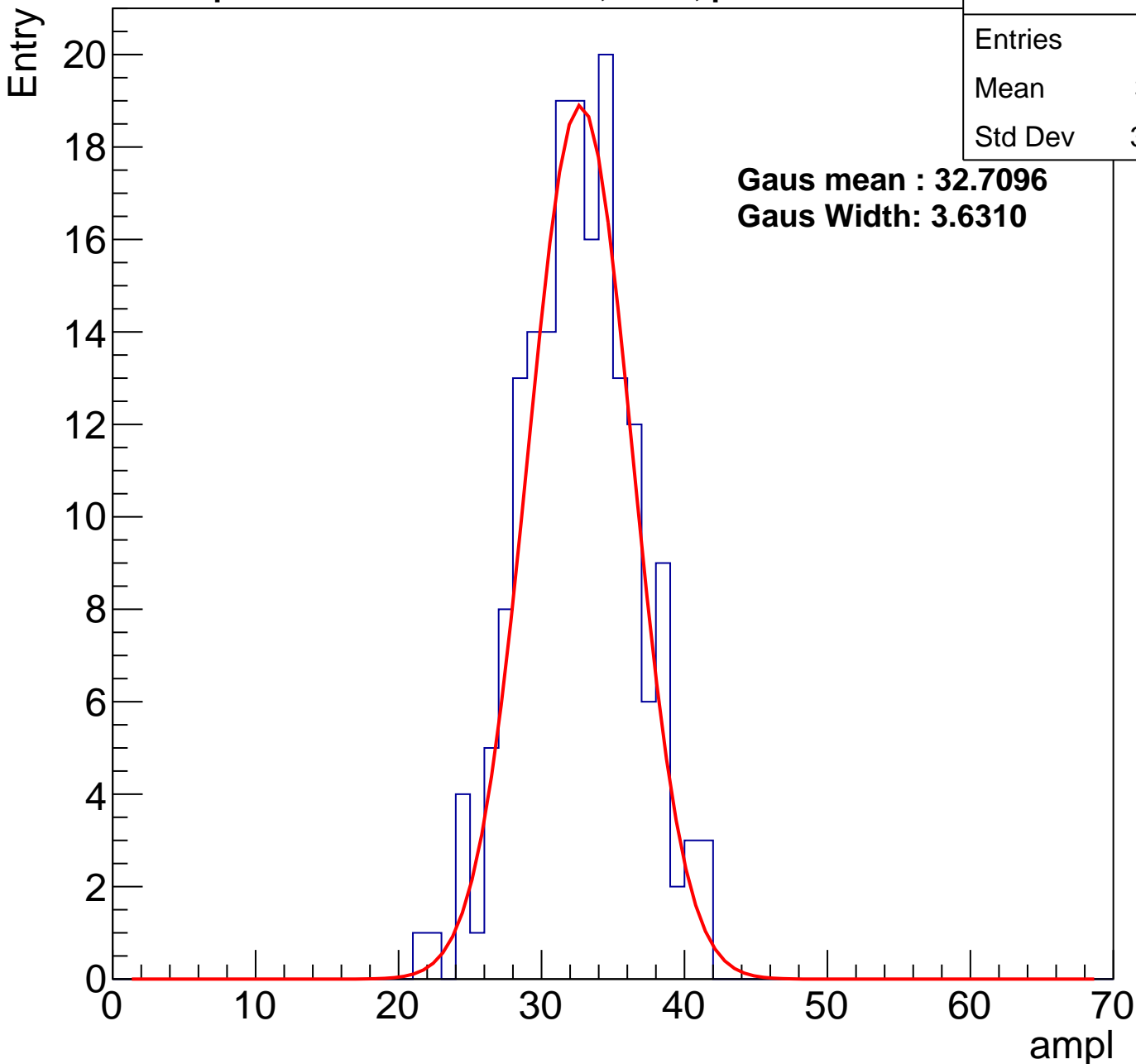
B0L101S, U20-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	32.11
Std Dev	3.859

Gaus mean : 32.7096

Gaus Width: 3.6310



B0L101S, U20-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	39.32
Std Dev	3.215

Gaus mean : 39.5478

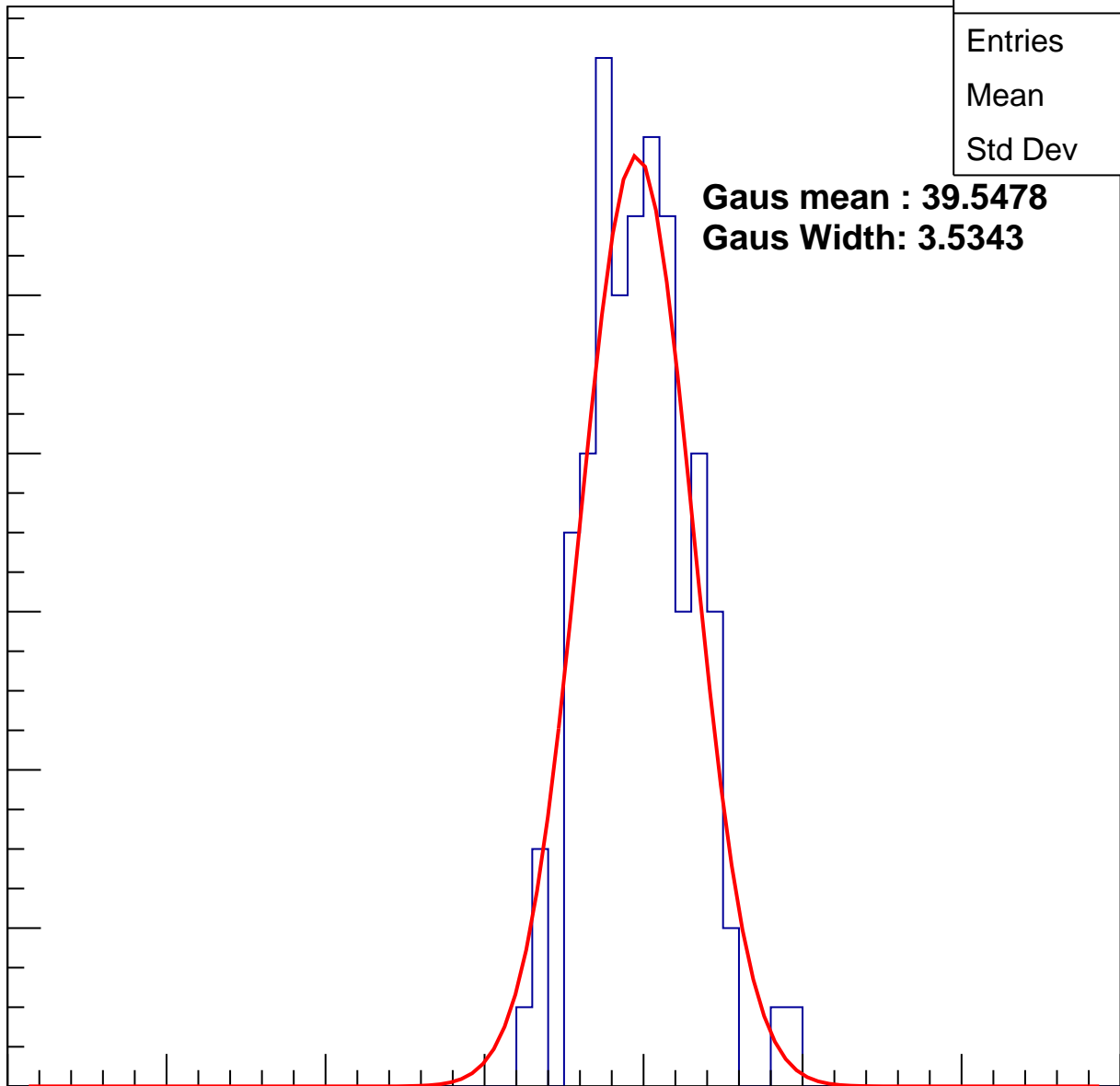
Gaus Width: 3.5343

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch57, adc2

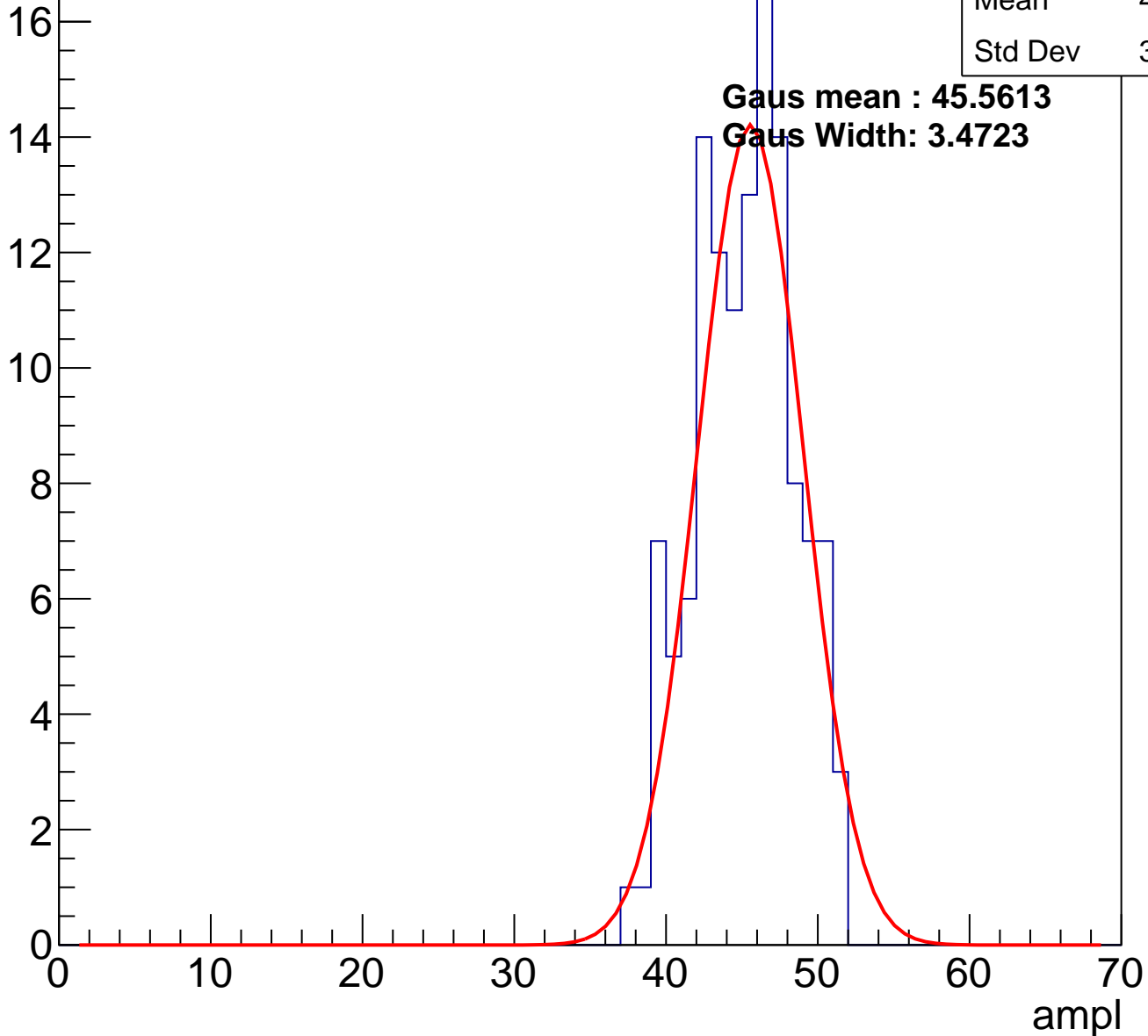
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	126
Mean	44.74
Std Dev	3.215

Gaus mean : 45.5613

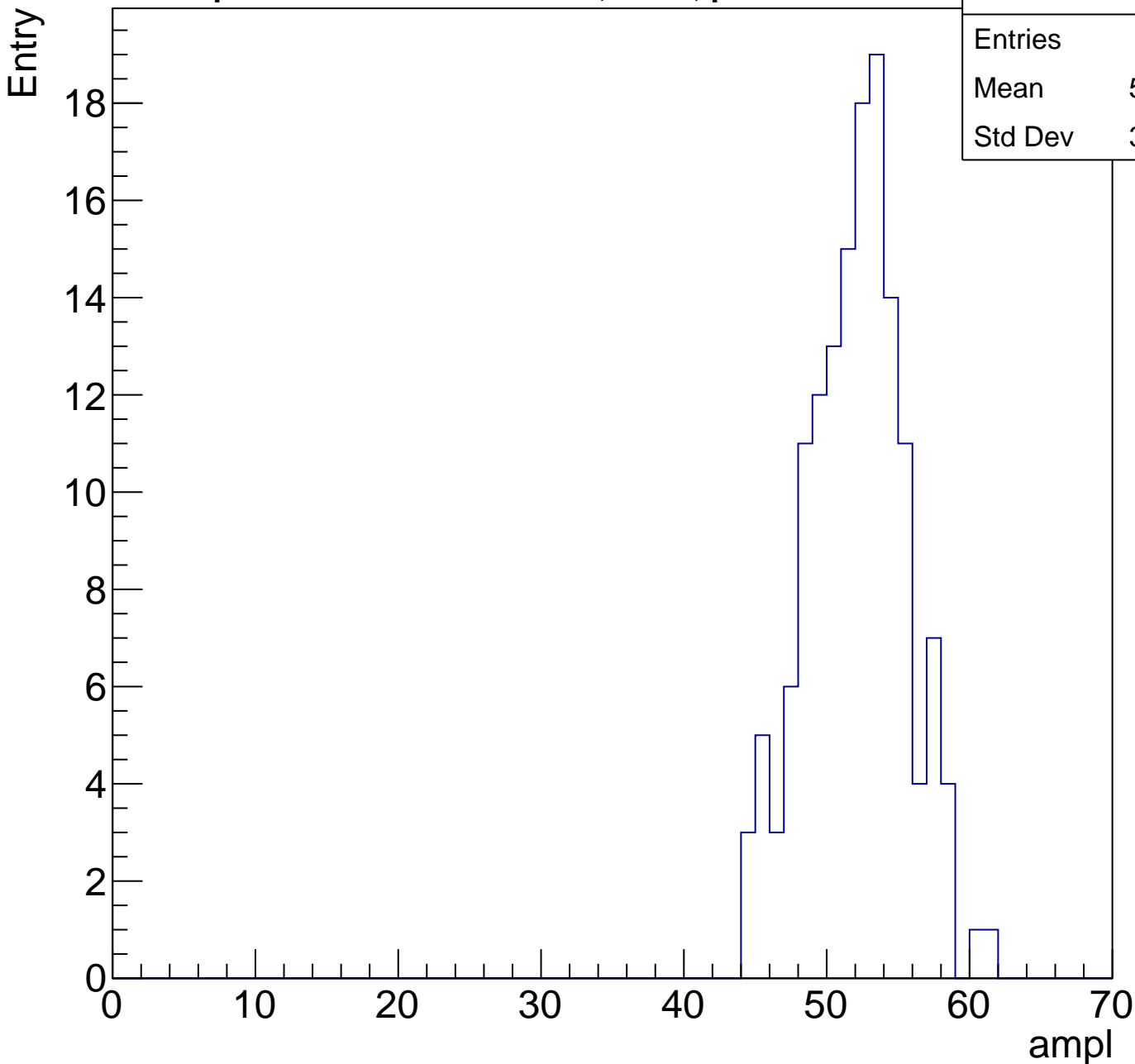
Gaus Width: 3.4723



B0L101S, U20-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	51.62
Std Dev	3.427



B0L101S, U20-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

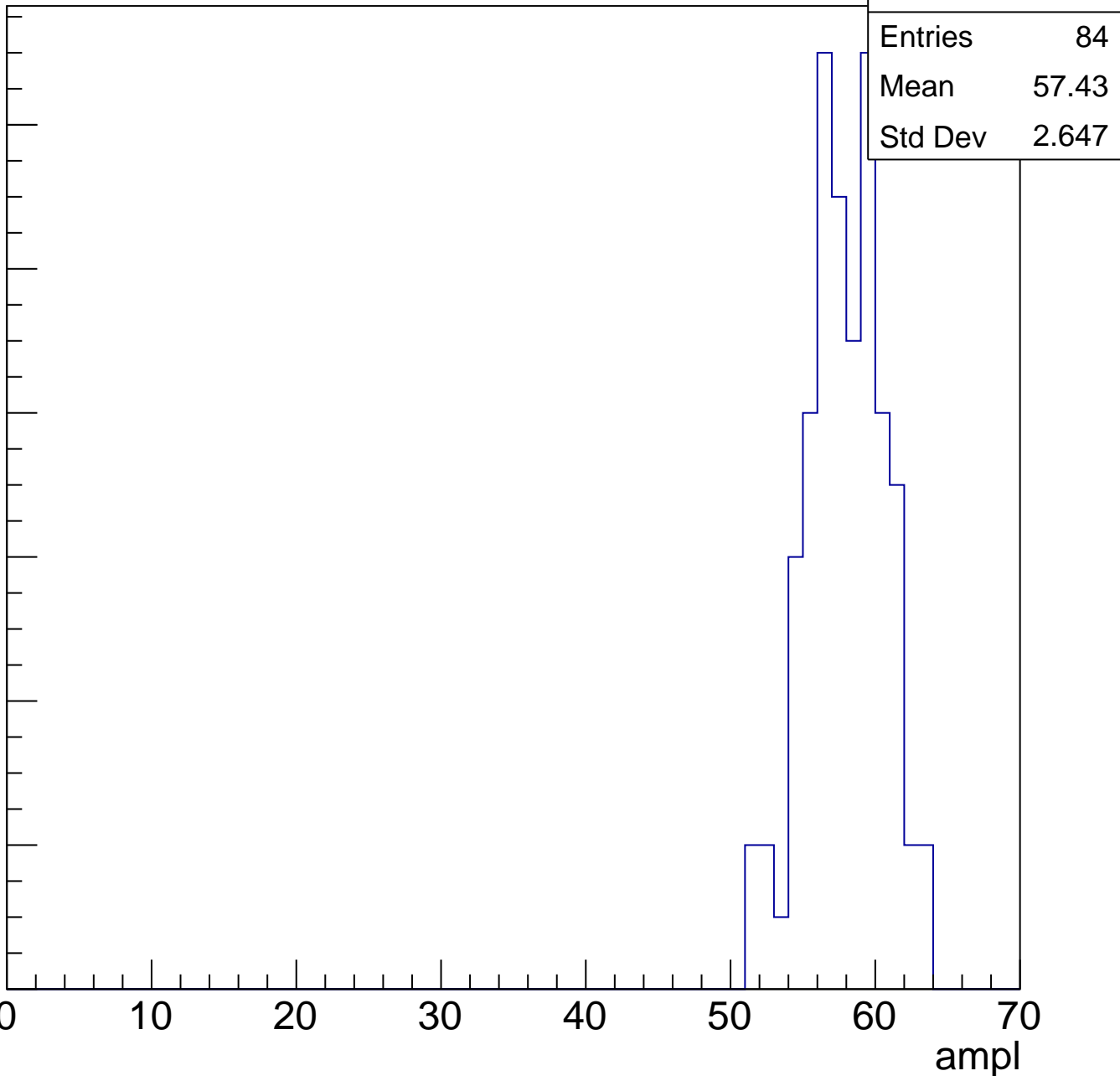
40

50

60

ampl

Entries	84
Mean	57.43
Std Dev	2.647



B0L101S, U20-ch57, adc5

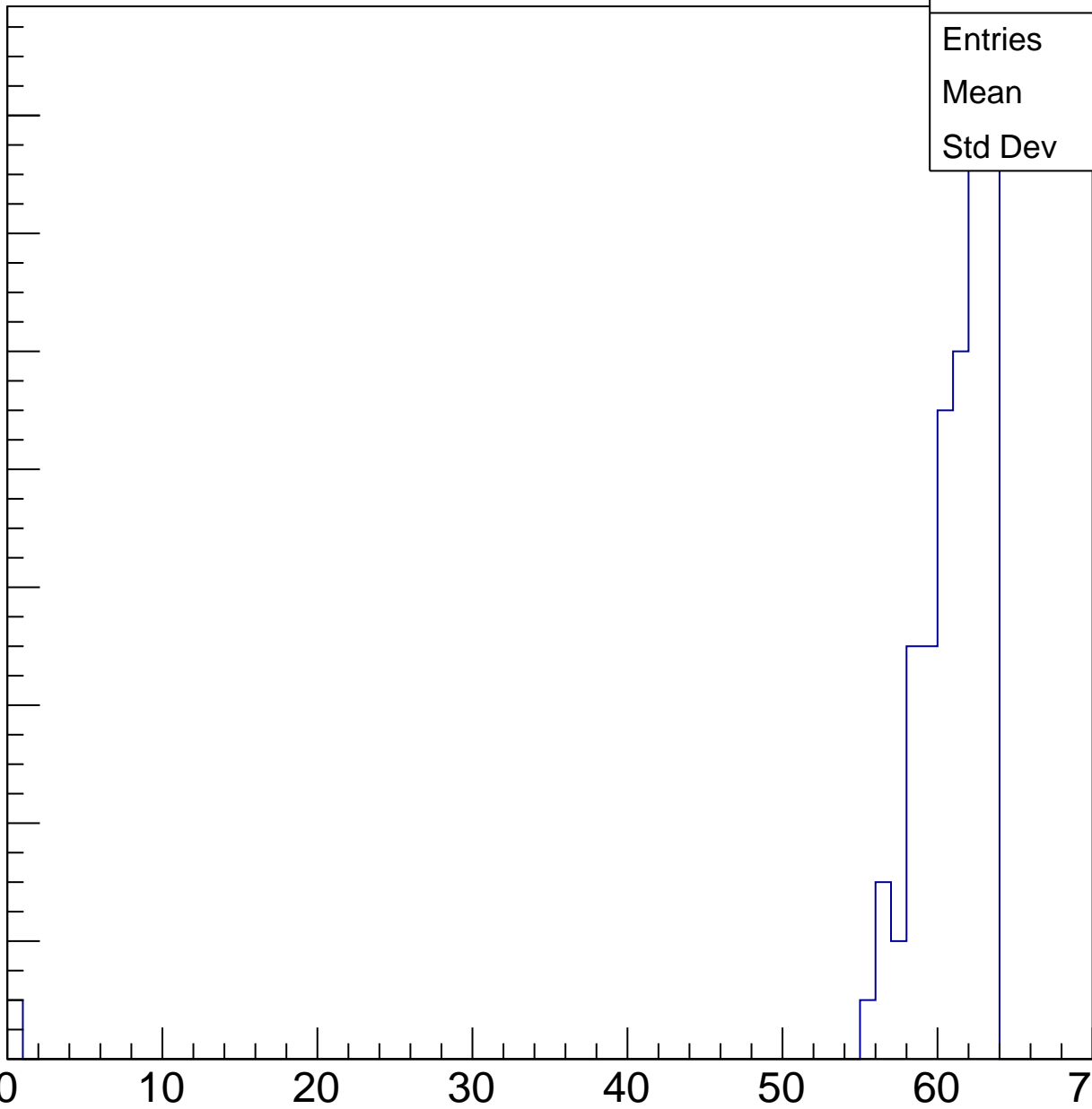
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.87
Std Dev	7.161

ampl



B0L101S, U20-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U20-ch58, adc0

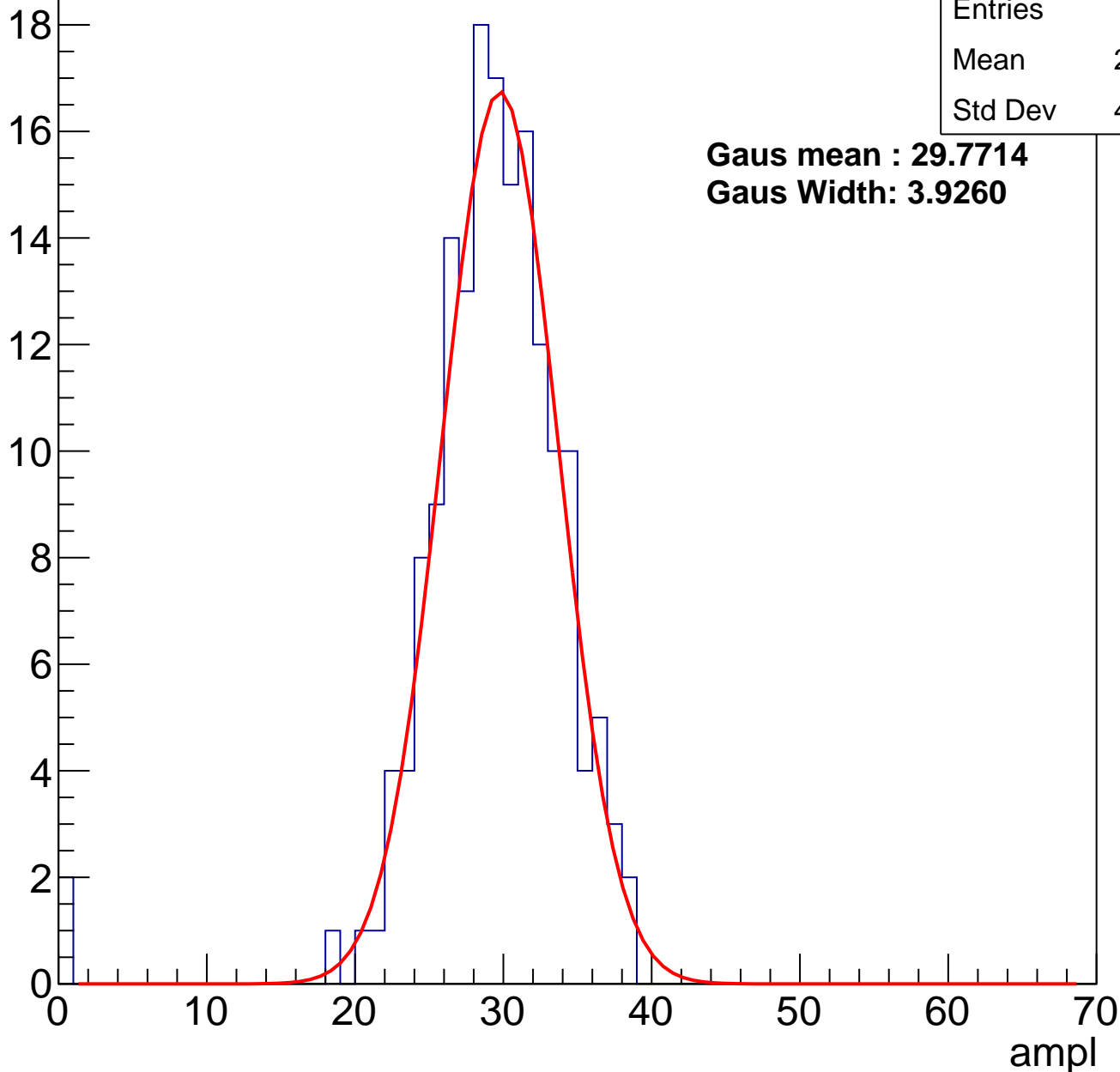
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	28.85
Std Dev	4.964

Gaus mean : 29.7714

Gaus Width: 3.9260

Entry



B0L101S, U20-ch58, adc1

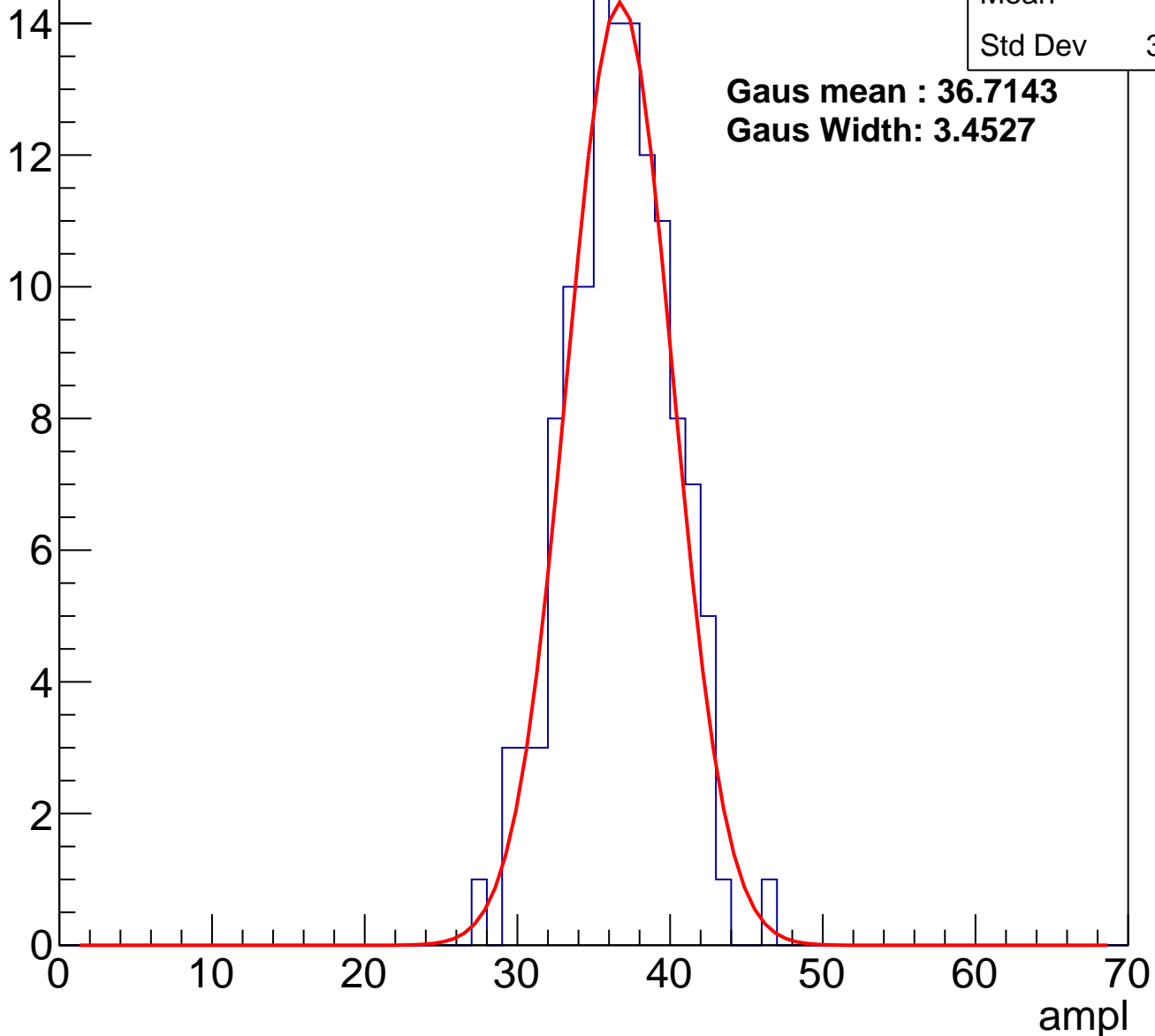
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	36.2
Std Dev	3.418

Gaus mean : 36.7143

Gaus Width: 3.4527

Entry



B0L101S, U20-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	140
Mean	43.44
Std Dev	3.522

Gaus mean : 43.8587

Gaus Width: 3.5395

0

10

20

30

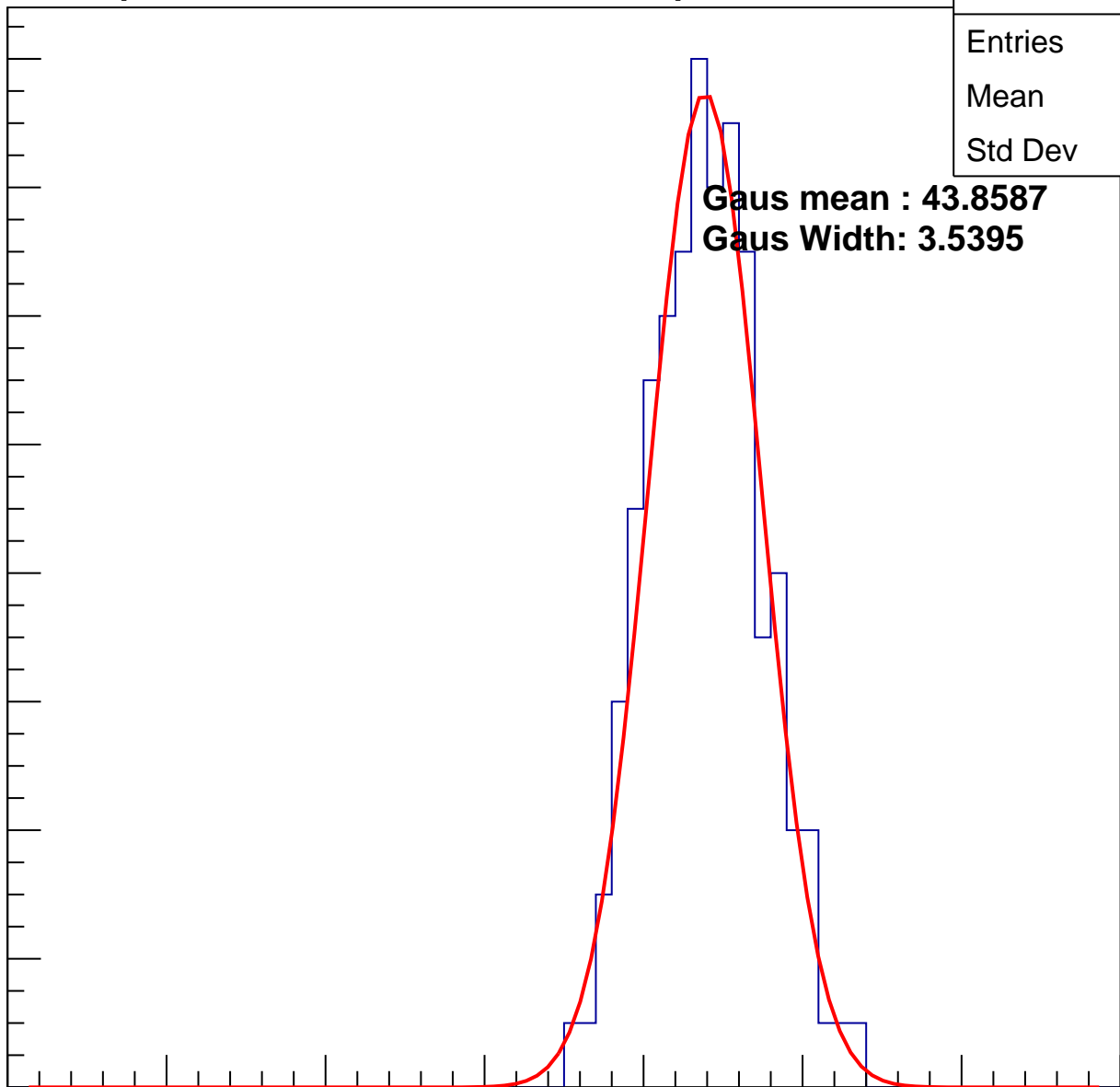
40

50

60

70

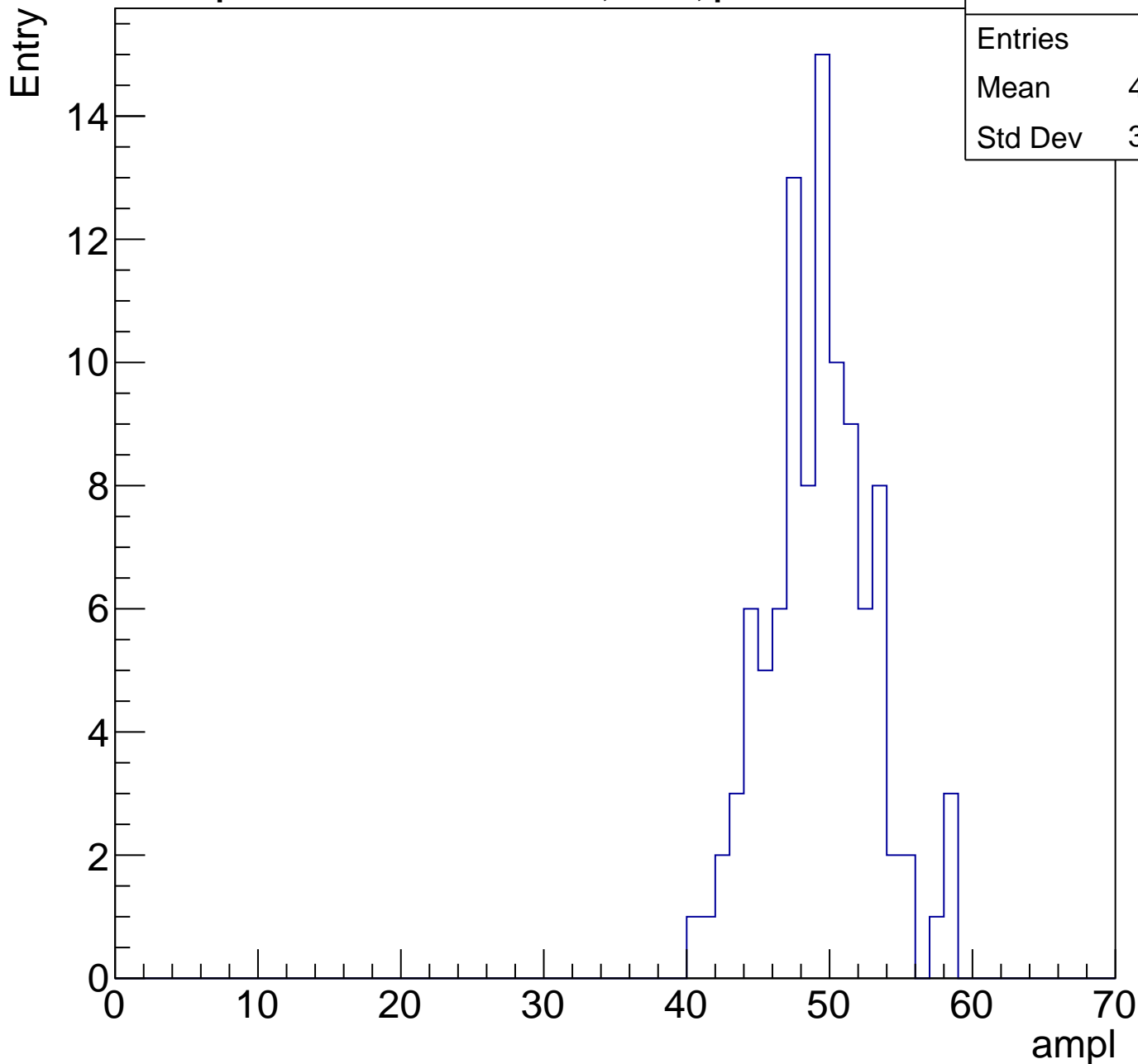
ampl



B0L101S, U20-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

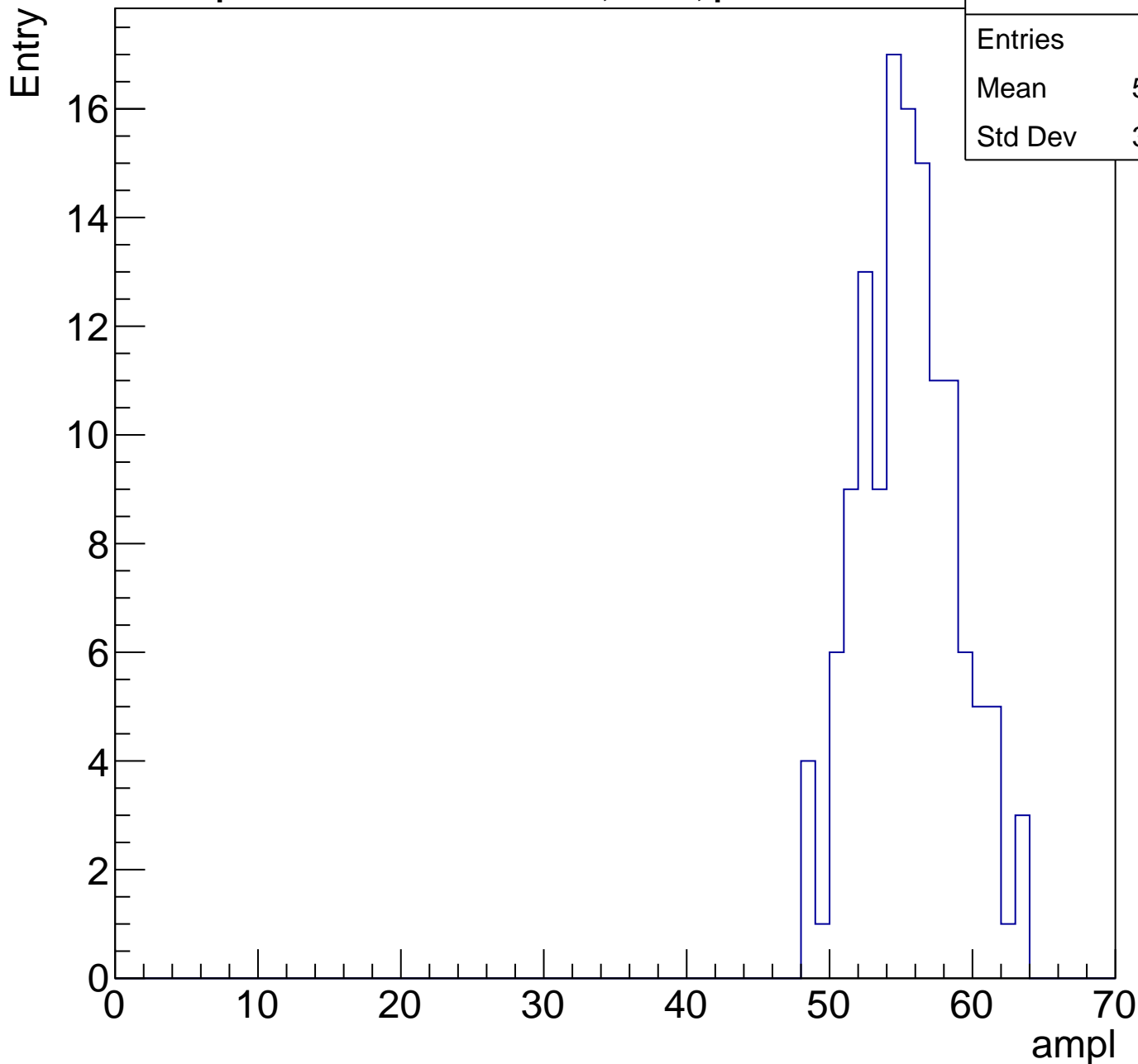
Entries	101
Mean	48.84
Std Dev	3.655



B0L101S, U20-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.05
Std Dev	3.366



B0L101S, U20-ch58, adc5

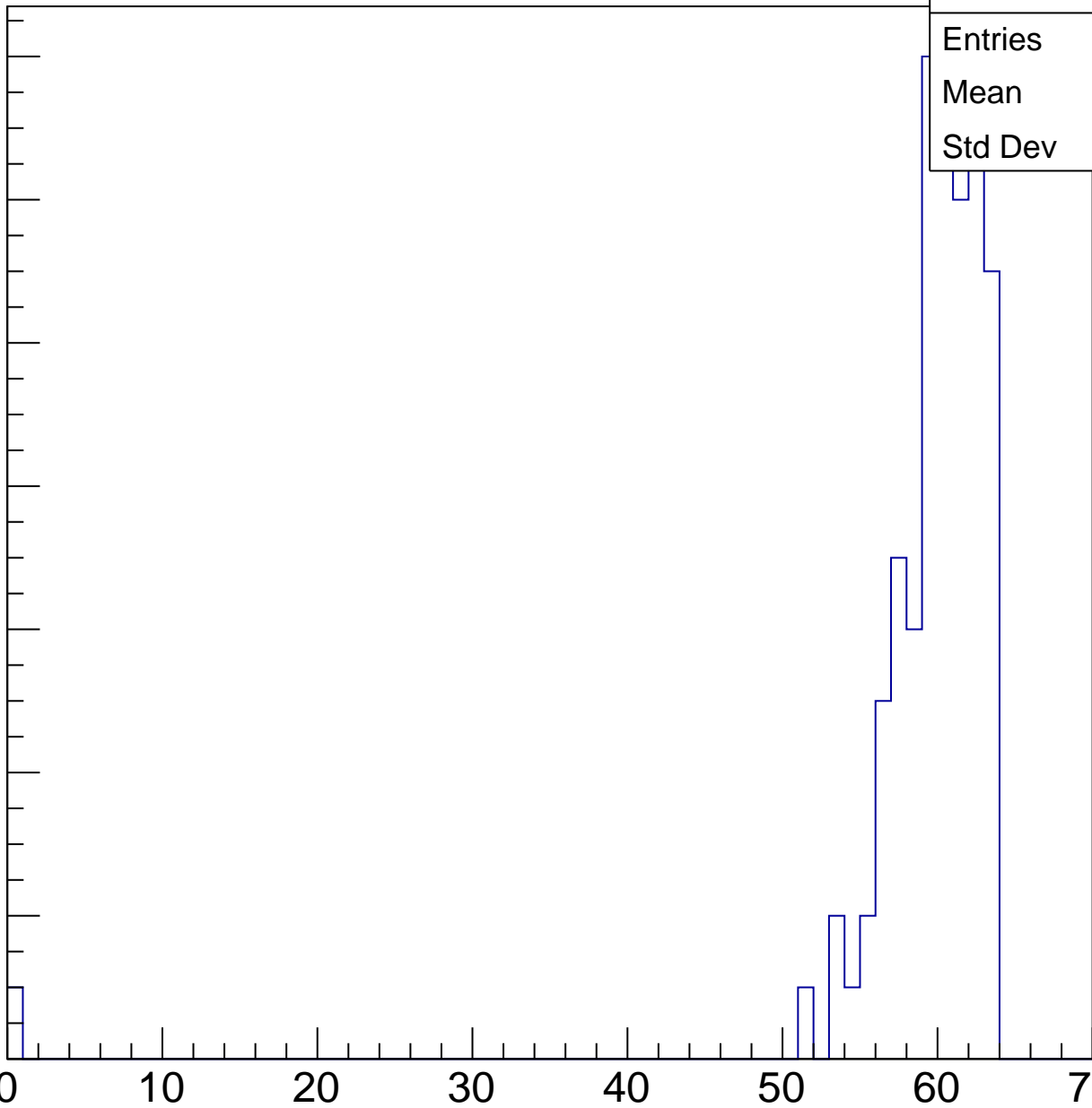
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.97
Std Dev	6.763

ampl

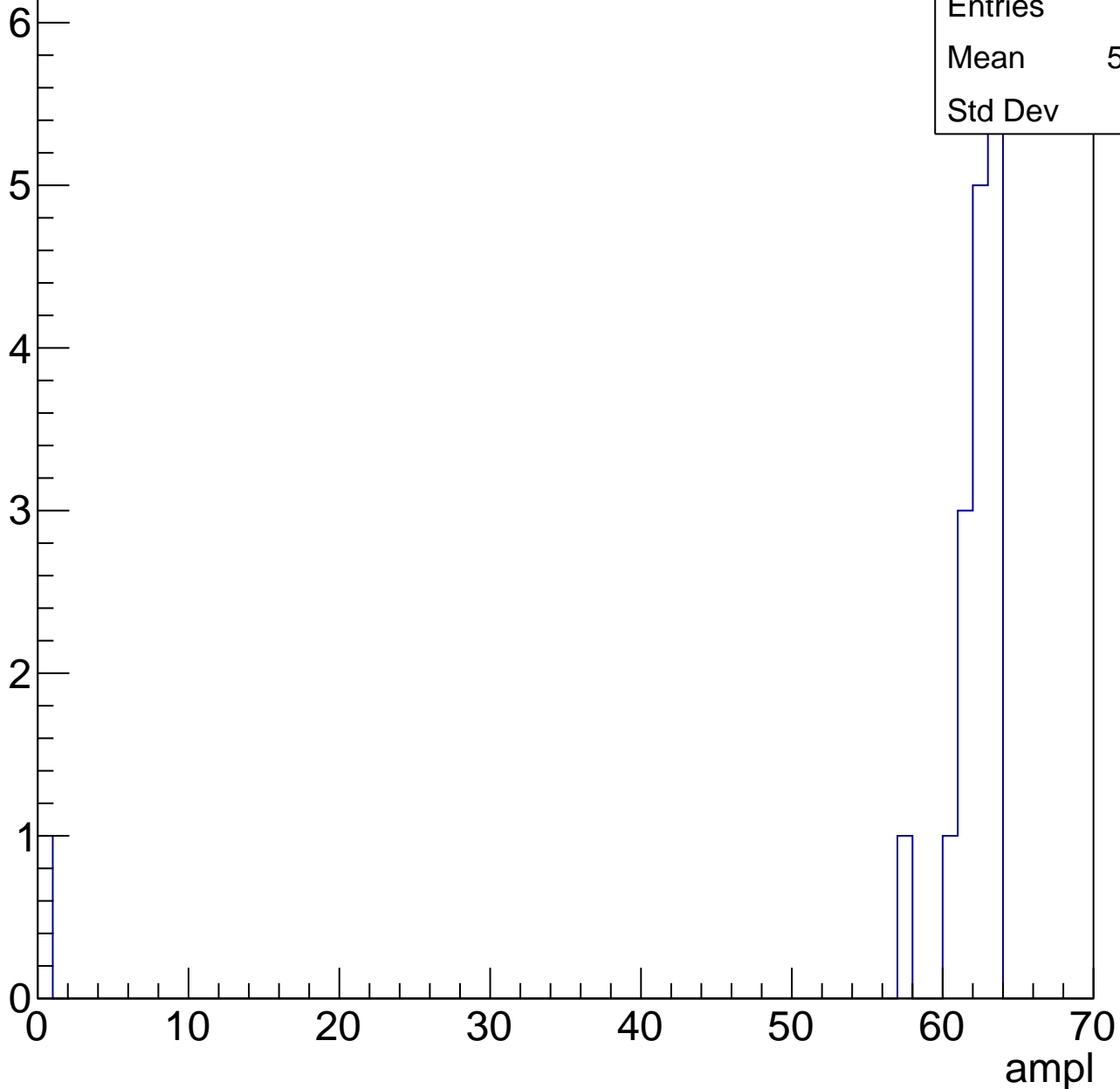


B0L101S, U20-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.12
Std Dev	14.6



B0L101S, U20-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch59, adc0

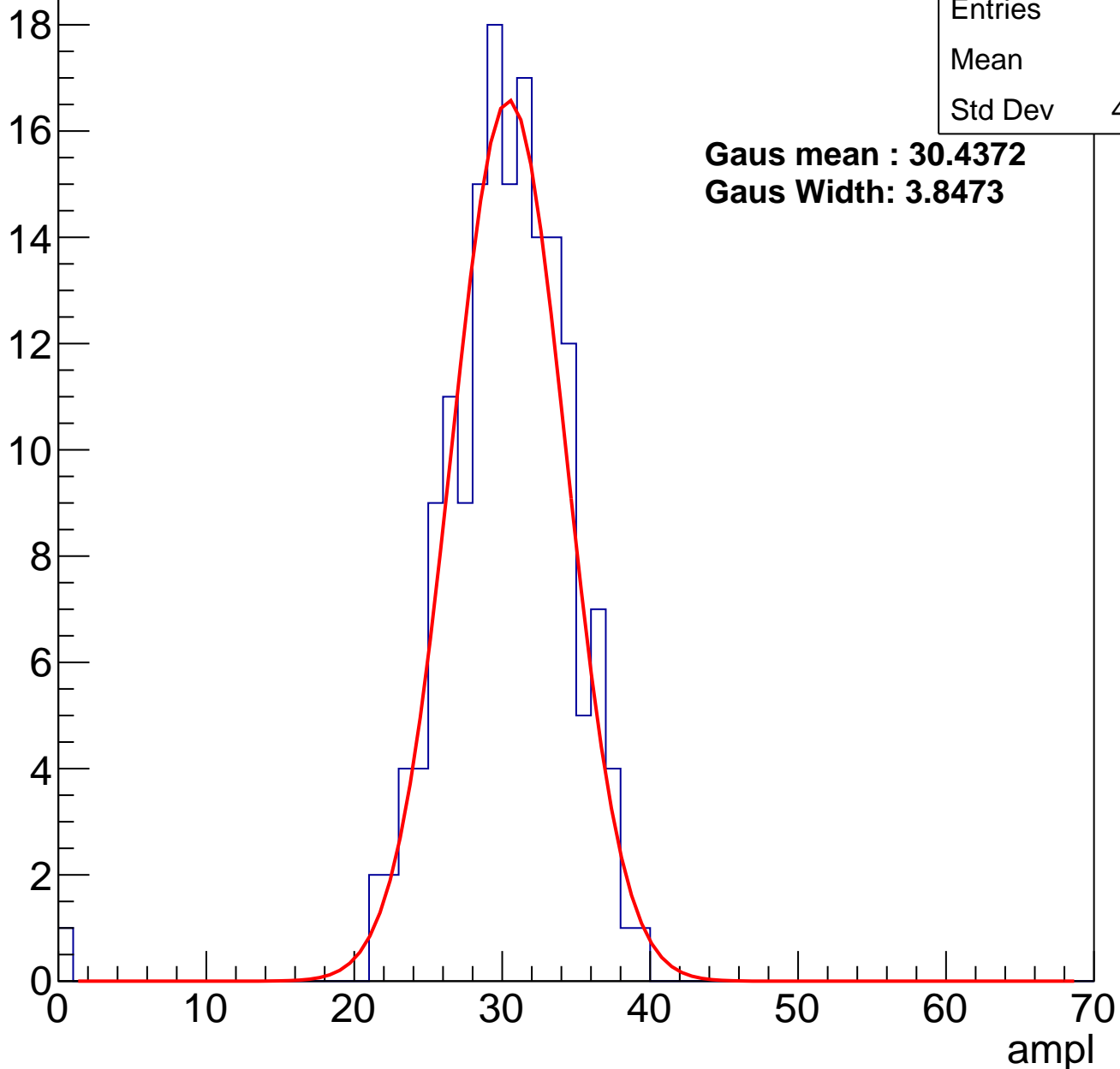
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	29.8
Std Dev	4.399

Gaus mean : 30.4372

Gaus Width: 3.8473

Entry



B0L101S, U20-ch59, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	36.95
Std Dev	3.849

Gaus mean : 36.9279

Gaus Width: 3.2960

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch59, adc2

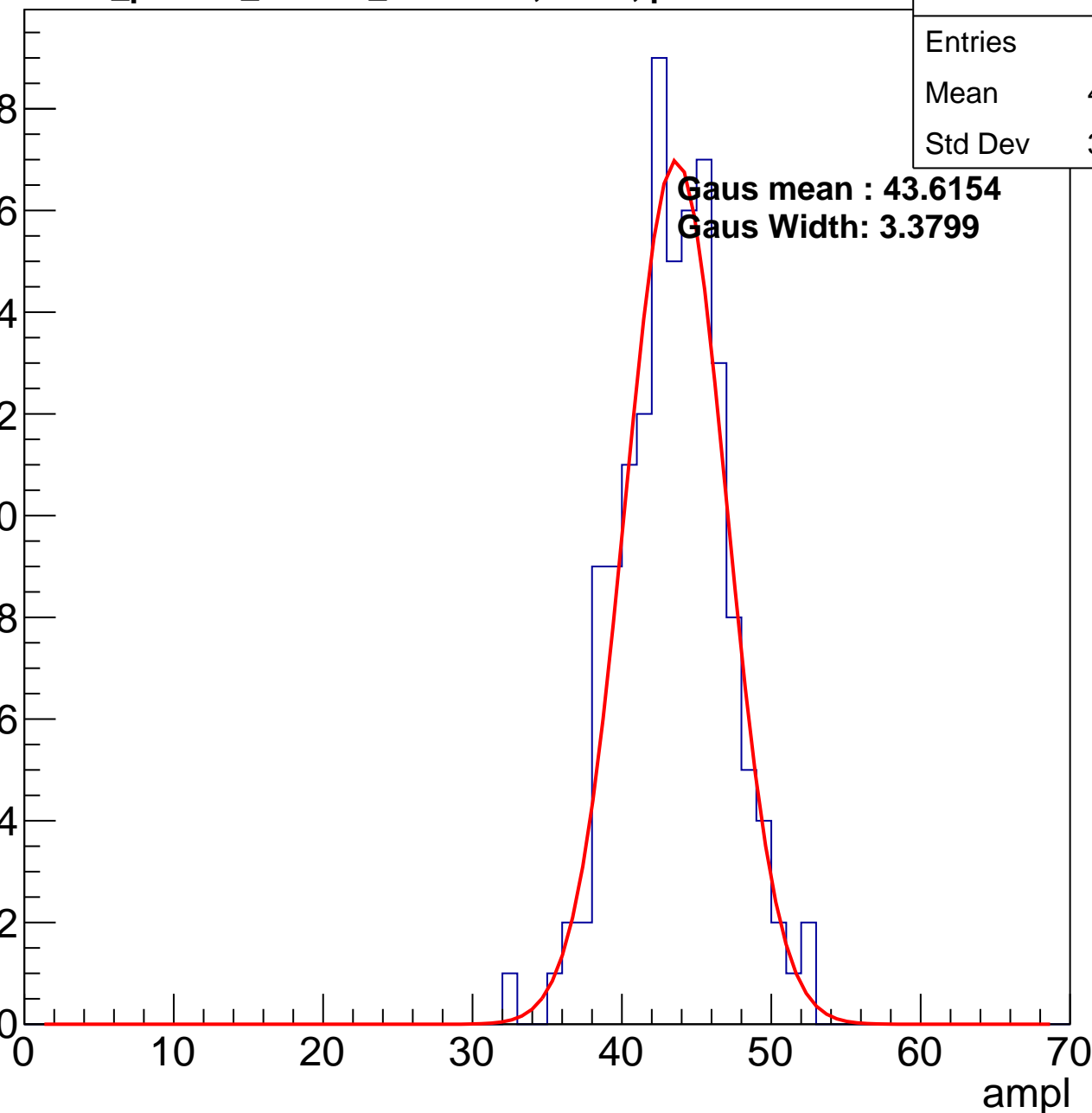
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	149
Mean	43.05
Std Dev	3.487

Gaus mean : 43.6154
Gaus Width: 3.3799



B0L101S, U20-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

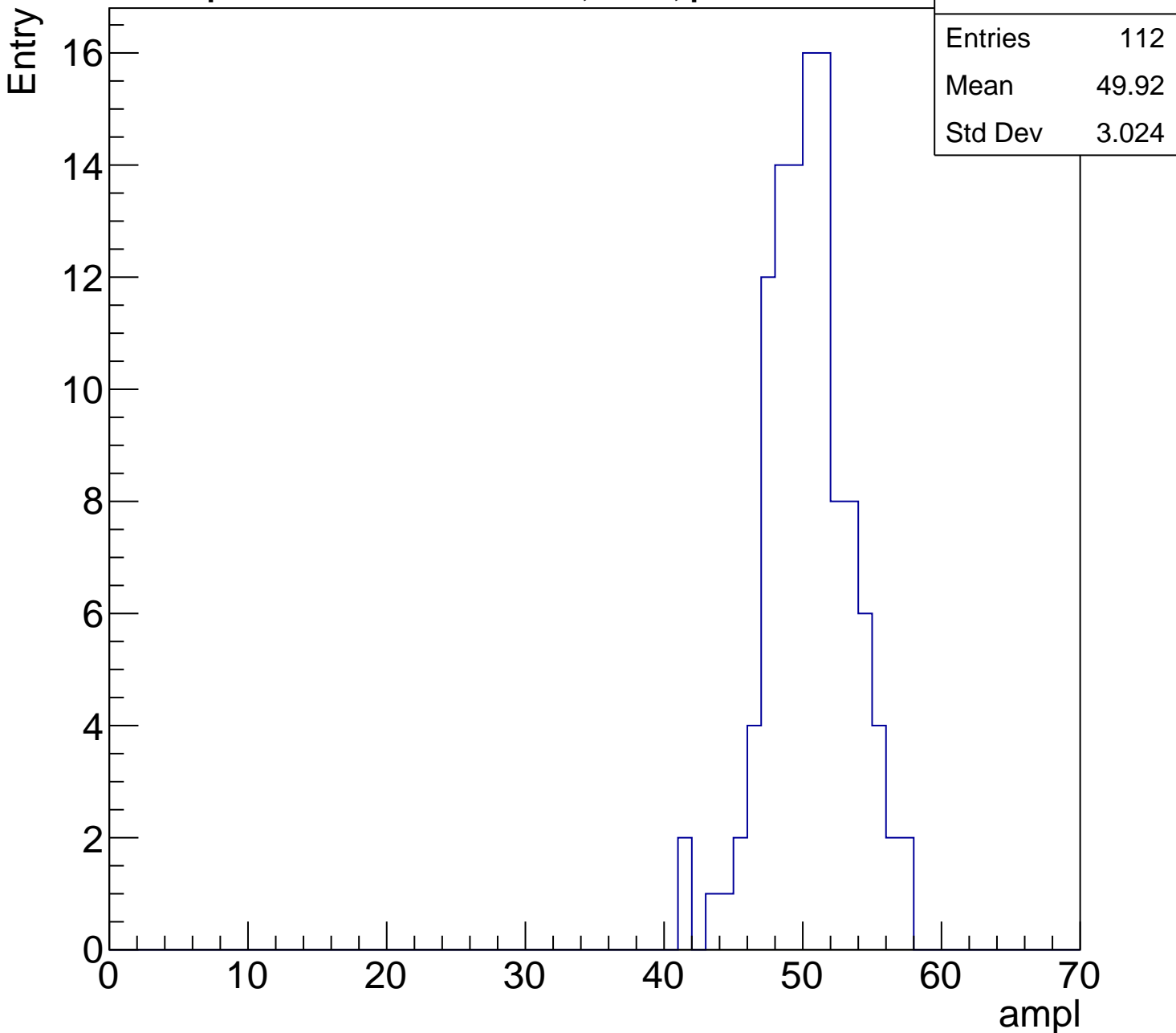
Entries	112
Mean	49.92
Std Dev	3.024

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch59, adc4

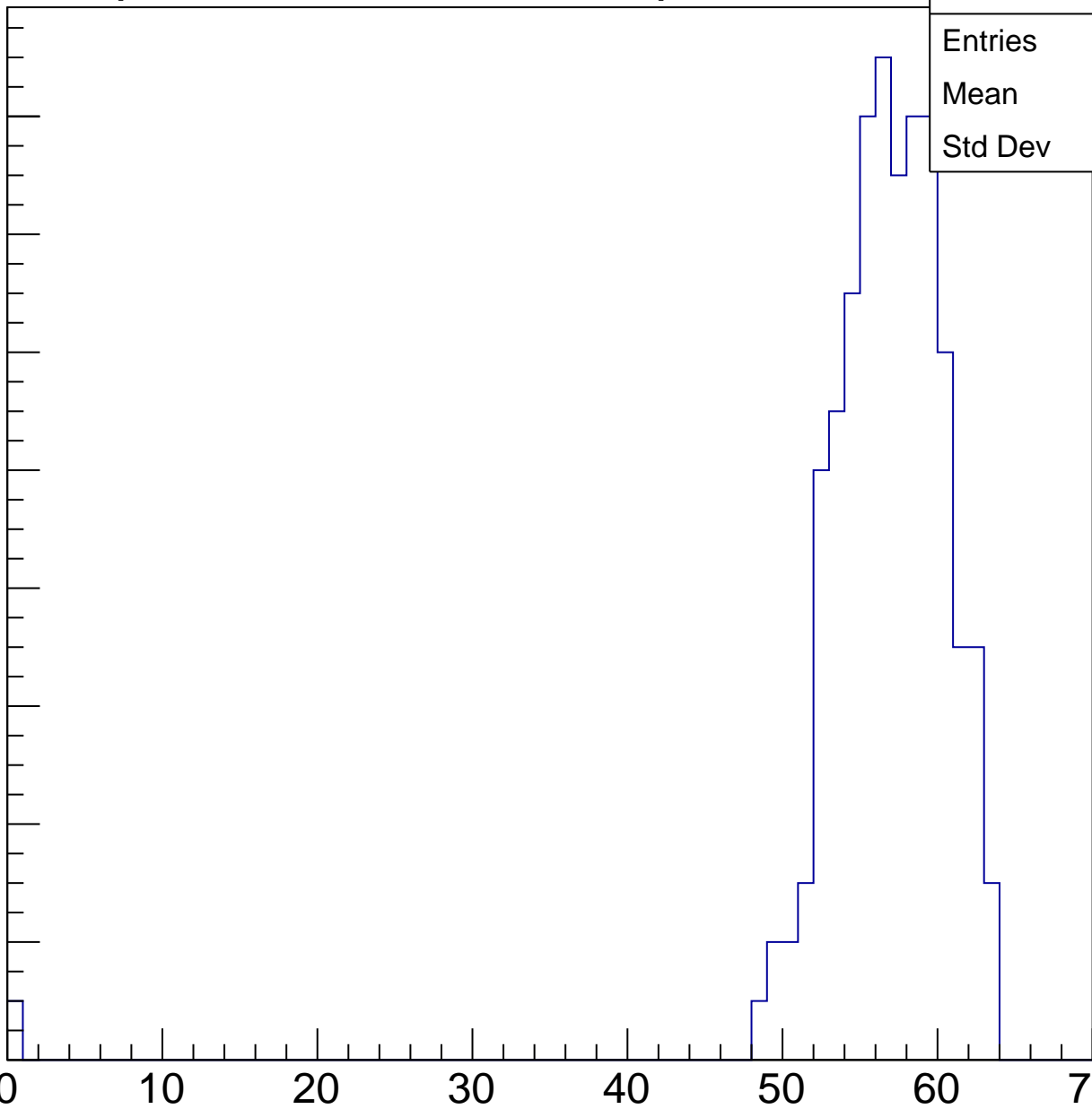
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	152
Mean	56.14
Std Dev	5.595

ampl



B0L101S, U20-ch59, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	57
Mean	59.47
Std Dev	8.263

ampl

0

10

20

30

40

50

60

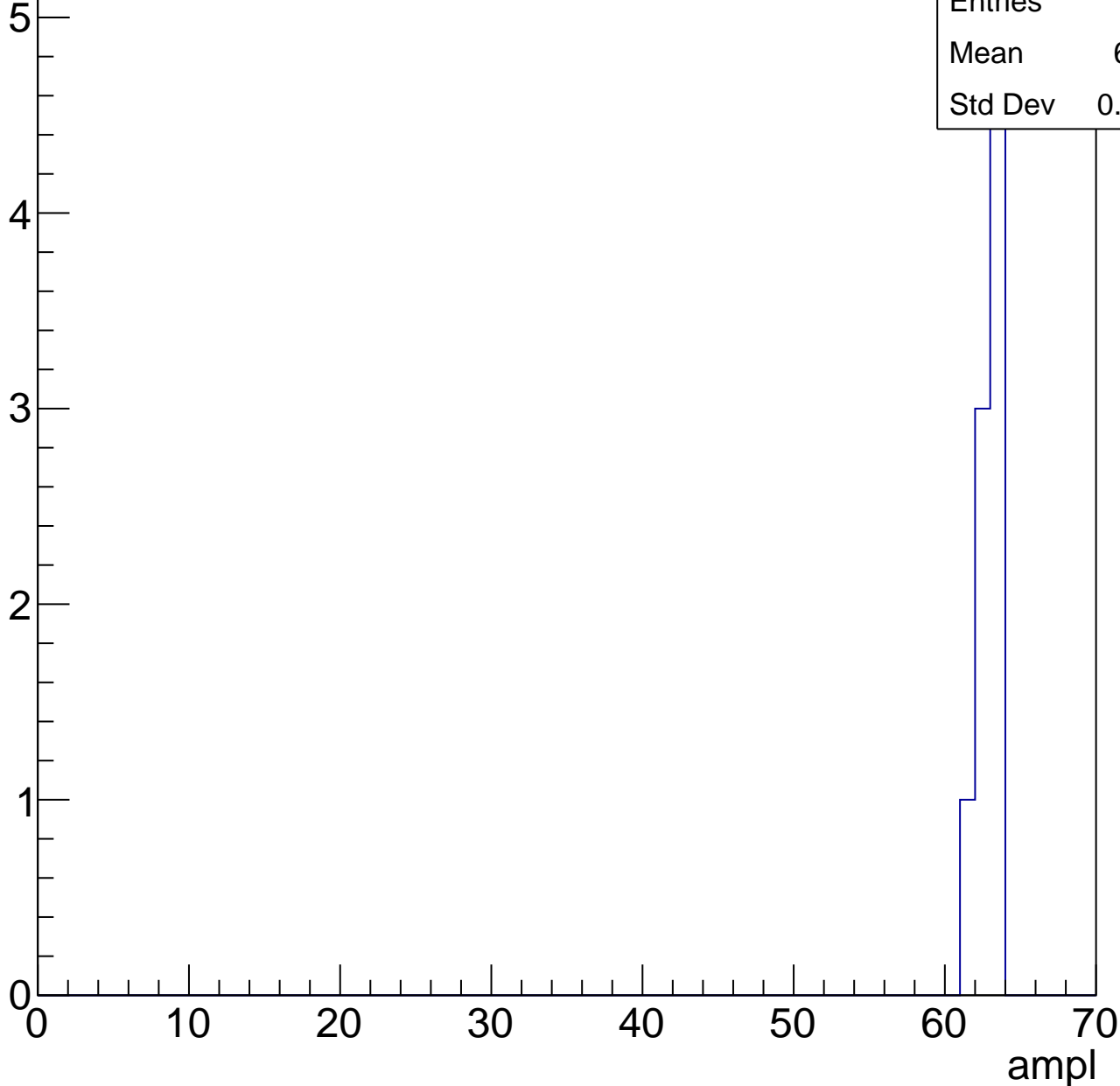
70

B0L101S, U20-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.44
Std Dev	0.6849



B0L101S, U20-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

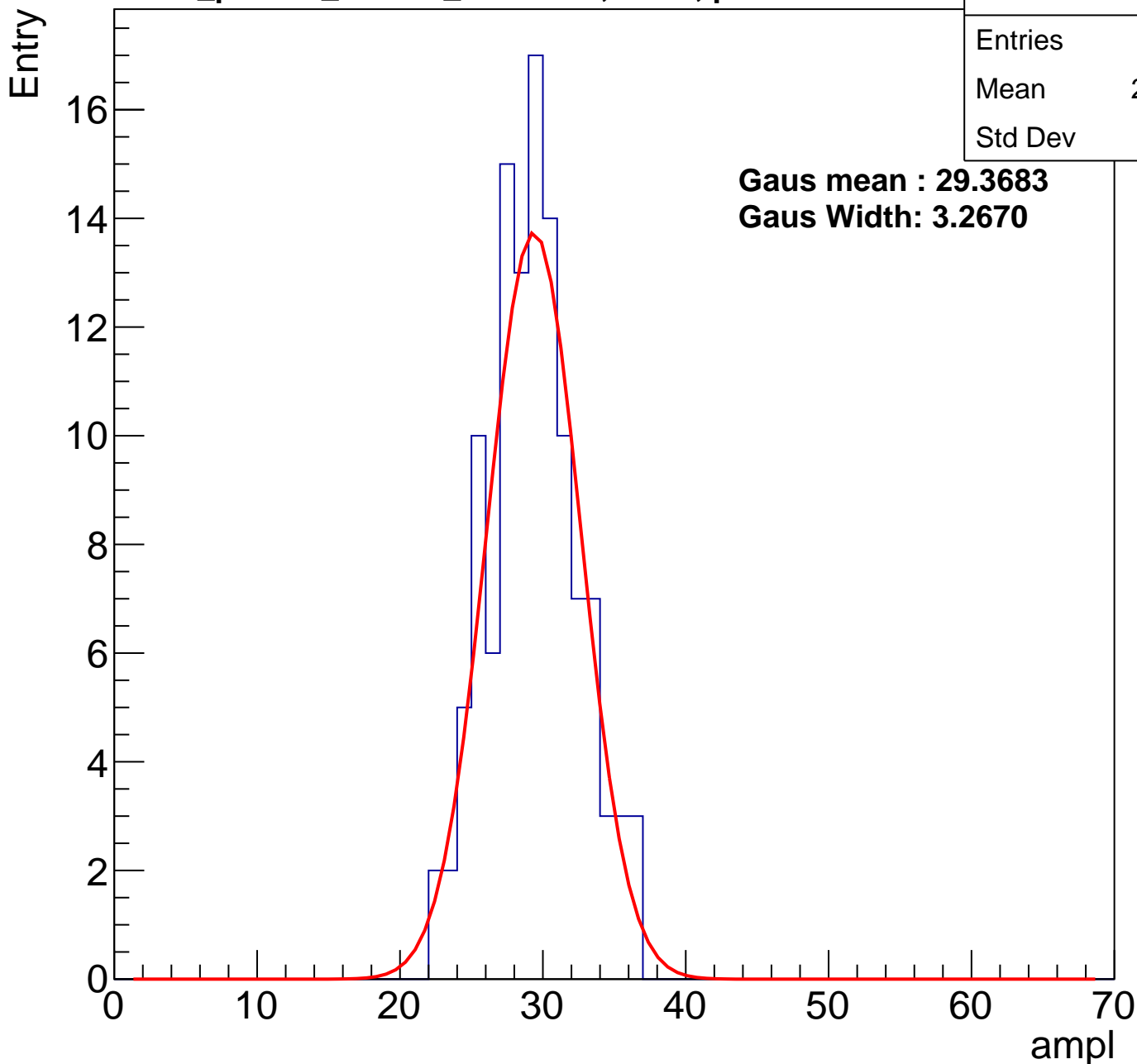
B0L101S, U20-ch60, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	28.87
Std Dev	3.12

Gaus mean : 29.3683

Gaus Width: 3.2670



B0L101S, U20-ch60, adc1

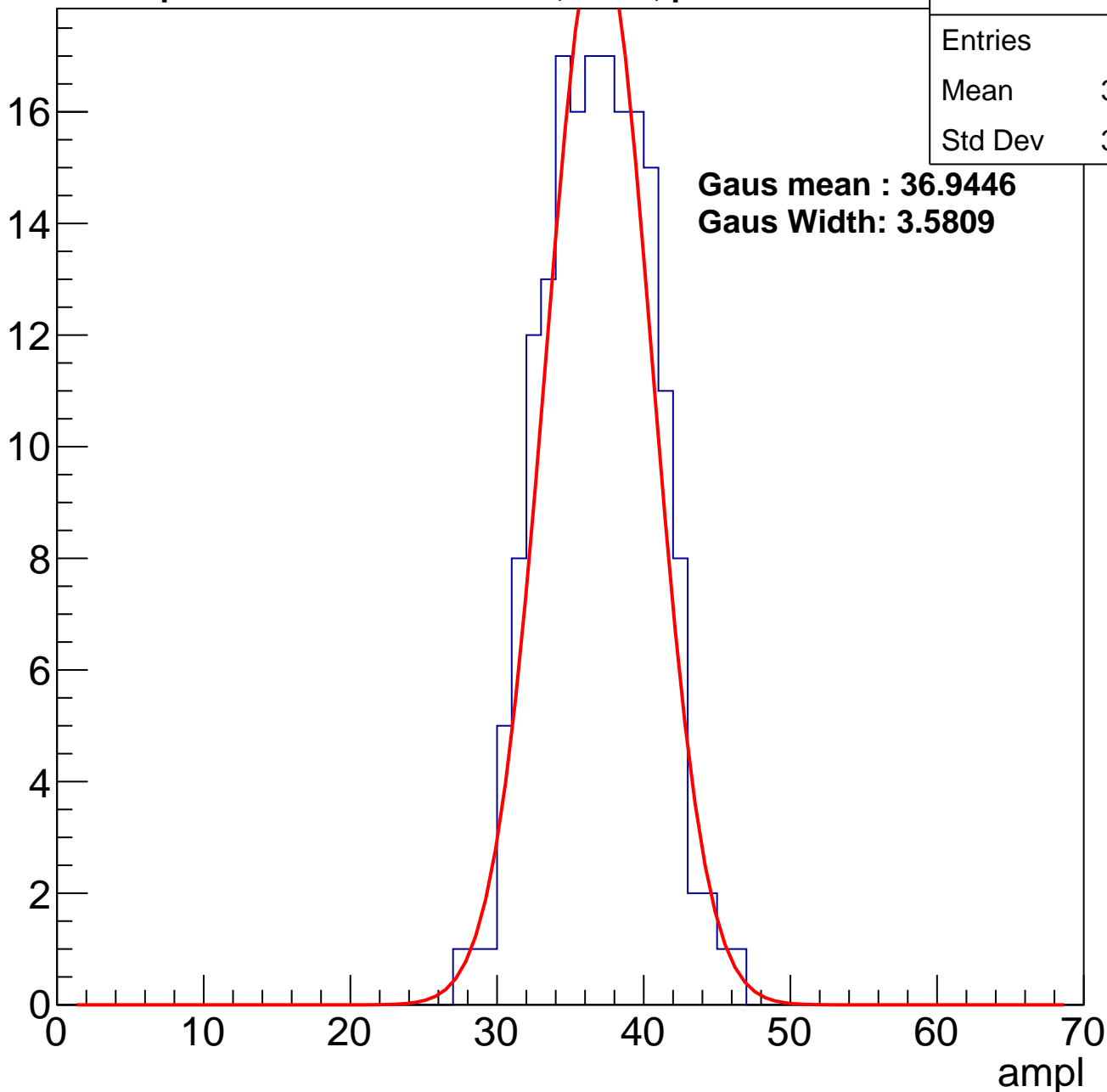
calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	36.43
Std Dev	3.627

Gaus mean : 36.9446

Gaus Width: 3.5809

Entry



B0L101S, U20-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

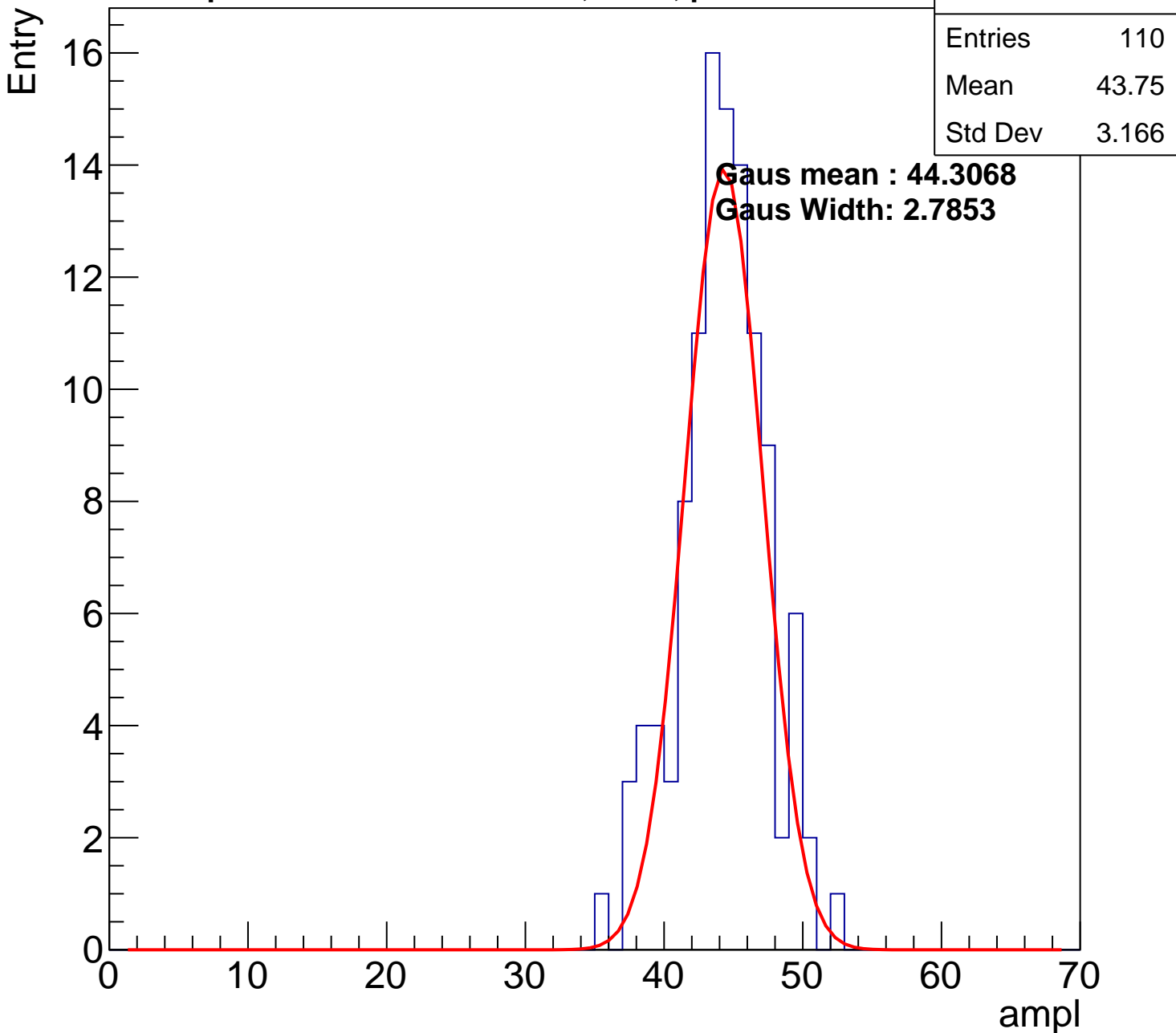
Entries	110
Mean	43.75
Std Dev	3.166

Gaus mean : 44.3068

Gaus Width: 2.7853

ampl

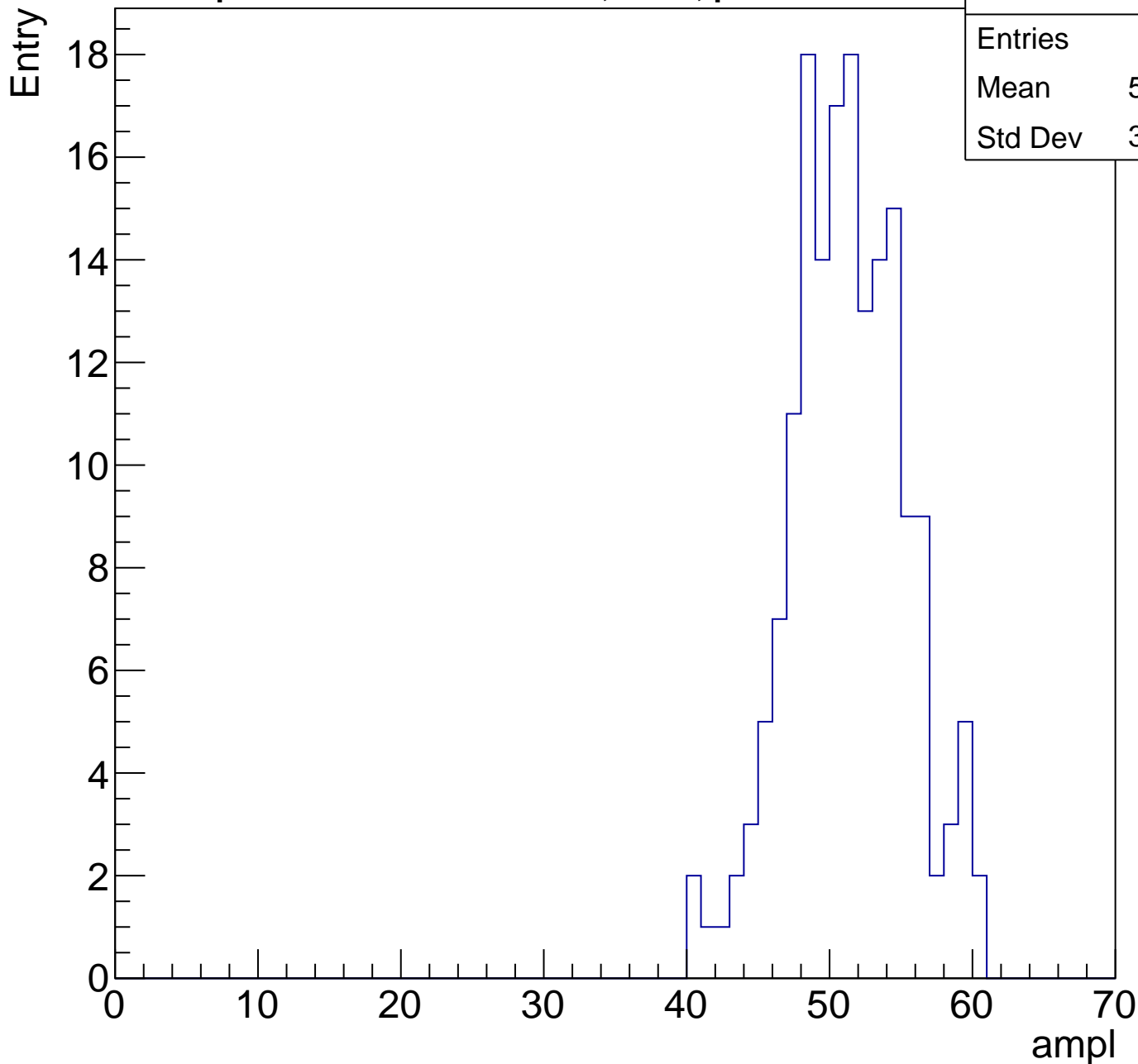
0 10 20 30 40 50 60 70



B0L101S, U20-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	50.84
Std Dev	3.998



B0L101S, U20-ch60, adc4

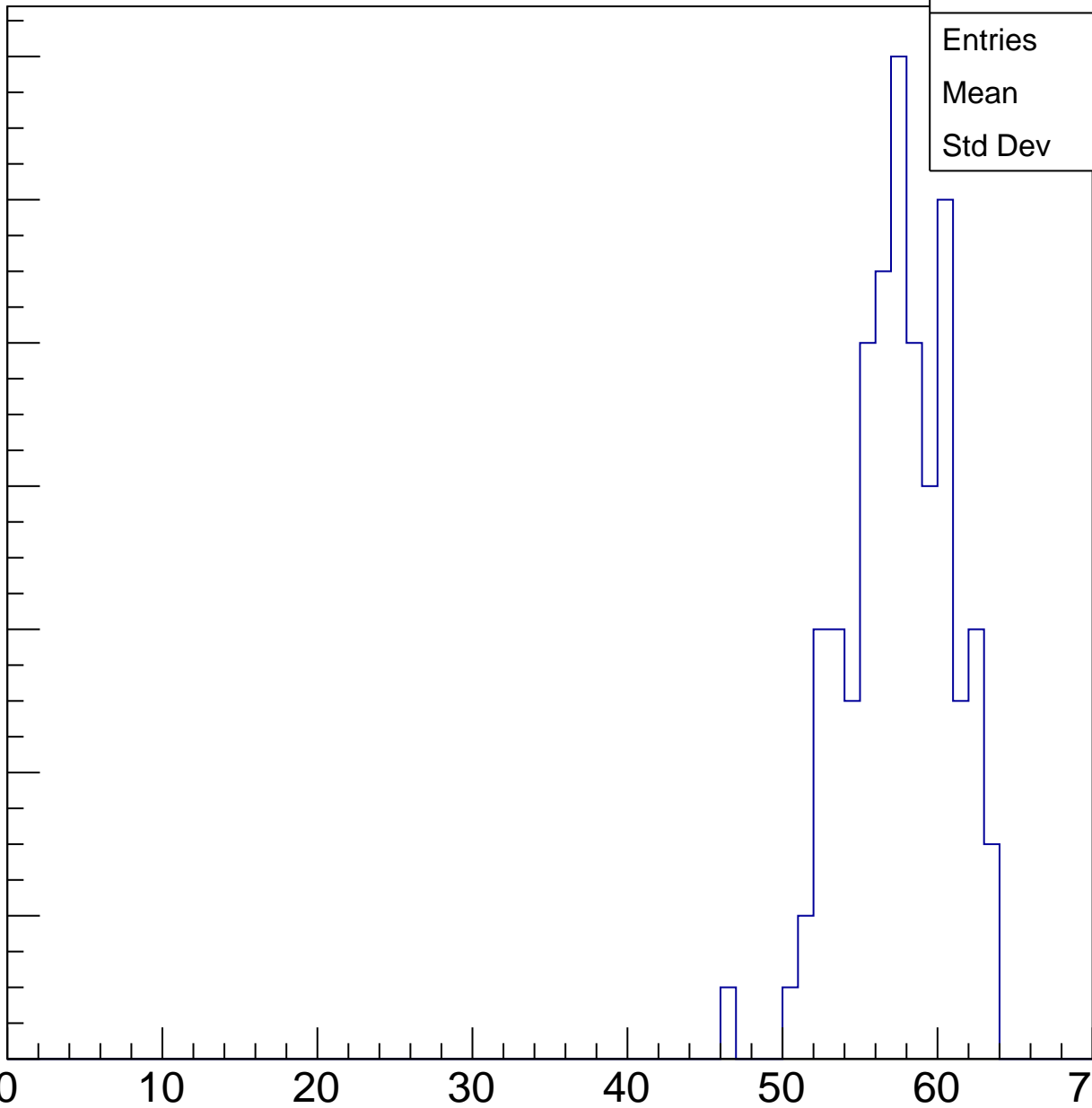
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	57
Std Dev	3.274

ampl



B0L101S, U20-ch60, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	64
Mean	58.7
Std Dev	10.72

ampl

0

10

20

30

40

50

60

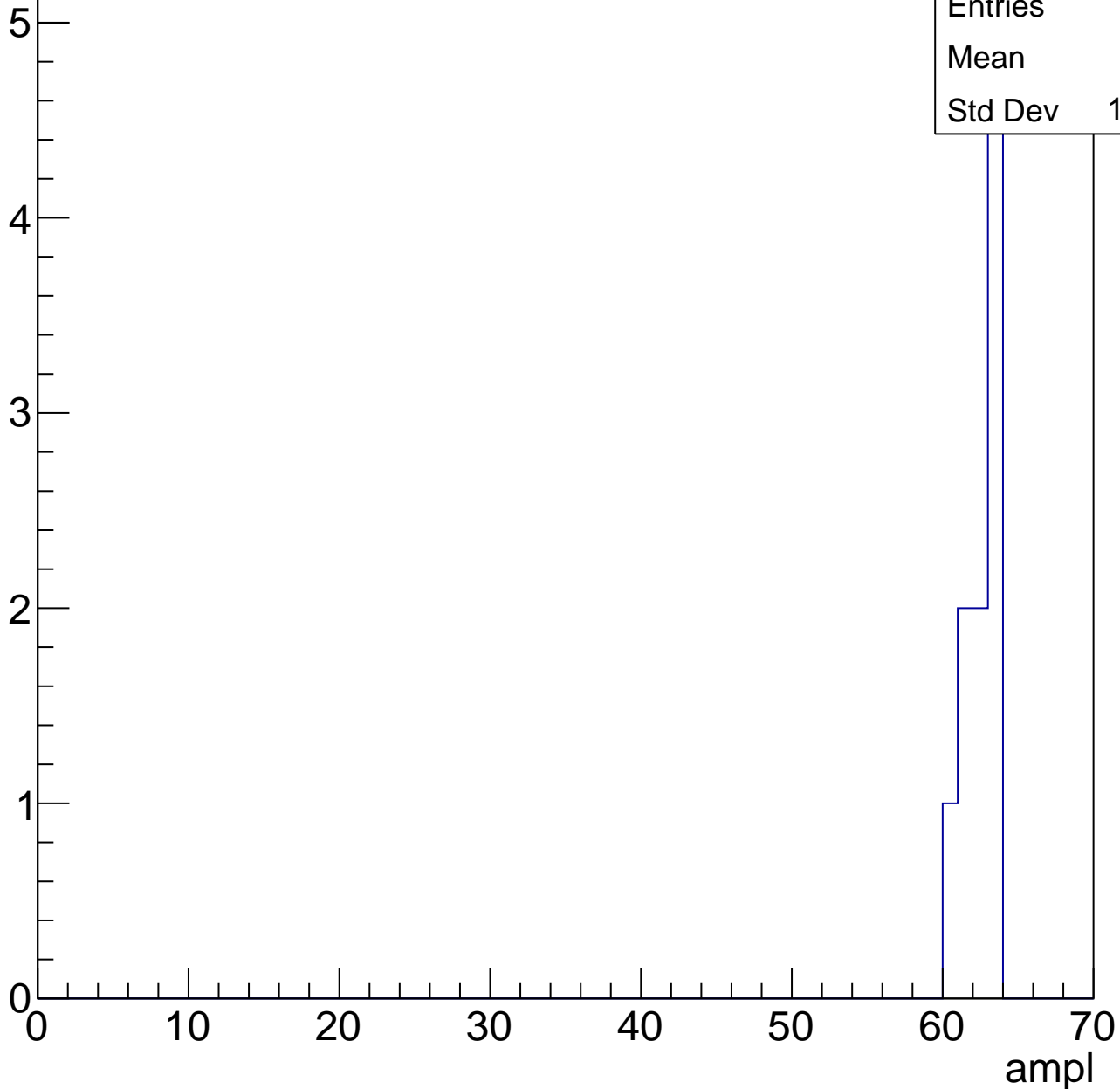
70

B0L101S, U20-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.1
Std Dev	1.044



B0L101S, U20-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch61, adc0

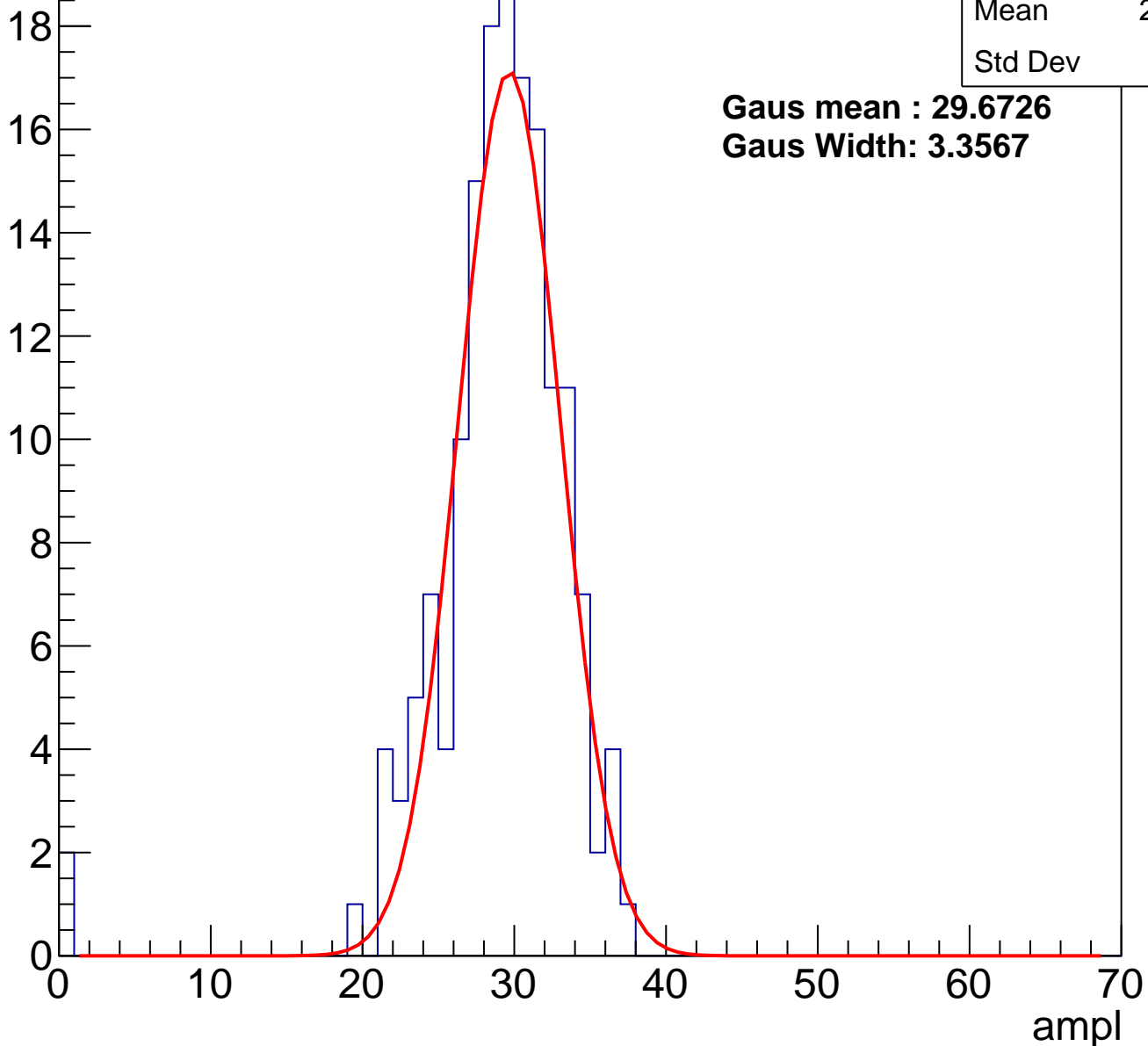
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	28.55
Std Dev	4.79

Gaus mean : 29.6726

Gaus Width: 3.3567

Entry



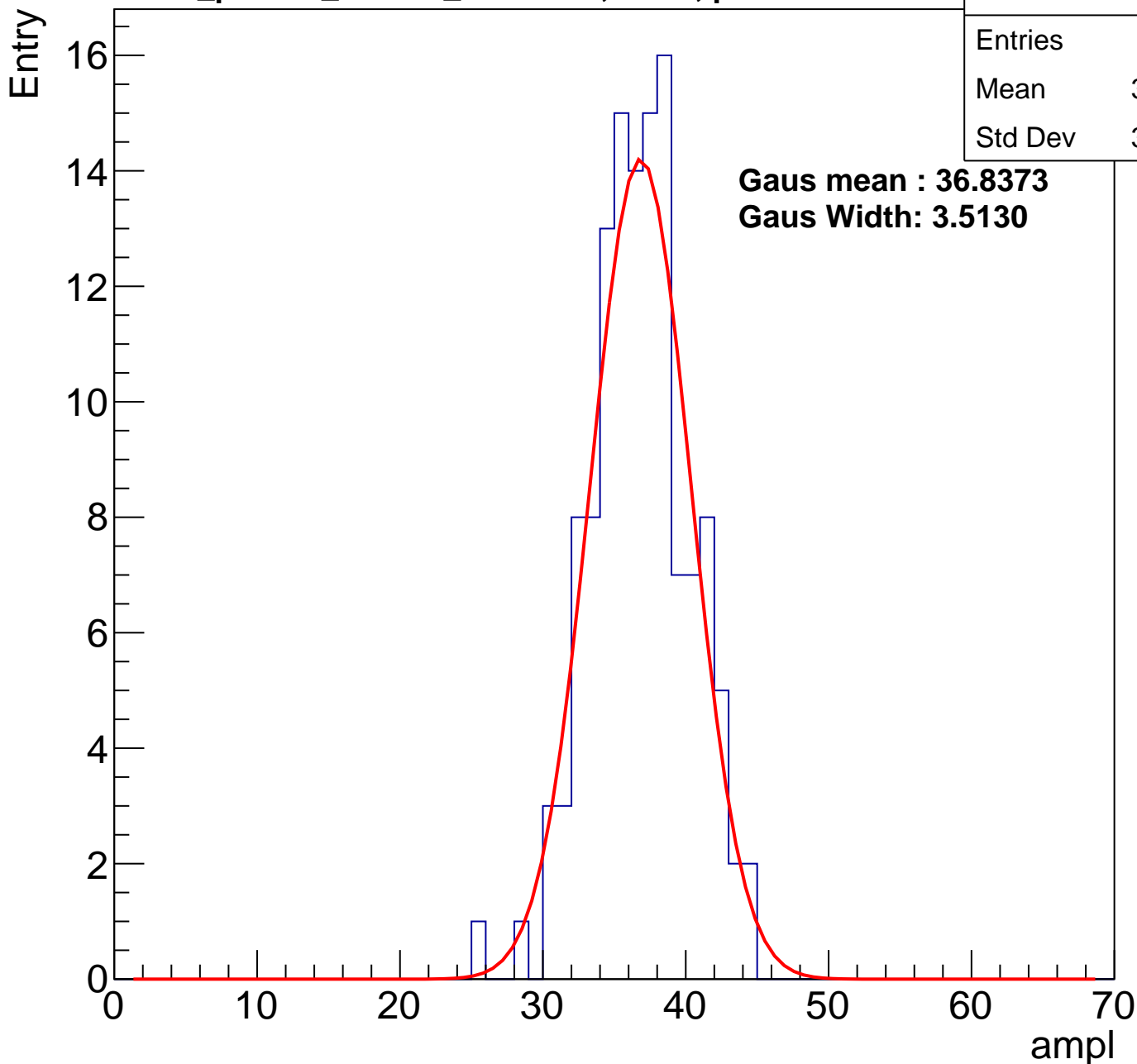
B0L101S, U20-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	36.37
Std Dev	3.398

Gaus mean : 36.8373

Gaus Width: 3.5130



B0L101S, U20-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	42.59
Std Dev	3.662

Gaus mean : 43.0020

Gaus Width: 3.5826

0

10

20

30

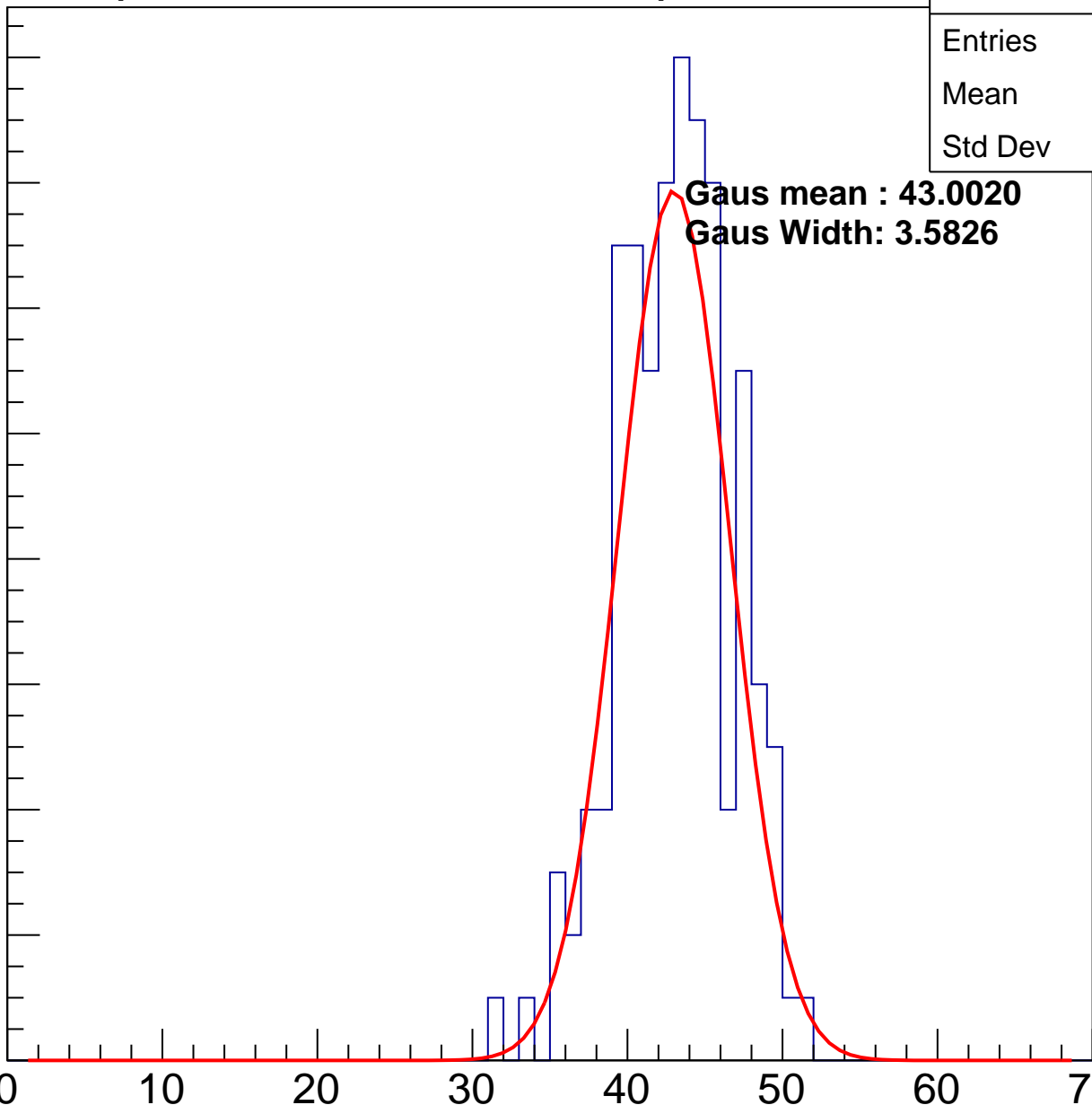
40

50

60

70

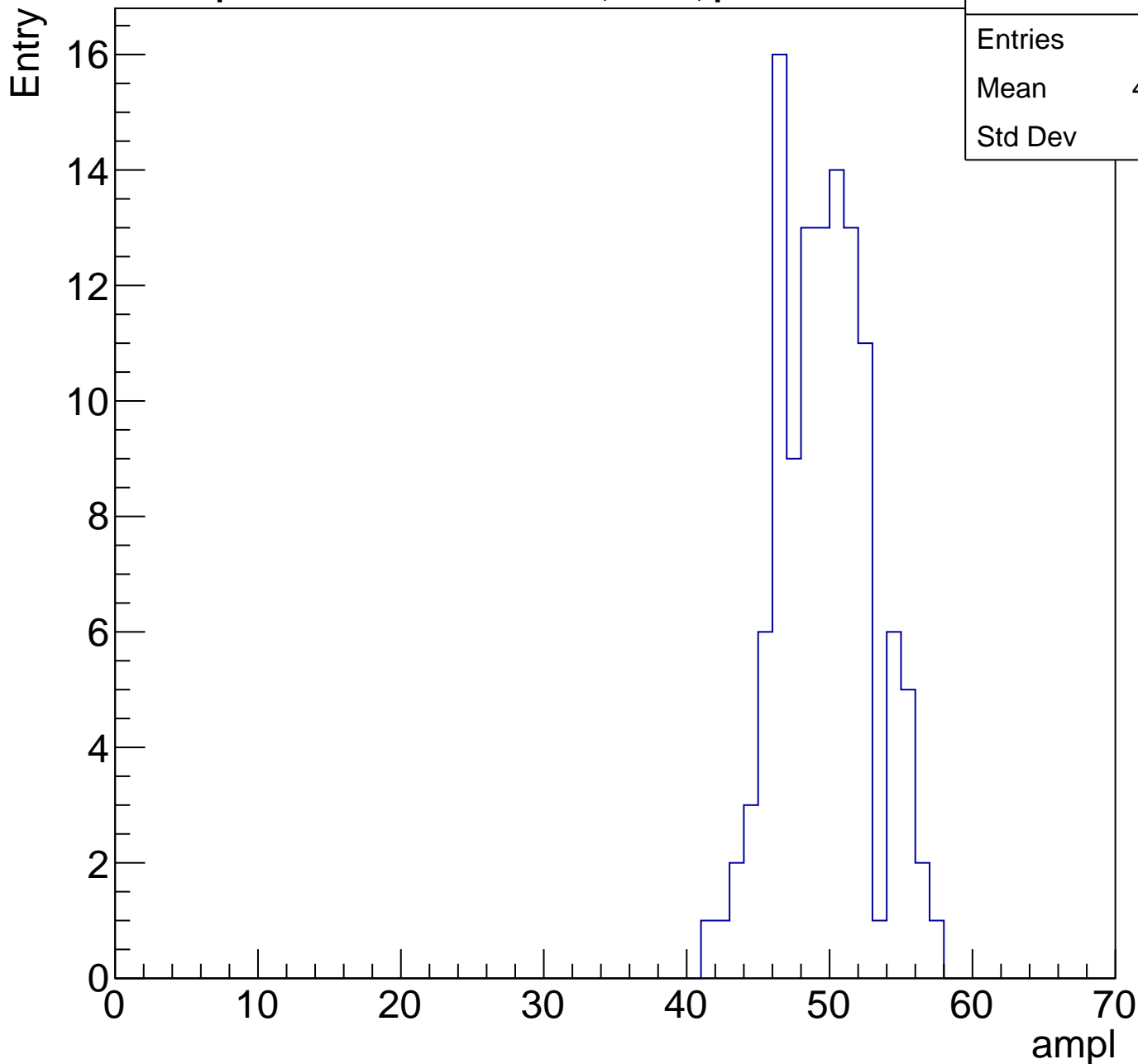
ampl



B0L101S, U20-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

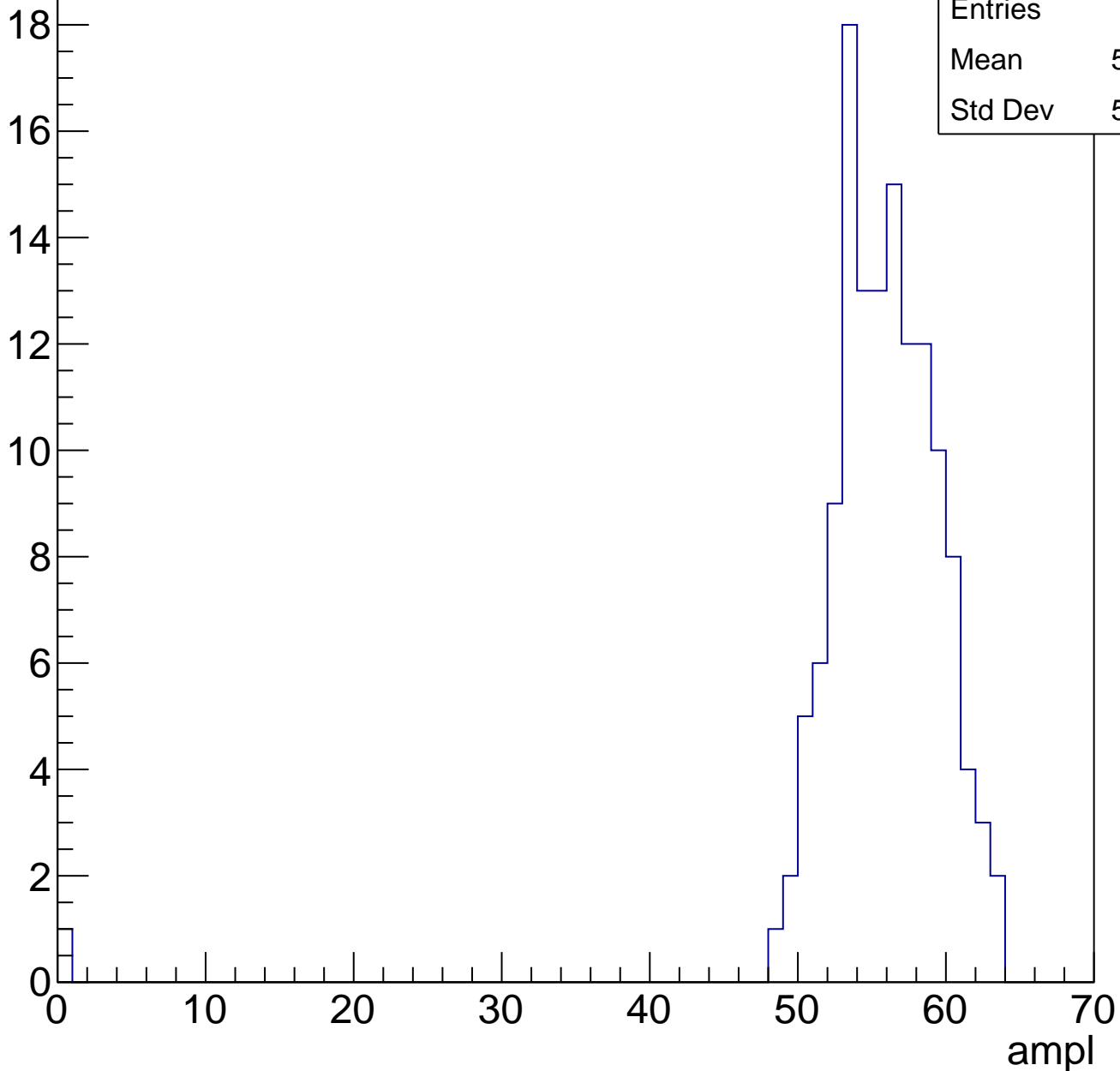
Entries	117
Mean	49.12
Std Dev	3.23



B0L101S, U20-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch61, adc5

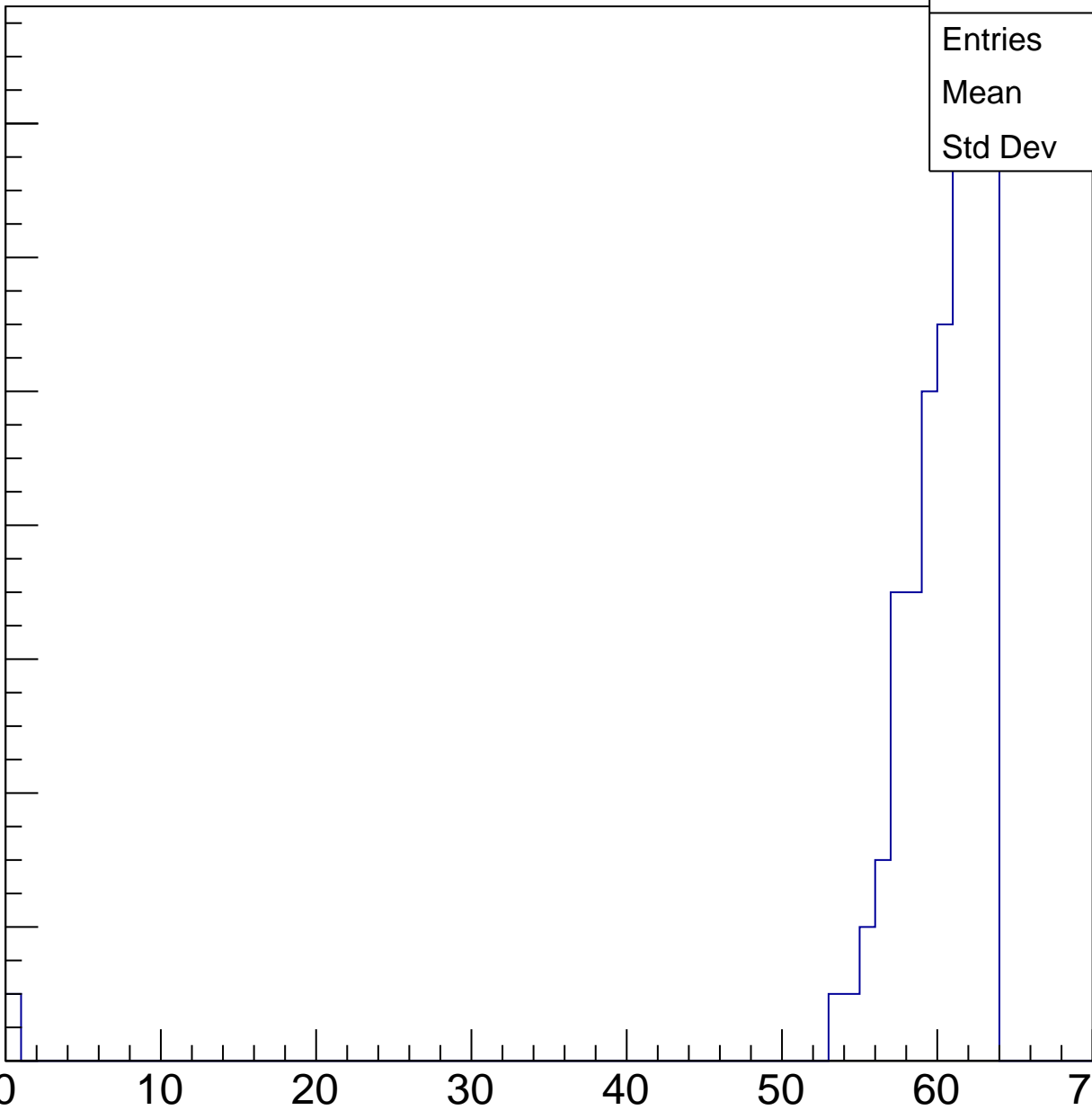
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	59.36
Std Dev	6.859

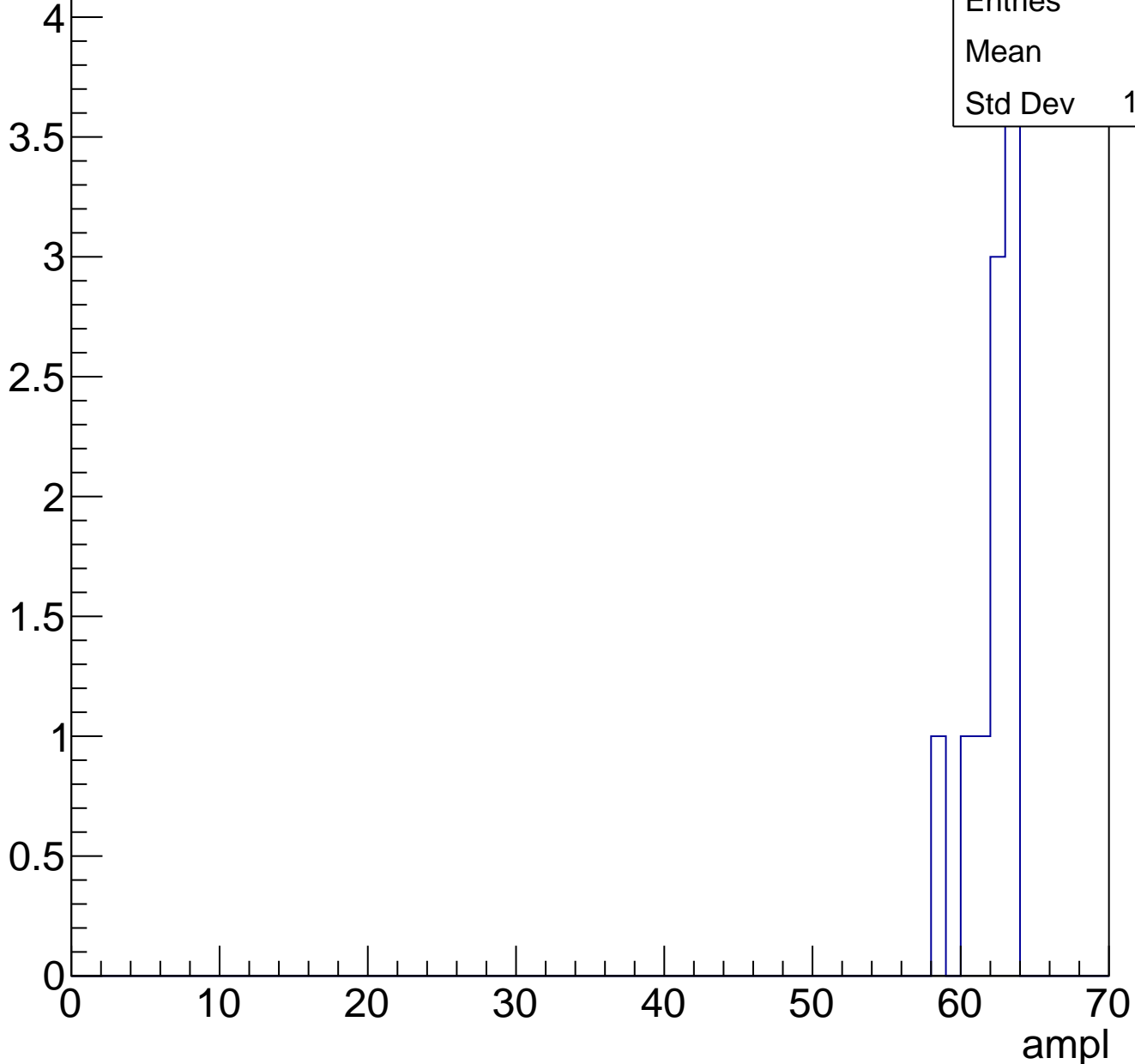
ampl



B0L101S, U20-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U20-ch62, adc0

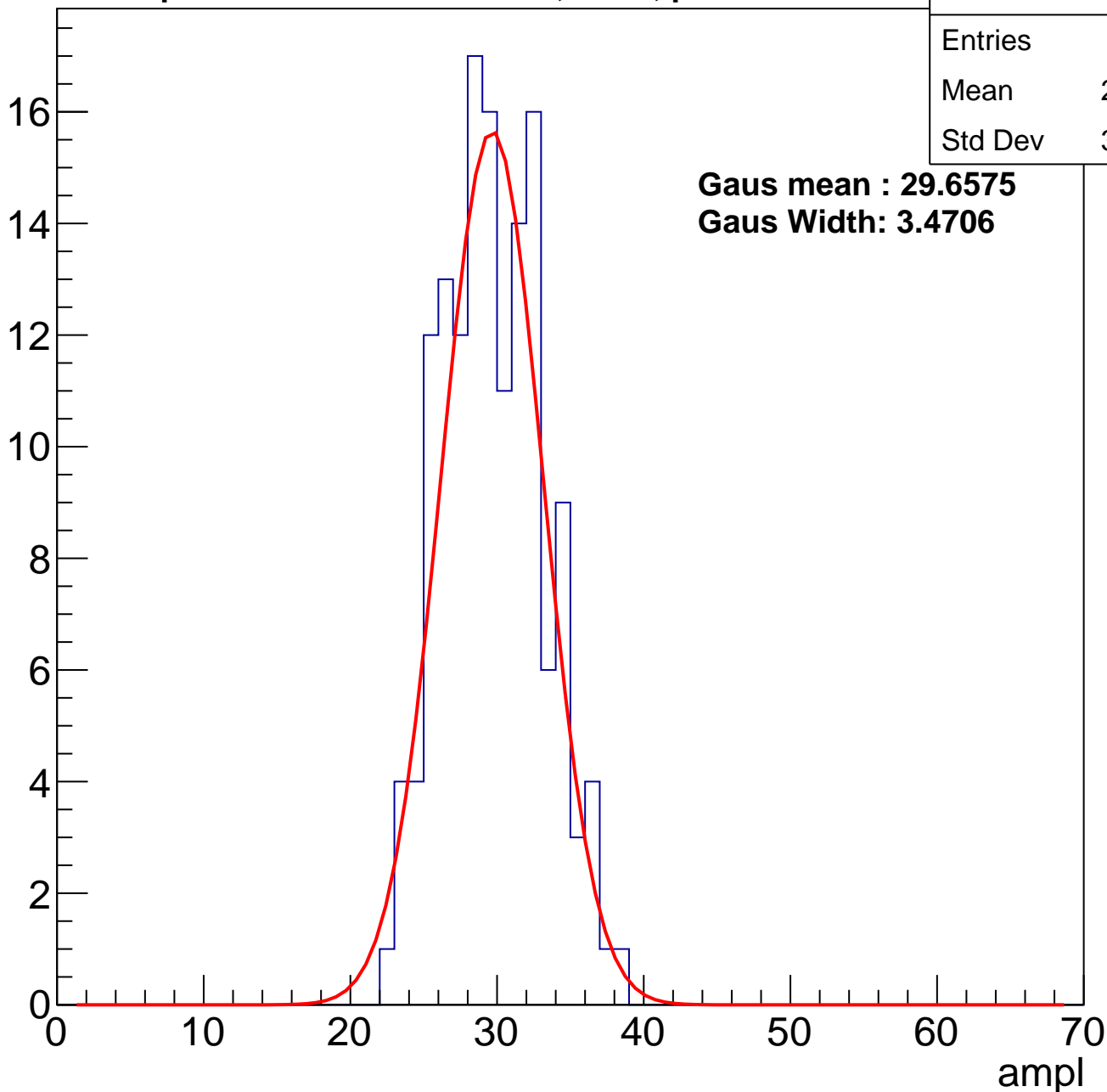
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.28
Std Dev	3.372

Gaus mean : 29.6575

Gaus Width: 3.4706

Entry



B0L101S, U20-ch62, adc1

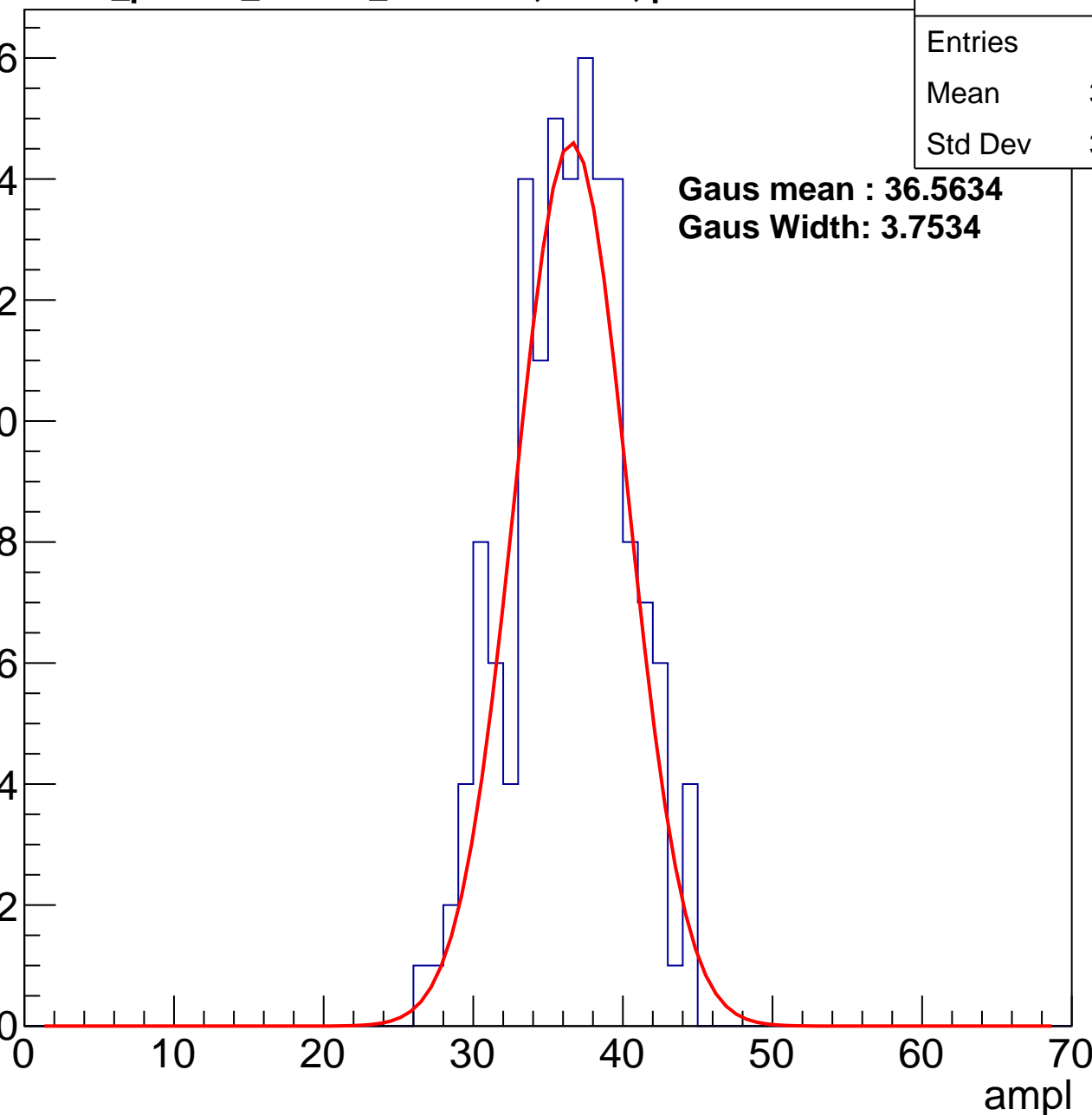
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	150
Mean	35.95
Std Dev	3.842

Gaus mean : 36.5634
Gaus Width: 3.7534



B0L101S, U20-ch62, adc2

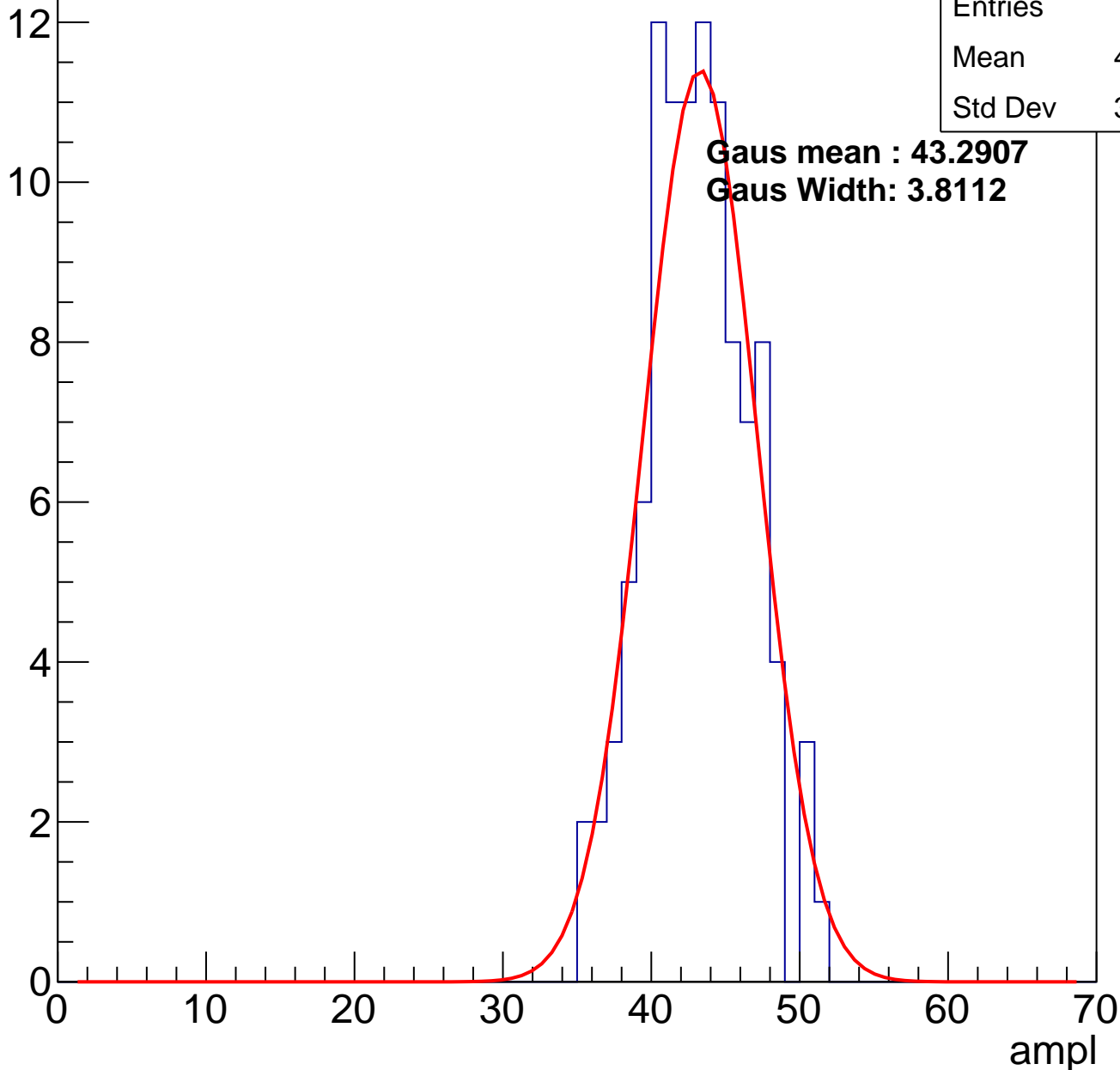
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	42.65
Std Dev	3.434

Gaus mean : 43.2907

Gaus Width: 3.8112

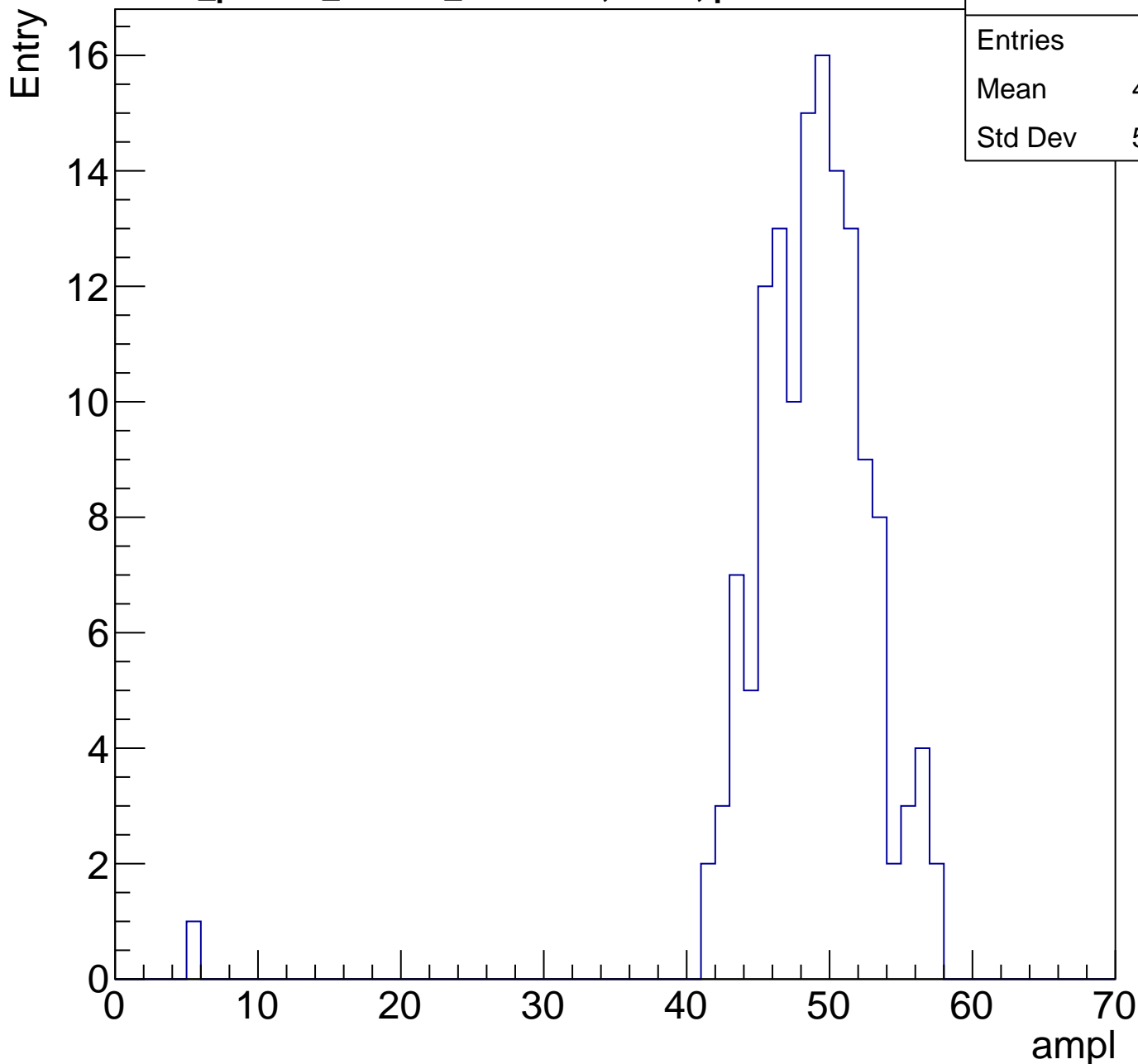
Entry



B0L101S, U20-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

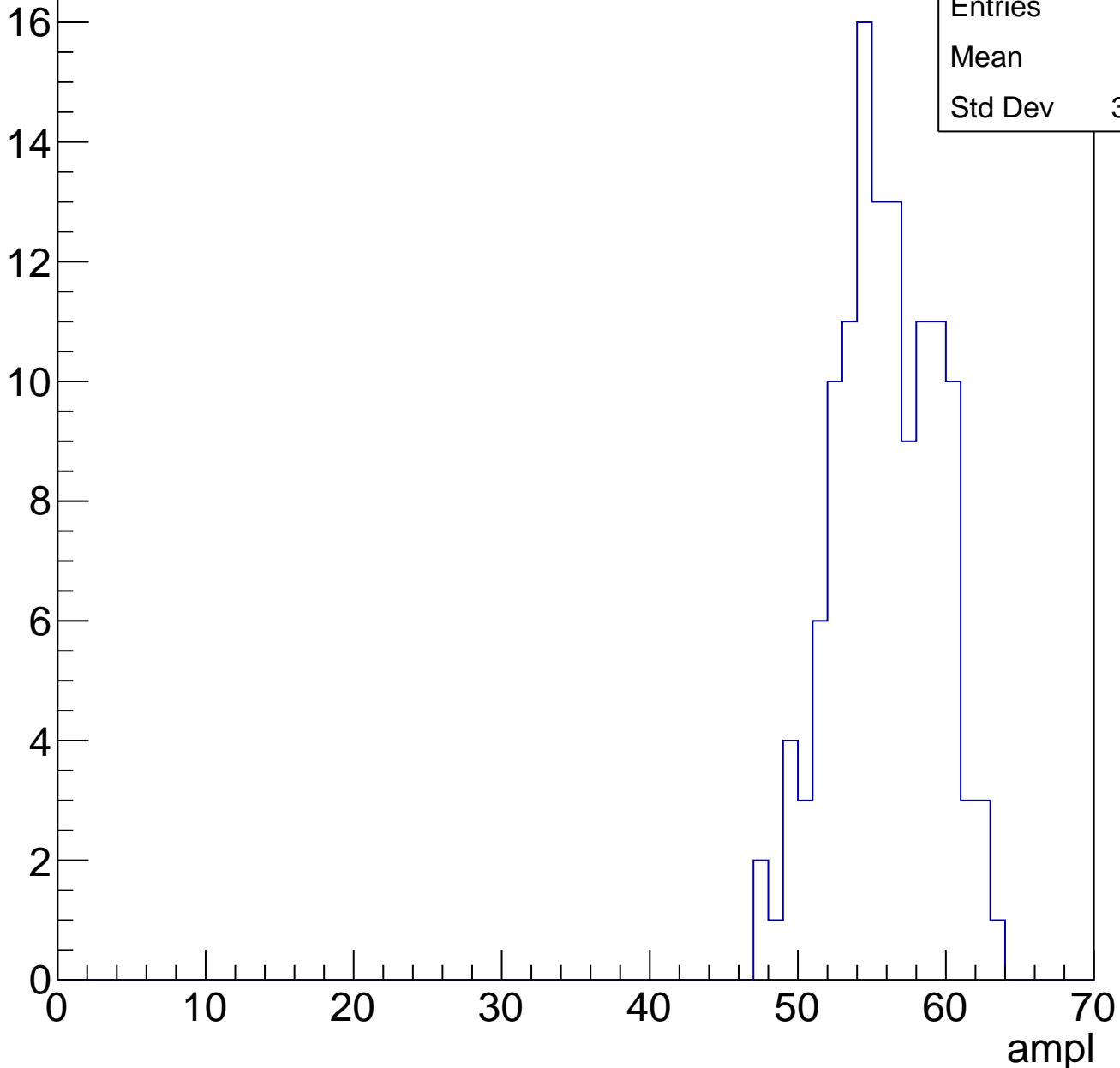
Entries	139
Mean	48.29
Std Dev	5.116



B0L101S, U20-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch62, adc5

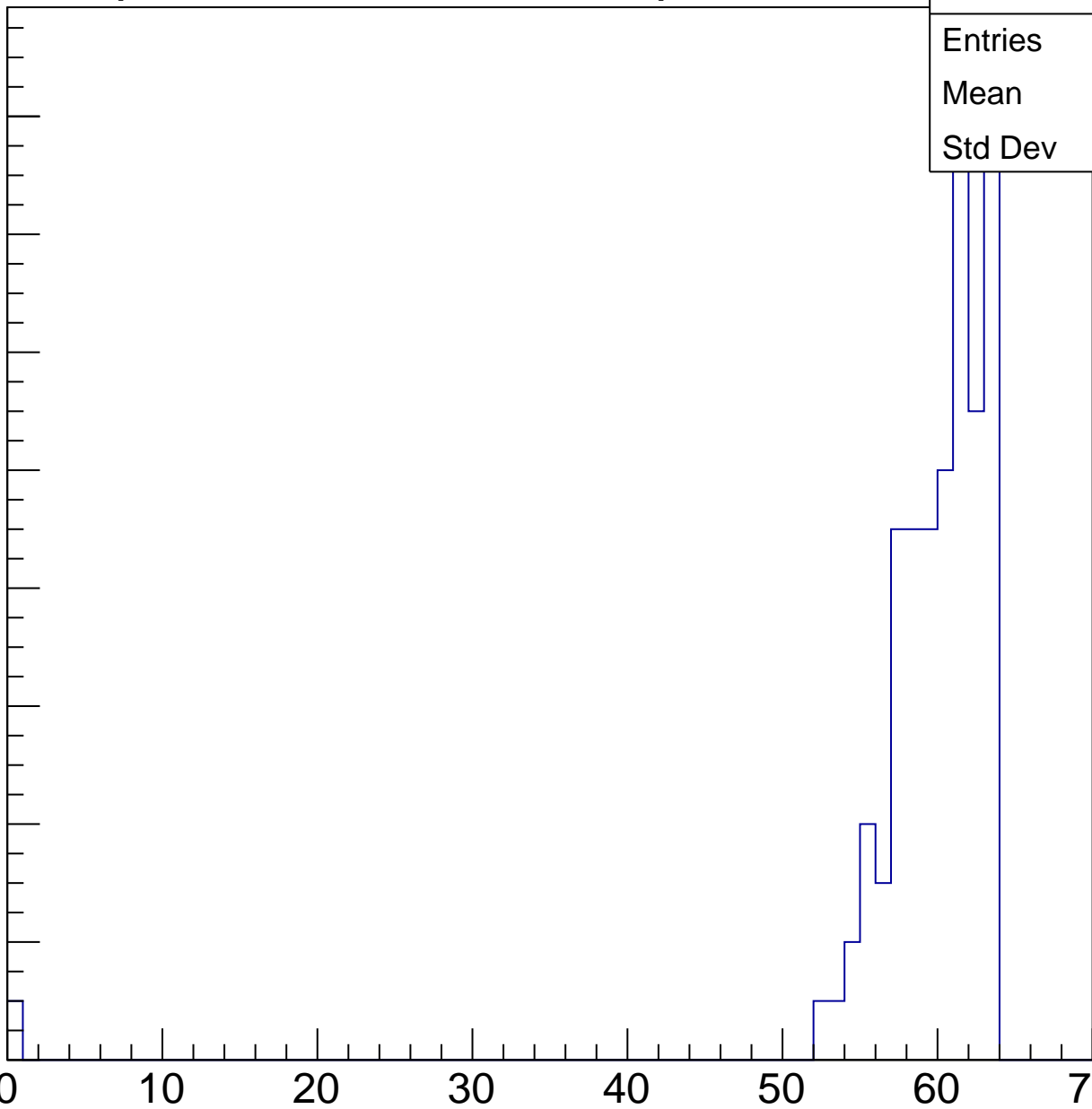
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.1
Std Dev	6.717

ampl

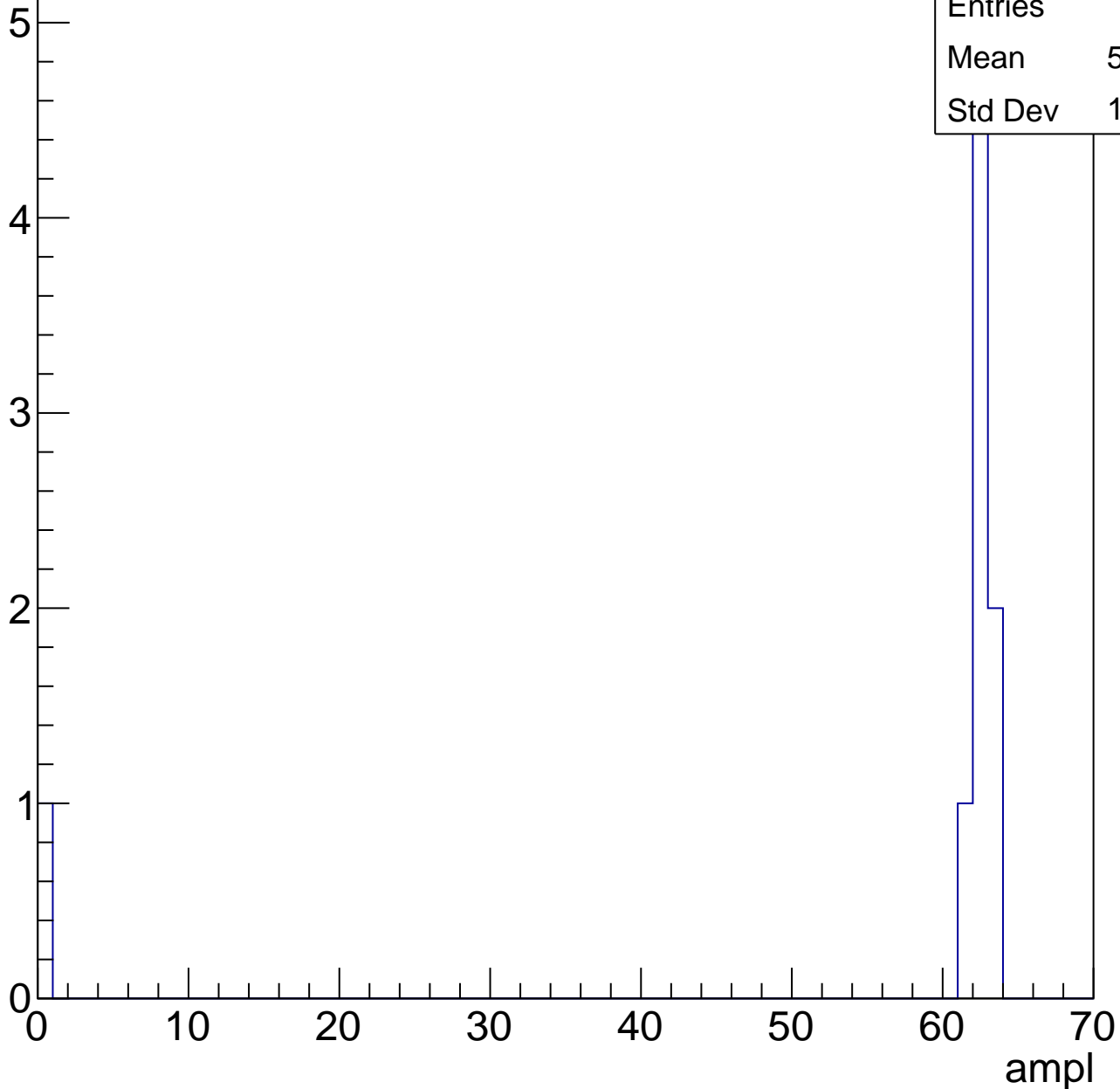


B0L101S, U20-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

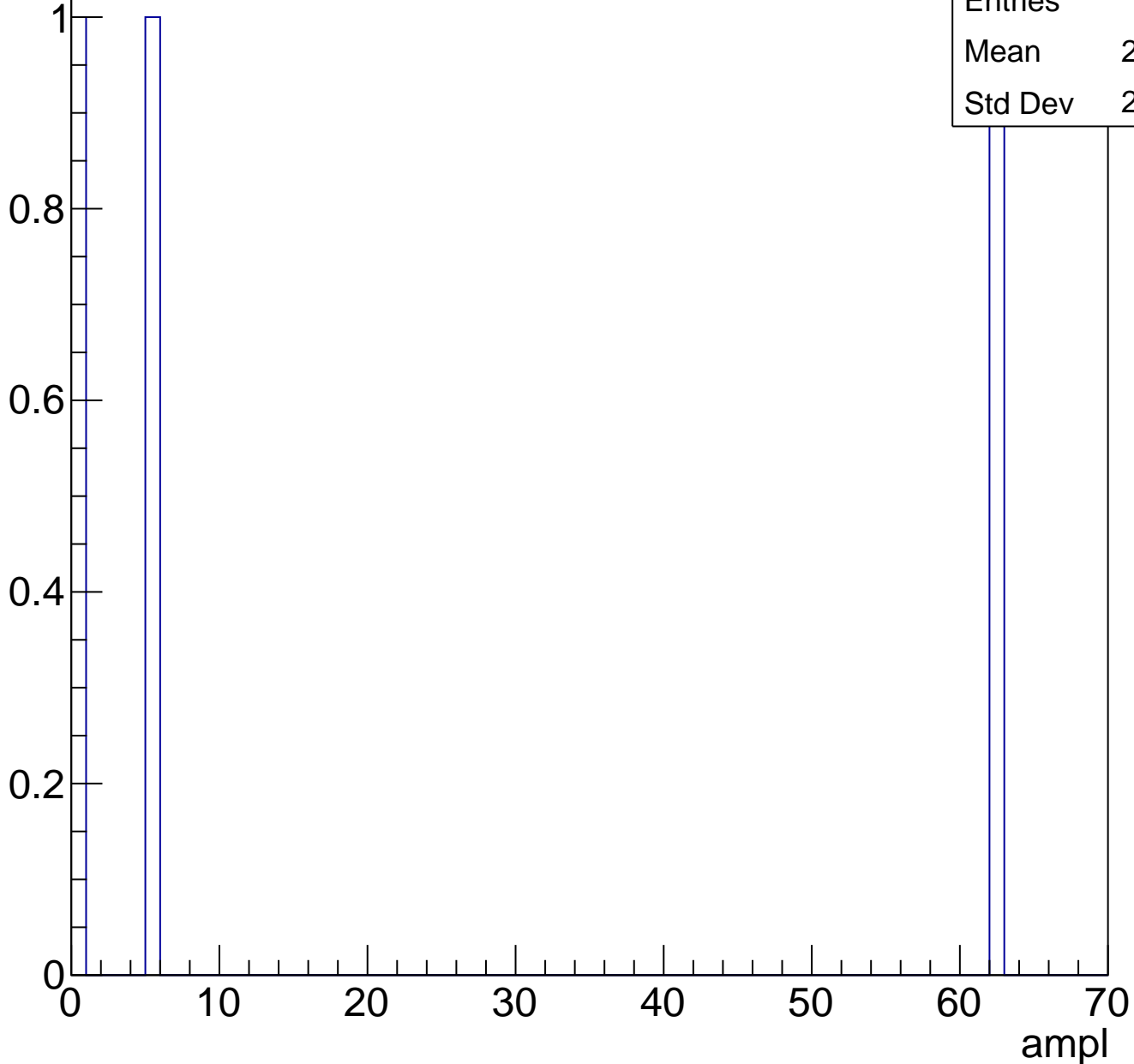
Entries	9
Mean	55.22
Std Dev	19.53



B0L101S, U20-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	22.33
Std Dev	28.12

B0L101S, U20-ch63, adc0

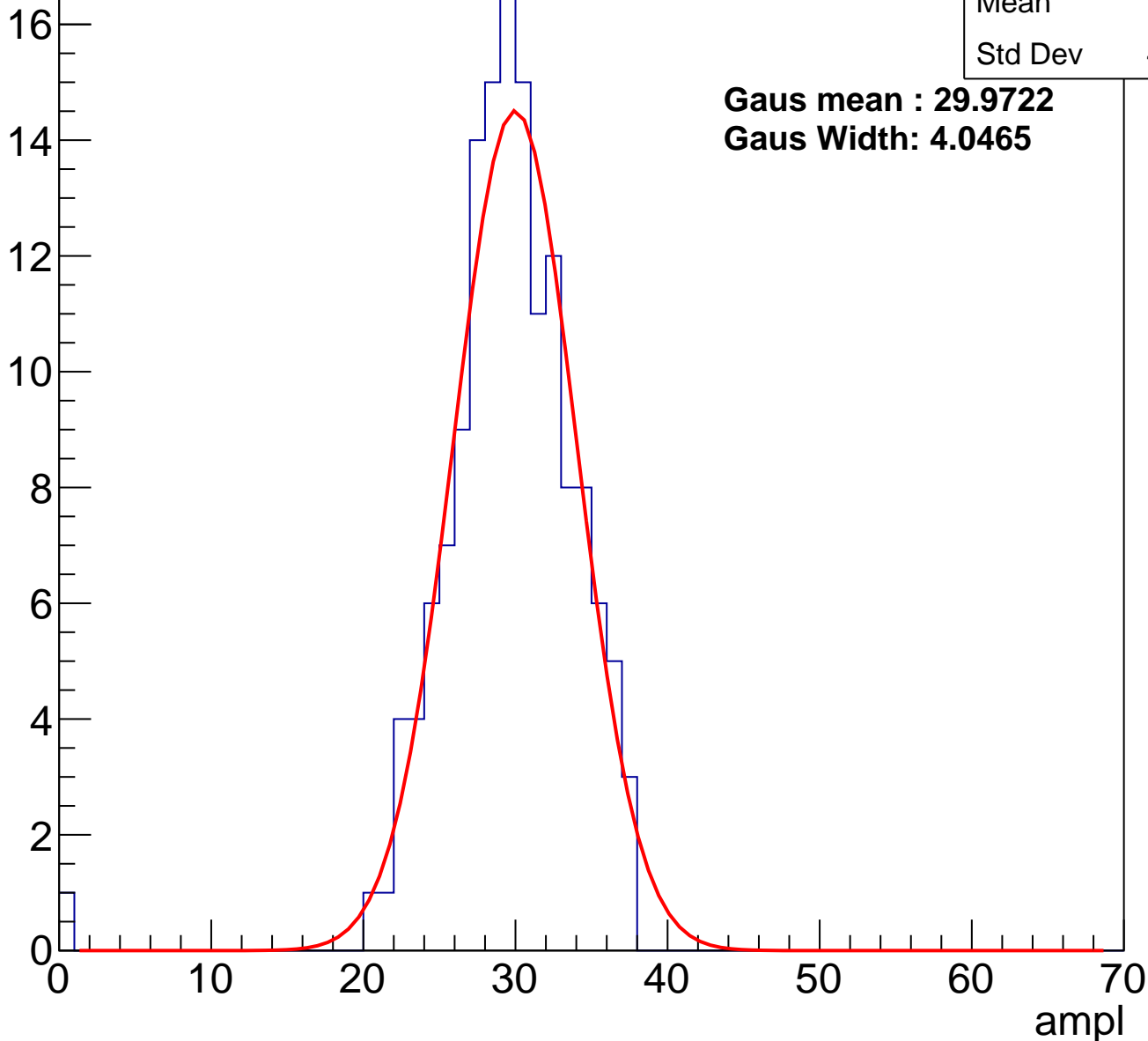
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	147
Mean	29.1
Std Dev	4.431

Gaus mean : 29.9722

Gaus Width: 4.0465



B0L101S, U20-ch63, adc1

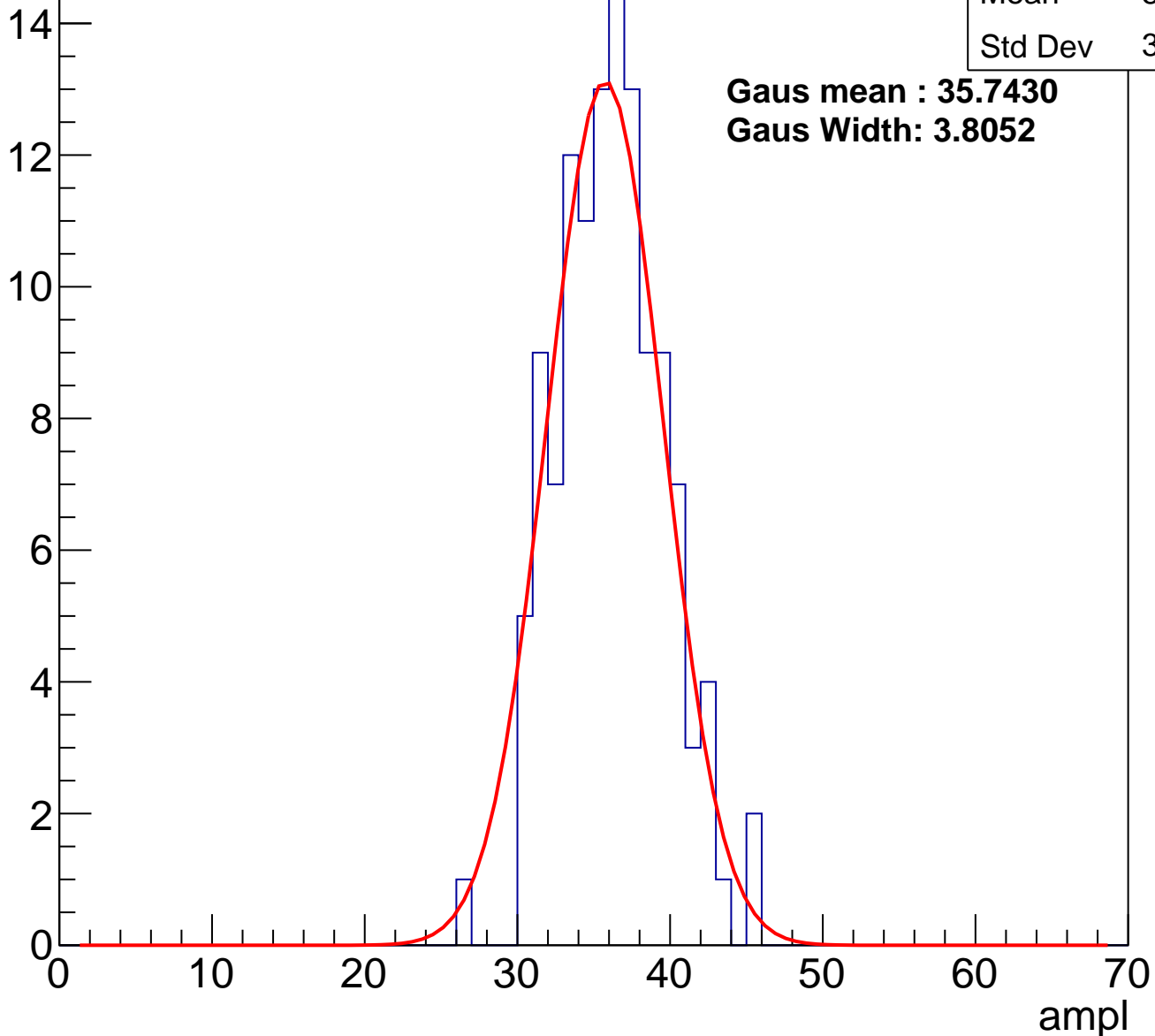
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	35.72
Std Dev	3.455

Gaus mean : 35.7430

Gaus Width: 3.8052

Entry



B0L101S, U20-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	42.43
Std Dev	4.832

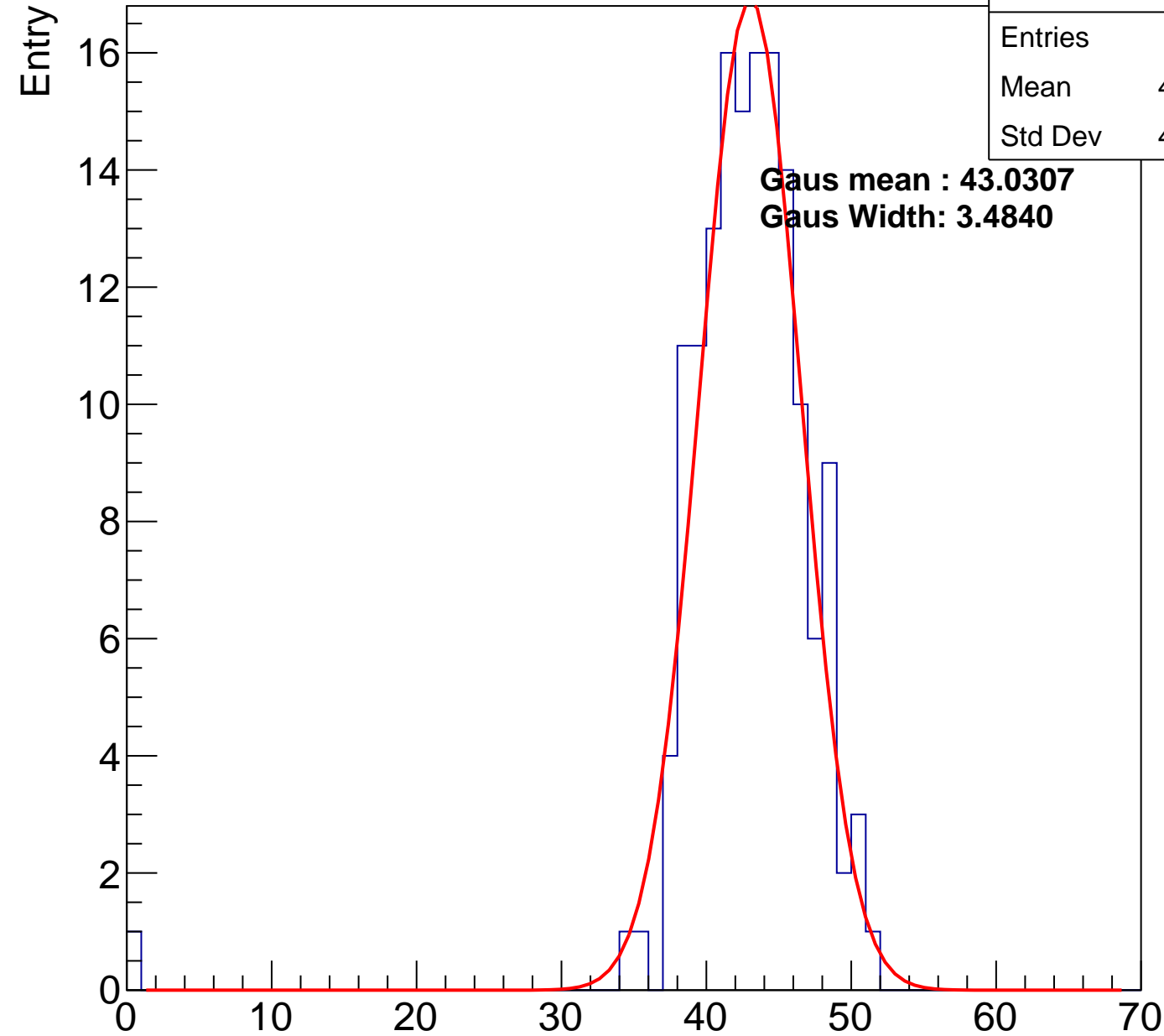
Gaus mean : 43.0307

Gaus Width: 3.4840

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

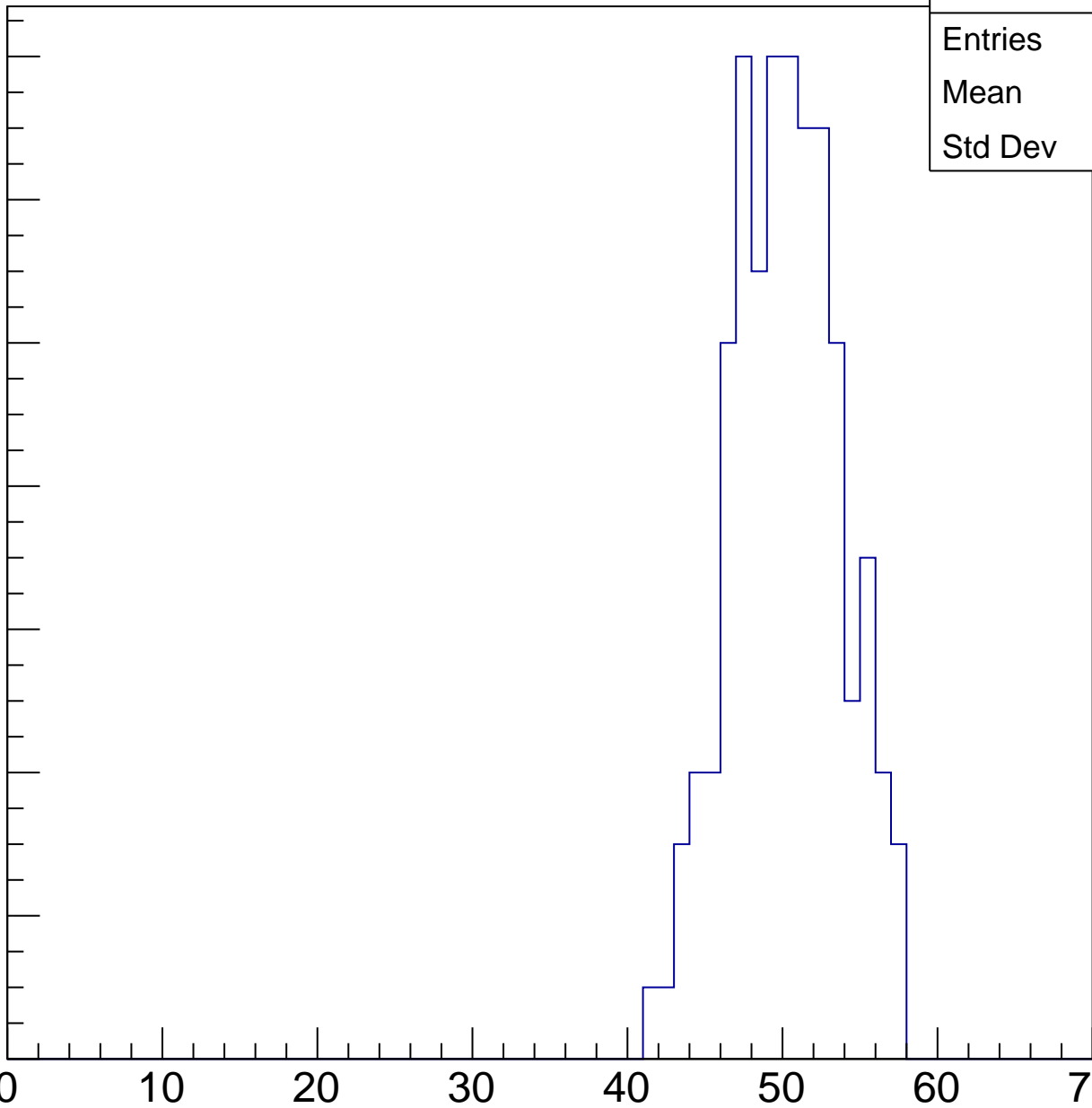
Entries	131
Mean	49.76
Std Dev	3.466

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

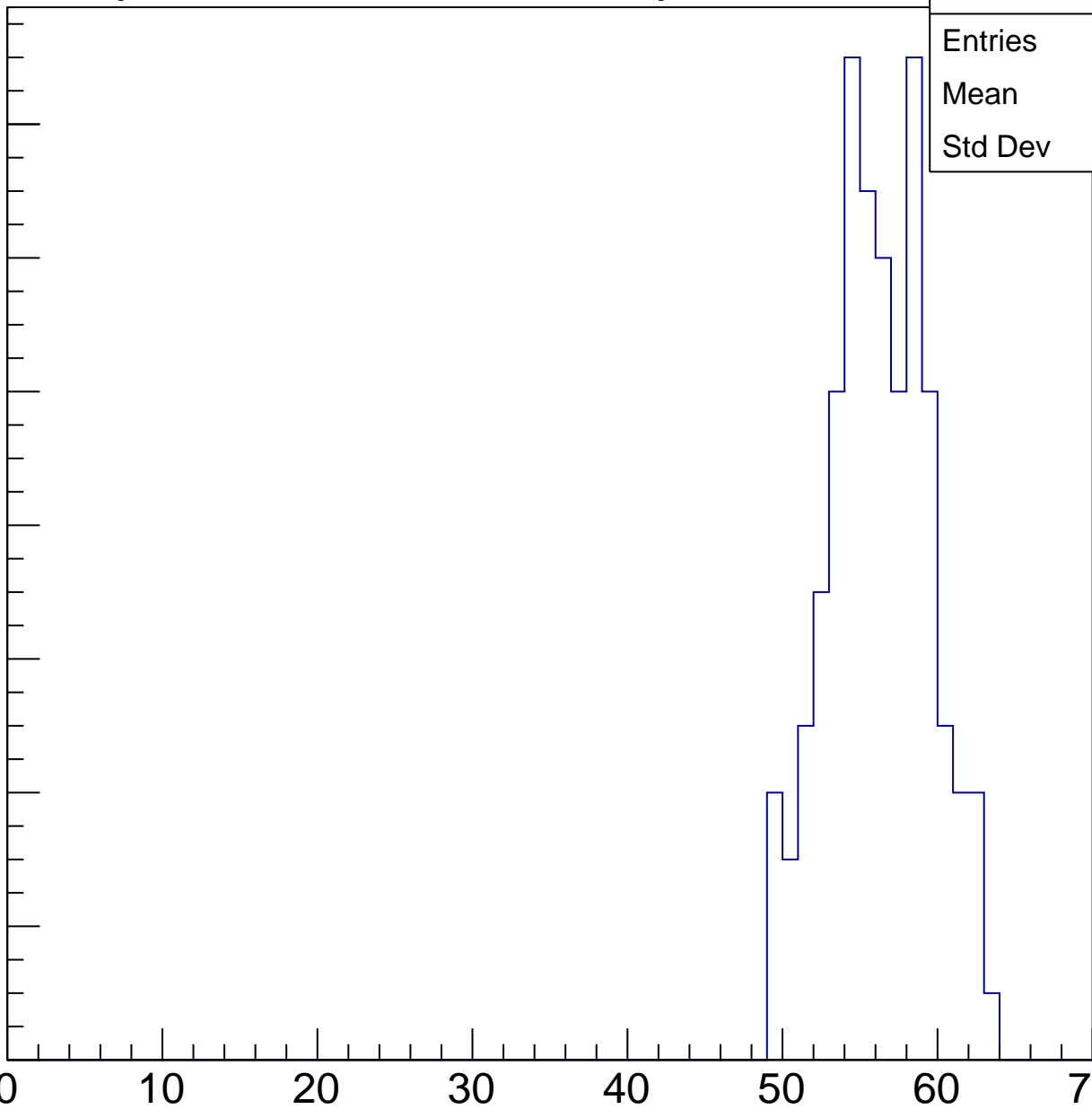
Entries	118
Mean	55.74
Std Dev	3.227

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch63, adc5

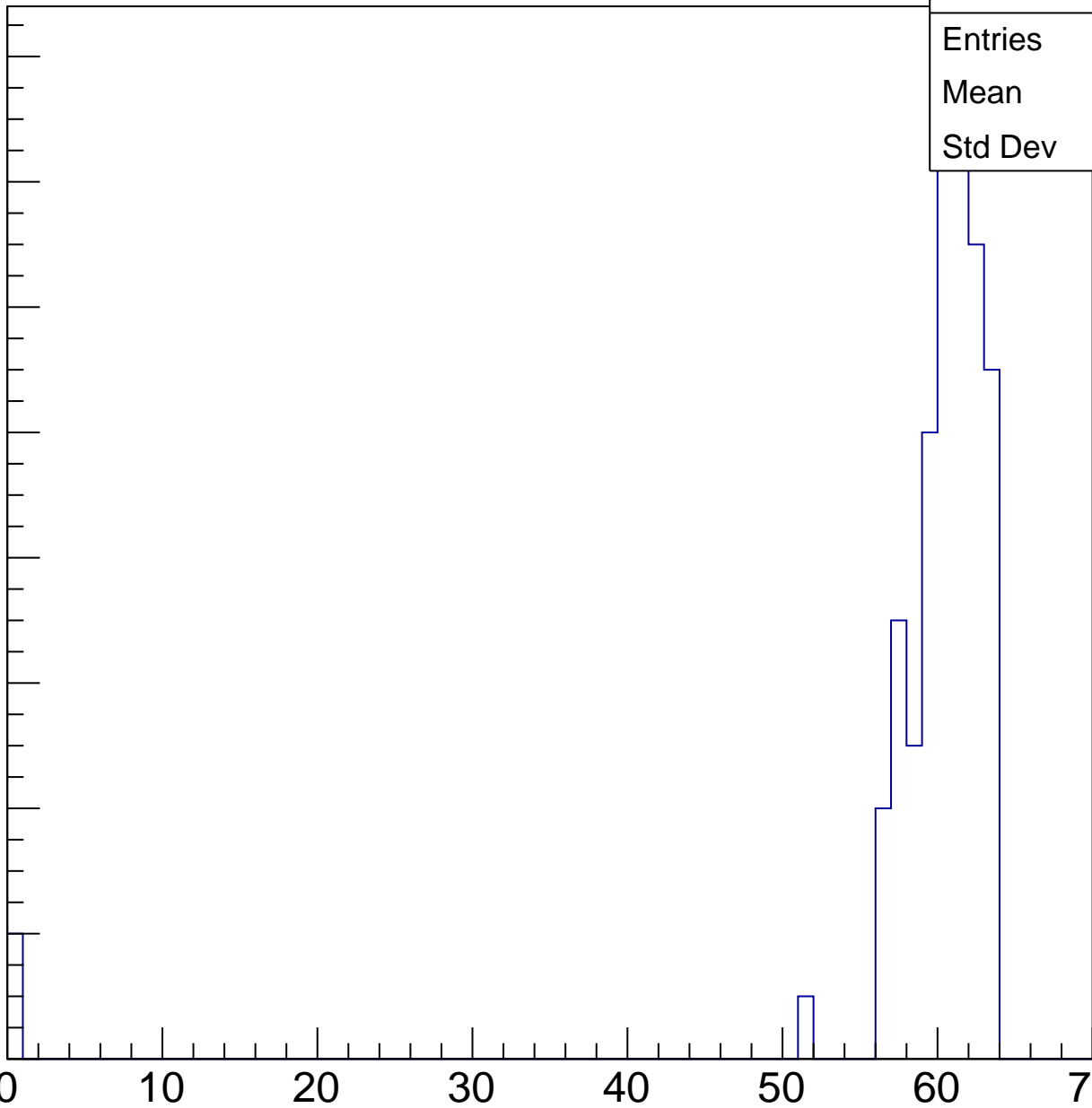
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.68
Std Dev	9.423

ampl

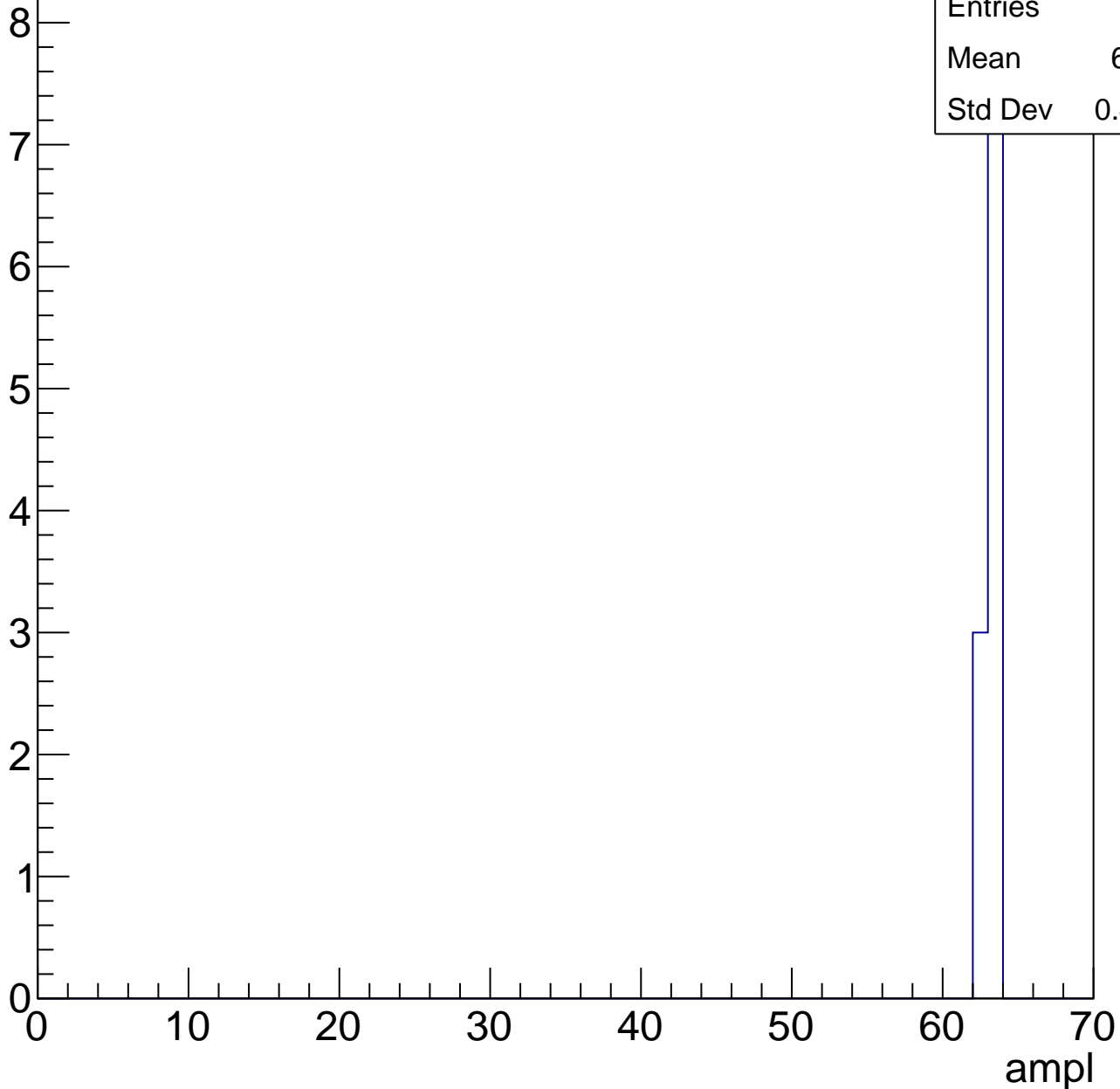


B0L101S, U20-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.73
Std Dev	0.4454



B0L101S, U20-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.42
Std Dev	3.777

Gaus mean : 29.4350

Gaus Width: 4.0704

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

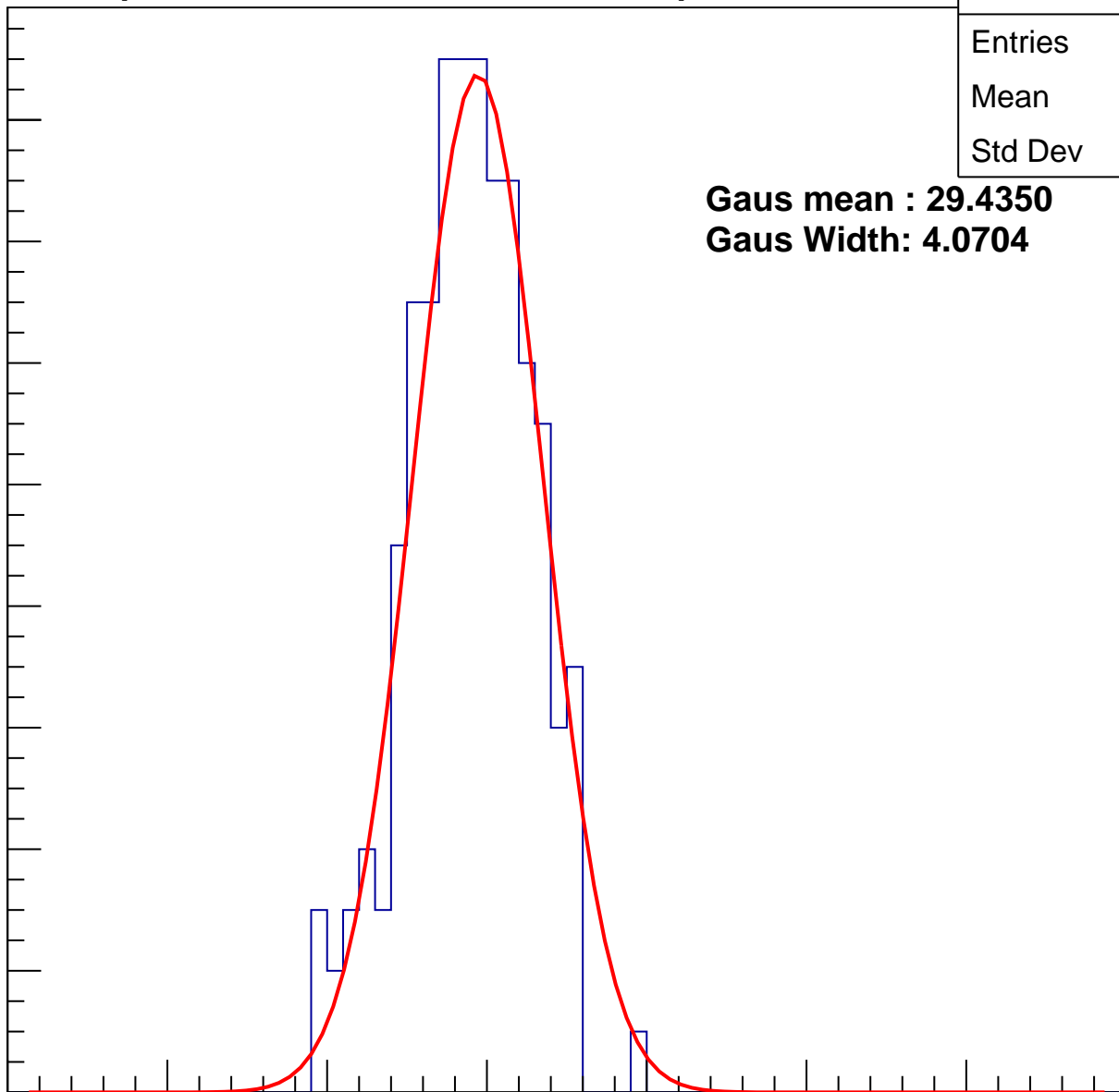
40

50

60

70

ampl

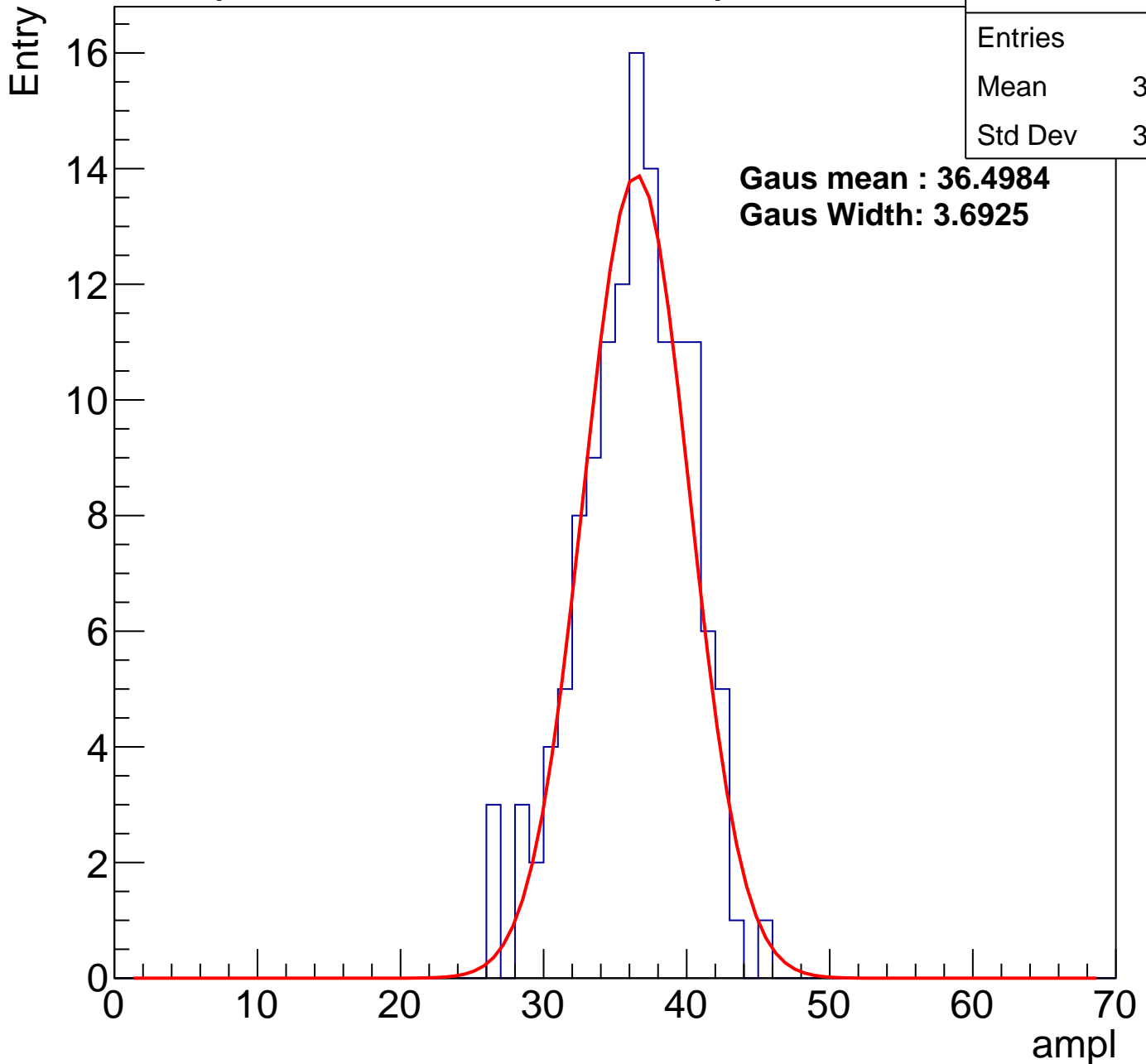


B0L101S, U20-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.84
Std Dev	3.793

Gaus mean : 36.4984
Gaus Width: 3.6925



B0L101S, U20-ch64, adc2

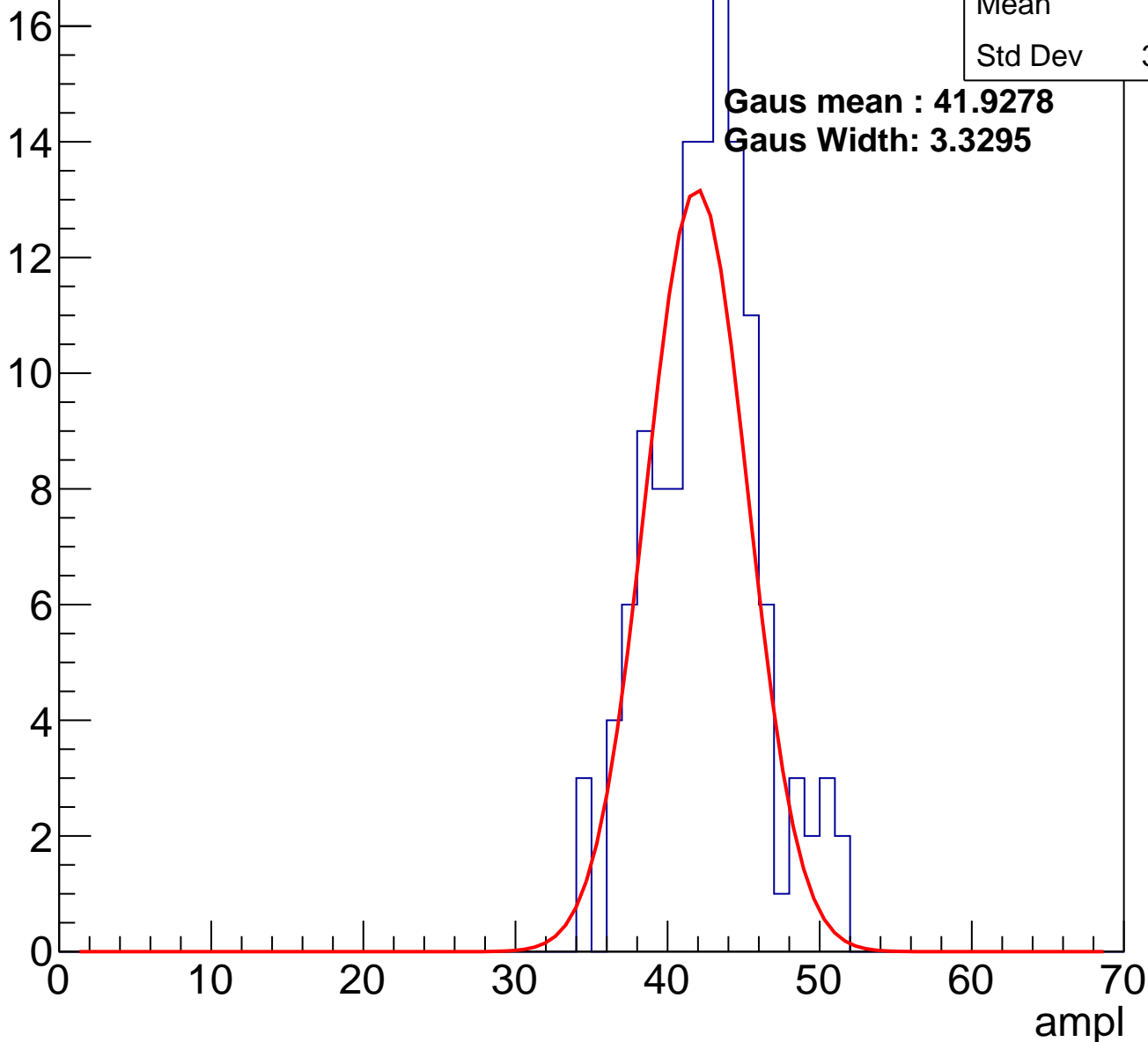
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	125
Mean	42.1
Std Dev	3.577

Gaus mean : 41.9278

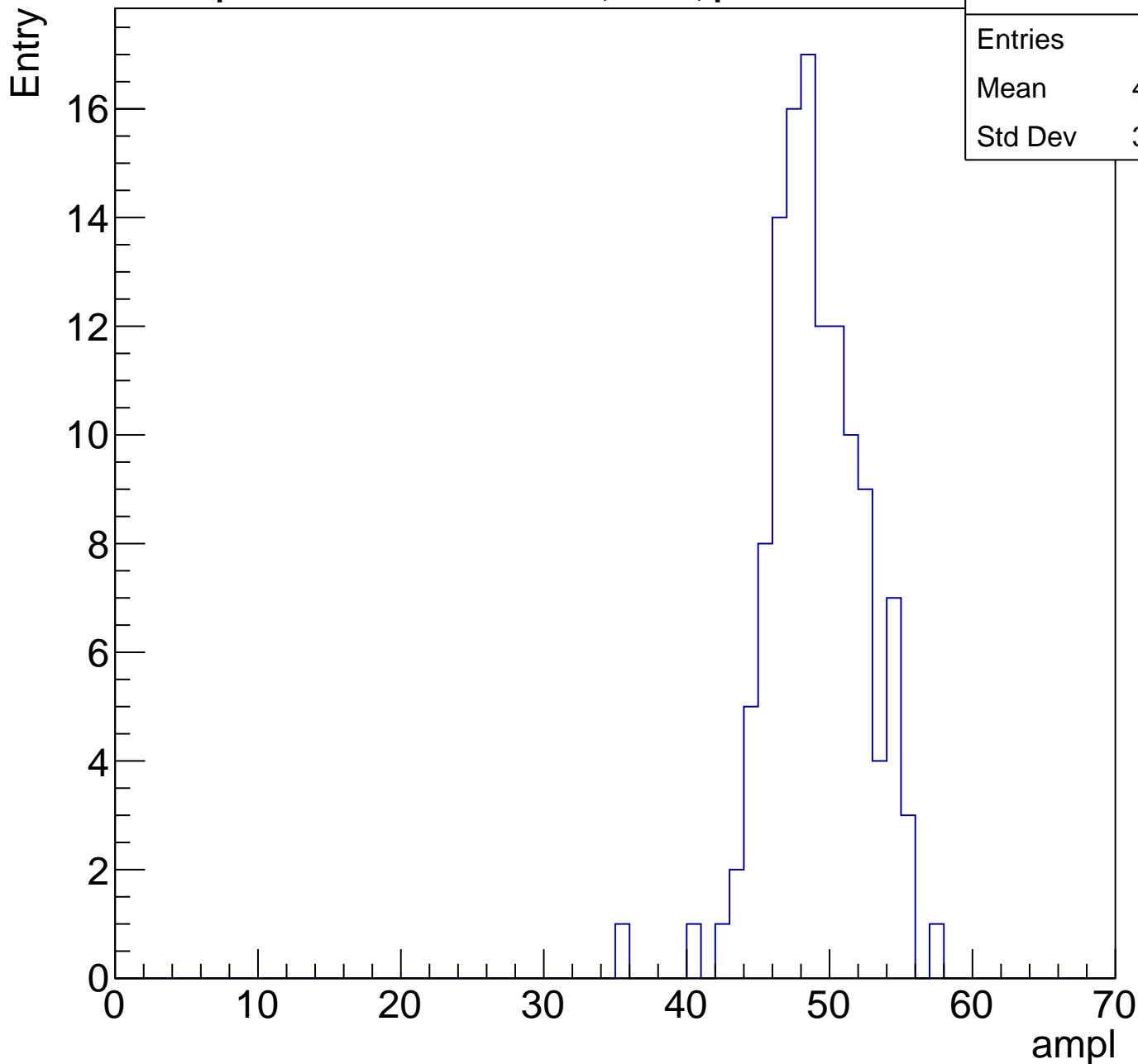
Gaus Width: 3.3295



B0L101S, U20-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

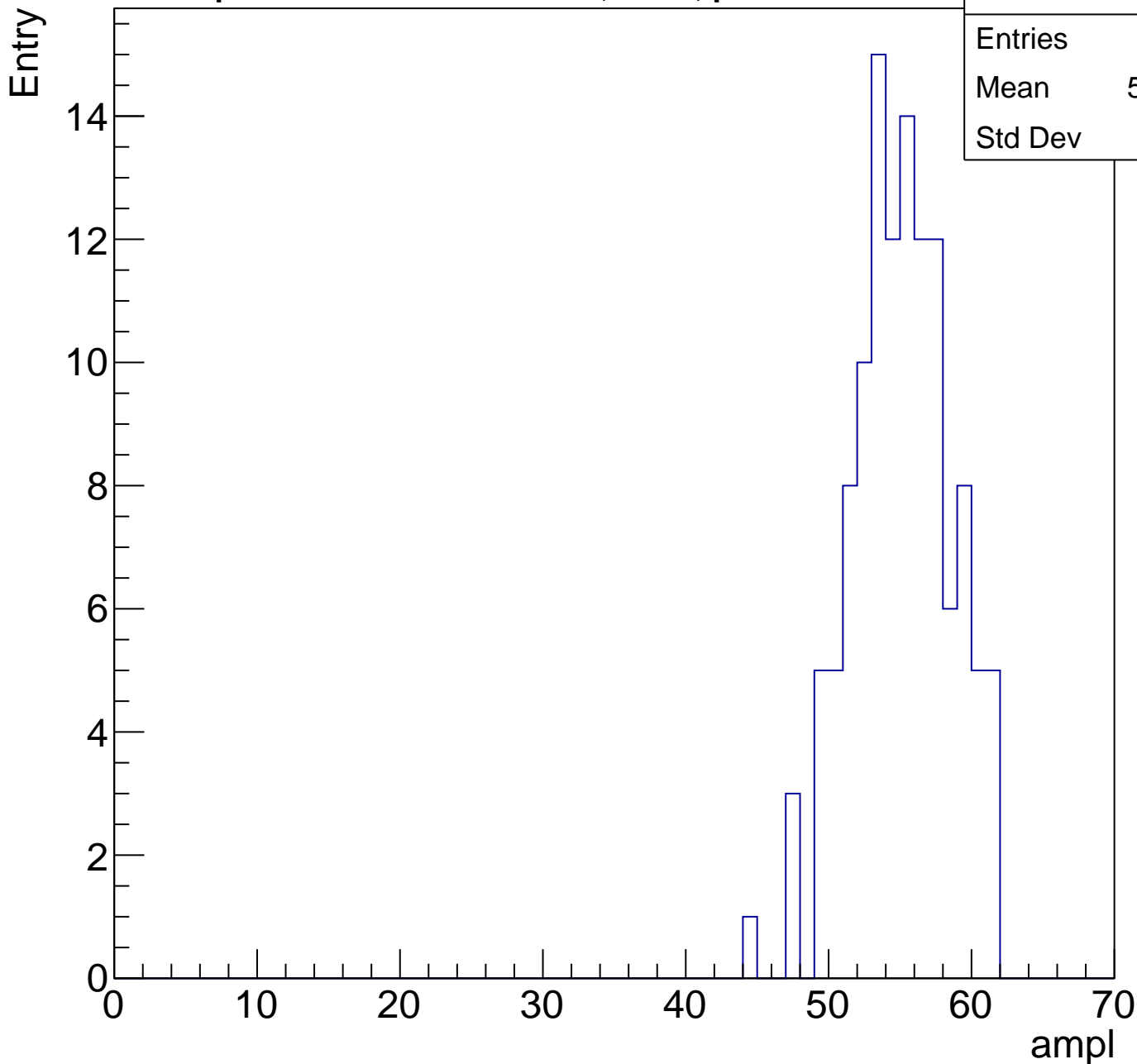
Entries	123
Mean	48.56
Std Dev	3.347



B0L101S, U20-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	54.56
Std Dev	3.44

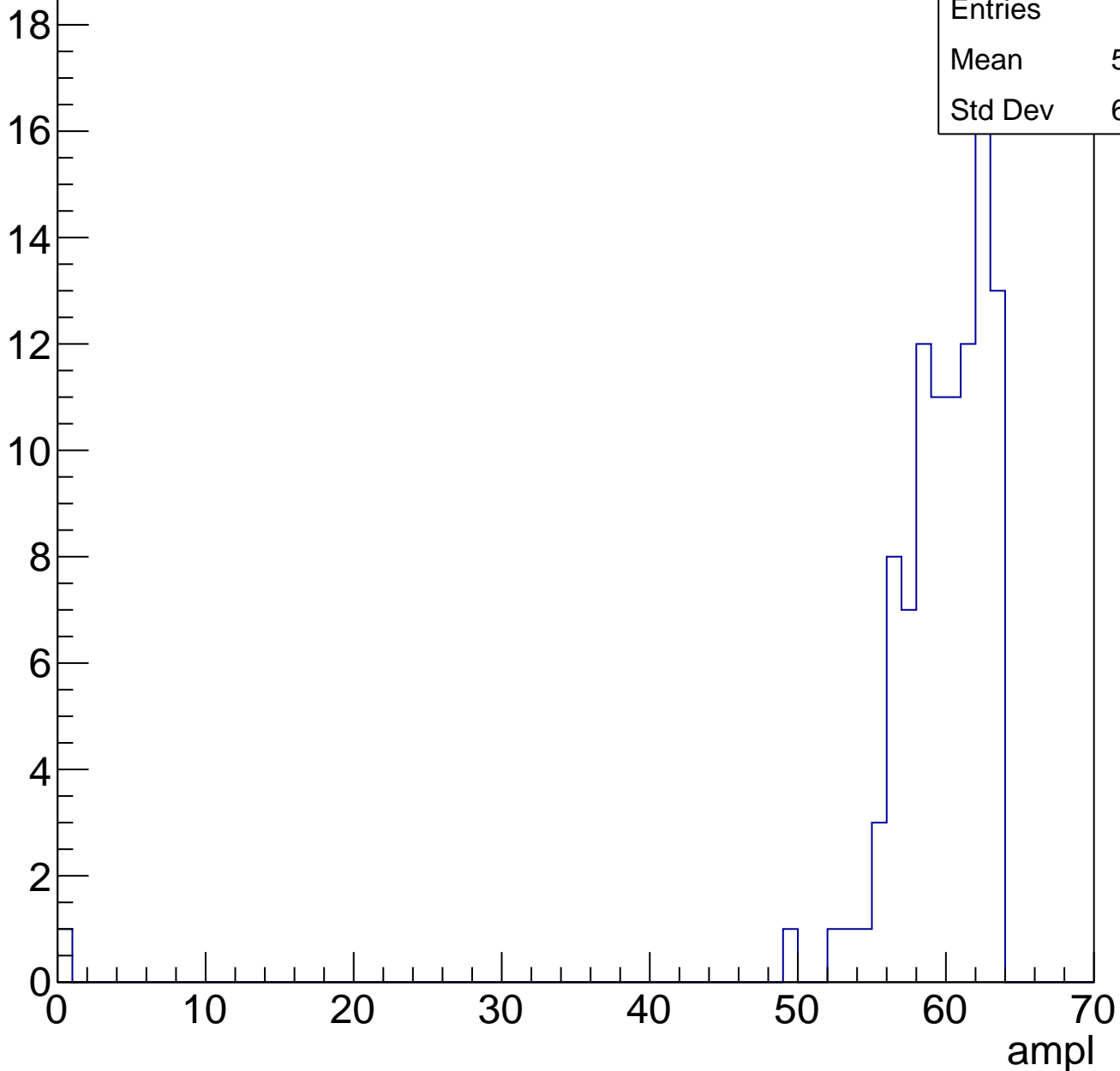


B0L101S, U20-ch64, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	58.92
Std Dev	6.542

Entry

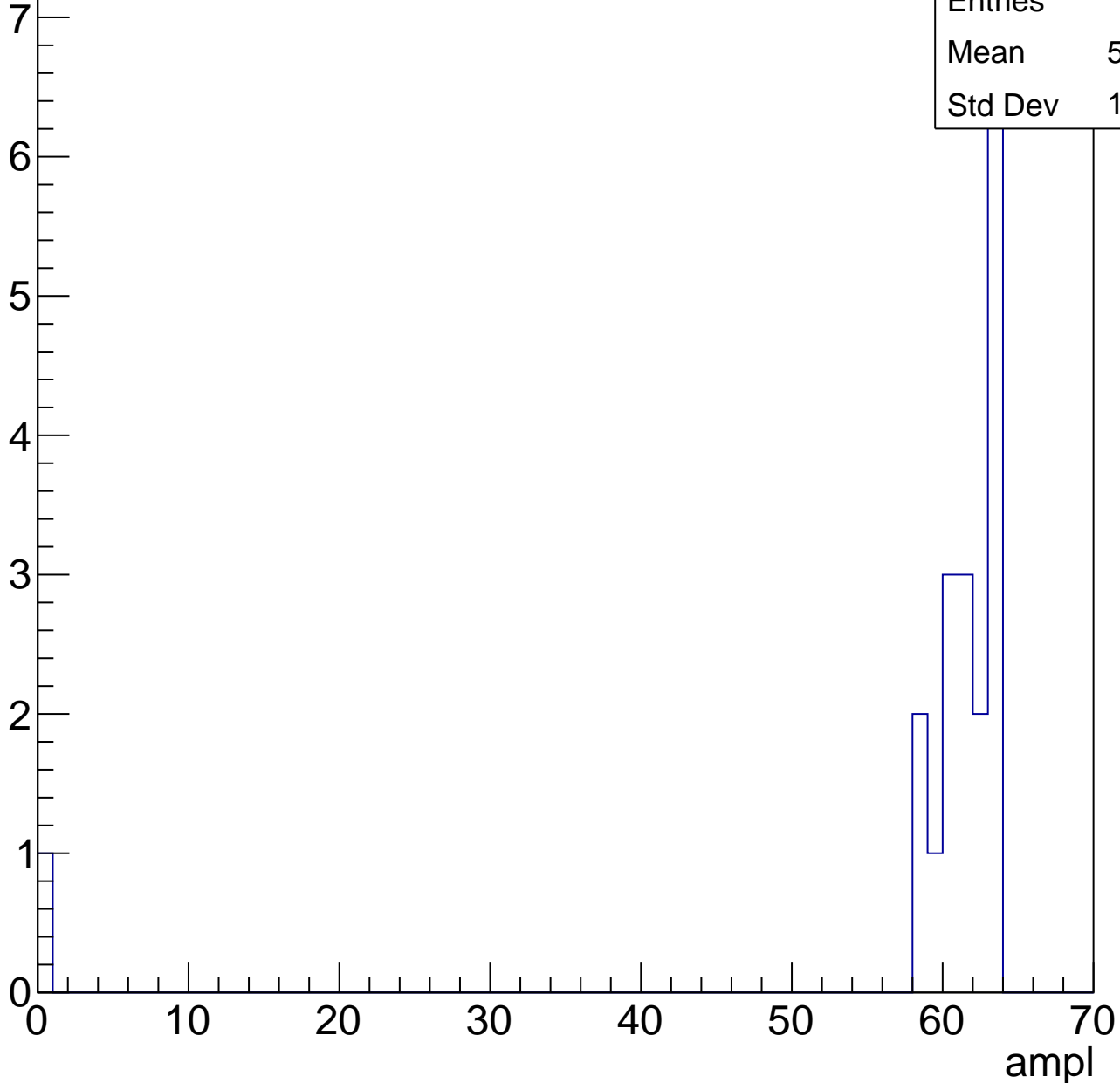


B0L101S, U20-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

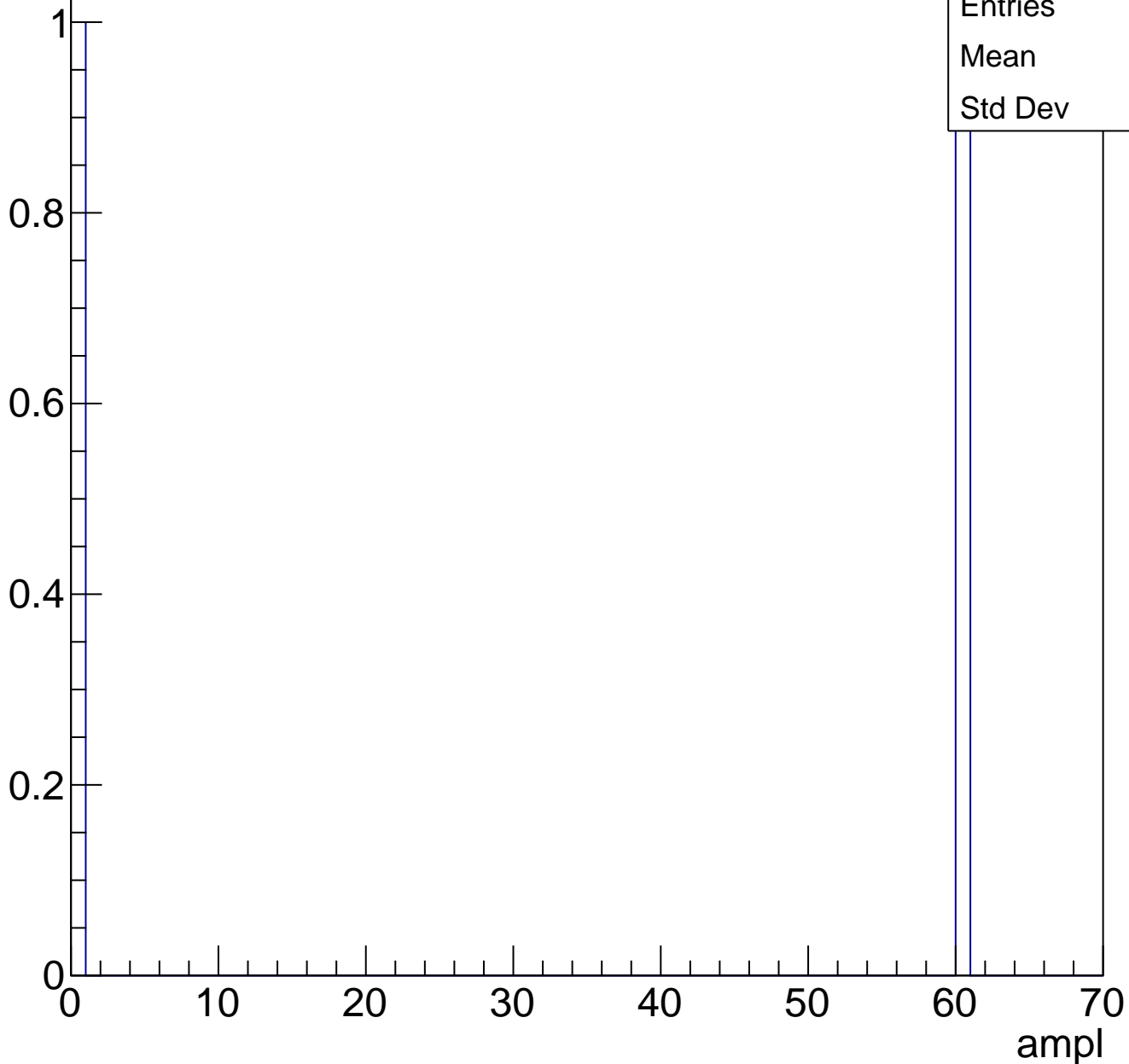
Entries	19
Mean	58.05
Std Dev	13.79



B0L101S, U20-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

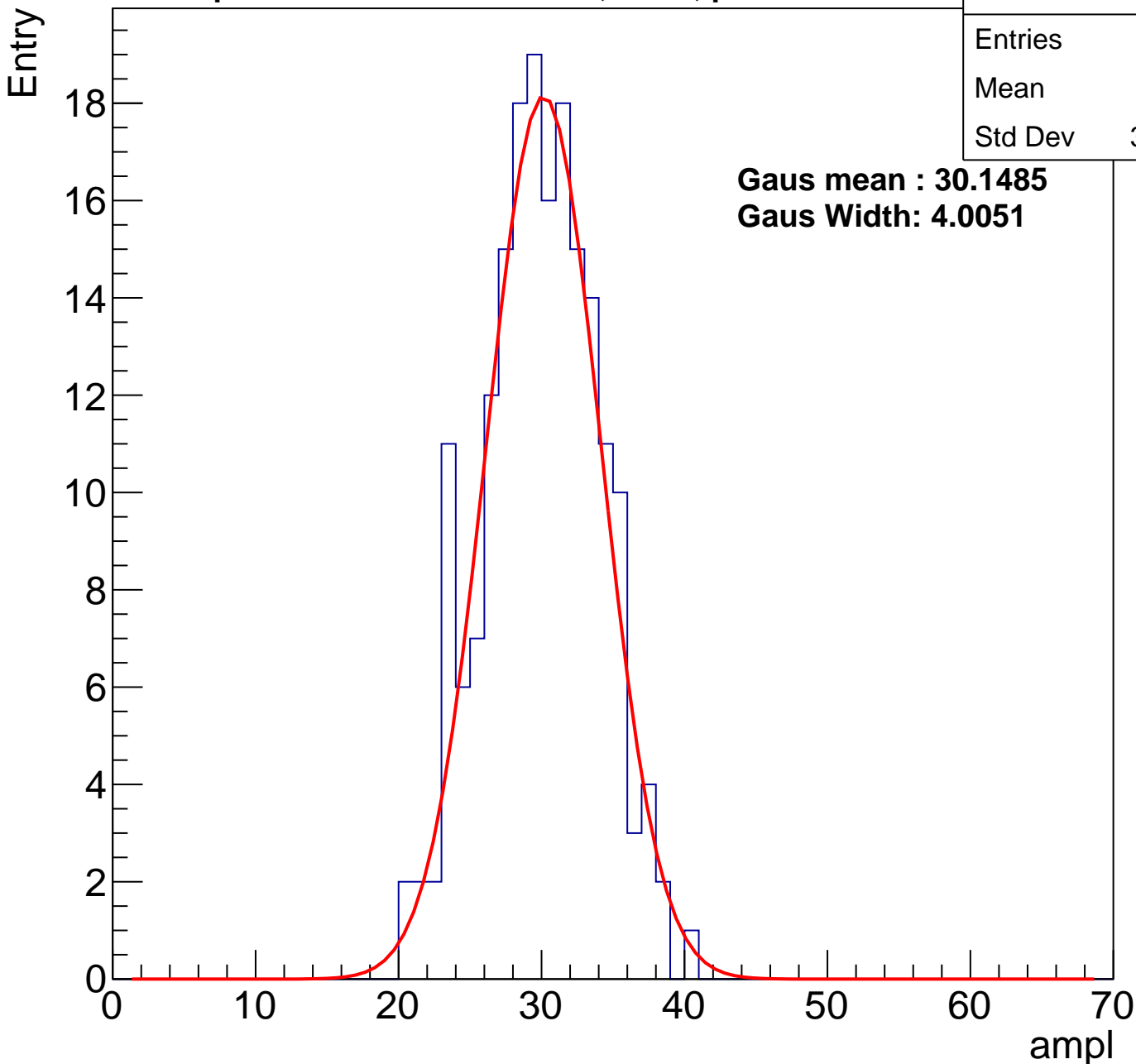


B0L101S, U20-ch65, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	188
Mean	29.5
Std Dev	3.963

Gaus mean : 30.1485
Gaus Width: 4.0051

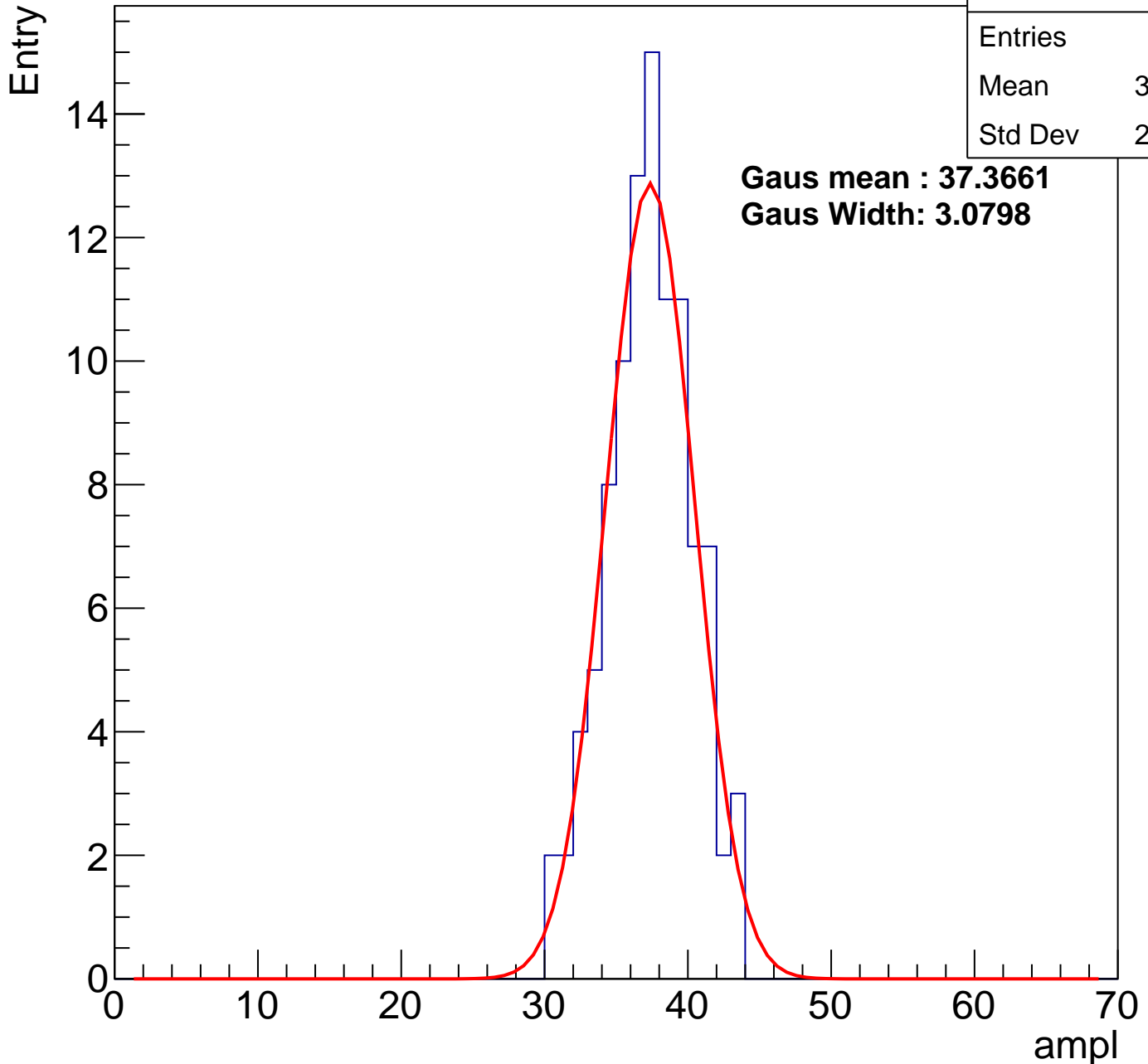


B0L101S, U20-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	36.87
Std Dev	2.935

Gaus mean : 37.3661
Gaus Width: 3.0798



B0L101S, U20-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	42.98
Std Dev	3.608

Gaus mean : 43.5243

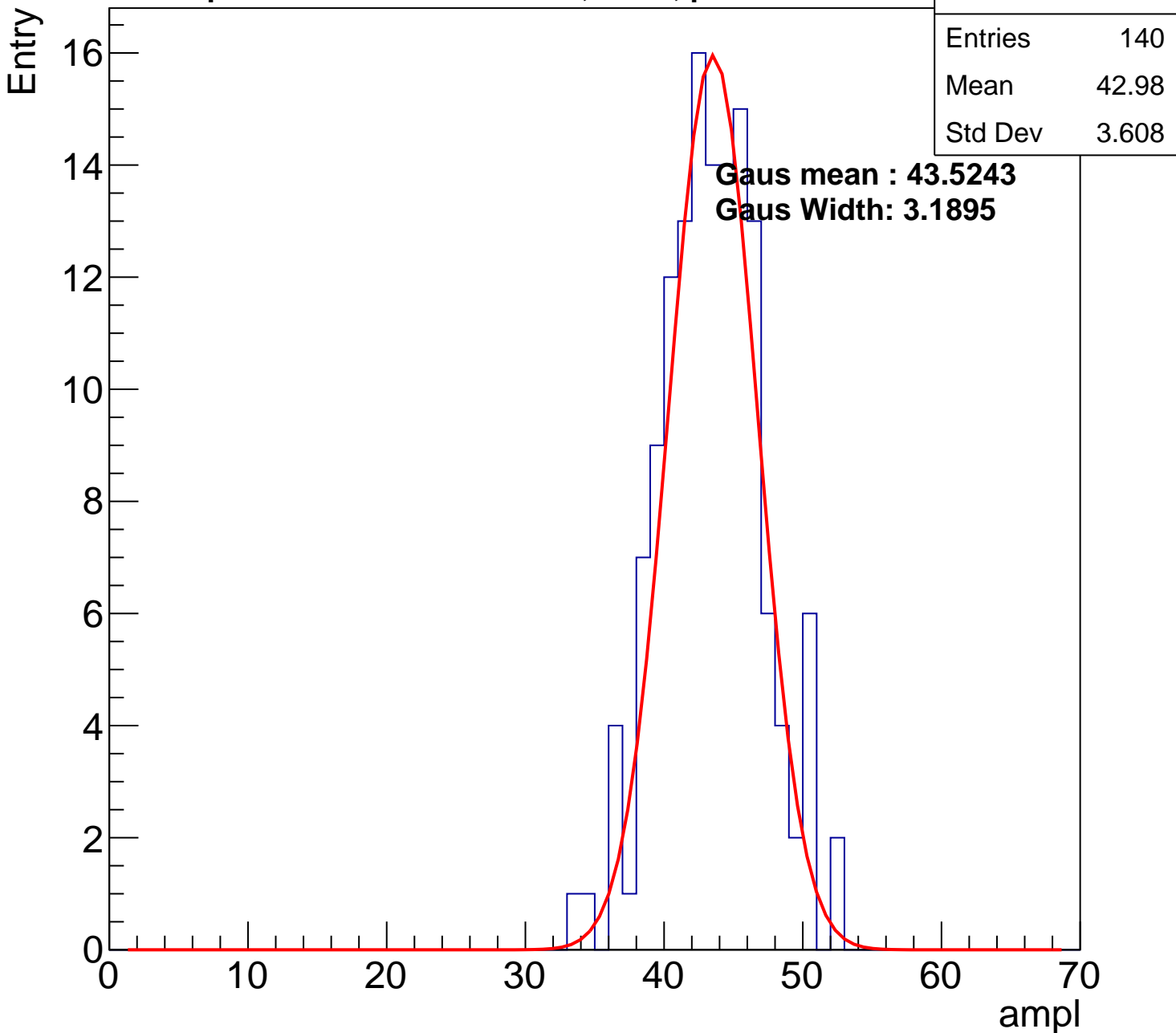
Gaus Width: 3.1895

Entry

16
14
12
10
8
6
4
2
0

ampl

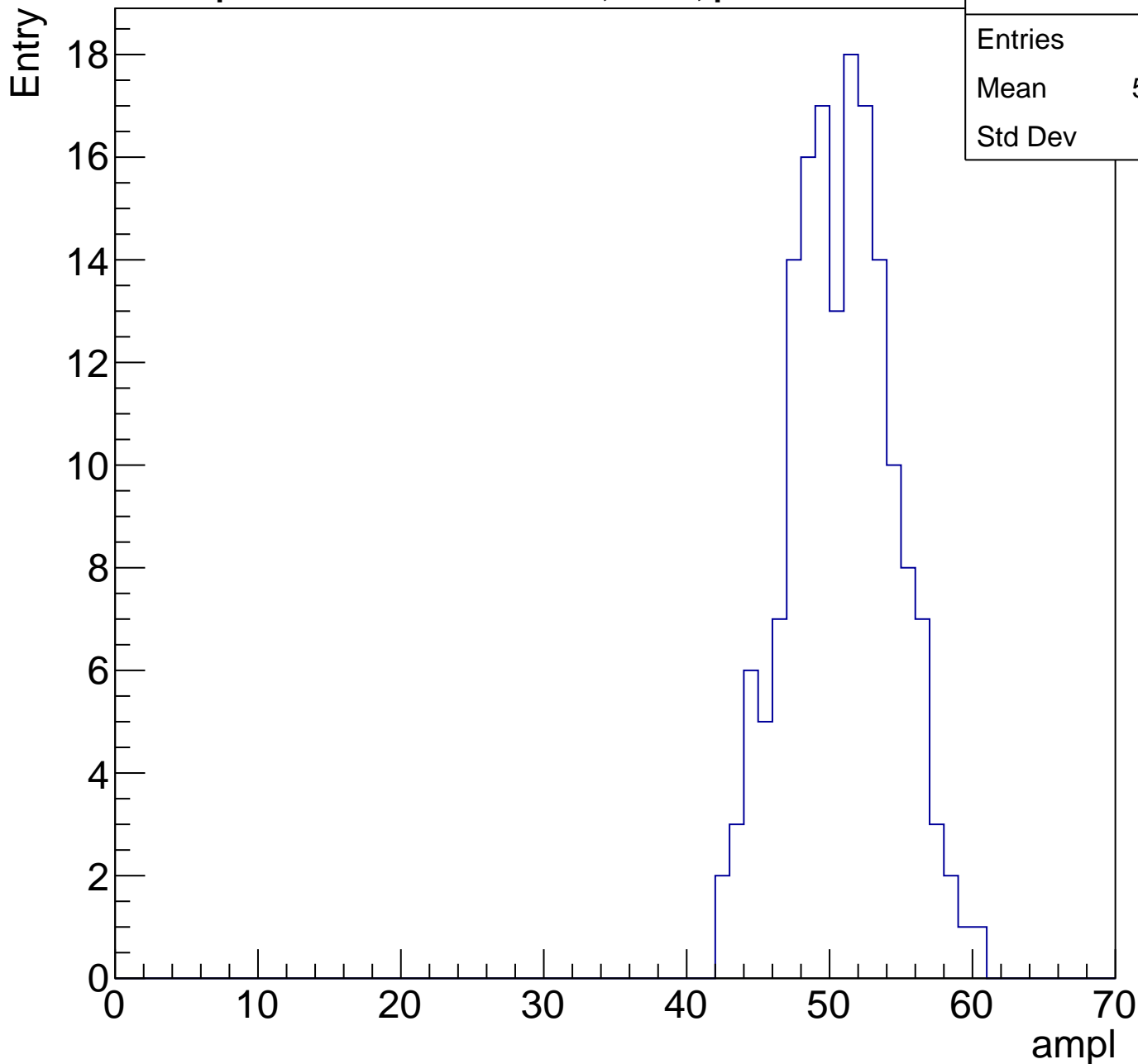
0 10 20 30 40 50 60 70



B0L101S, U20-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	50.34
Std Dev	3.66

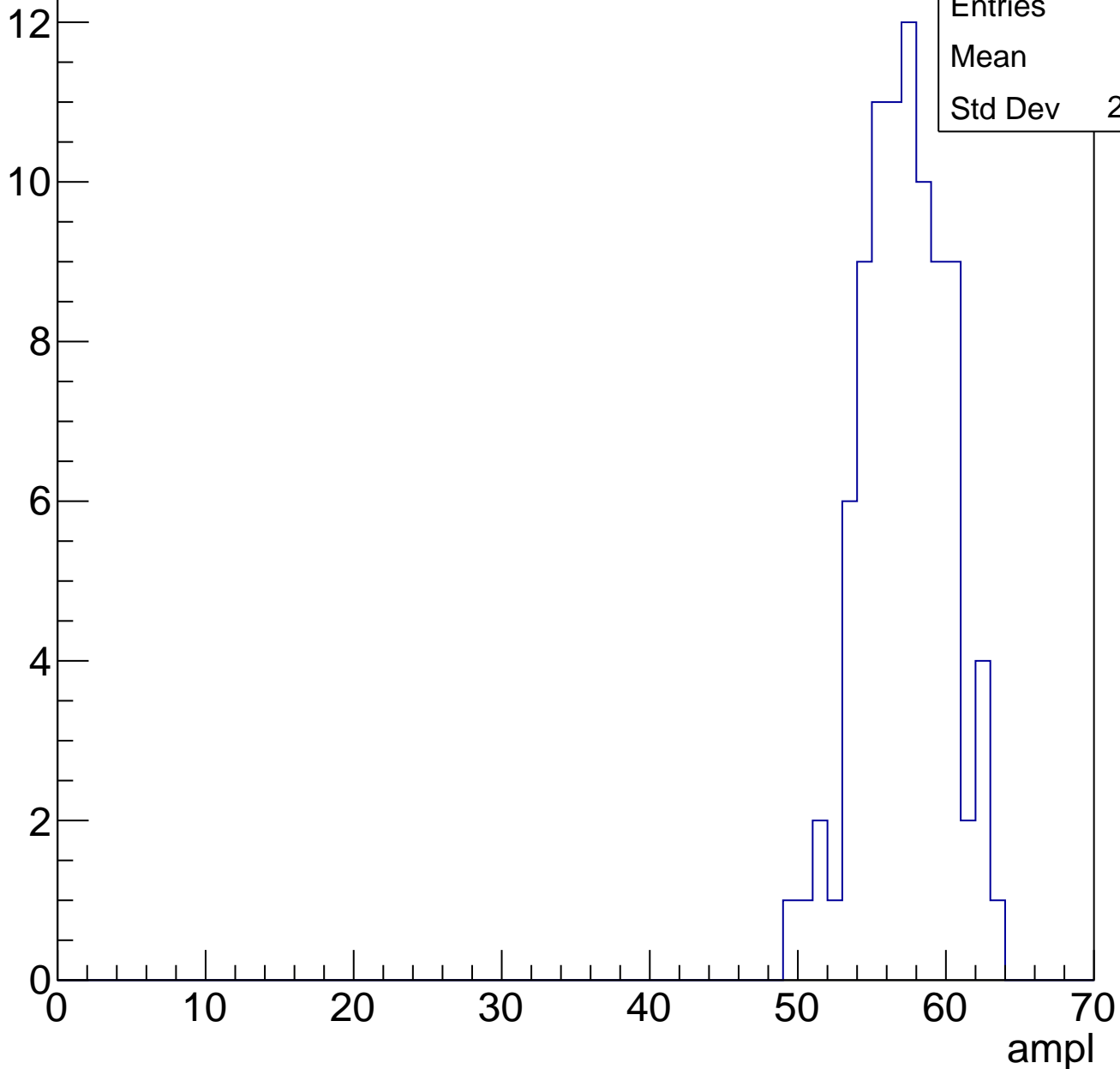


B0L101S, U20-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	56.7
Std Dev	2.862

Entry



B0L101S, U20-ch65, adc5

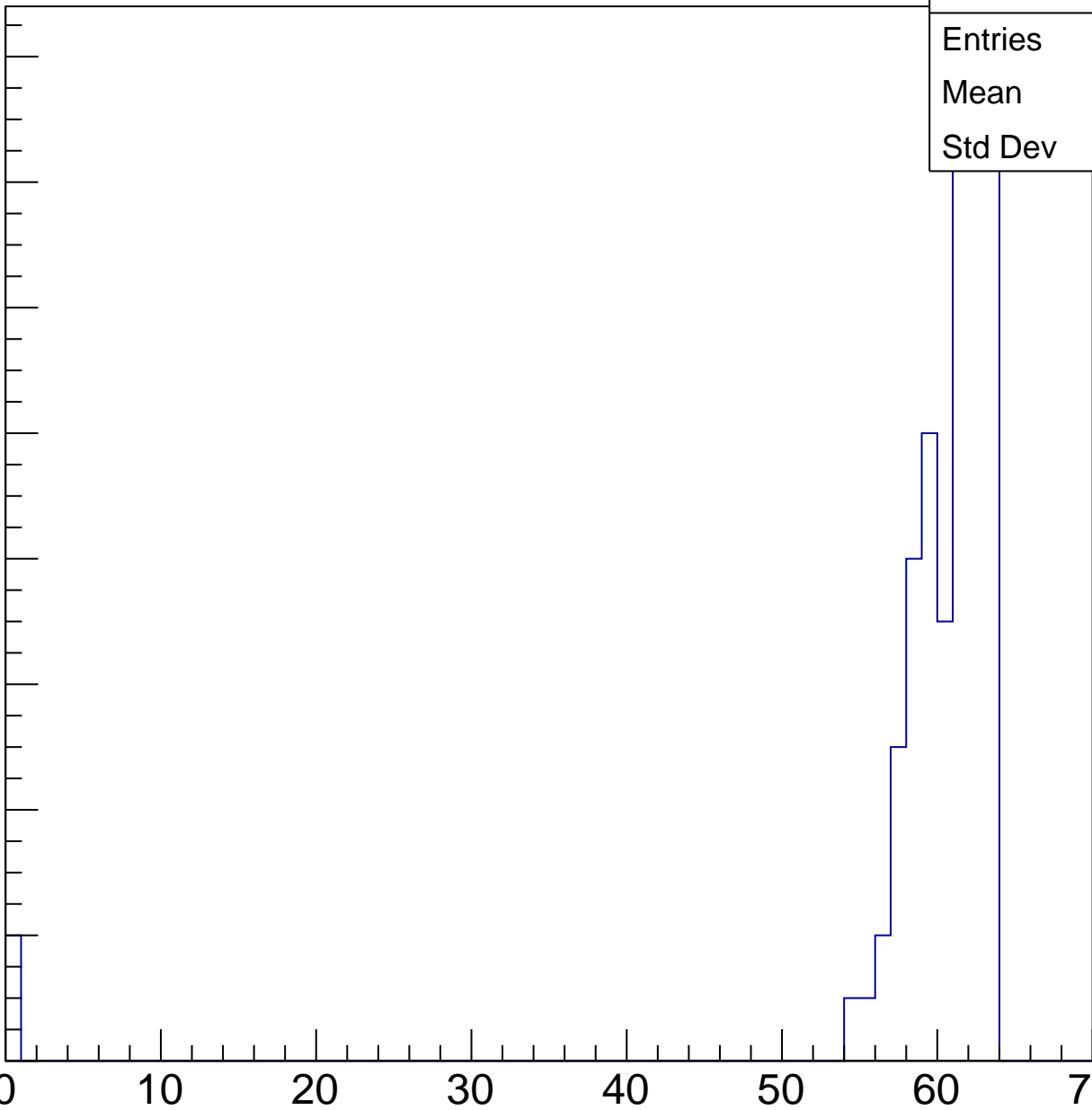
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.98
Std Dev	9.516

ampl



B0L101S, U20-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.33

Std Dev

1.247

B0L101S, U20-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch66, adc0

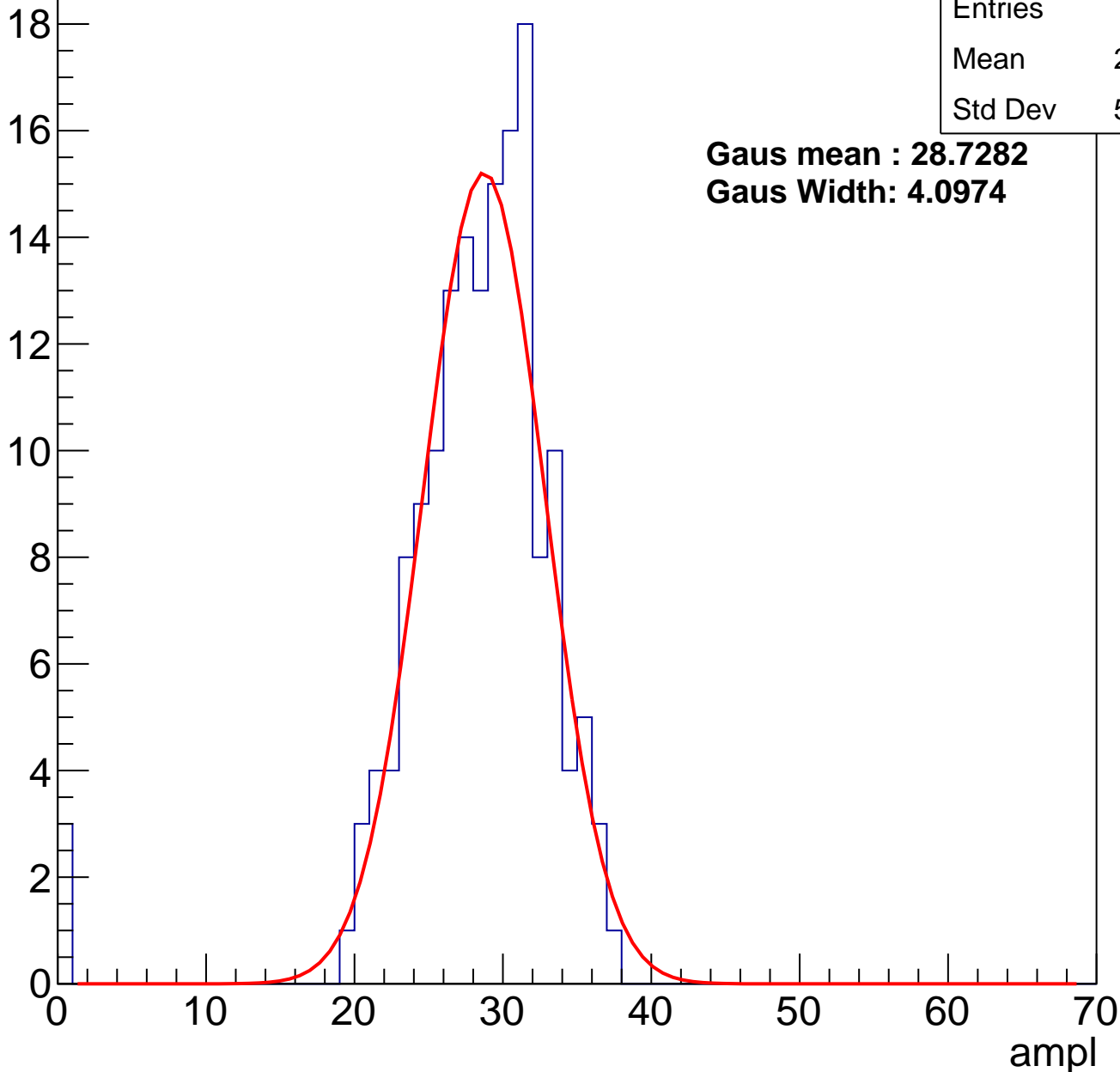
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	27.75
Std Dev	5.407

Gaus mean : 28.7282

Gaus Width: 4.0974

Entry



B0L101S, U20-ch66, adc1

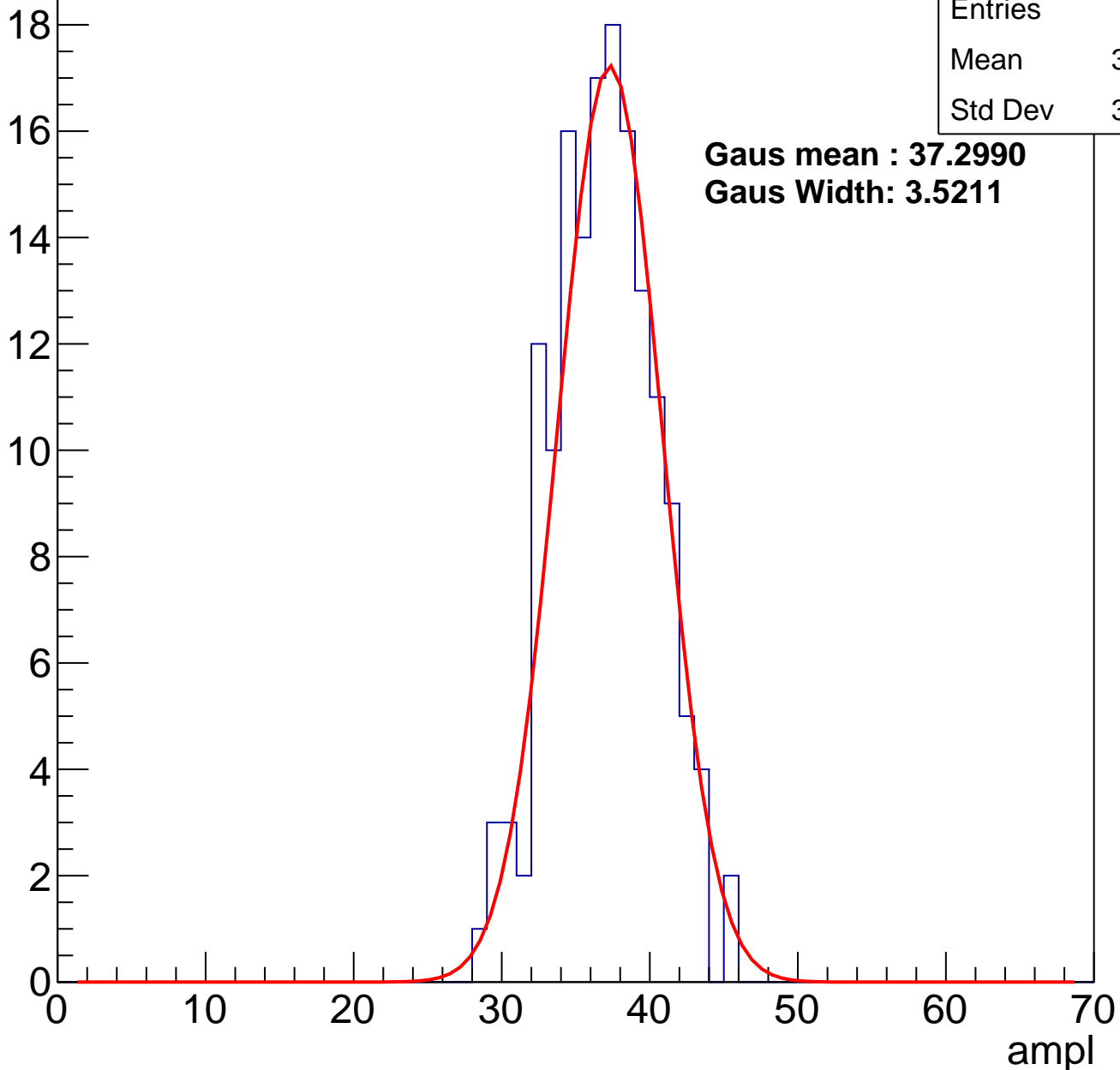
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	36.47
Std Dev	3.428

Gaus mean : 37.2990

Gaus Width: 3.5211

Entry



B0L101S, U20-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

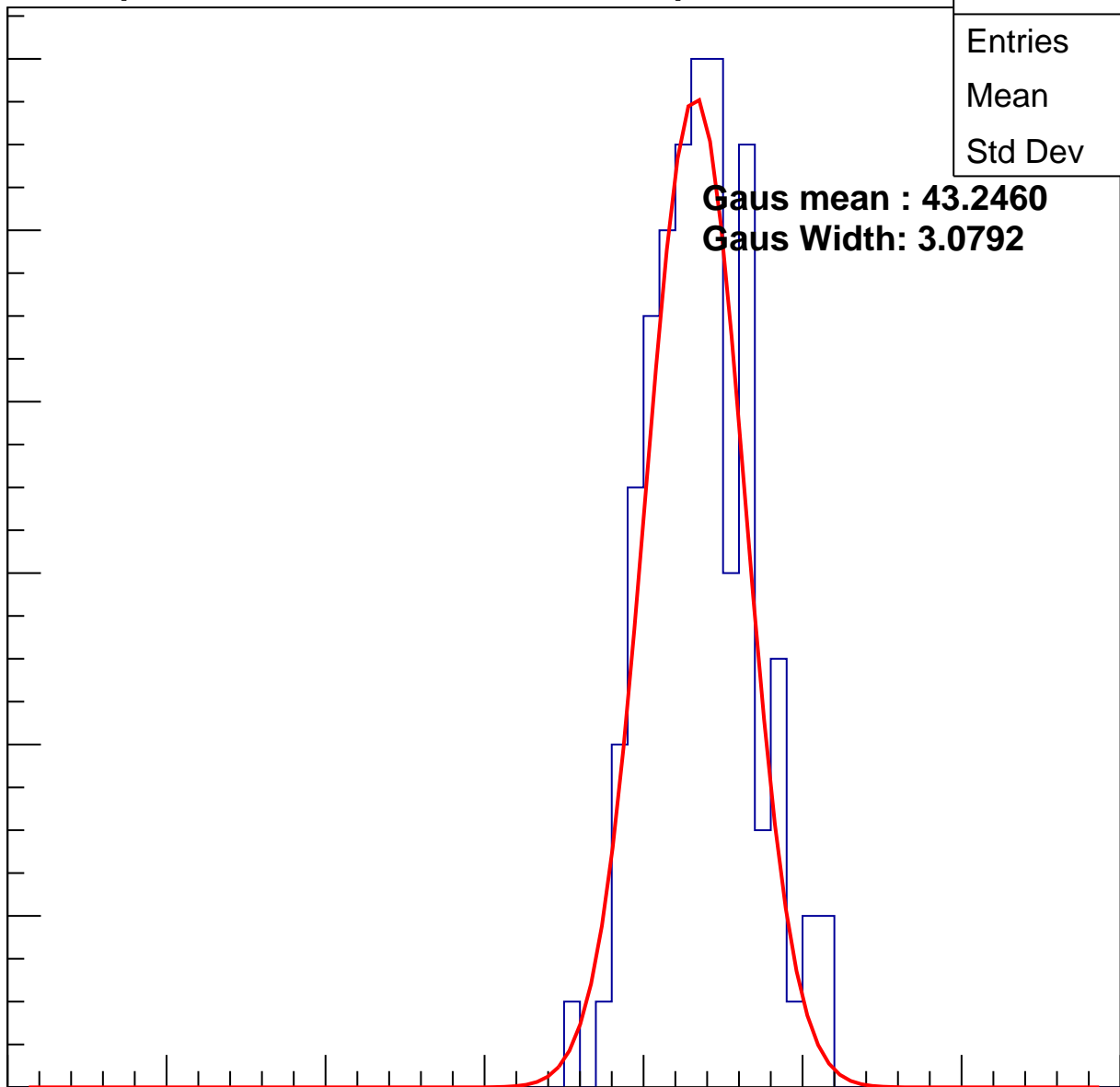
Entries	97
Mean	43.1
Std Dev	3.232

Gaus mean : 43.2460

Gaus Width: 3.0792

0 10 20 30 40 50 60 70

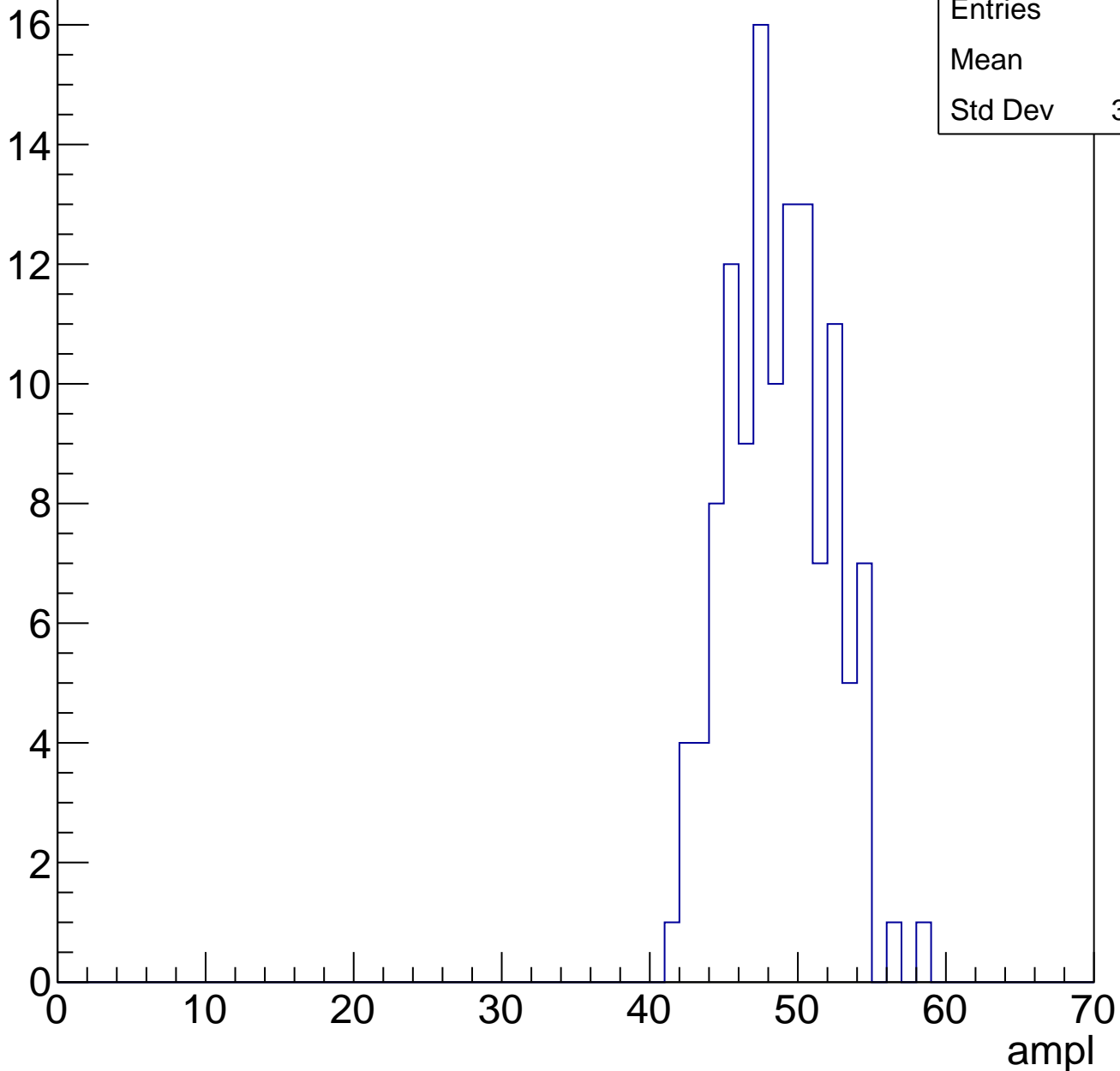
ampl



B0L101S, U20-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

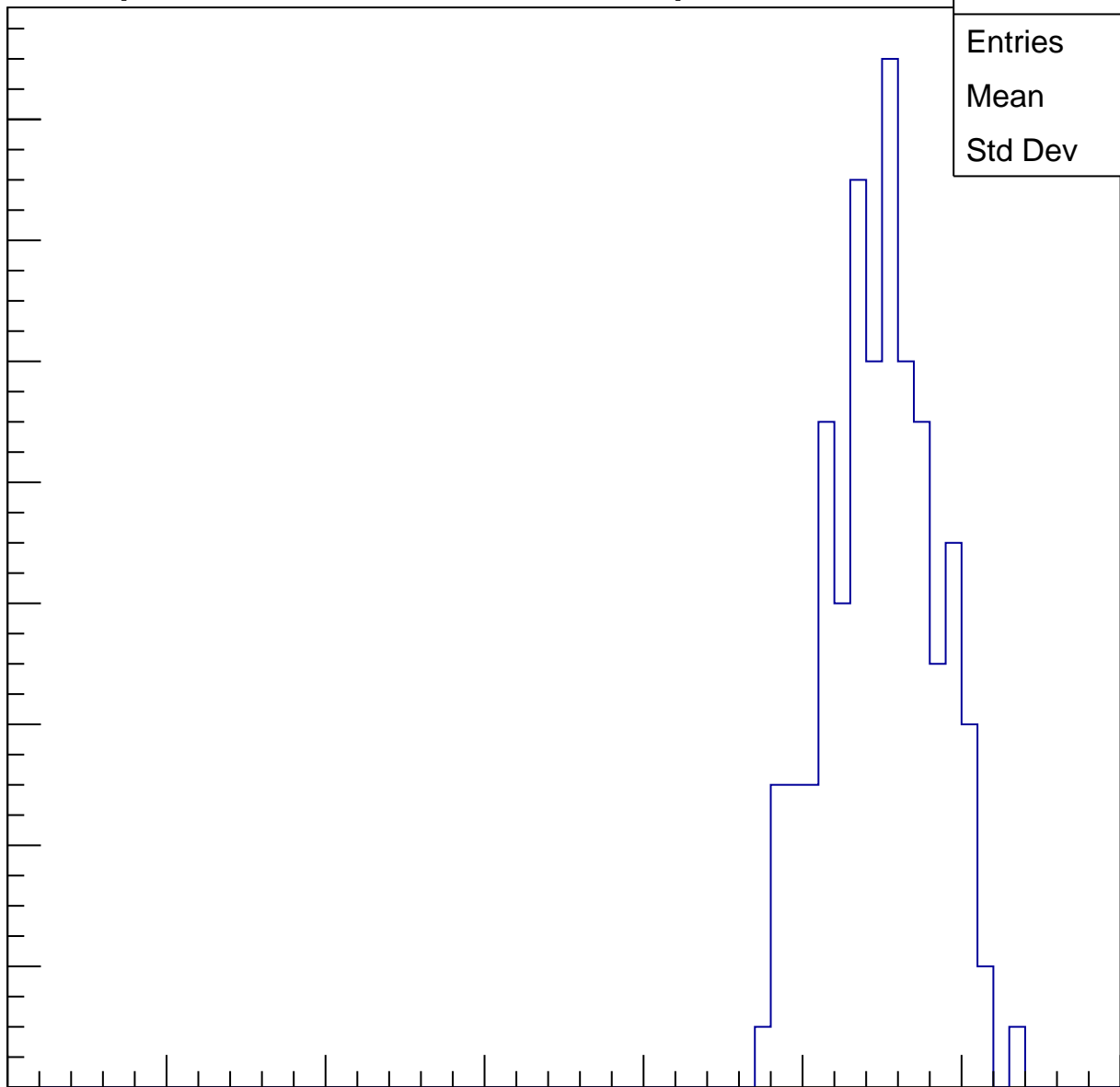
Entries	127
Mean	54.47
Std Dev	3.376

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch66, adc5

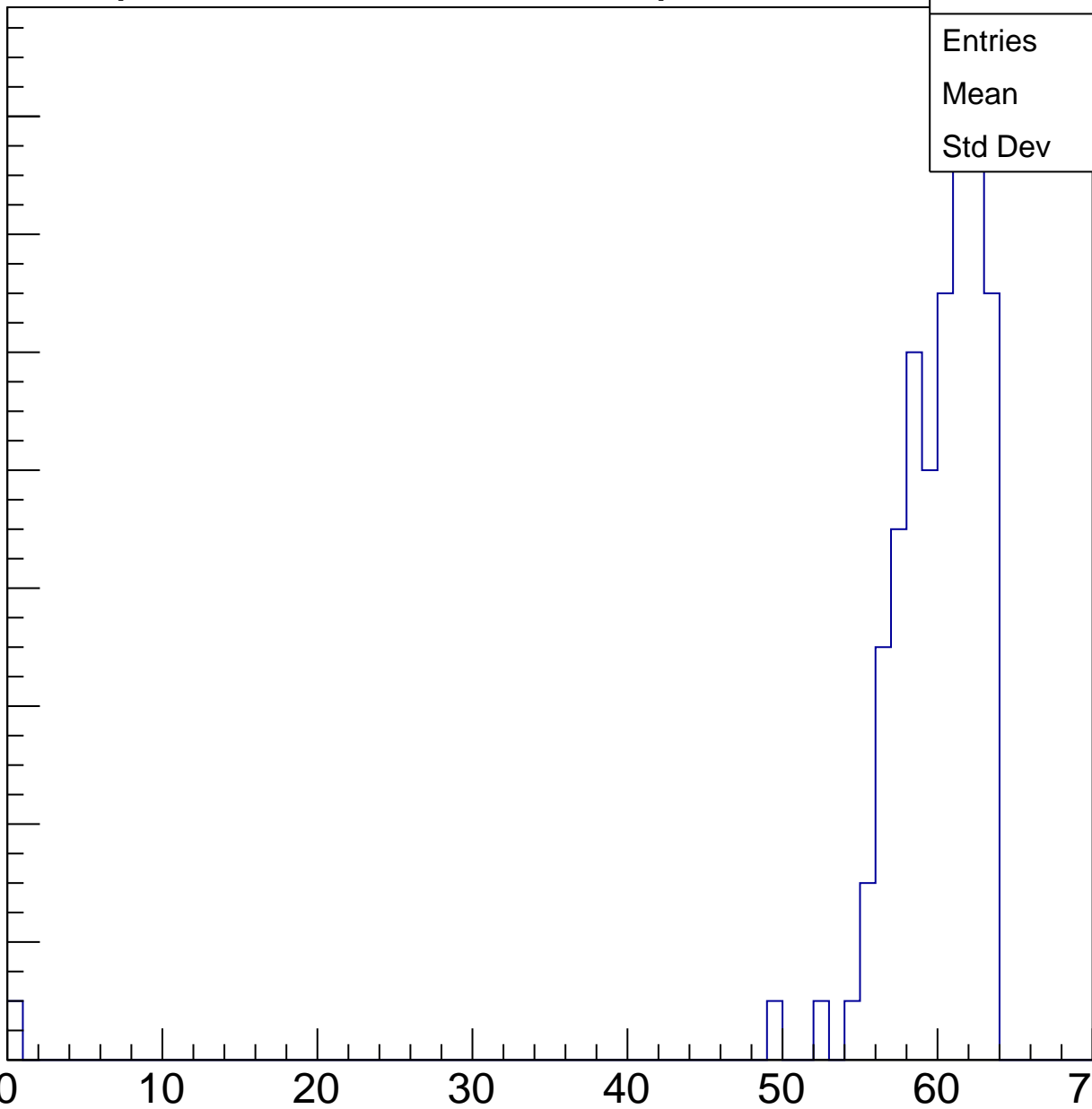
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	59.03
Std Dev	6.39

ampl

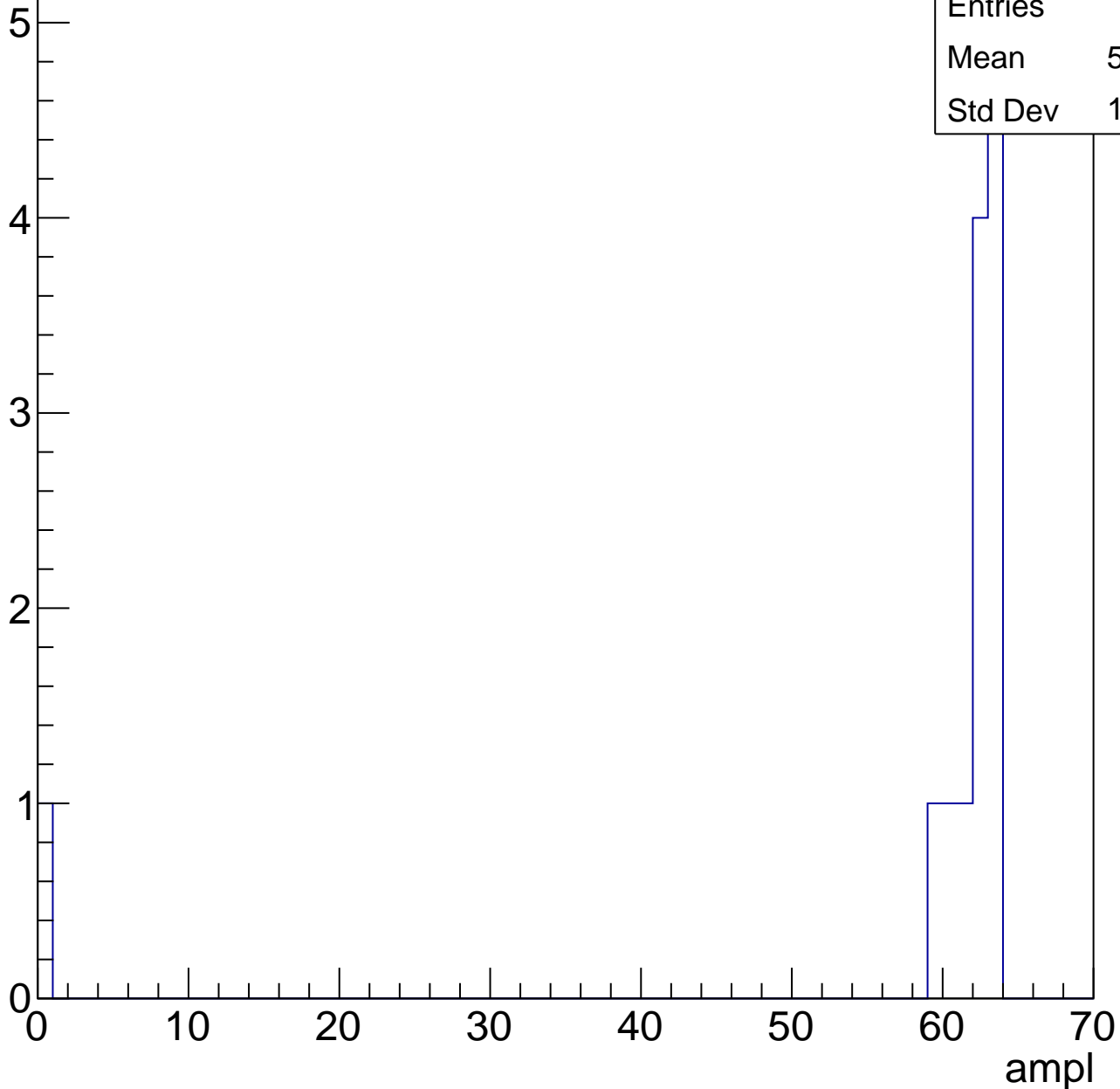


B0L101S, U20-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.15
Std Dev	16.54



B0L101S, U20-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

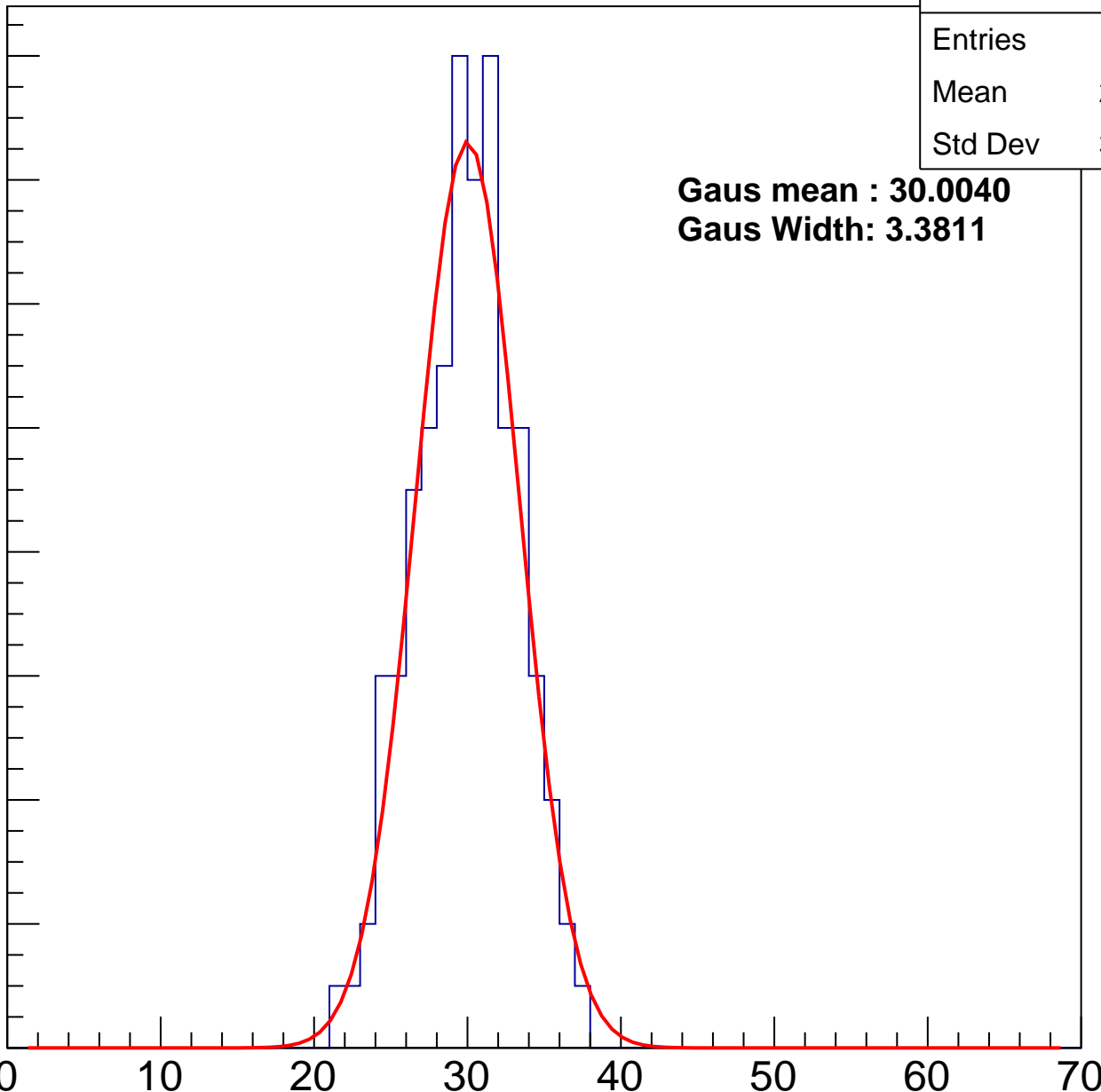
16
14
12
10
8
6
4
2
0

Entries	125
Mean	29.42
Std Dev	3.255

Gaus mean : 30.0040

Gaus Width: 3.3811

ampl



B0L101S, U20-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.49
Std Dev	3.463

Gaus mean : 37.1233

Gaus Width: 3.4623

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

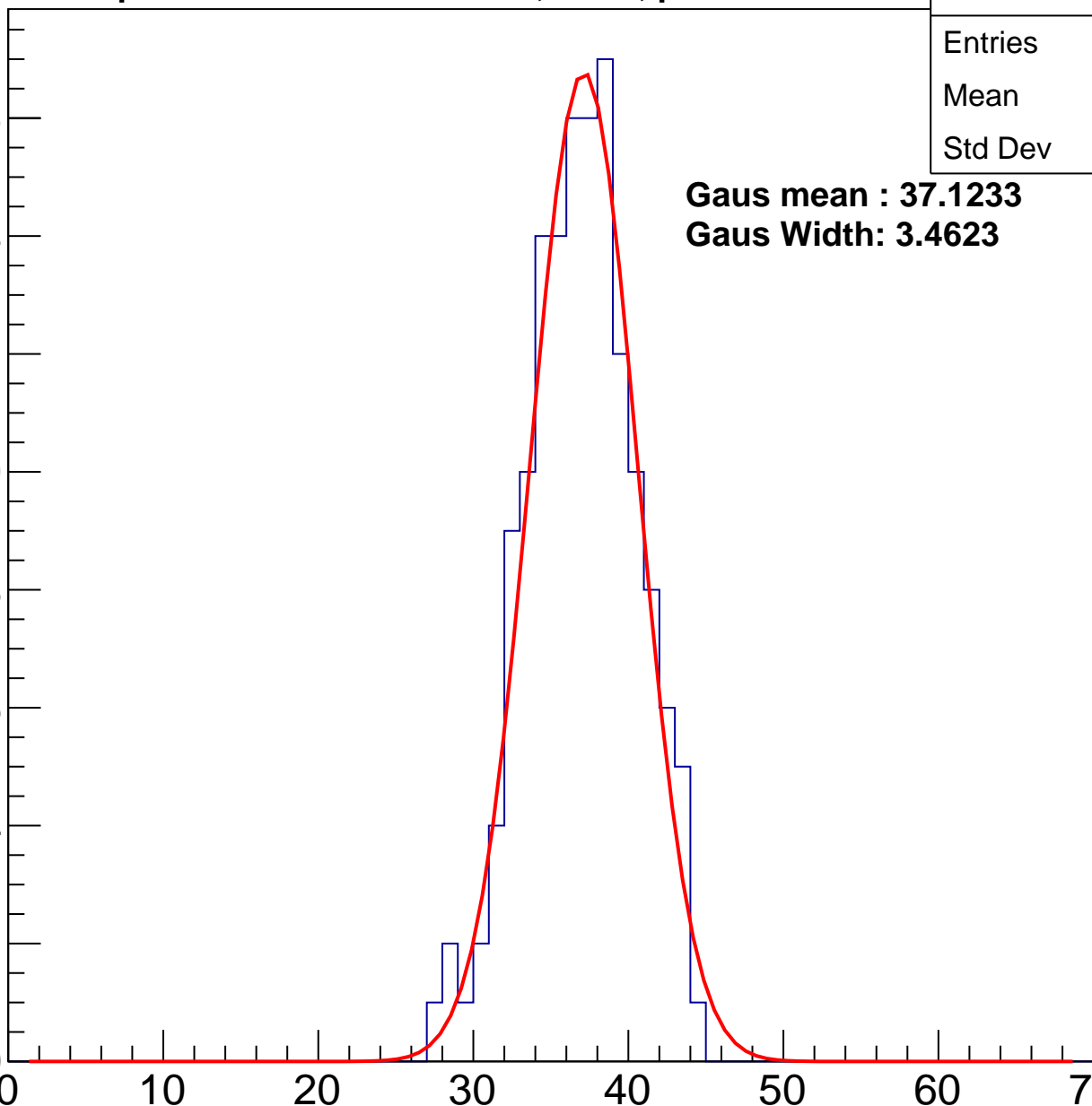
40

50

60

70

ampl



B0L101S, U20-ch67, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

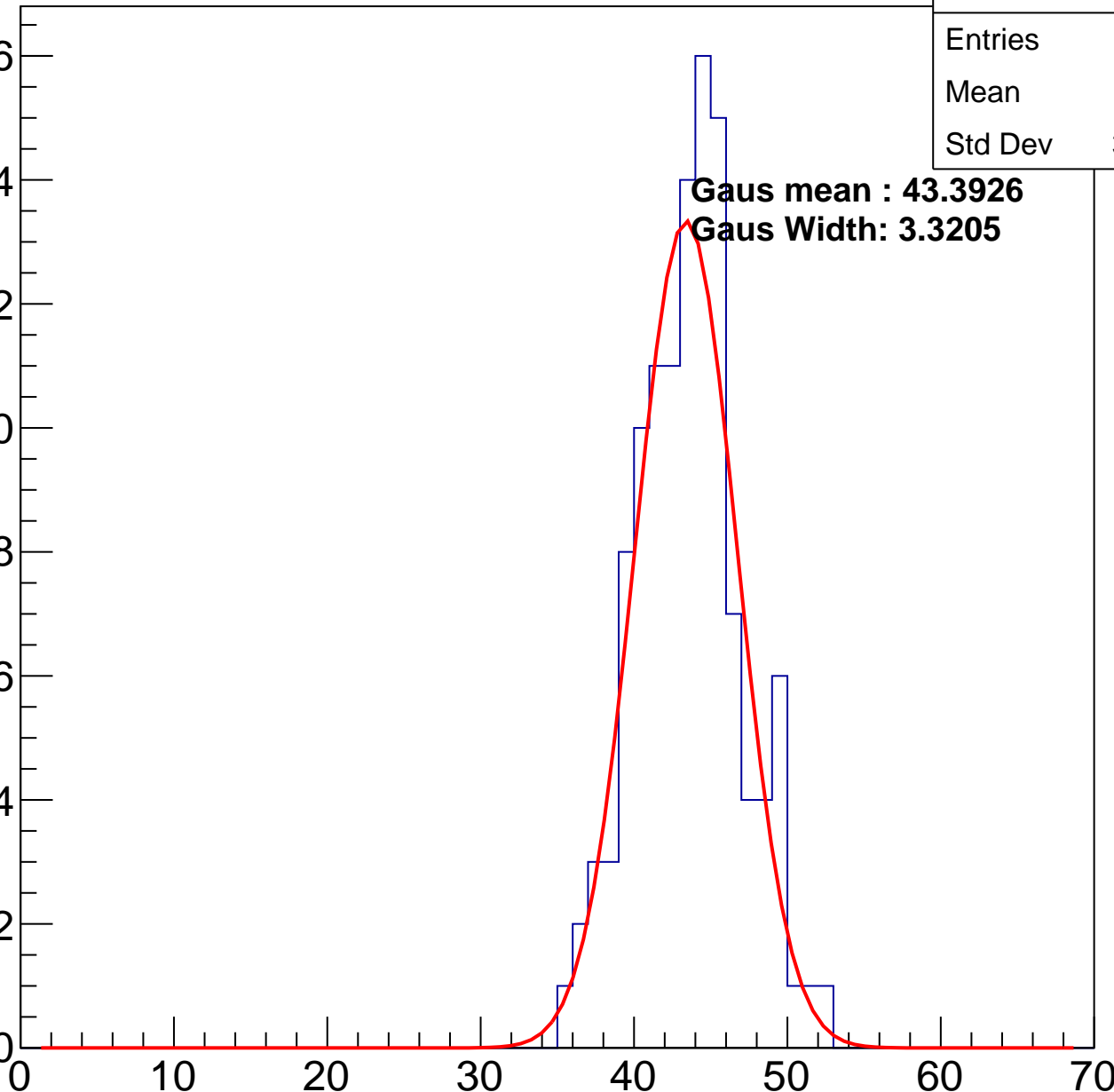
16
14
12
10
8
6
4
2
0

Entries	118
Mean	43.11
Std Dev	3.357

Gaus mean : 43.3926

Gaus Width: 3.3205

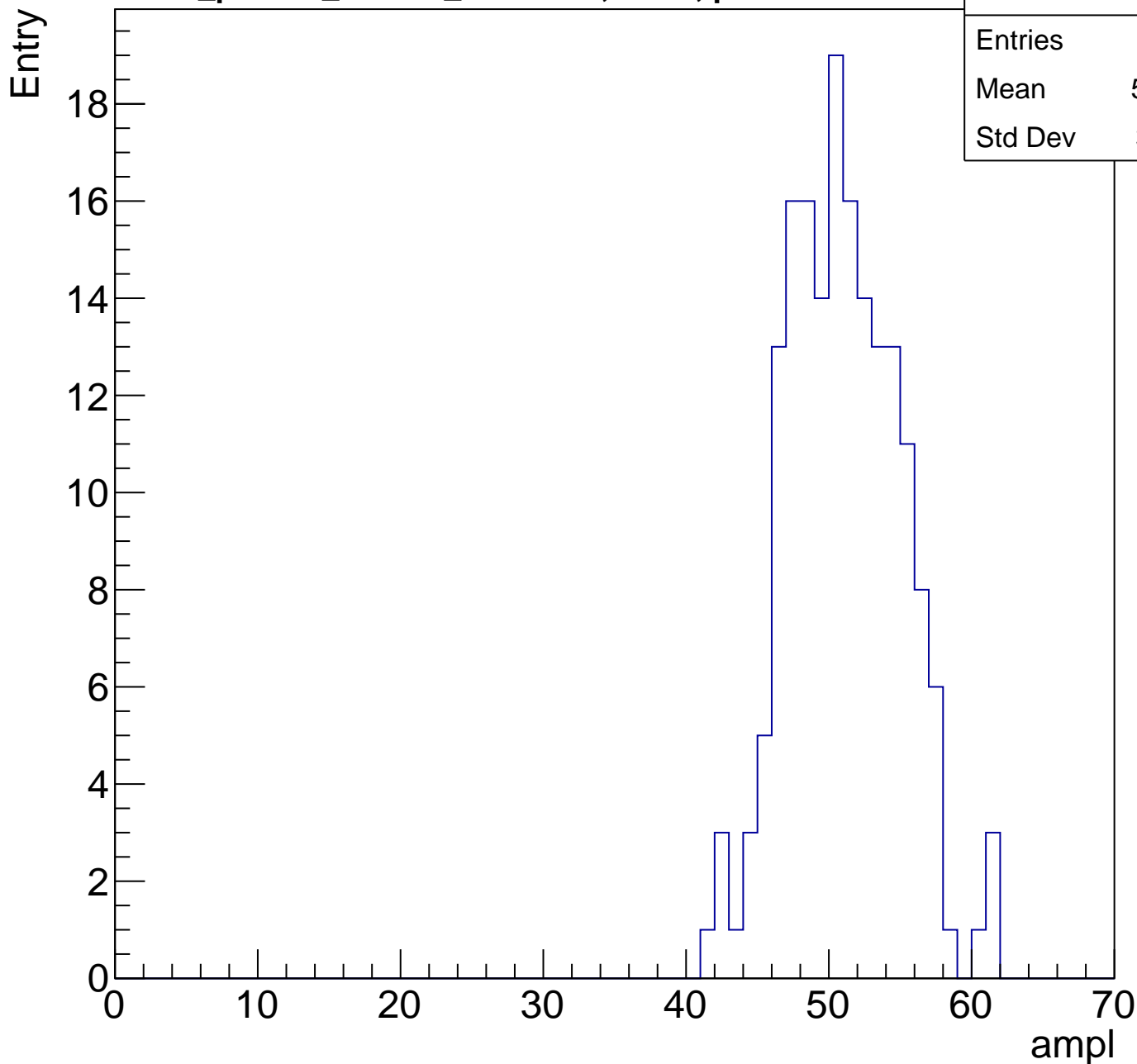
ampl



B0L101S, U20-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	50.58
Std Dev	3.911



B0L101S, U20-ch67, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	56.56
Std Dev	6.639

Entry

10

8

6

4

2

0

0

10

20

30

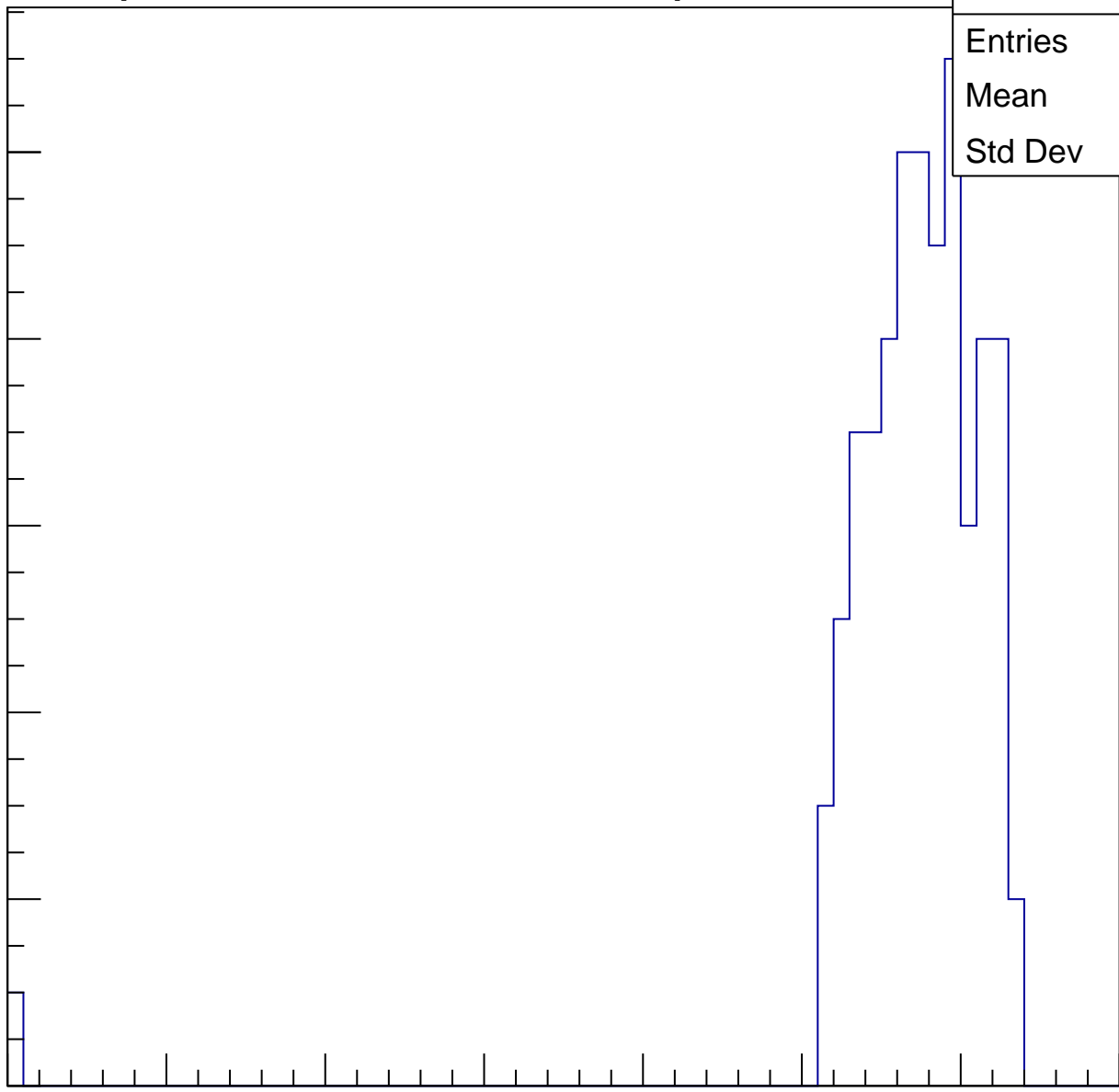
40

50

60

70

ampl



B0L101S, U20-ch67, adc5

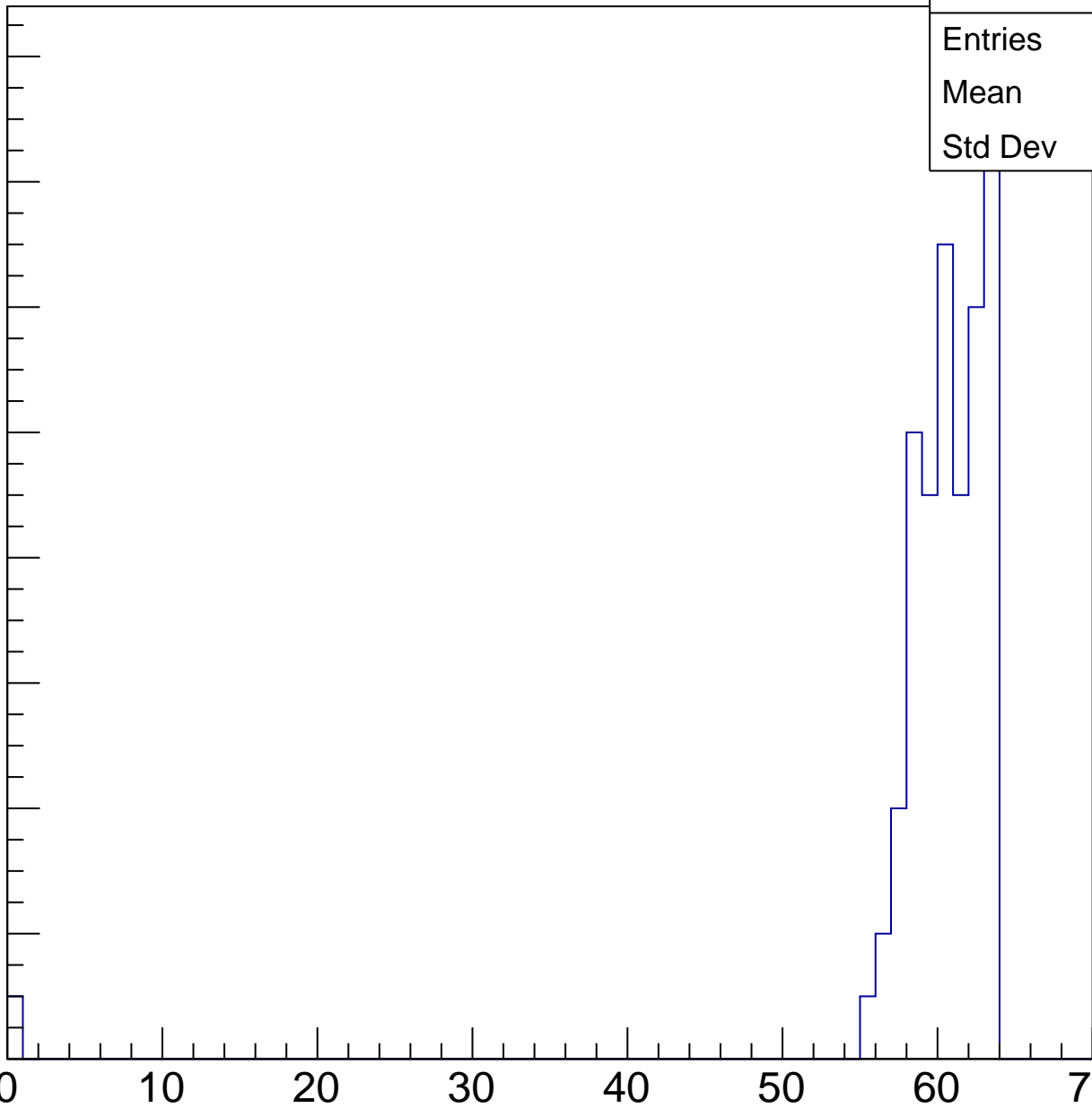
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.57
Std Dev	7.143

ampl



B0L101S, U20-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	28.14
Std Dev	4.437

Gaus mean : 28.7180

Gaus Width: 3.4909

Entry

12

10

8

6

4

2

0

0

10

20

30

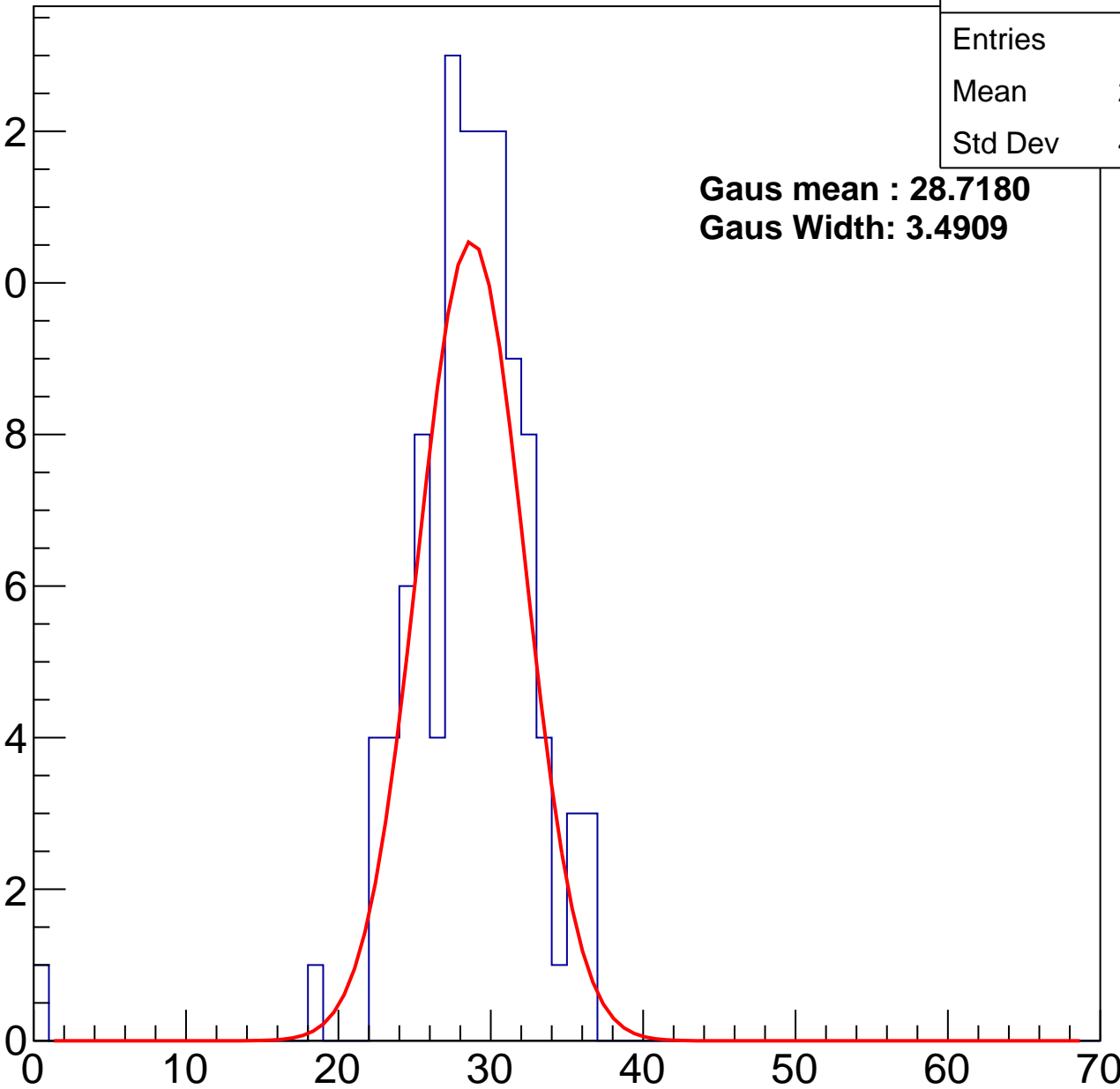
40

50

60

70

ampl



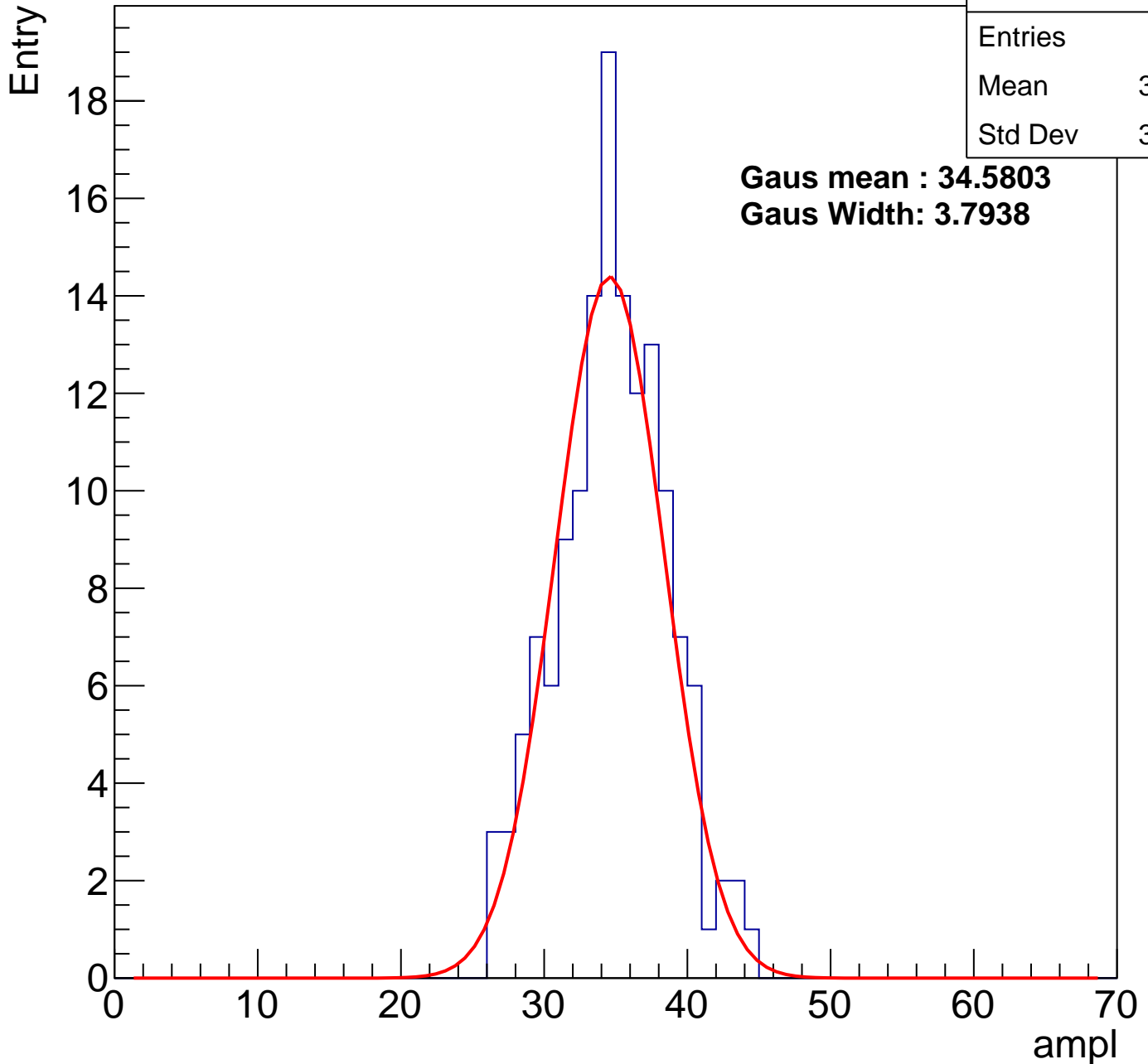
B0L101S, U20-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.31
Std Dev	3.801

Gaus mean : 34.5803

Gaus Width: 3.7938



B0L101S, U20-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	41.42
Std Dev	3.658

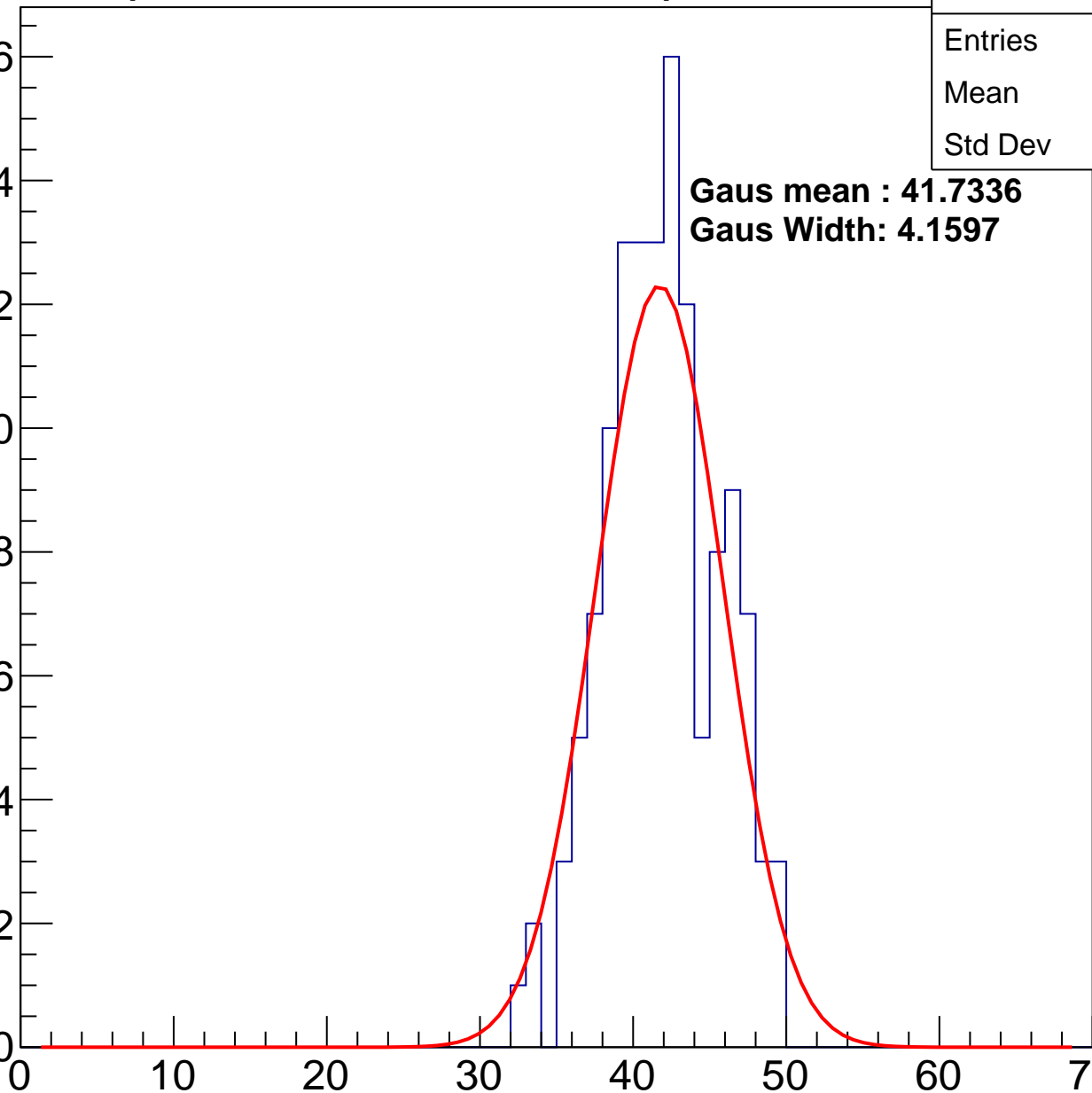
Gaus mean : 41.7336

Gaus Width: 4.1597

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

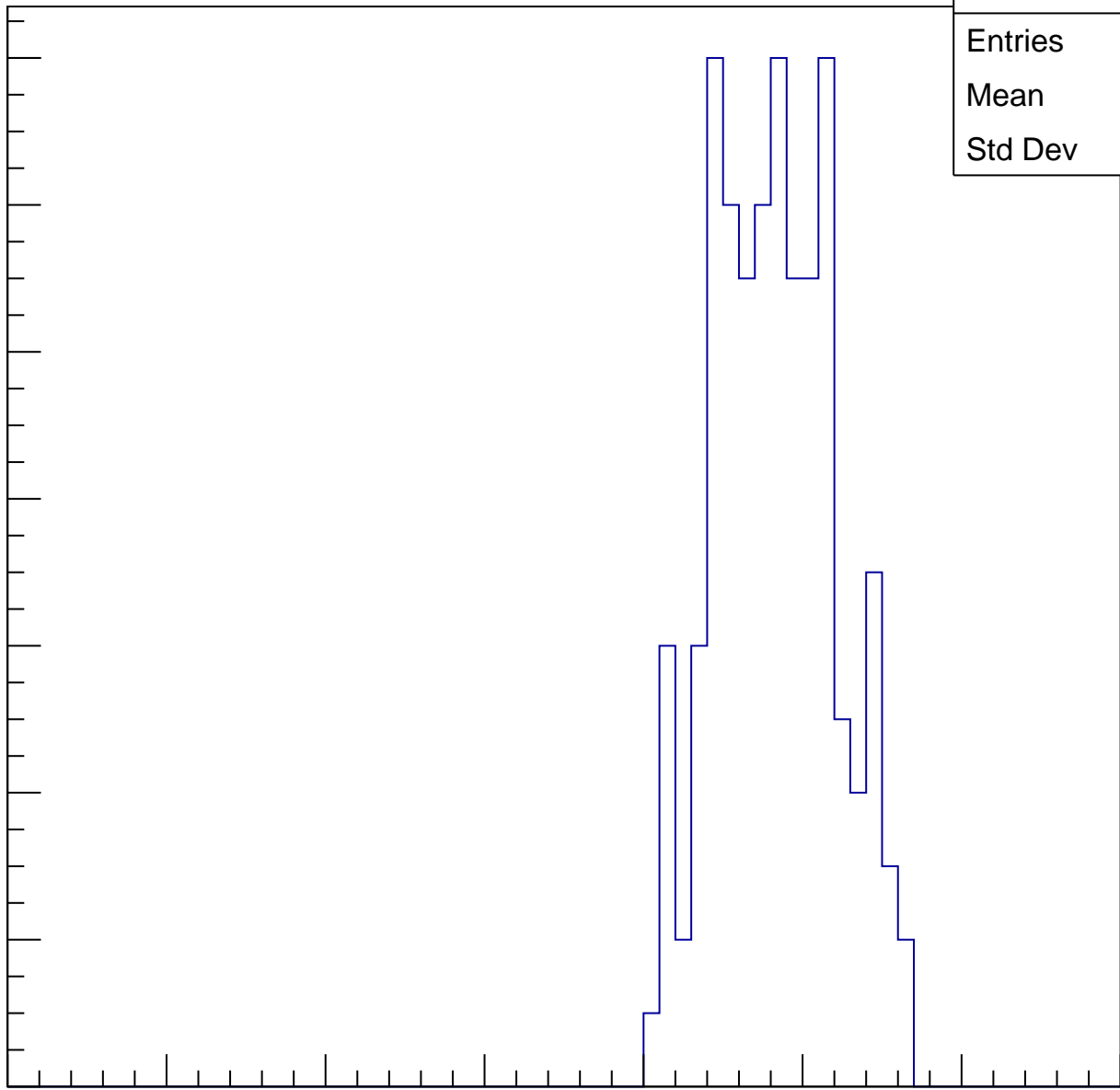
Entries	135
Mean	47.82
Std Dev	3.704

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch68, adc4

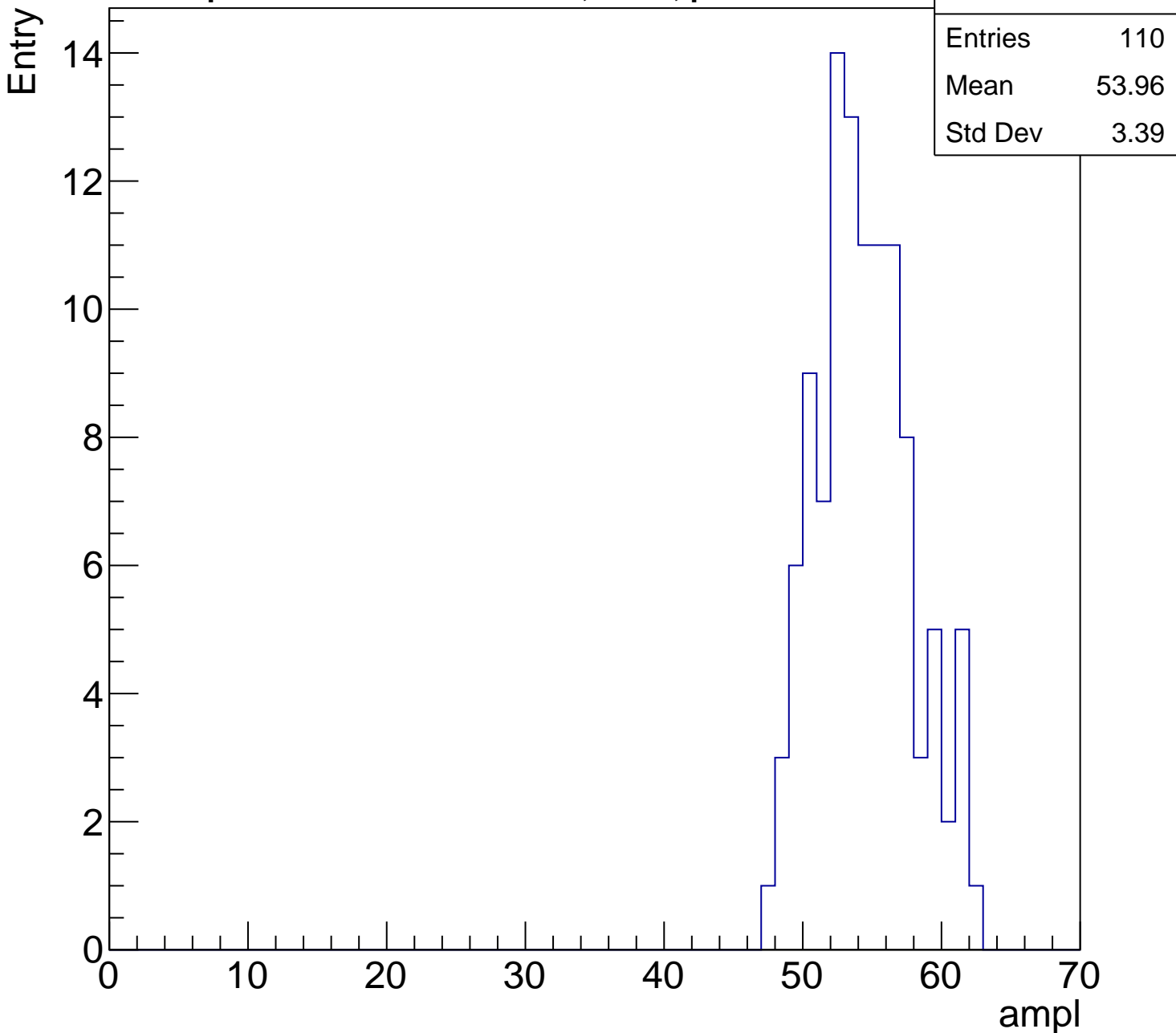
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	53.96
Std Dev	3.39

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch68, adc5

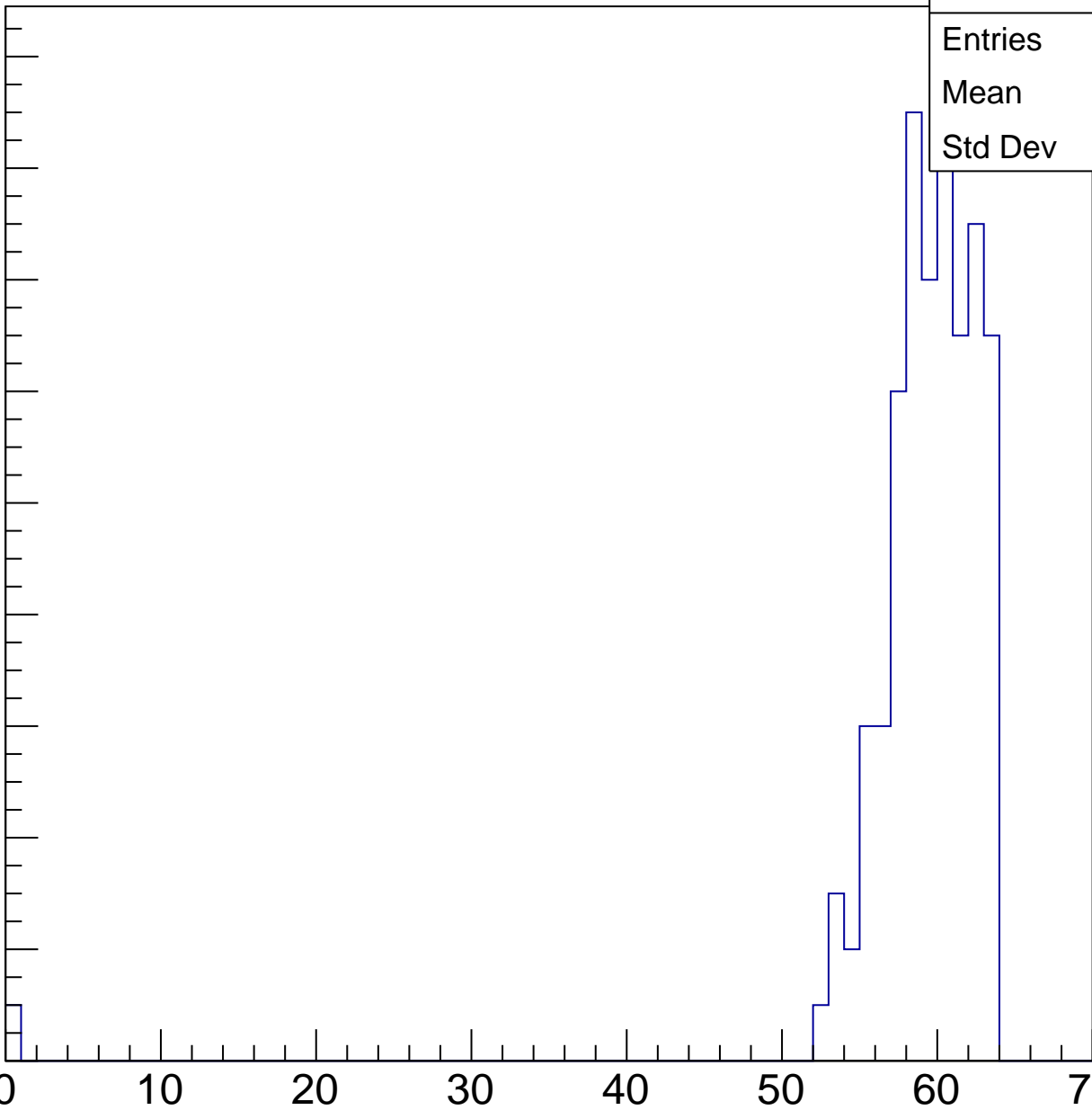
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	121
Mean	58.7
Std Dev	5.965

ampl

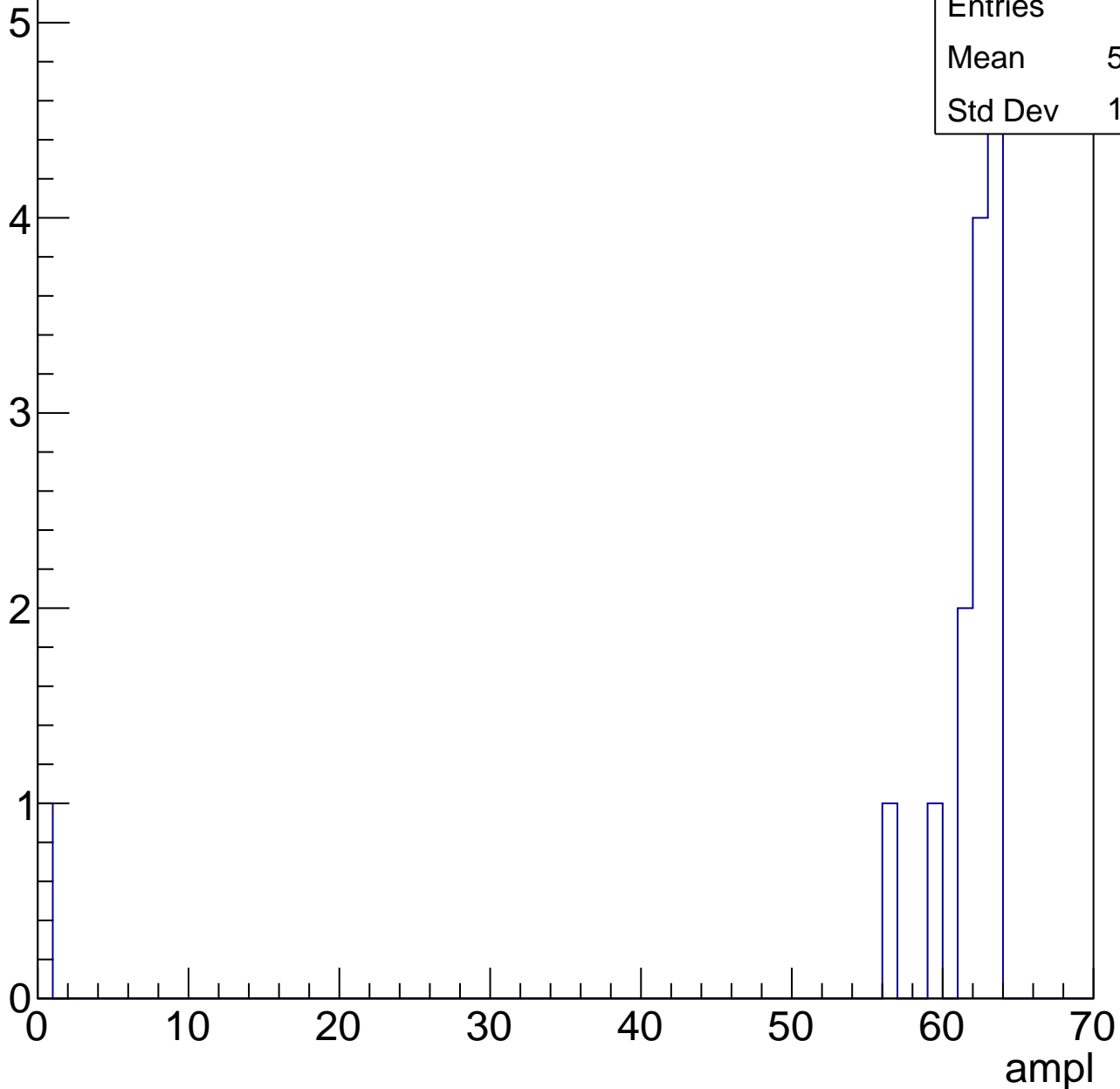


B0L101S, U20-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.14
Std Dev	15.96



B0L101S, U20-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch69, adc0

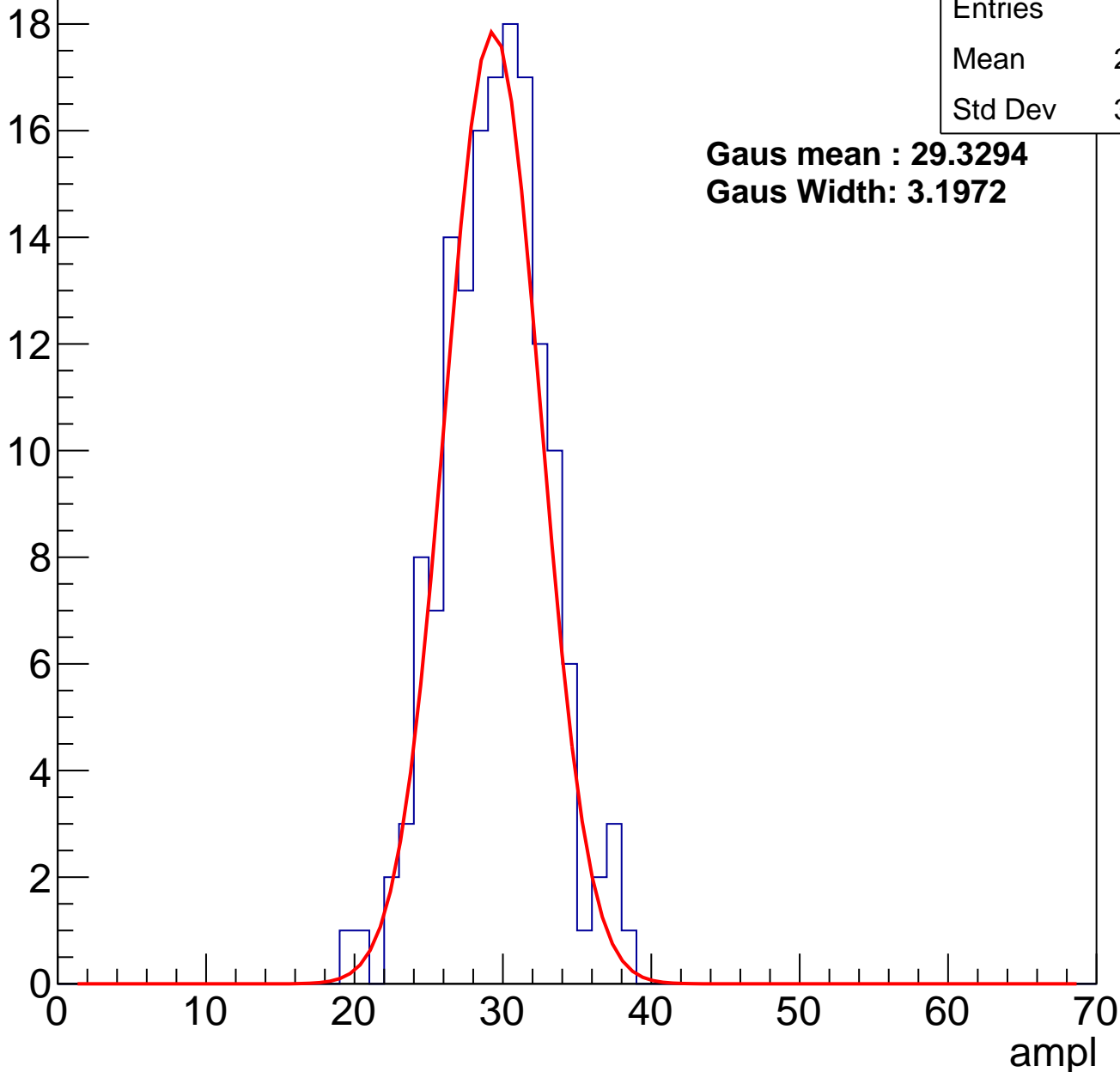
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.05
Std Dev	3.445

Gaus mean : 29.3294

Gaus Width: 3.1972

Entry



B0L101S, U20-ch69, adc1

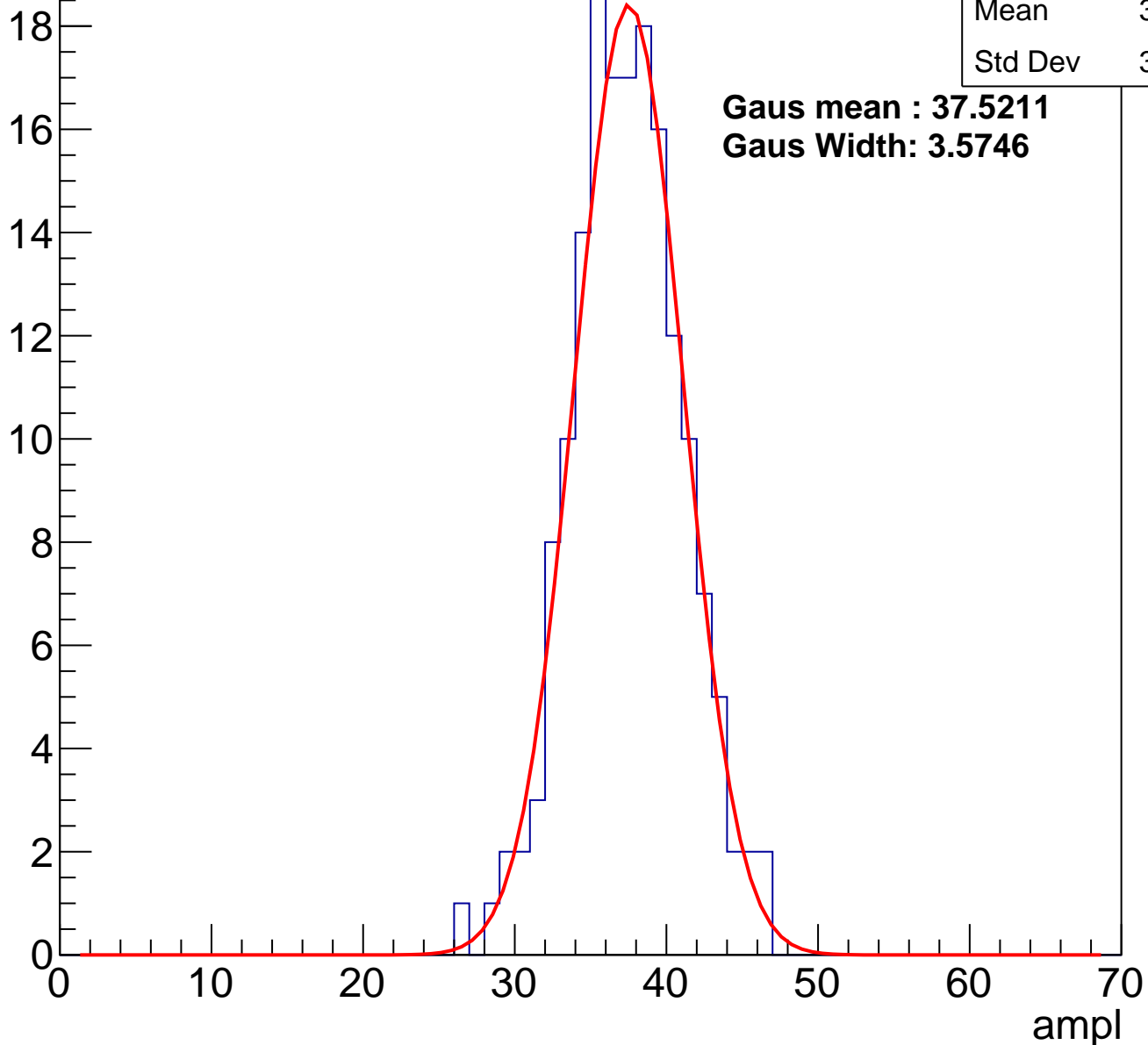
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	36.96
Std Dev	3.625

Gaus mean : 37.5211

Gaus Width: 3.5746

Entry



B0L101S, U20-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries 115

Mean 44.47

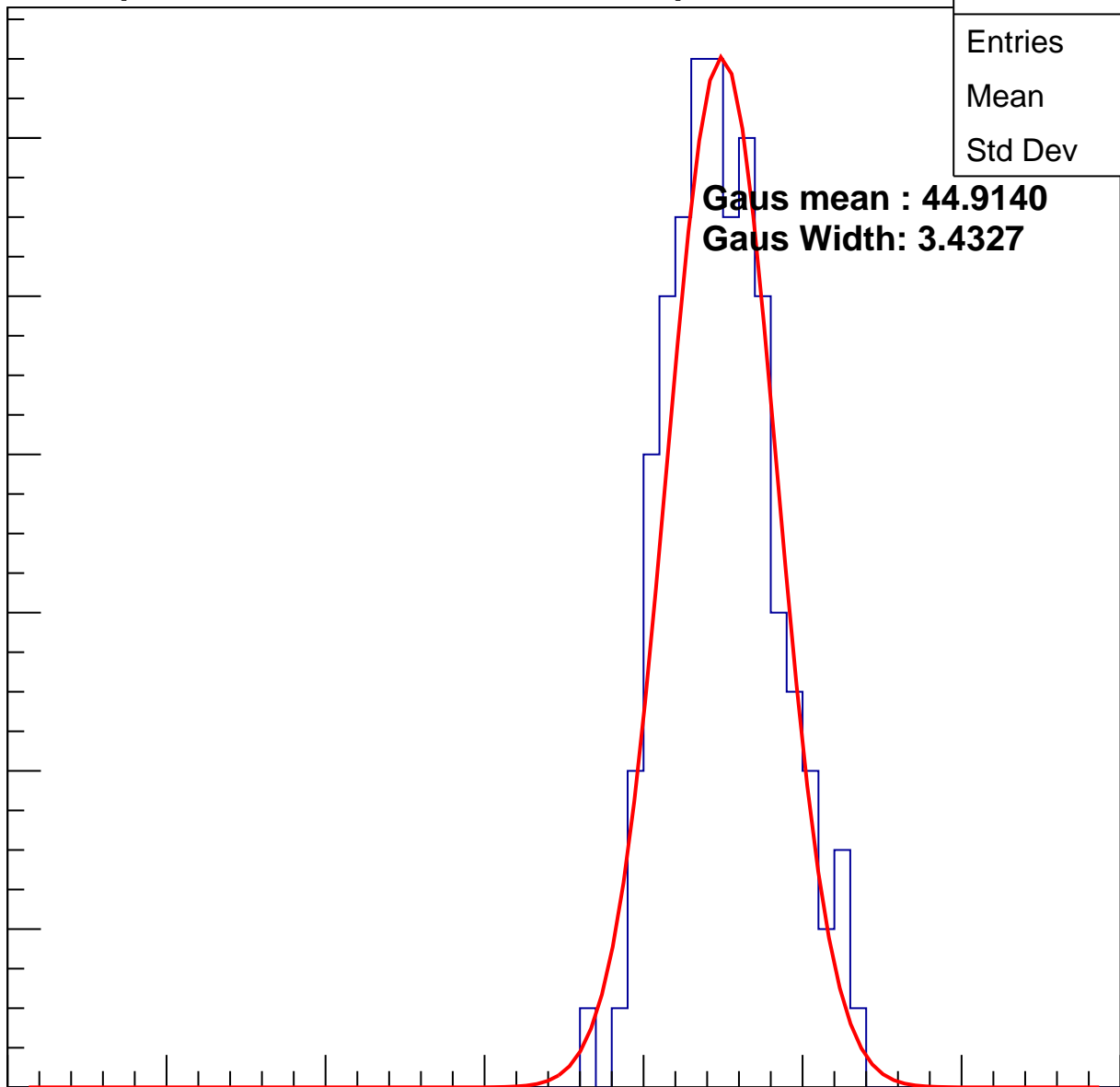
Std Dev 3.407

Gaus mean : 44.9140

Gaus Width: 3.4327

0 10 20 30 40 50 60 70

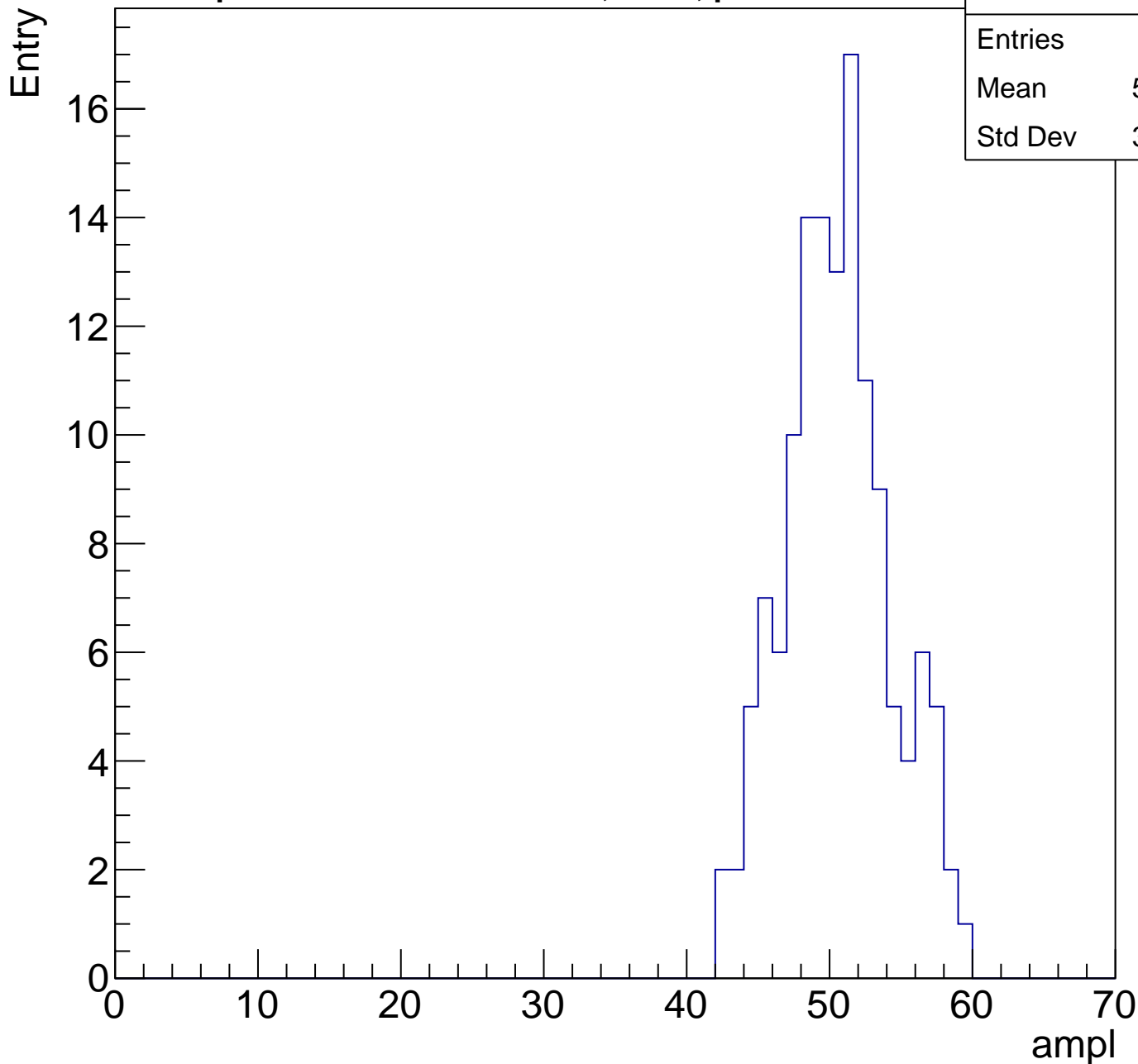
ampl



B0L101S, U20-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	50.08
Std Dev	3.707



B0L101S, U20-ch69, adc4

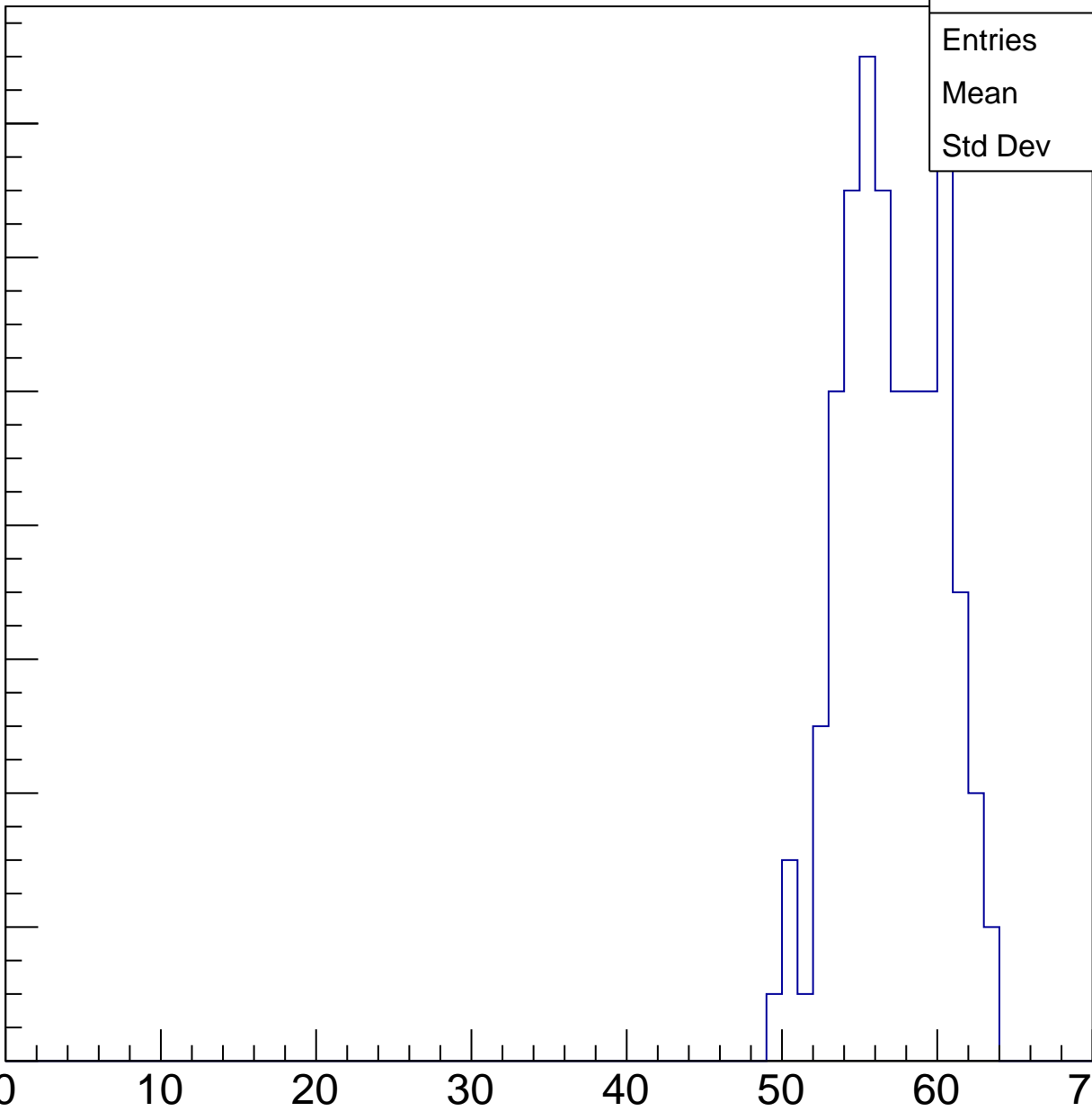
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	56.58
Std Dev	3.136

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch69, adc5

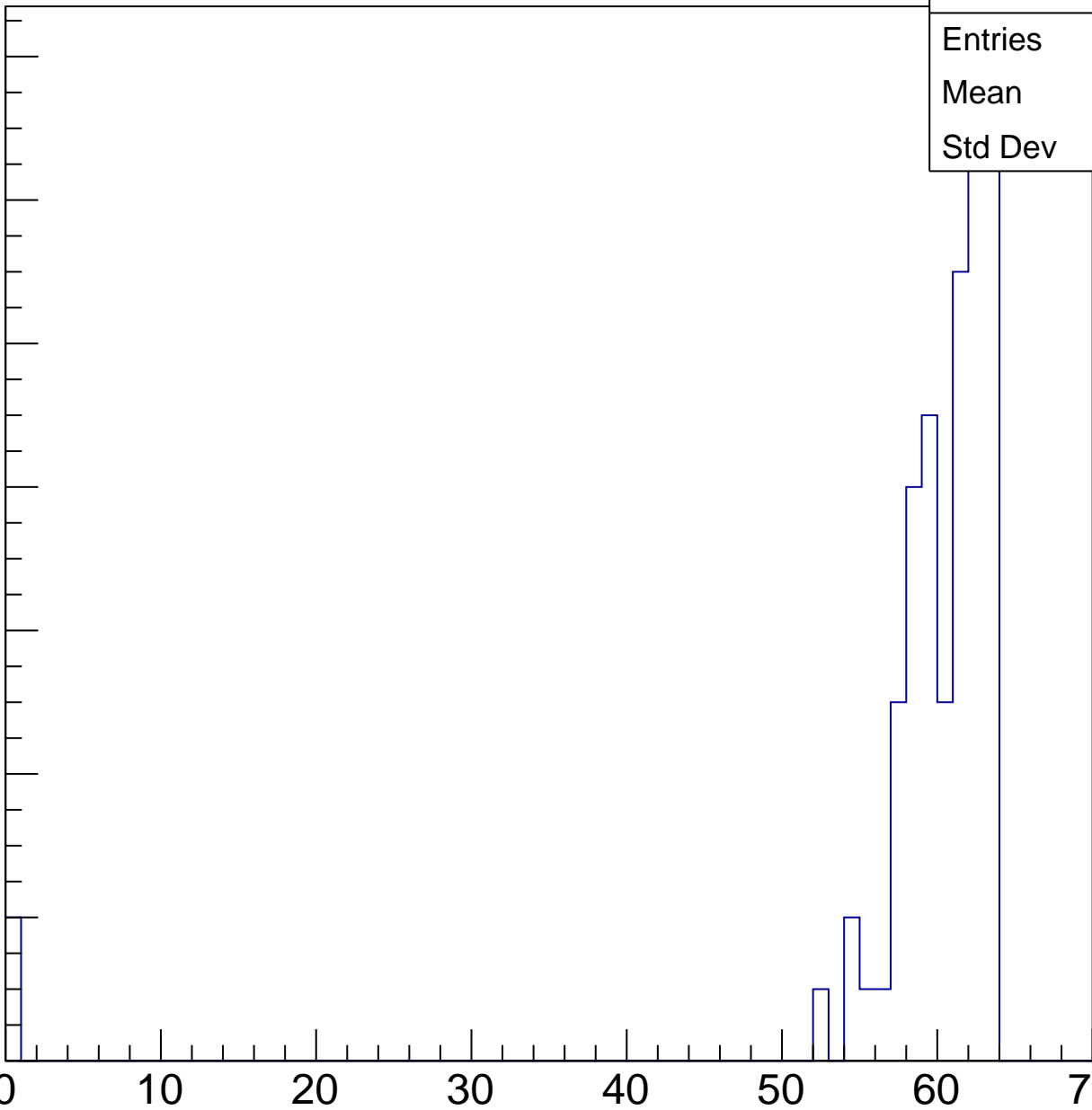
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	58.52
Std Dev	10.13

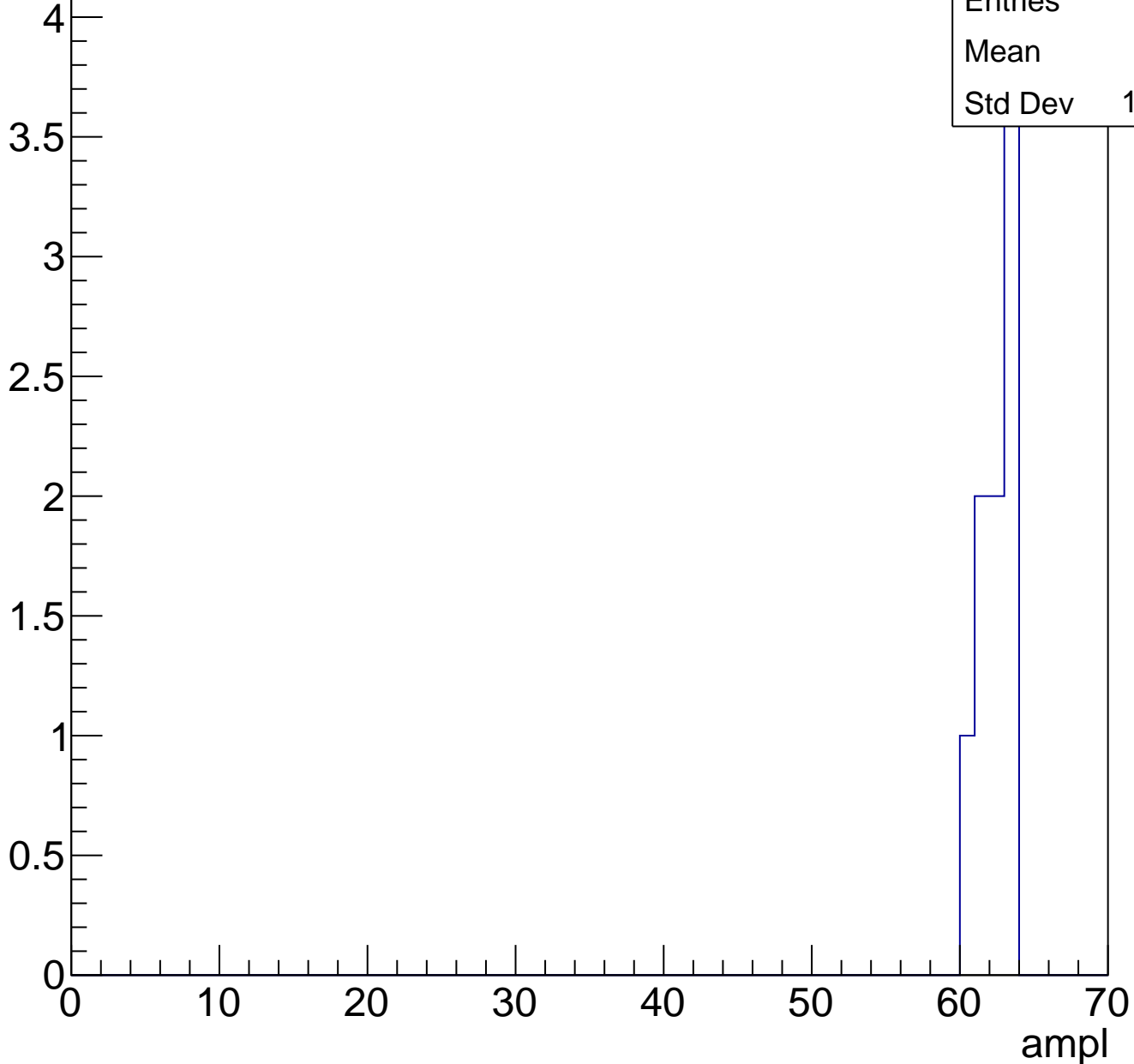
ampl



B0L101S, U20-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch70, adc0

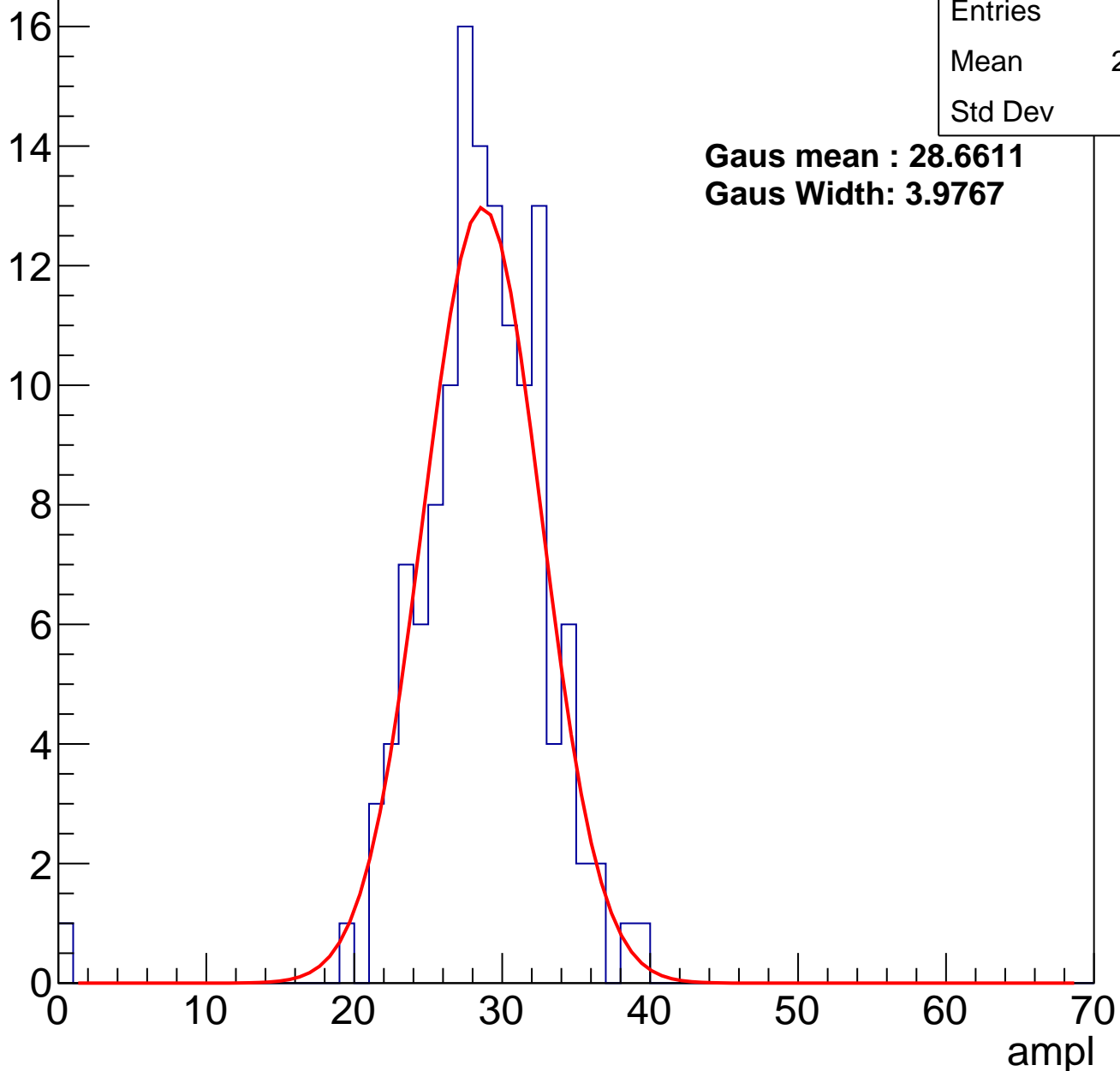
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	28.17
Std Dev	4.48

Gaus mean : 28.6611

Gaus Width: 3.9767

Entry

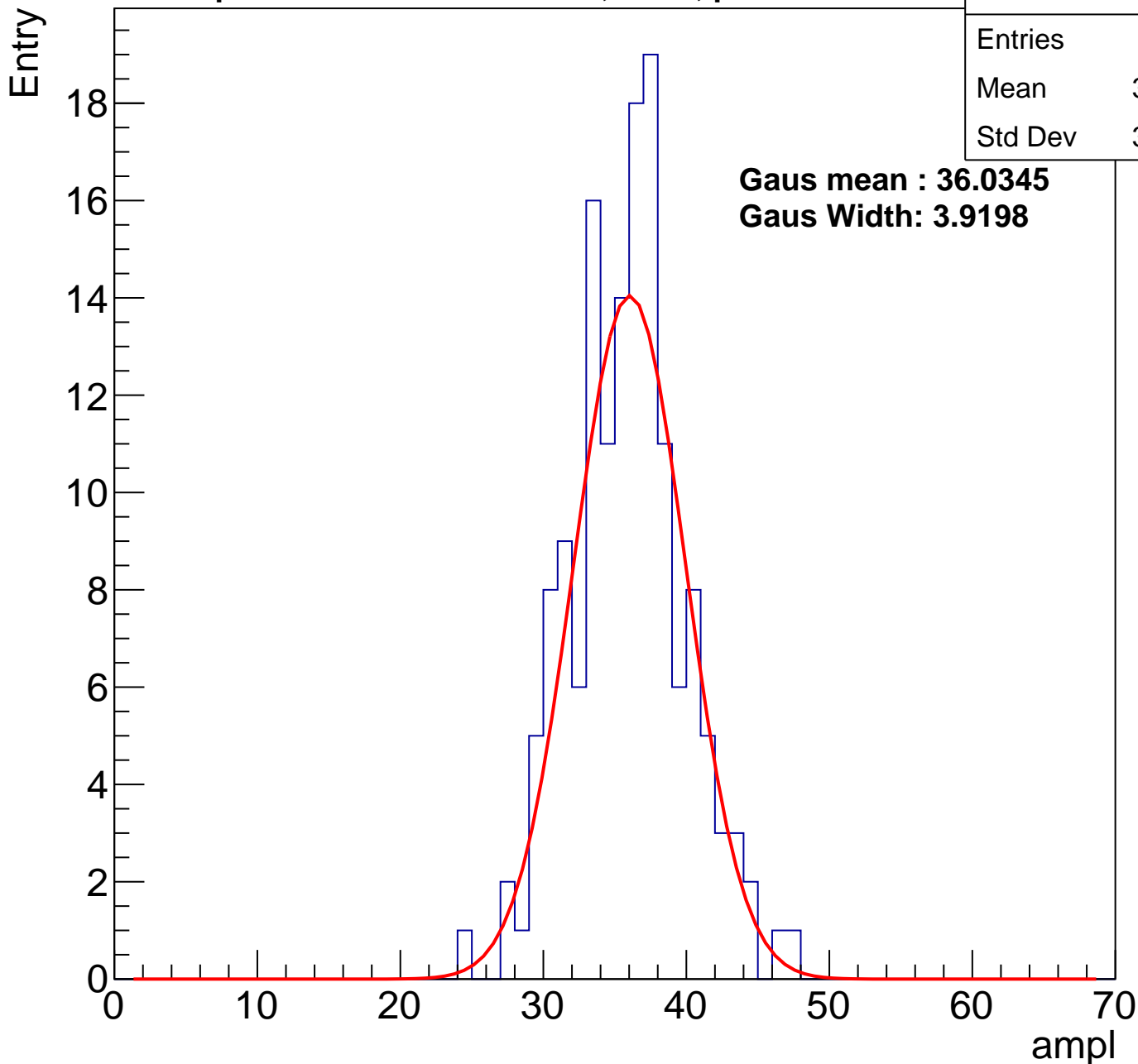


B0L101S, U20-ch70, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	35.45
Std Dev	3.962

Gaus mean : 36.0345
Gaus Width: 3.9198



B0L101S, U20-ch70, adc2

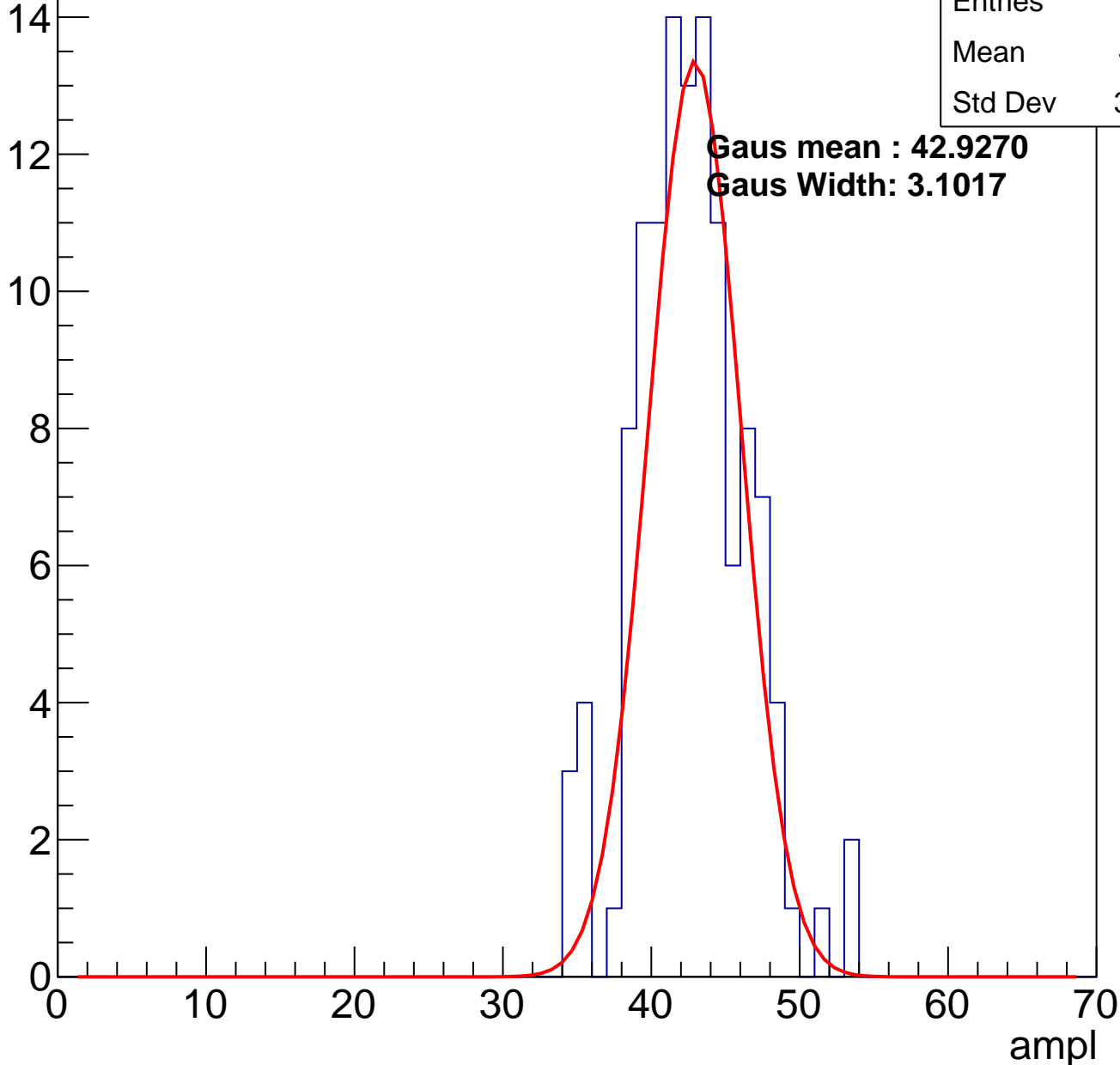
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	42.21
Std Dev	3.694

Gaus mean : 42.9270

Gaus Width: 3.1017

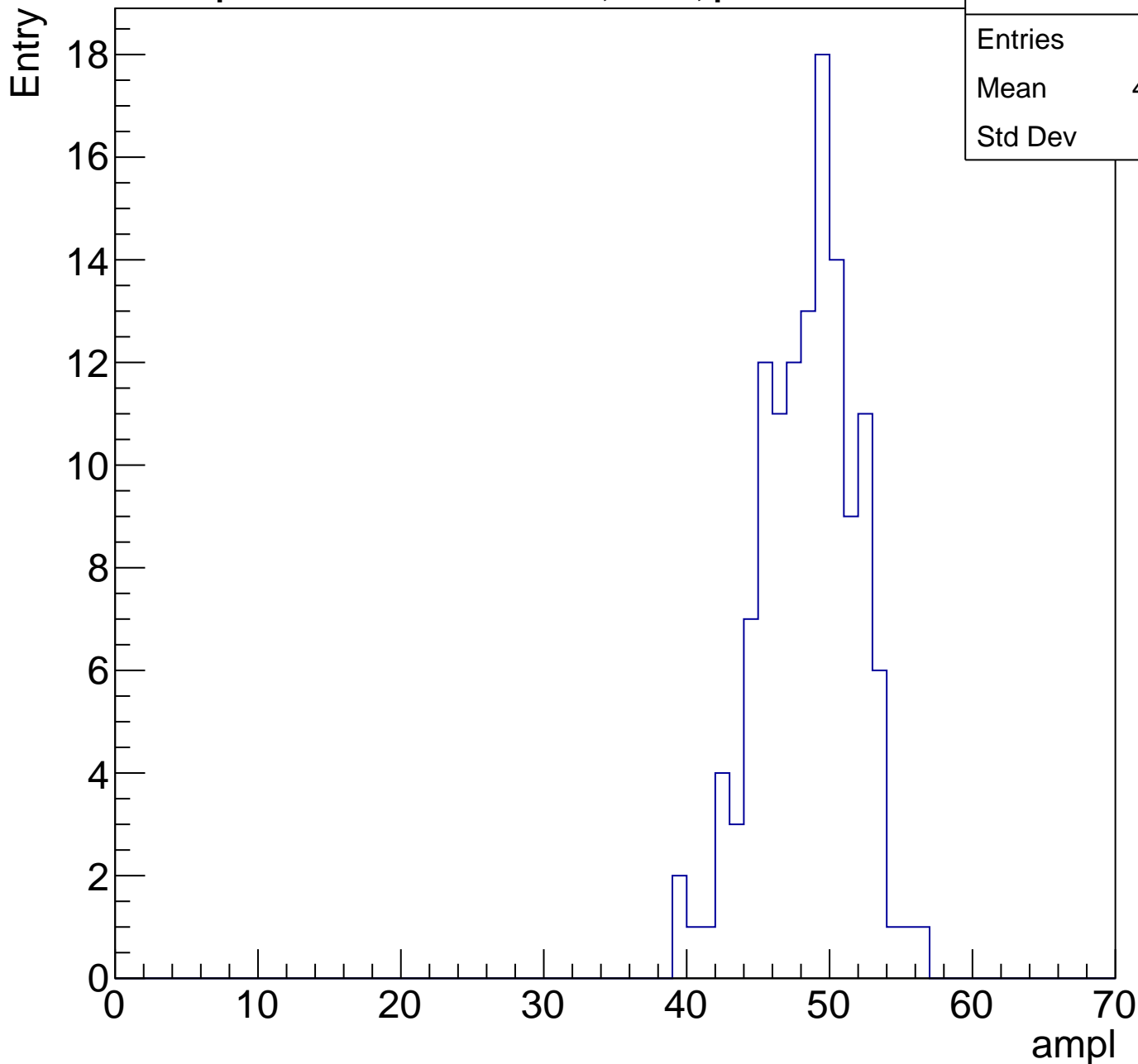
Entry



B0L101S, U20-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

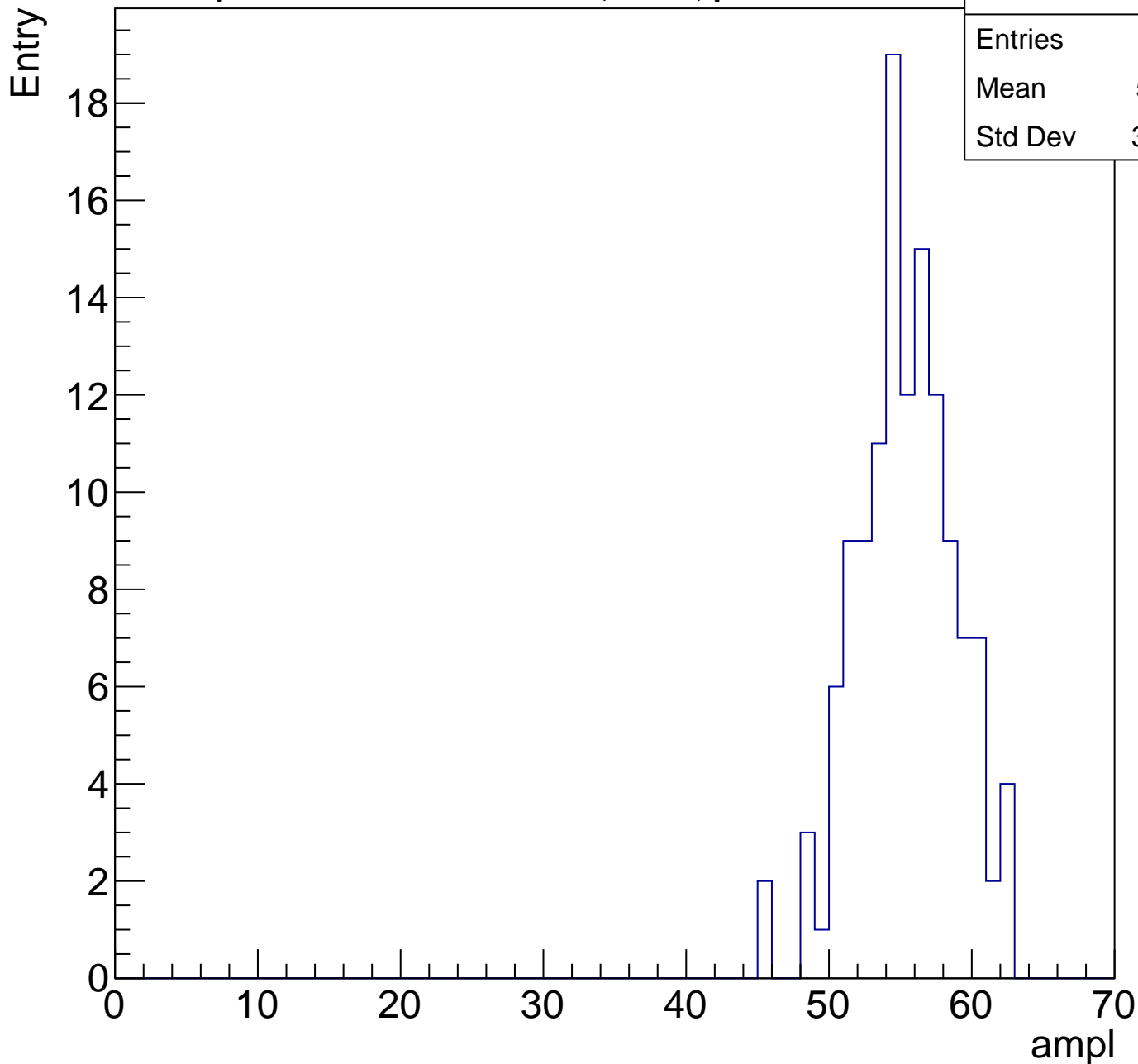
Entries	127
Mean	47.98
Std Dev	3.32



B0L101S, U20-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	54.91
Std Dev	3.445



B0L101S, U20-ch70, adc5

calib_packv5_042523_0143.root, FC#1, port C1

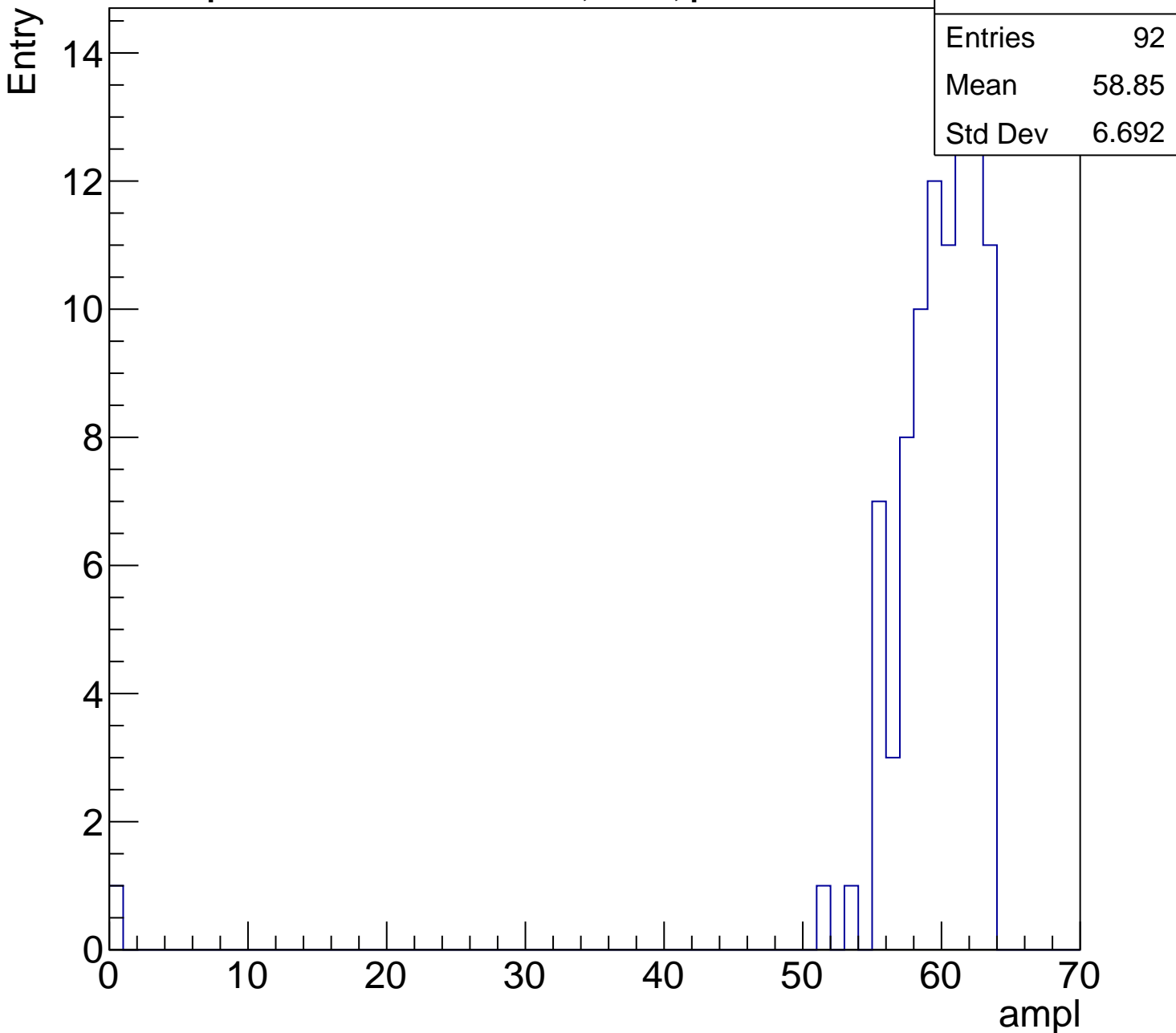
Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.85
Std Dev	6.692

ampl

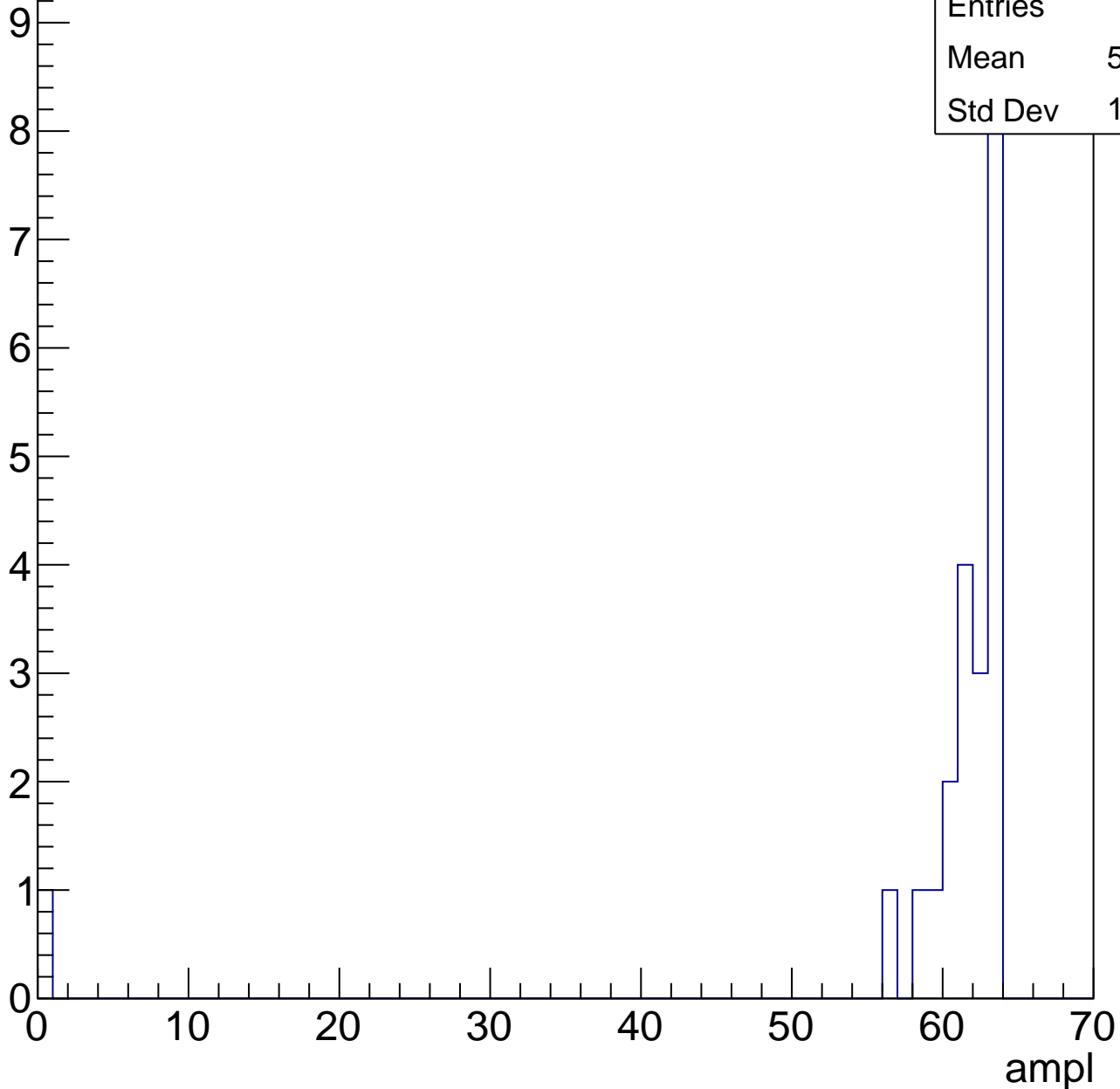
0 10 20 30 40 50 60 70



B0L101S, U20-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch71, adc0

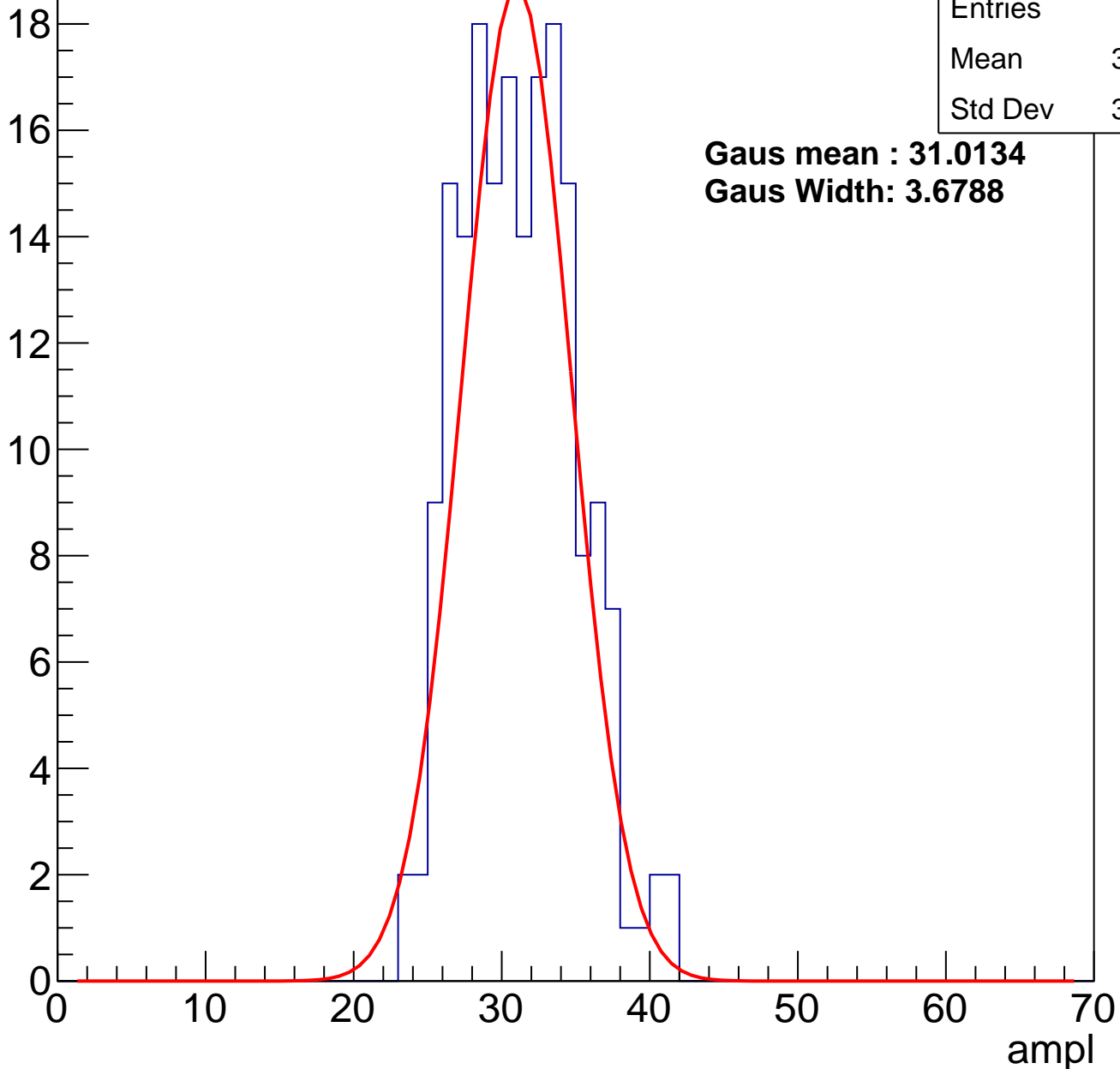
calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	30.75
Std Dev	3.796

Gaus mean : 31.0134

Gaus Width: 3.6788

Entry

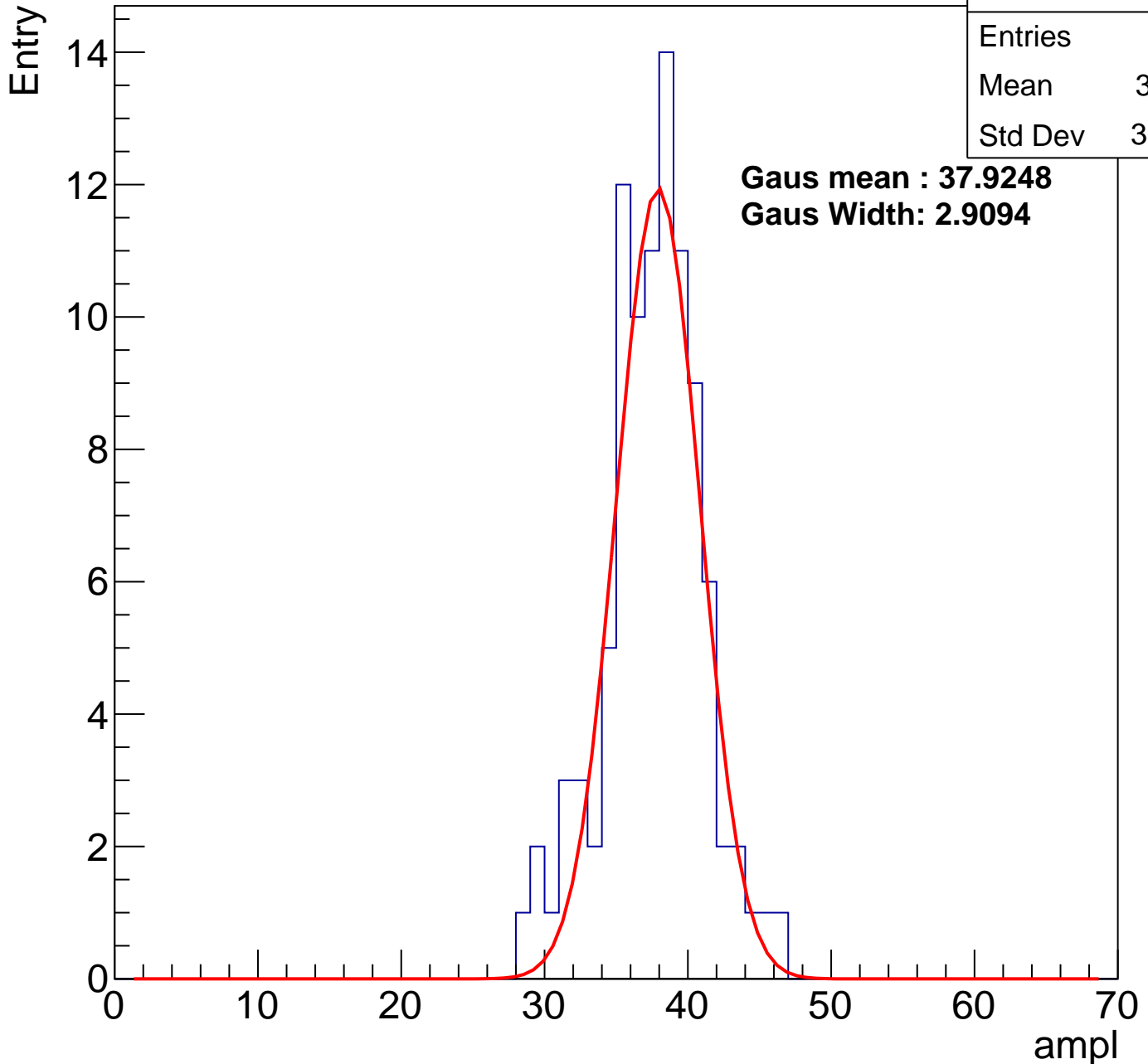


B0L101S, U20-ch71, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	37.11
Std Dev	3.407

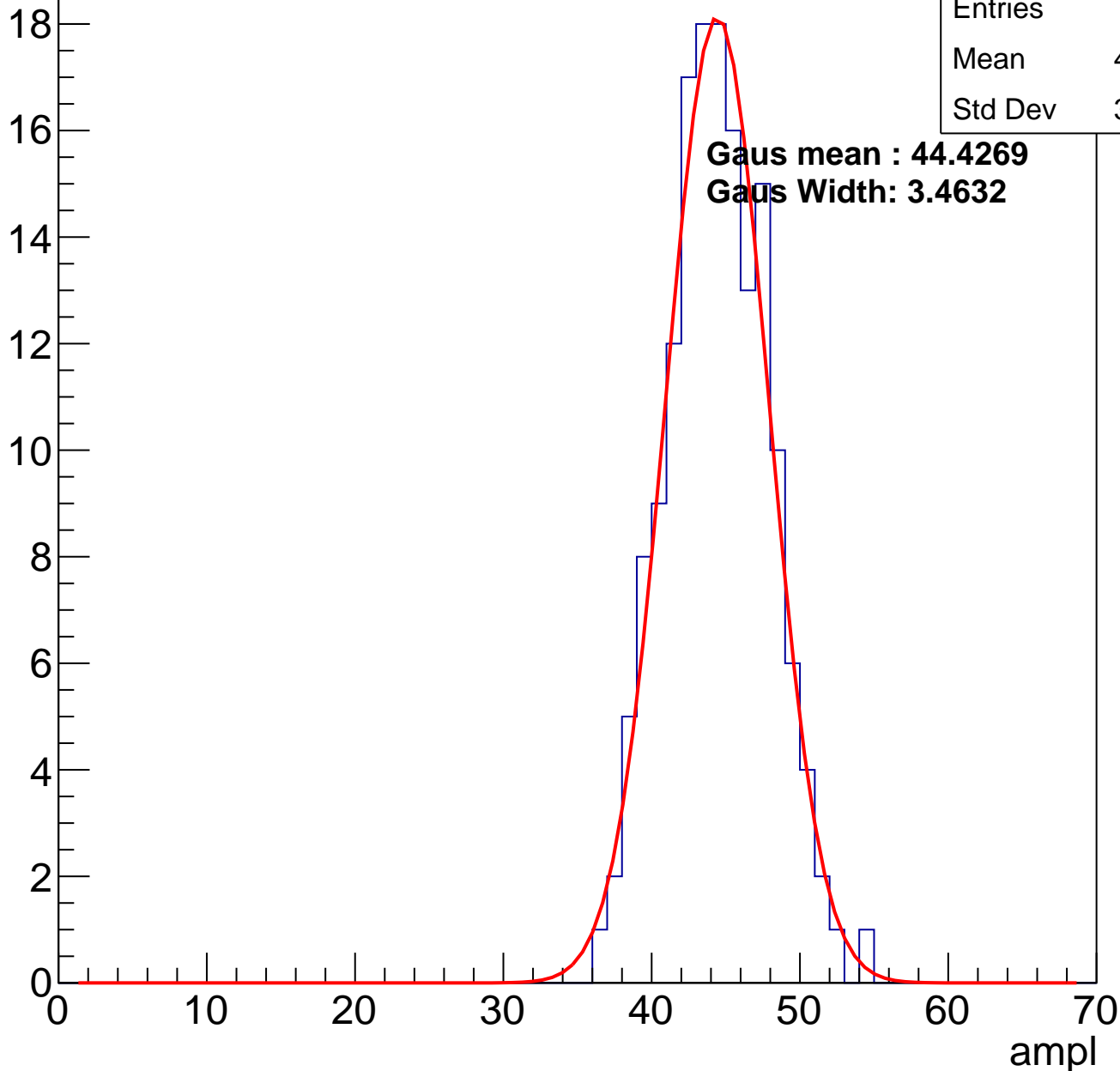
Gaus mean : 37.9248
Gaus Width: 2.9094



B0L101S, U20-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

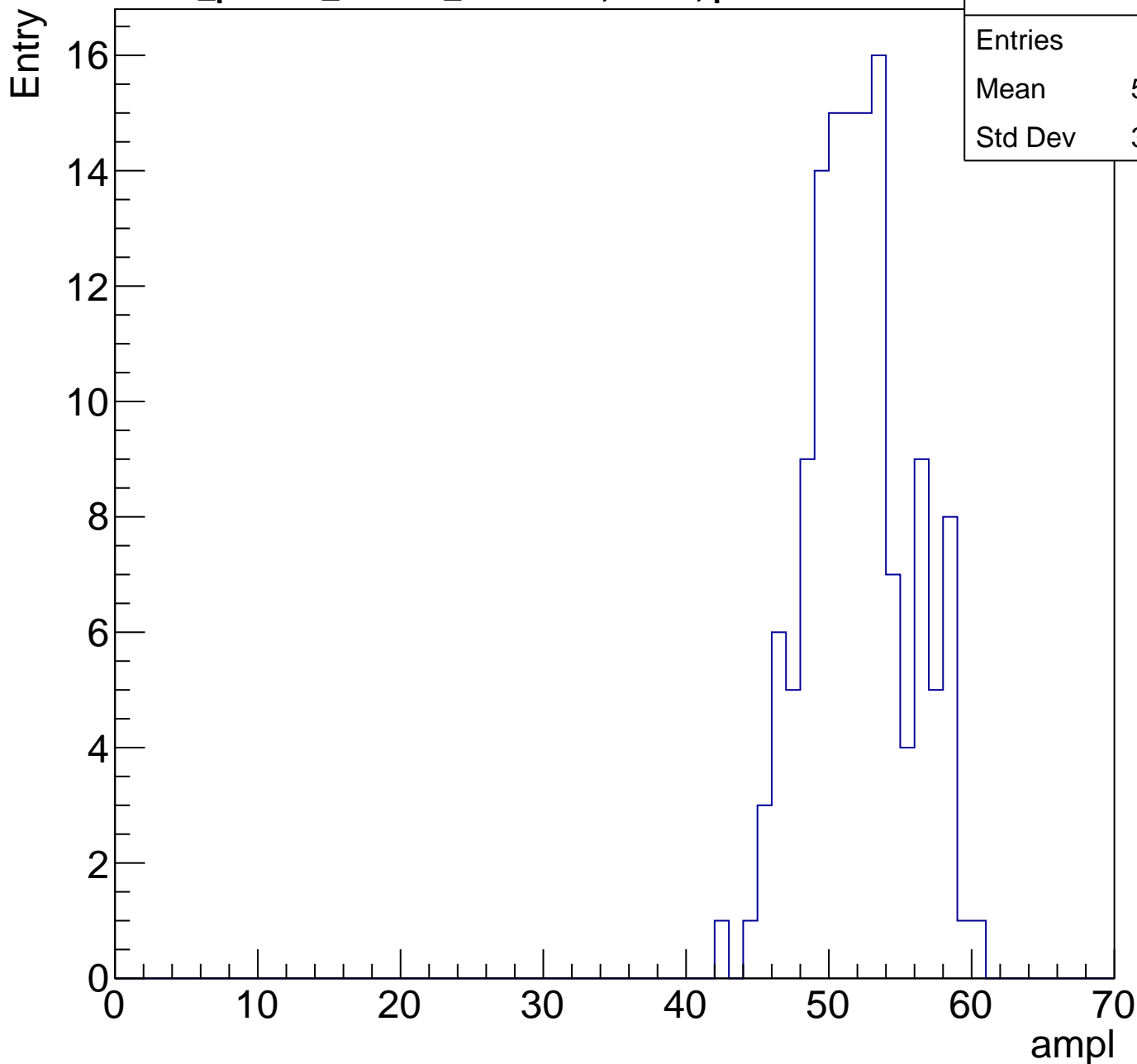
Entry



B0L101S, U20-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	51.58
Std Dev	3.572



B0L101S, U20-ch71, adc4

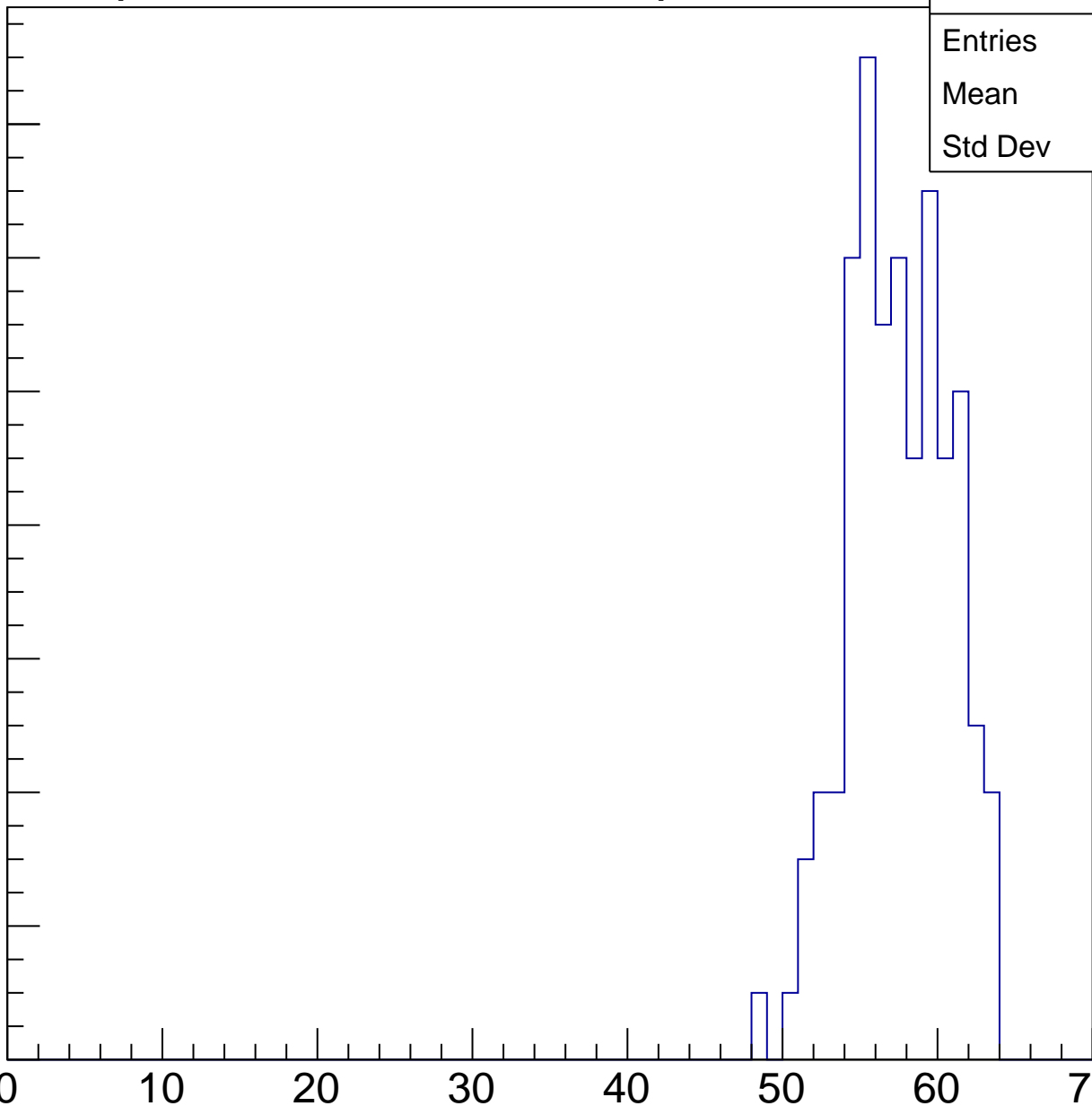
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	57.04
Std Dev	3.193

ampl



B0L101S, U20-ch71, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

57

Mean

58.68

Std Dev

11.34

ampl

0

10

20

30

40

50

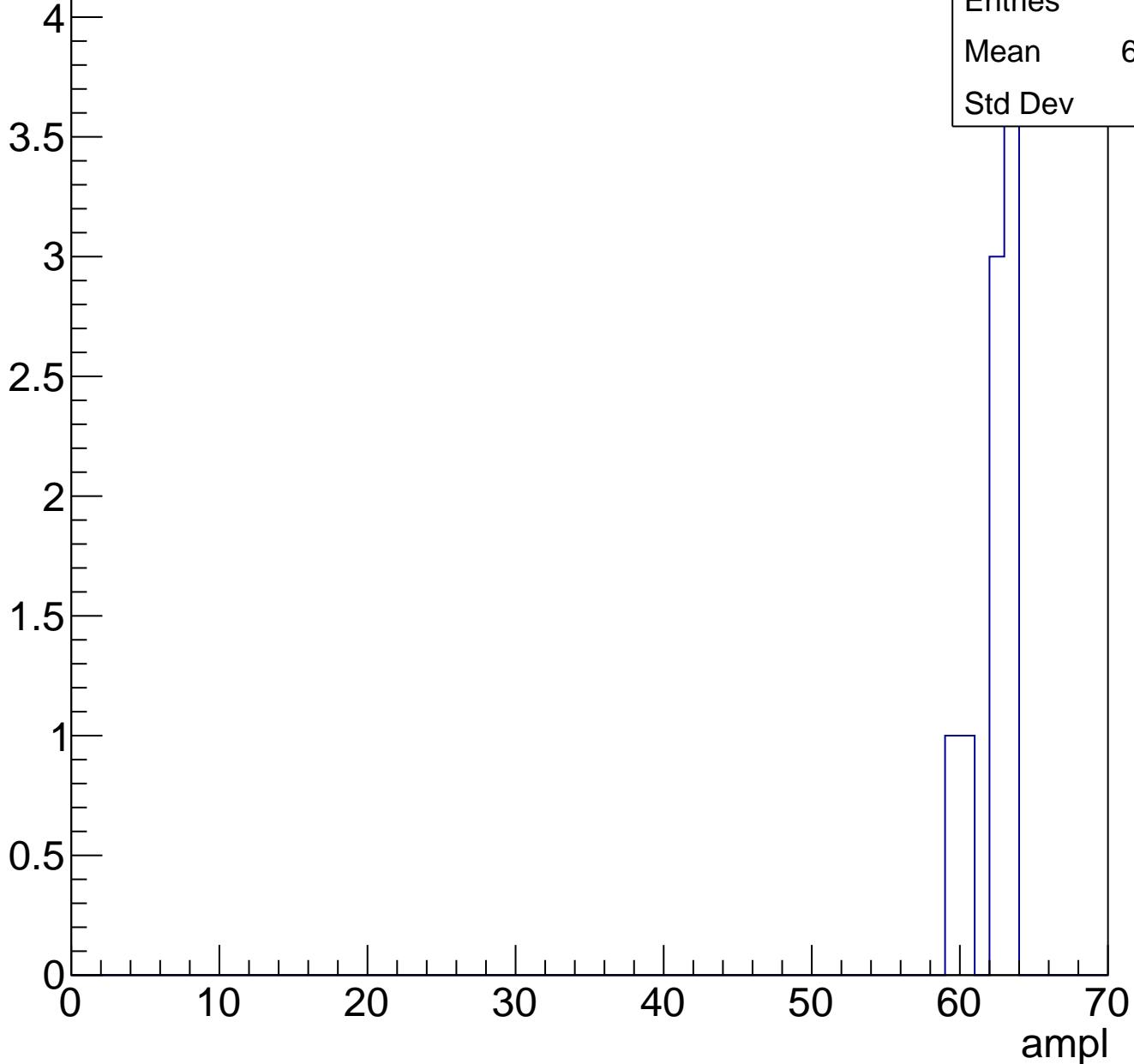
60

70

B0L101S, U20-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



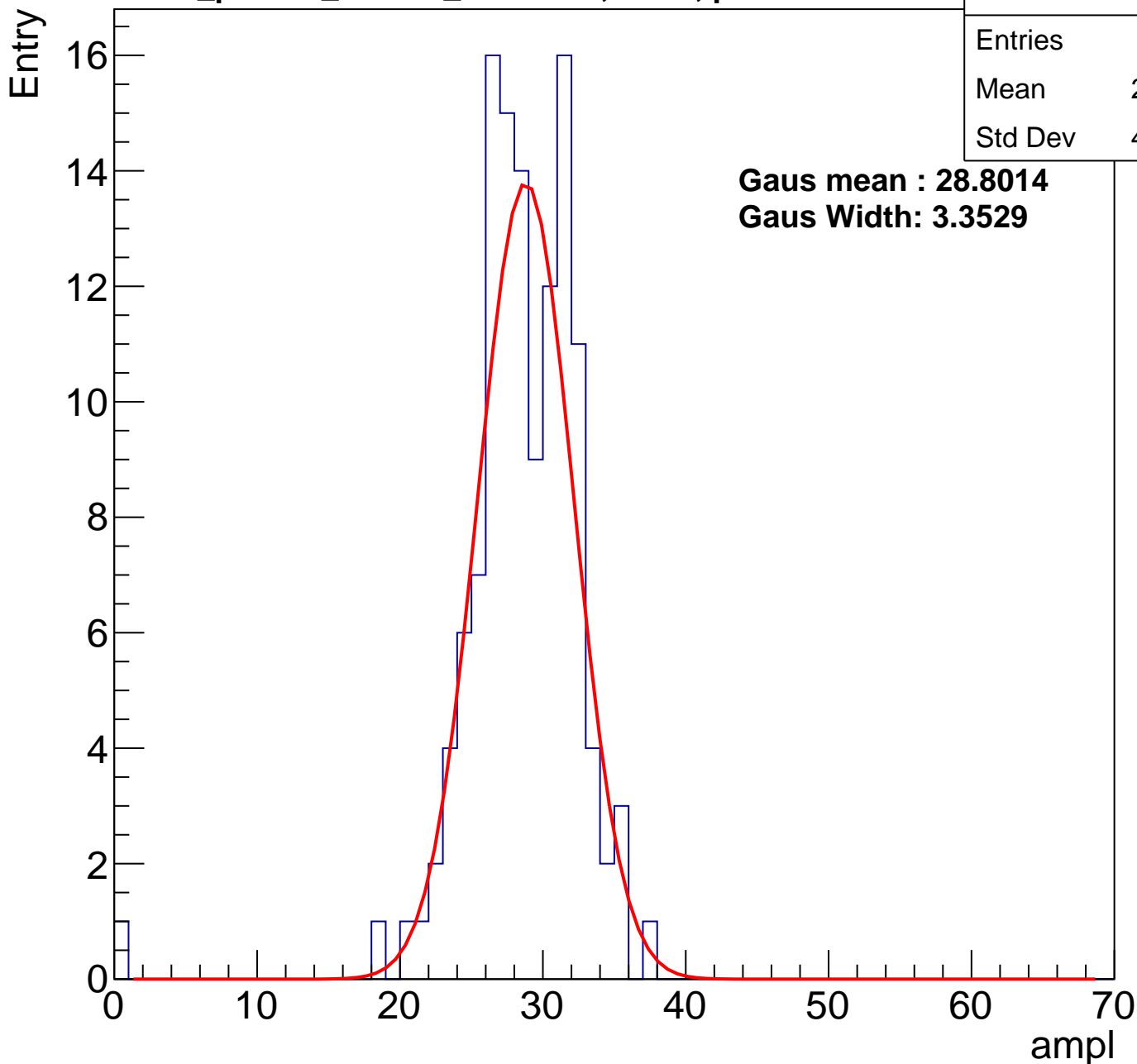
B0L101S, U20-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	28.08
Std Dev	4.163

Gaus mean : 28.8014

Gaus Width: 3.3529



B0L101S, U20-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	34.91
Std Dev	3.462

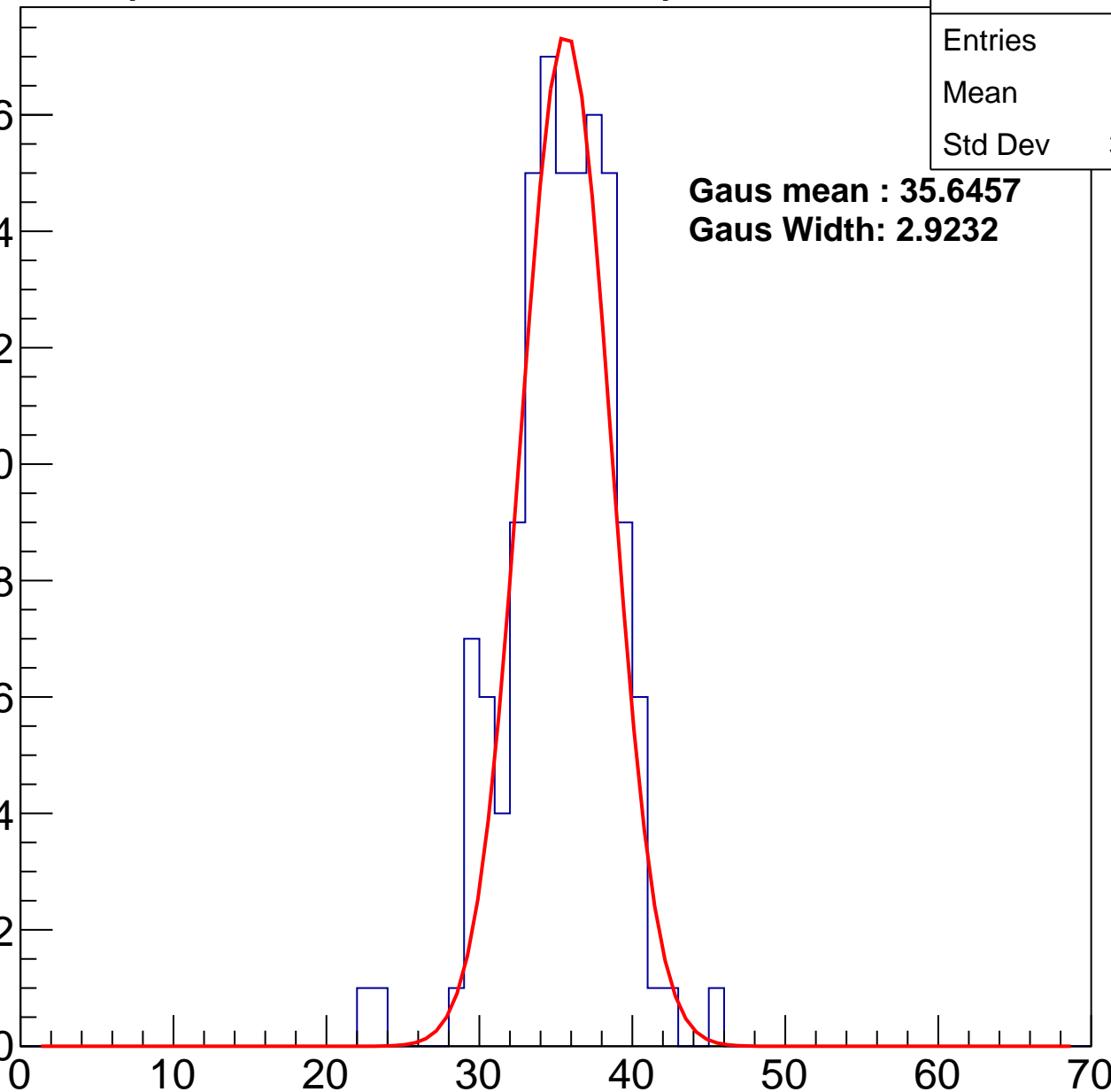
Gaus mean : 35.6457

Gaus Width: 2.9232

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

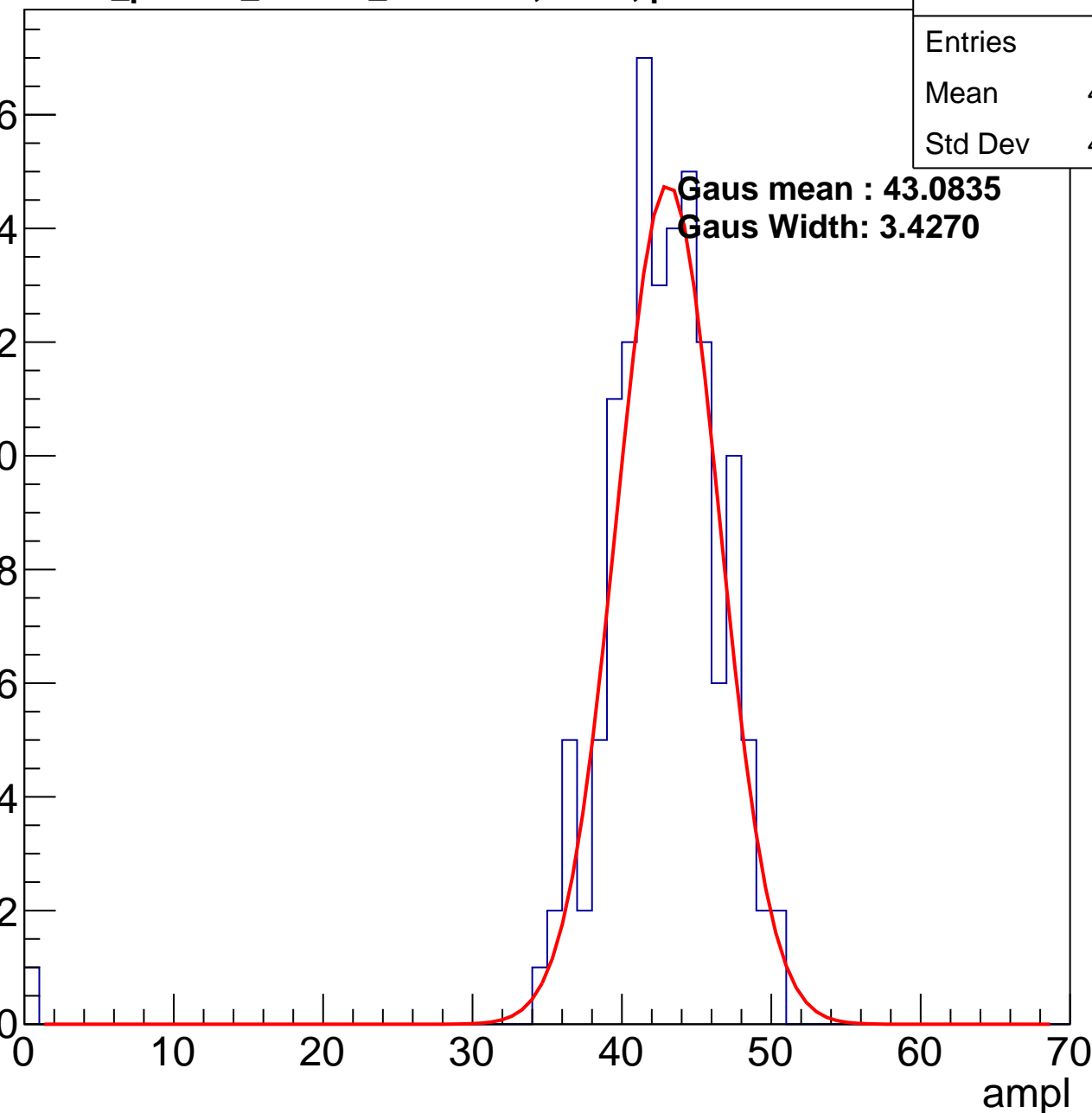
Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	42.12
Std Dev	4.968

Gaus mean : 43.0835

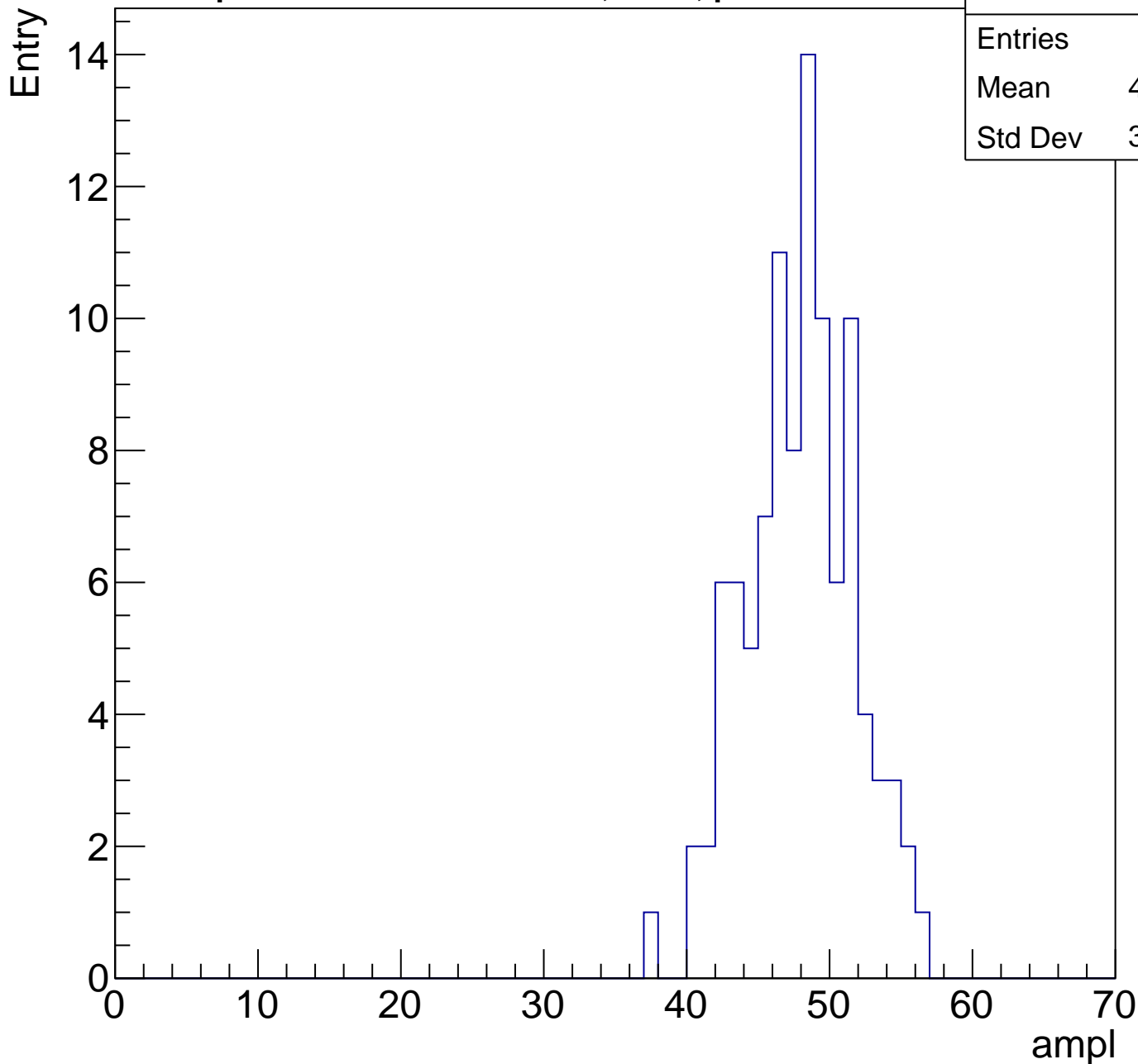
Gaus Width: 3.4270



B0L101S, U20-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

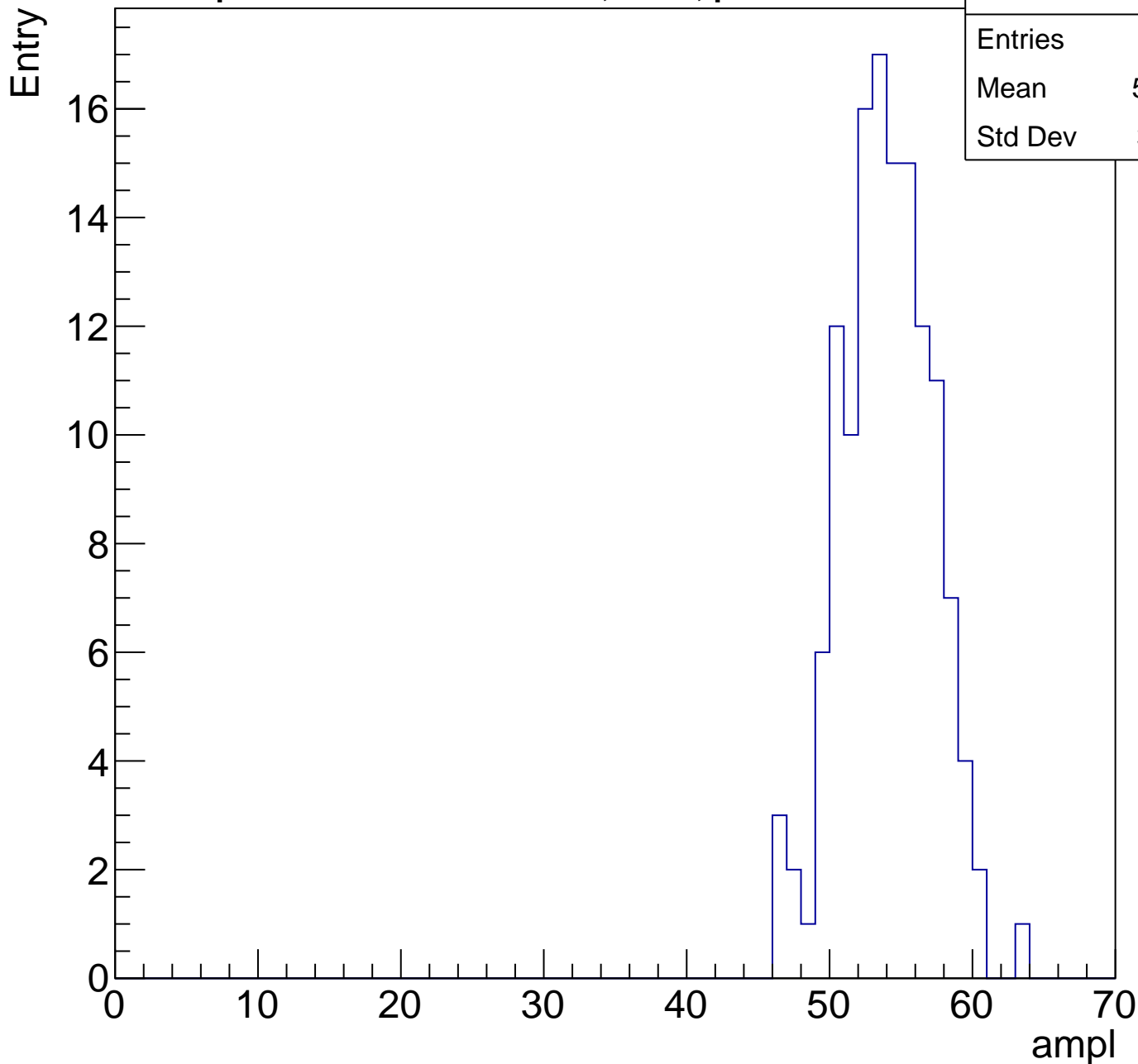
Entries	101
Mean	47.46
Std Dev	3.738



B0L101S, U20-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	53.55
Std Dev	3.161



B0L101S, U20-ch72, adc5

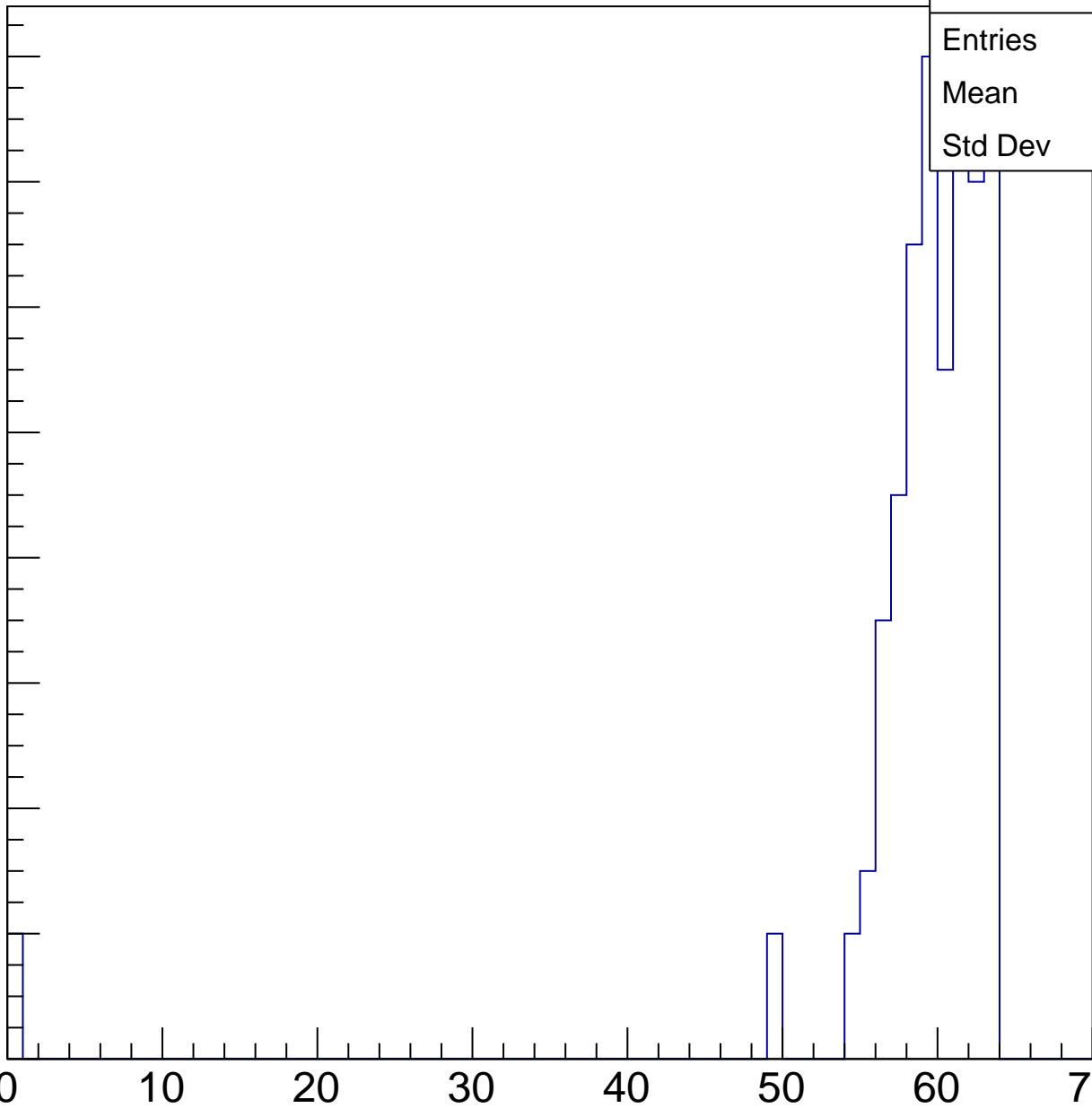
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.39
Std Dev	8.406

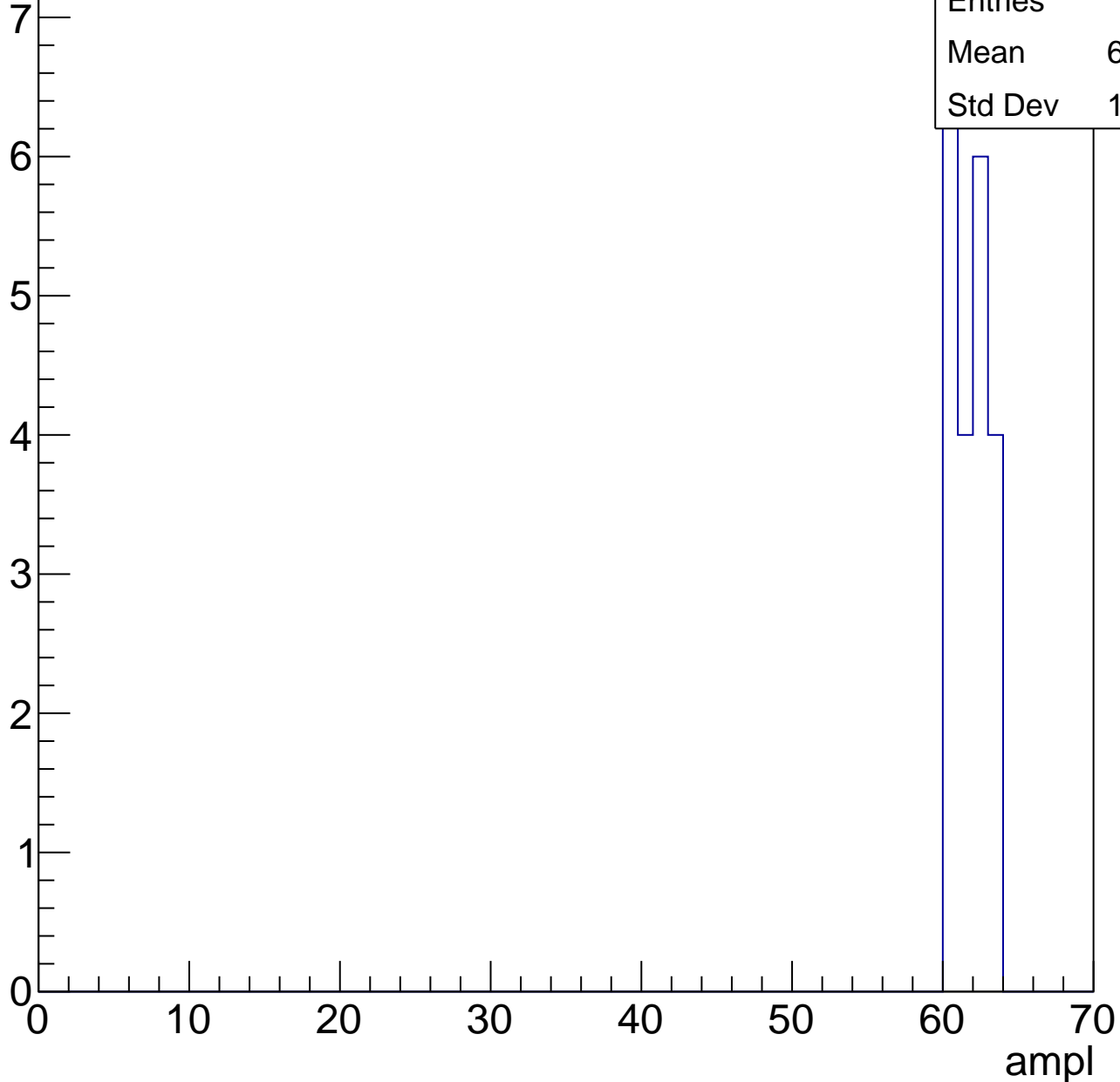
ampl



B0L101S, U20-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

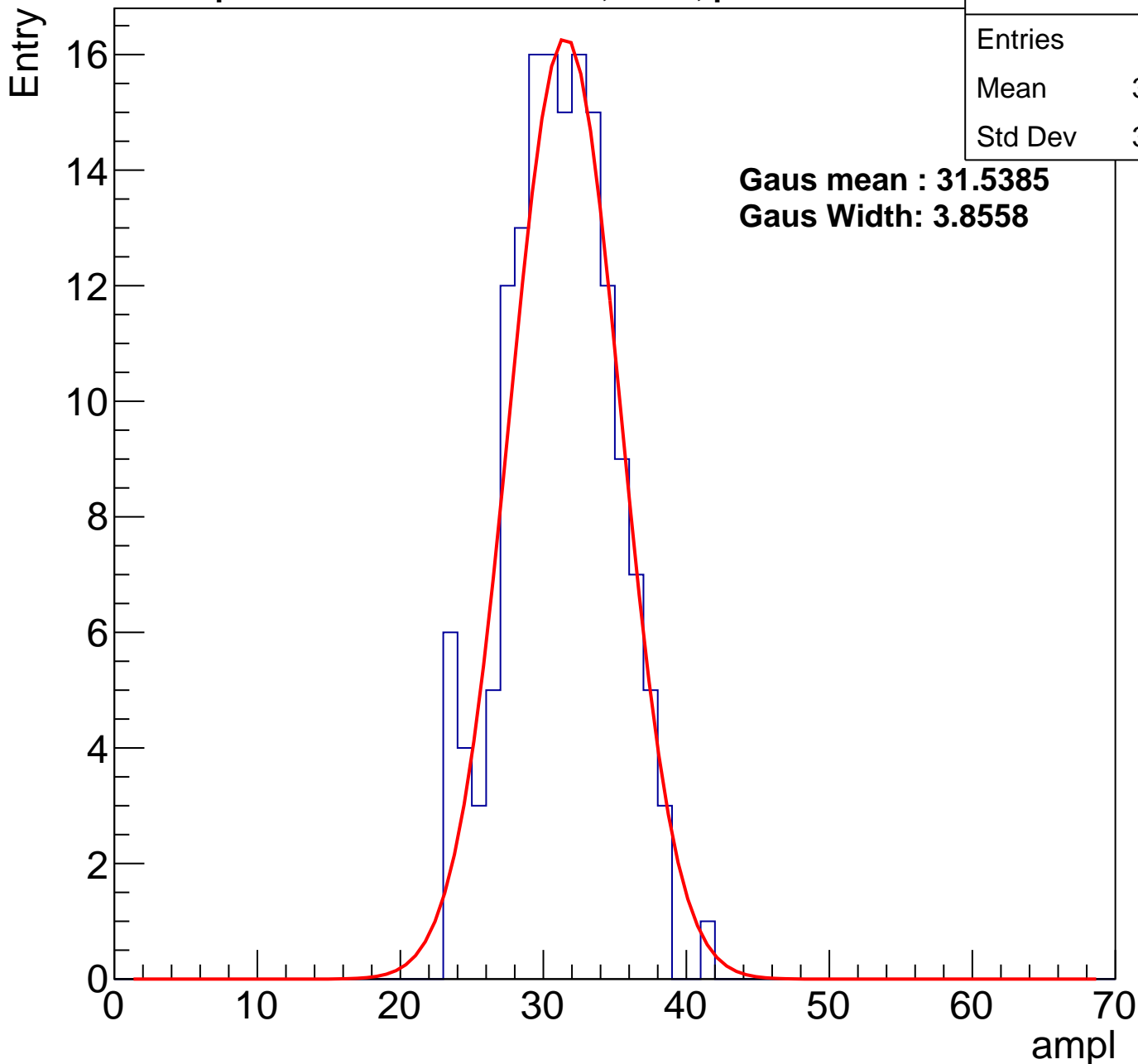
B0L101S, U20-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	30.75
Std Dev	3.665

Gaus mean : 31.5385

Gaus Width: 3.8558



B0L101S, U20-ch73, adc1

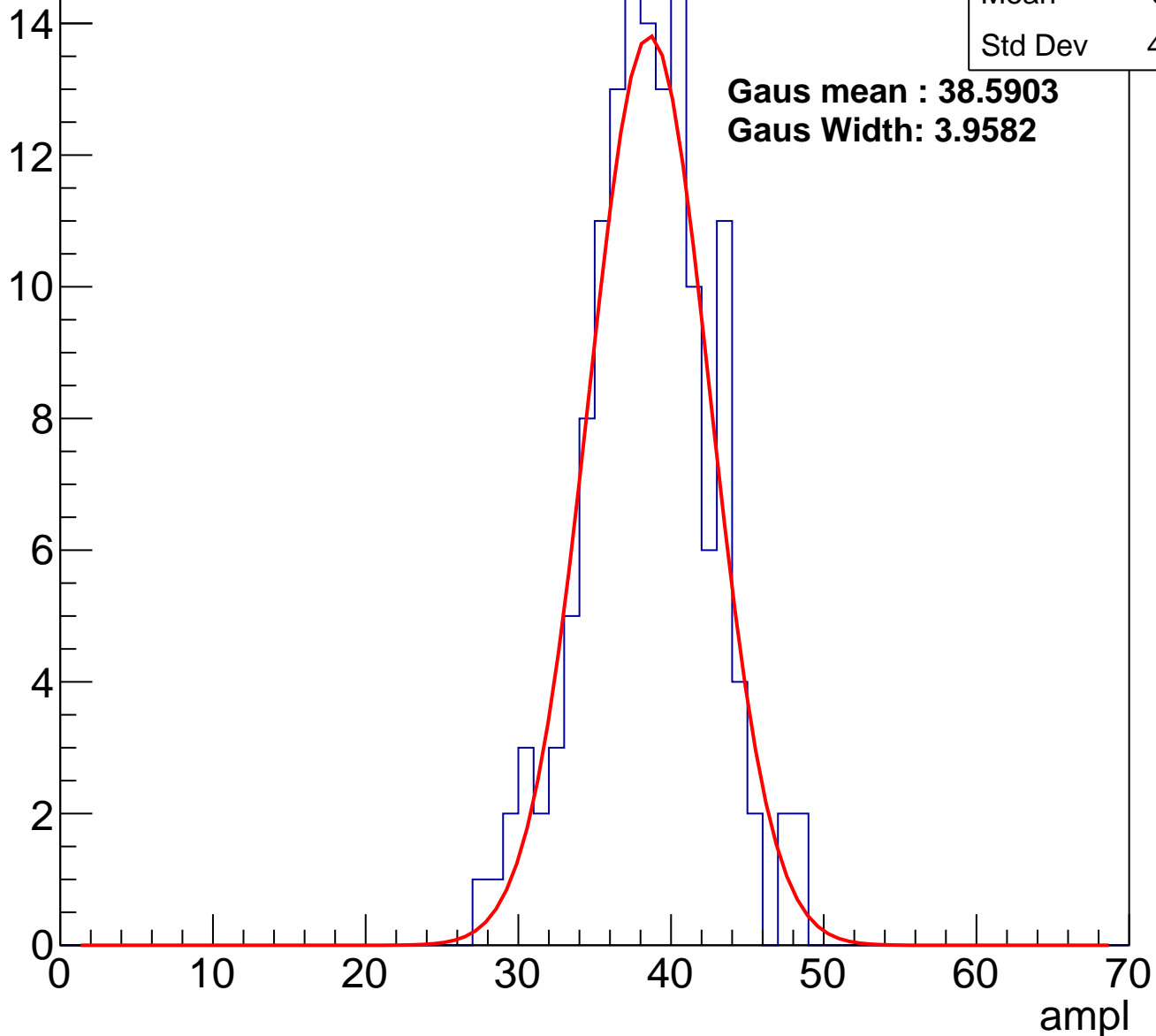
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	38.01
Std Dev	4.043

Gaus mean : 38.5903

Gaus Width: 3.9582

Entry



B0L101S, U20-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	44.44
Std Dev	3.193

Gaus mean : 44.4168

Gaus Width: 3.0147

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

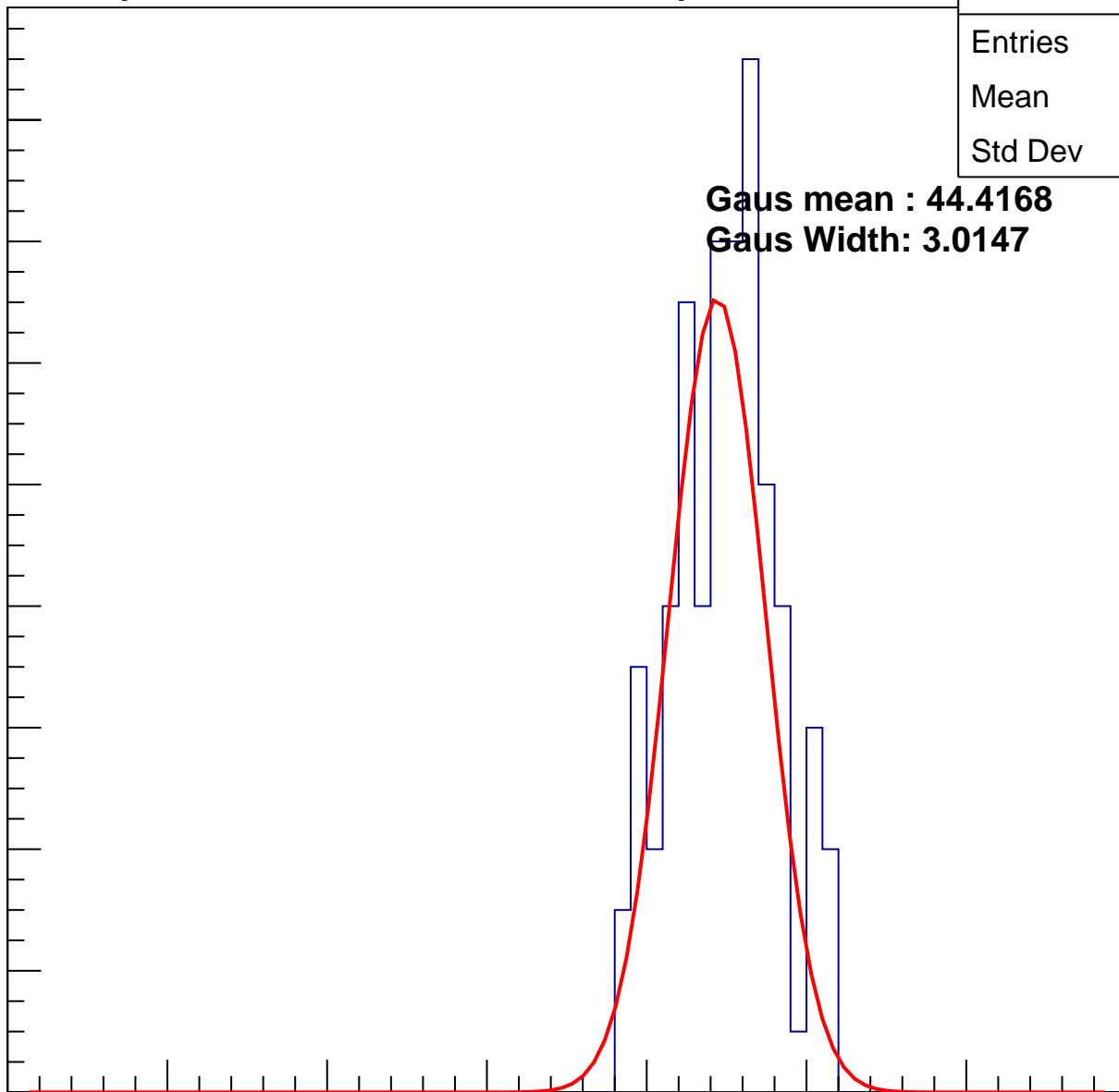
40

50

60

70

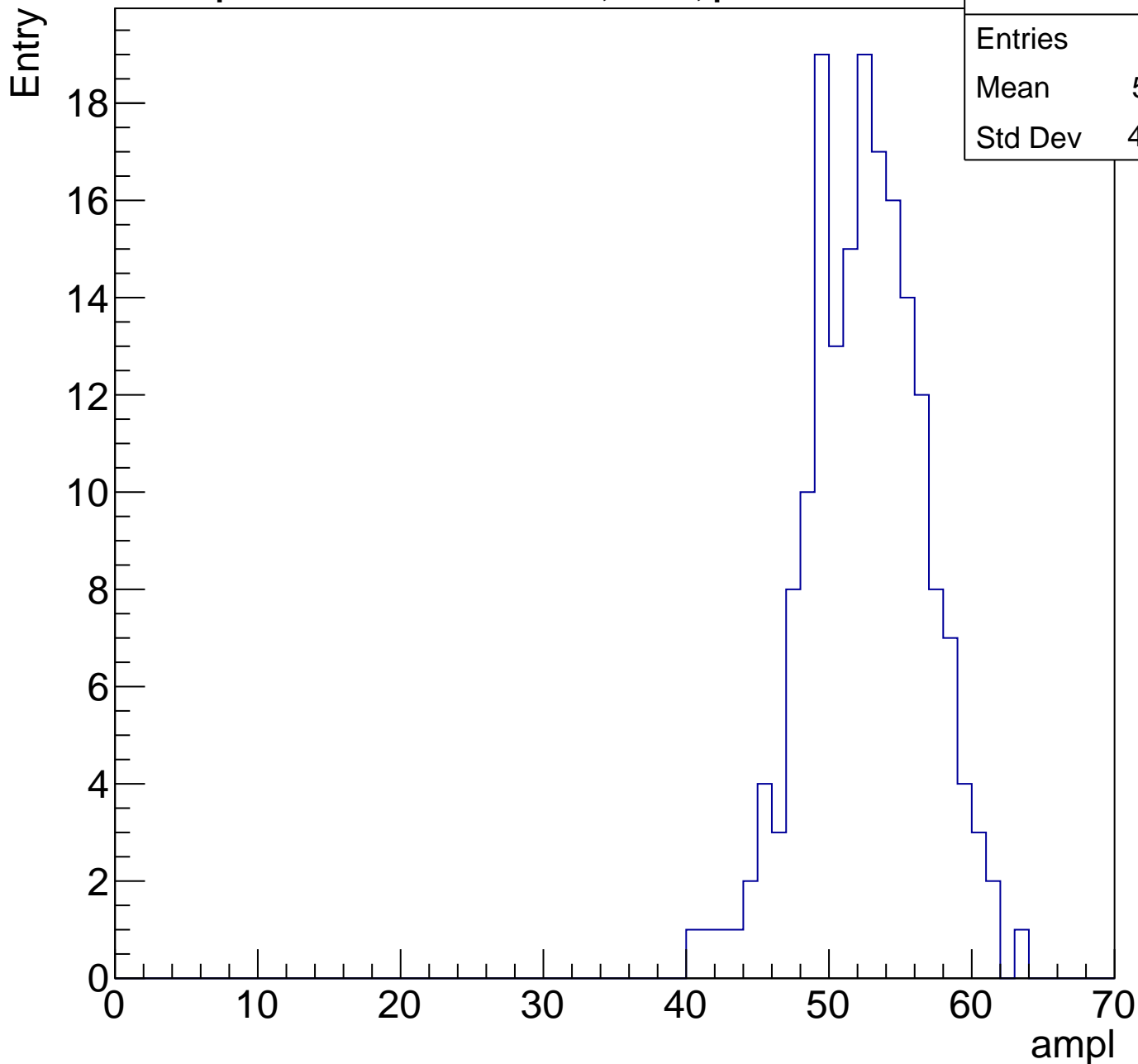
ampl



B0L101S, U20-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	52.11
Std Dev	4.068



B0L101S, U20-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	88
Mean	58.27
Std Dev	2.957



B0L101S, U20-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	51
Mean	58.67
Std Dev	11.99

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch74, adc0

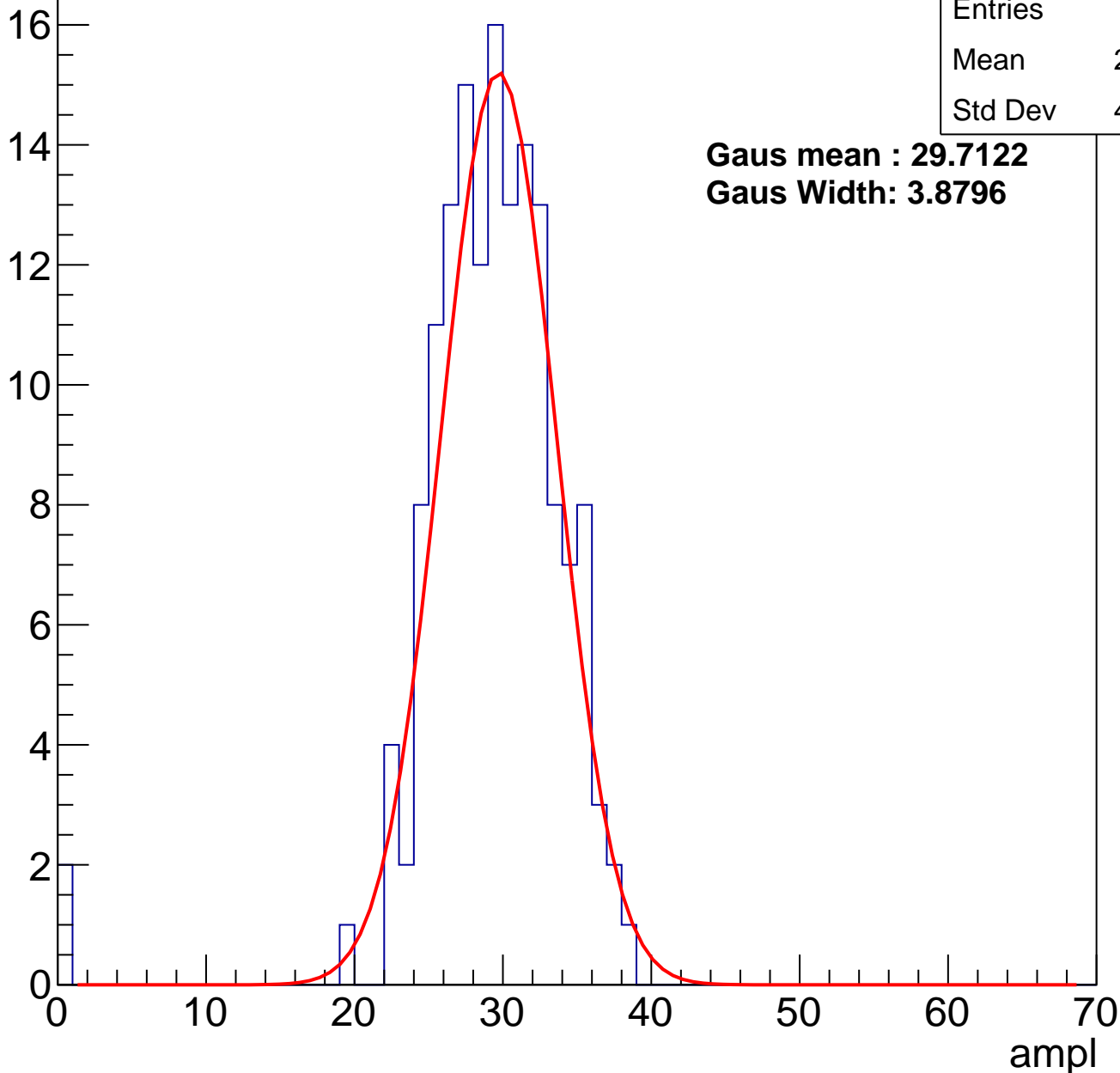
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.79
Std Dev	4.945

Gaus mean : 29.7122

Gaus Width: 3.8796

Entry



B0L101S, U20-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

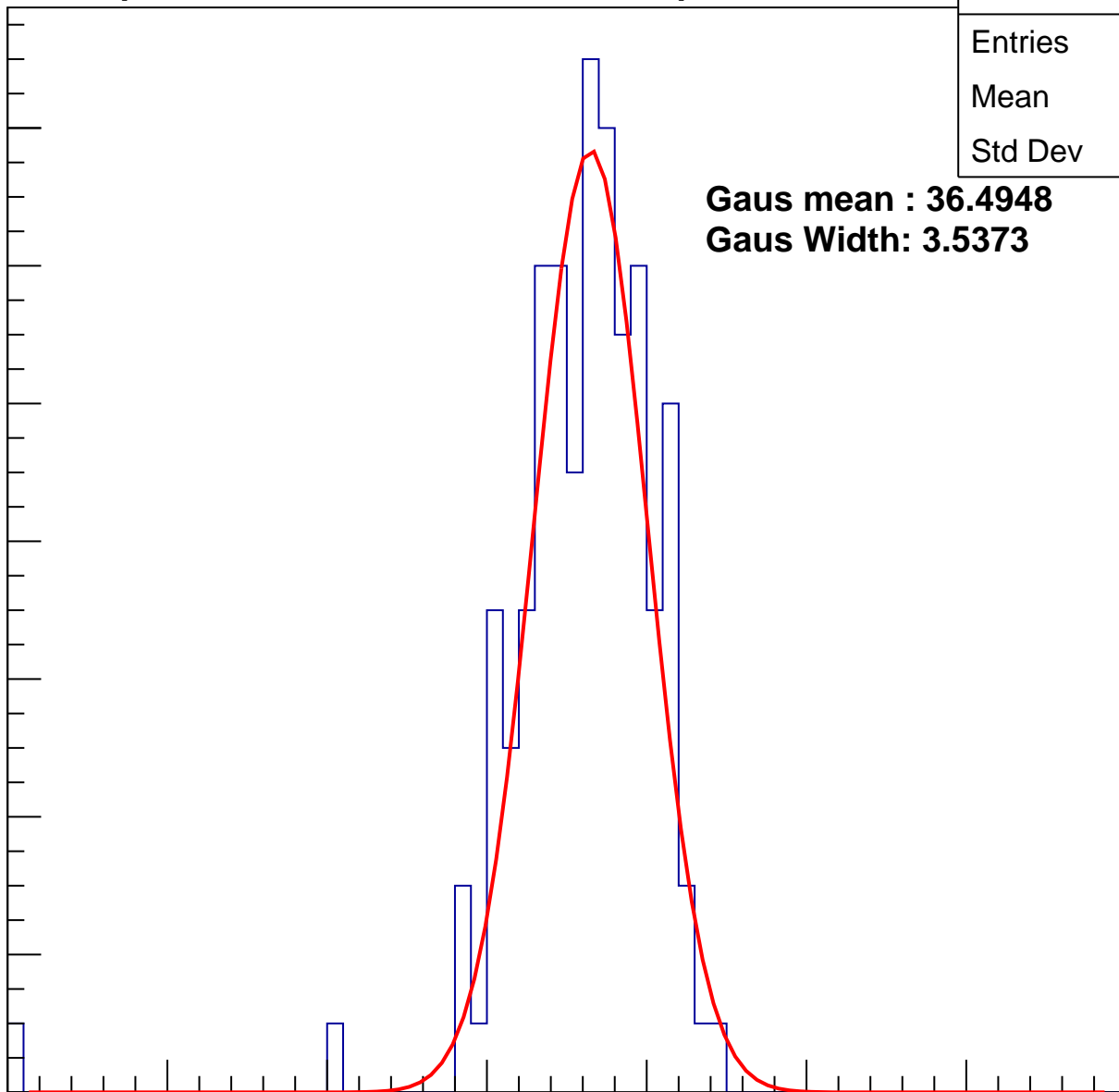
Entries	132
Mean	35.52
Std Dev	4.897

Gaus mean : 36.4948

Gaus Width: 3.5373

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch74, adc2

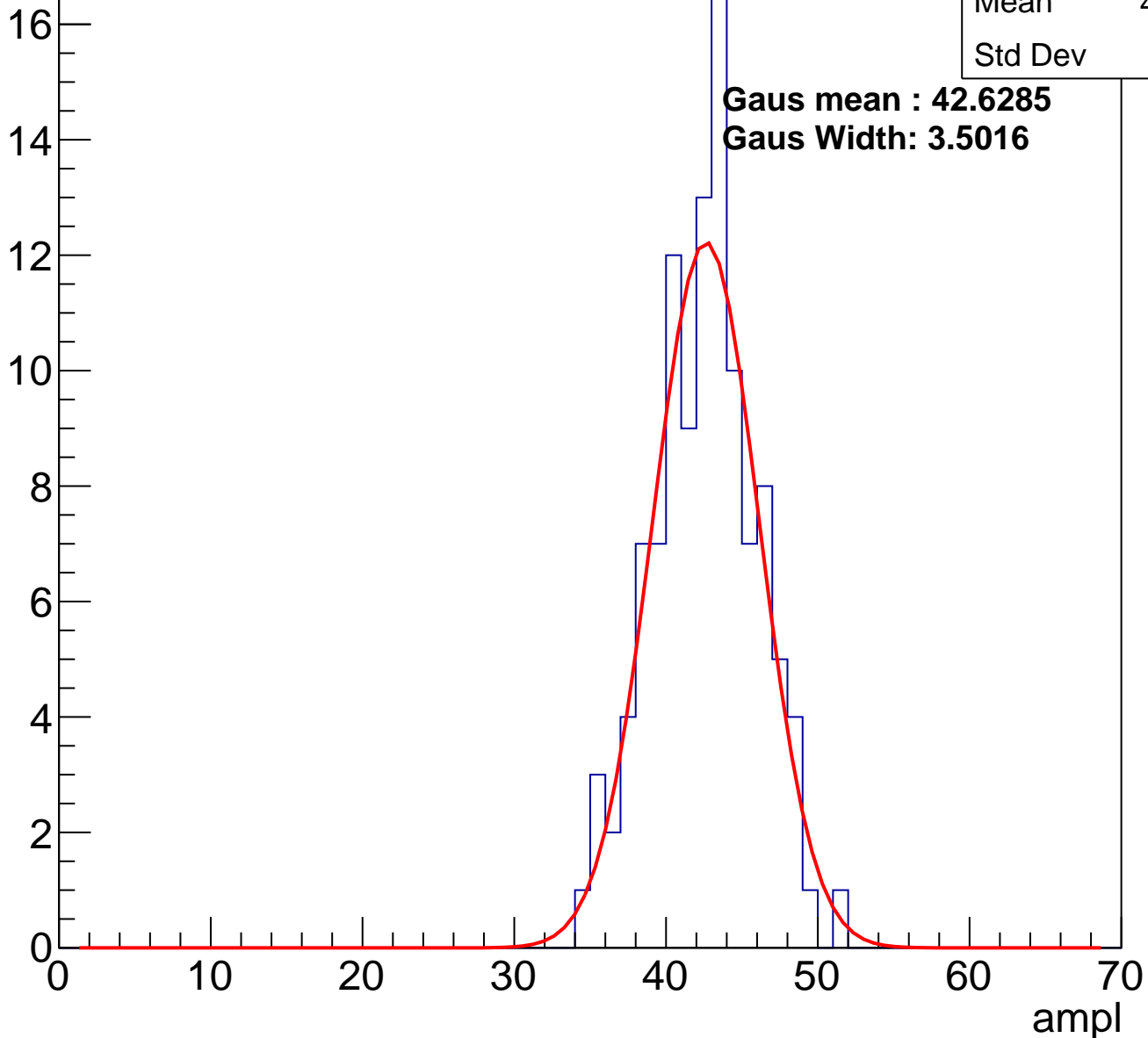
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	111
Mean	42.11
Std Dev	3.37

Gaus mean : 42.6285

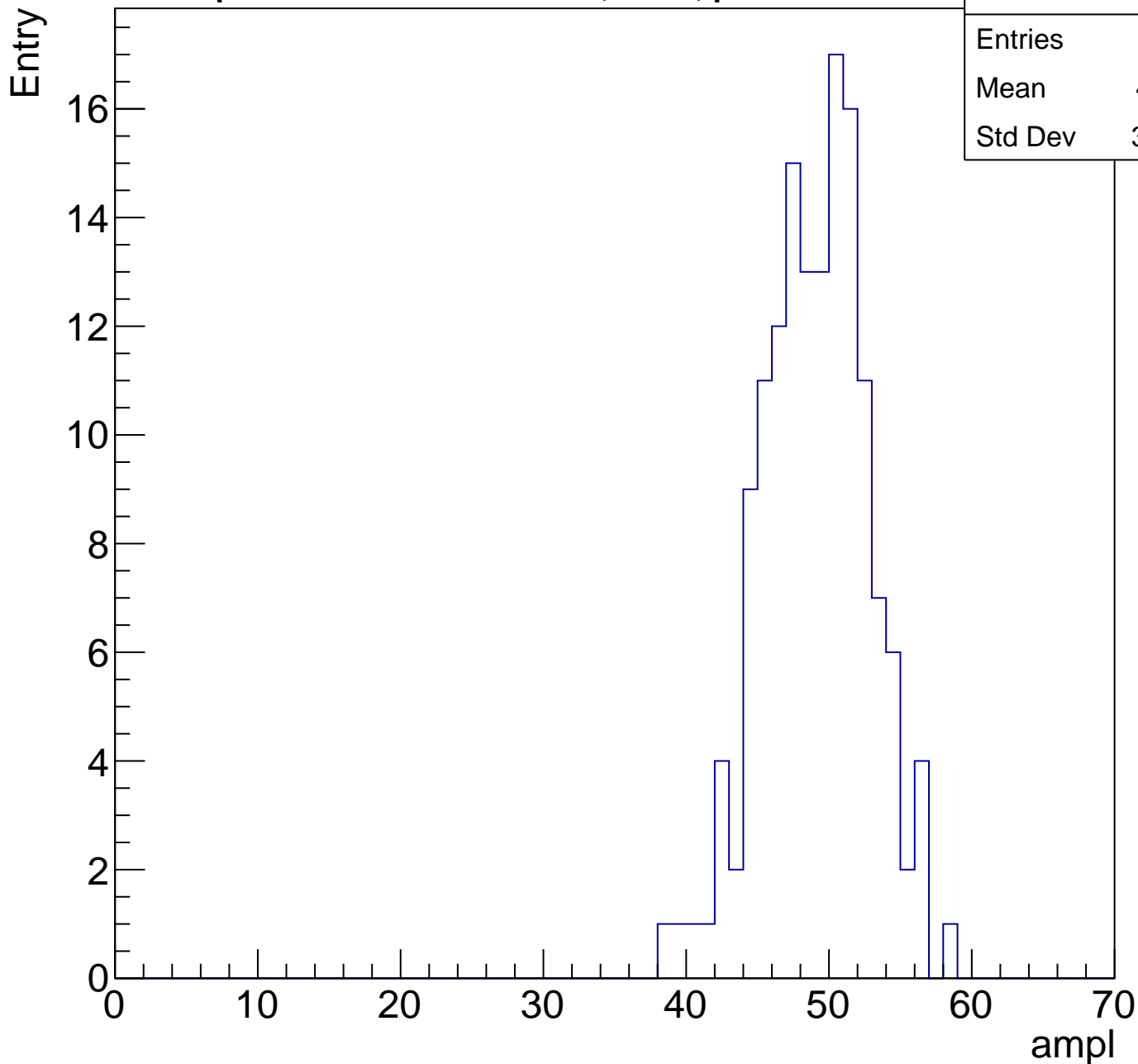
Gaus Width: 3.5016



B0L101S, U20-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	48.61
Std Dev	3.675



B0L101S, U20-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.26
Std Dev	3.537

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

40

50

60

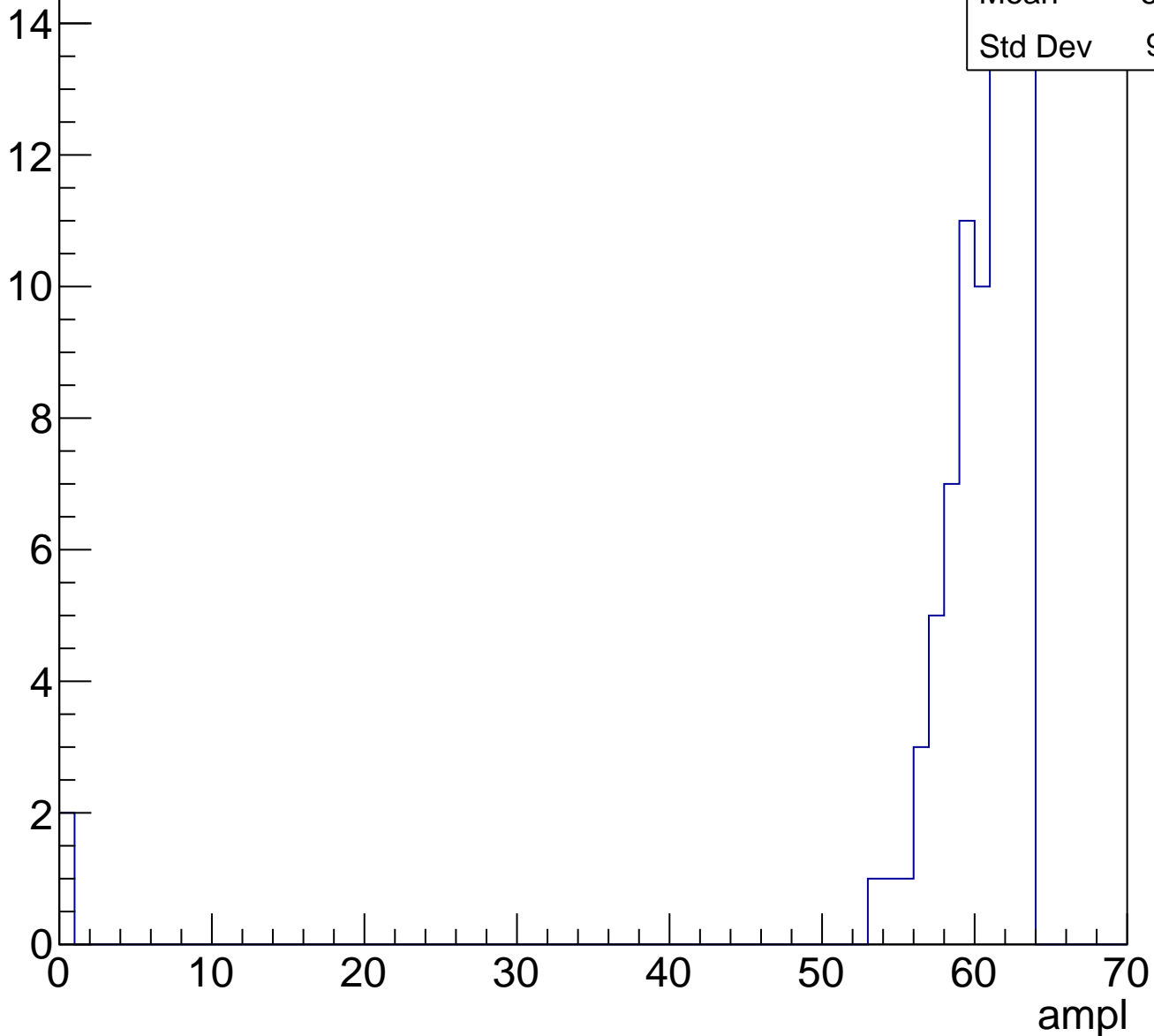
70

ampl

B0L101S, U20-ch74, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

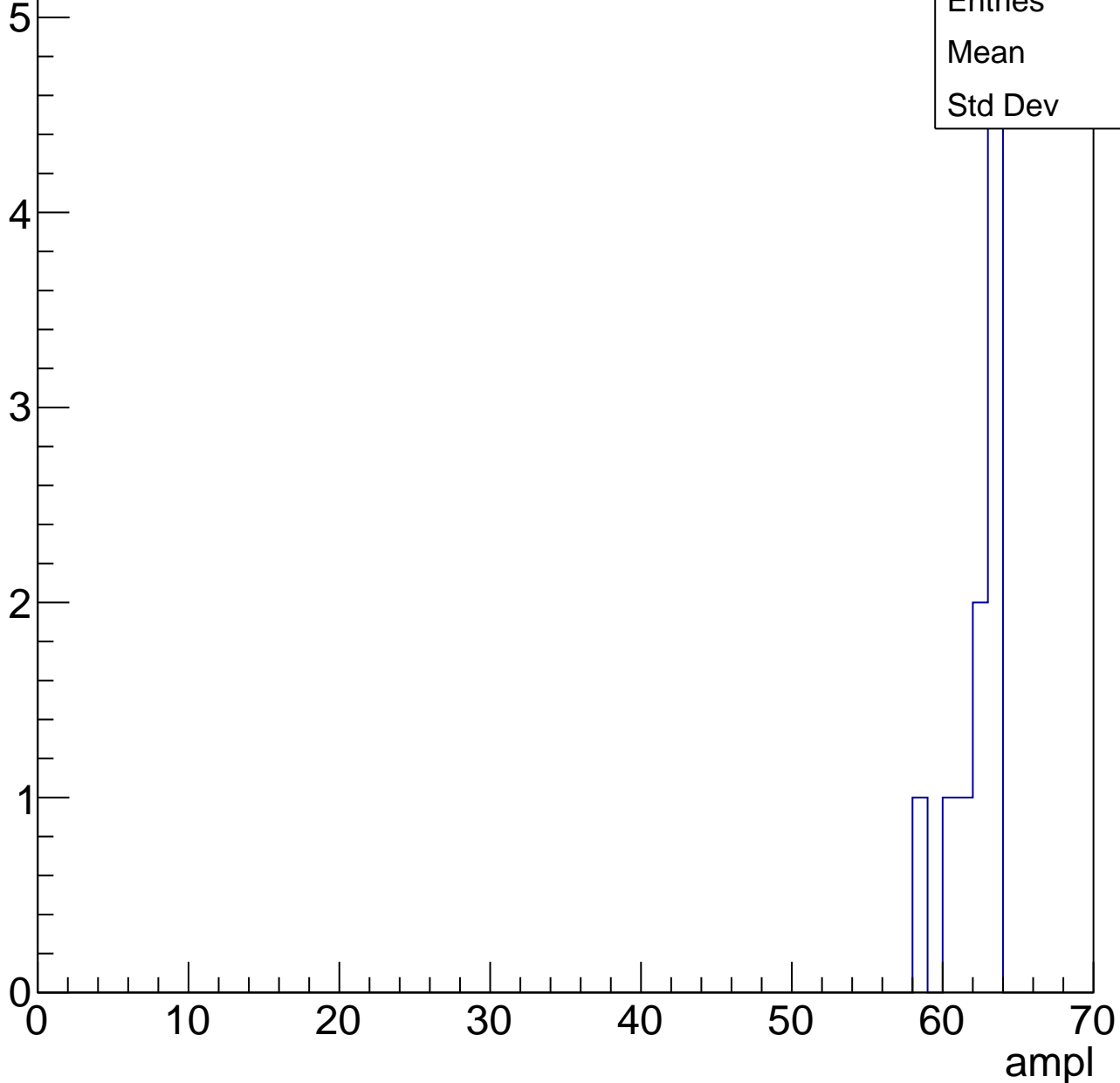


B0L101S, U20-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	61.8
Std Dev	1.6



B0L101S, U20-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	28.81
Std Dev	4.511

Gaus mean : 29.5888

Gaus Width: 3.7288

Entry

12

10

8

6

4

2

0

0

10

20

30

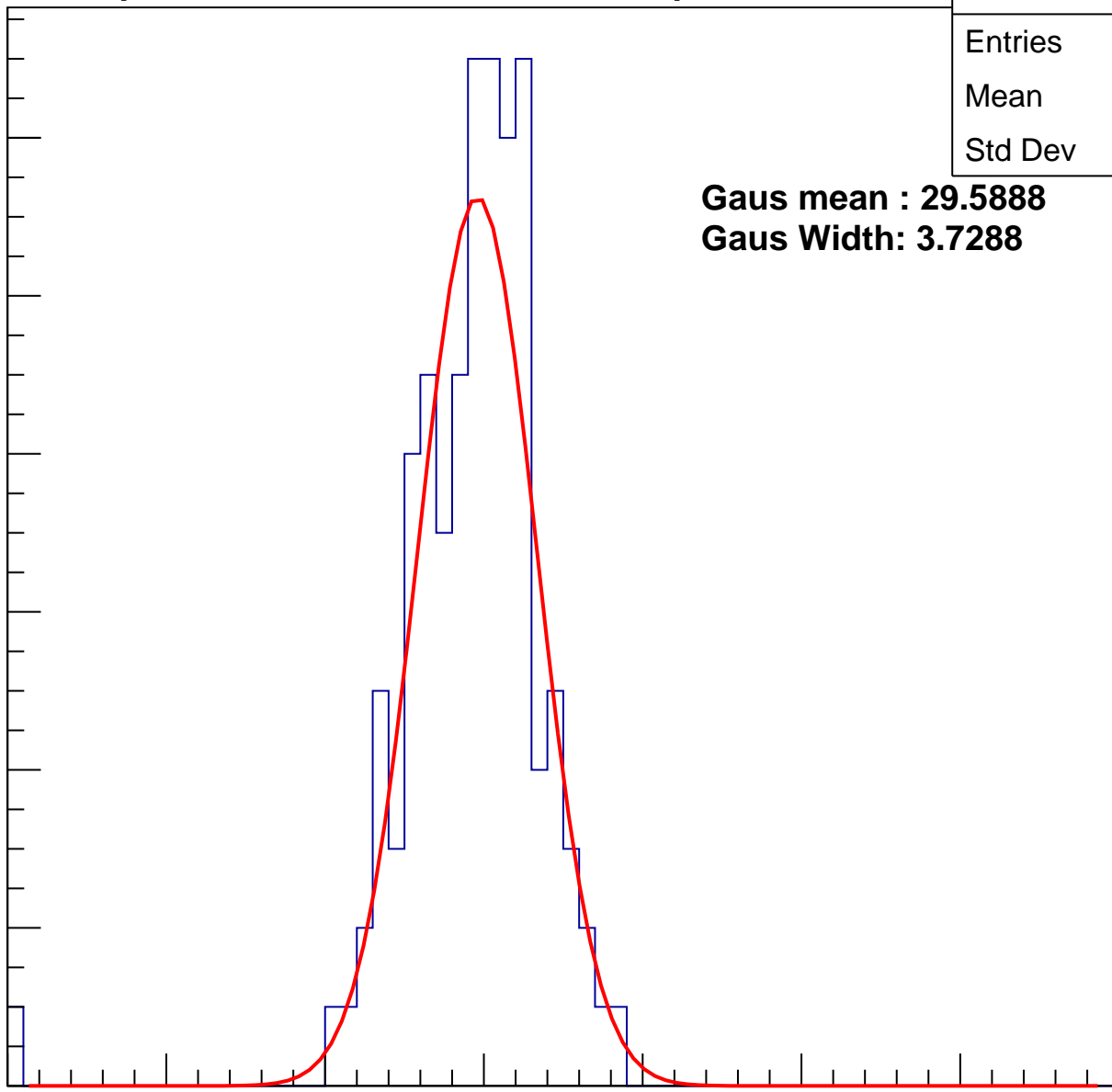
40

50

60

70

ampl



B0L101S, U20-ch75, adc1

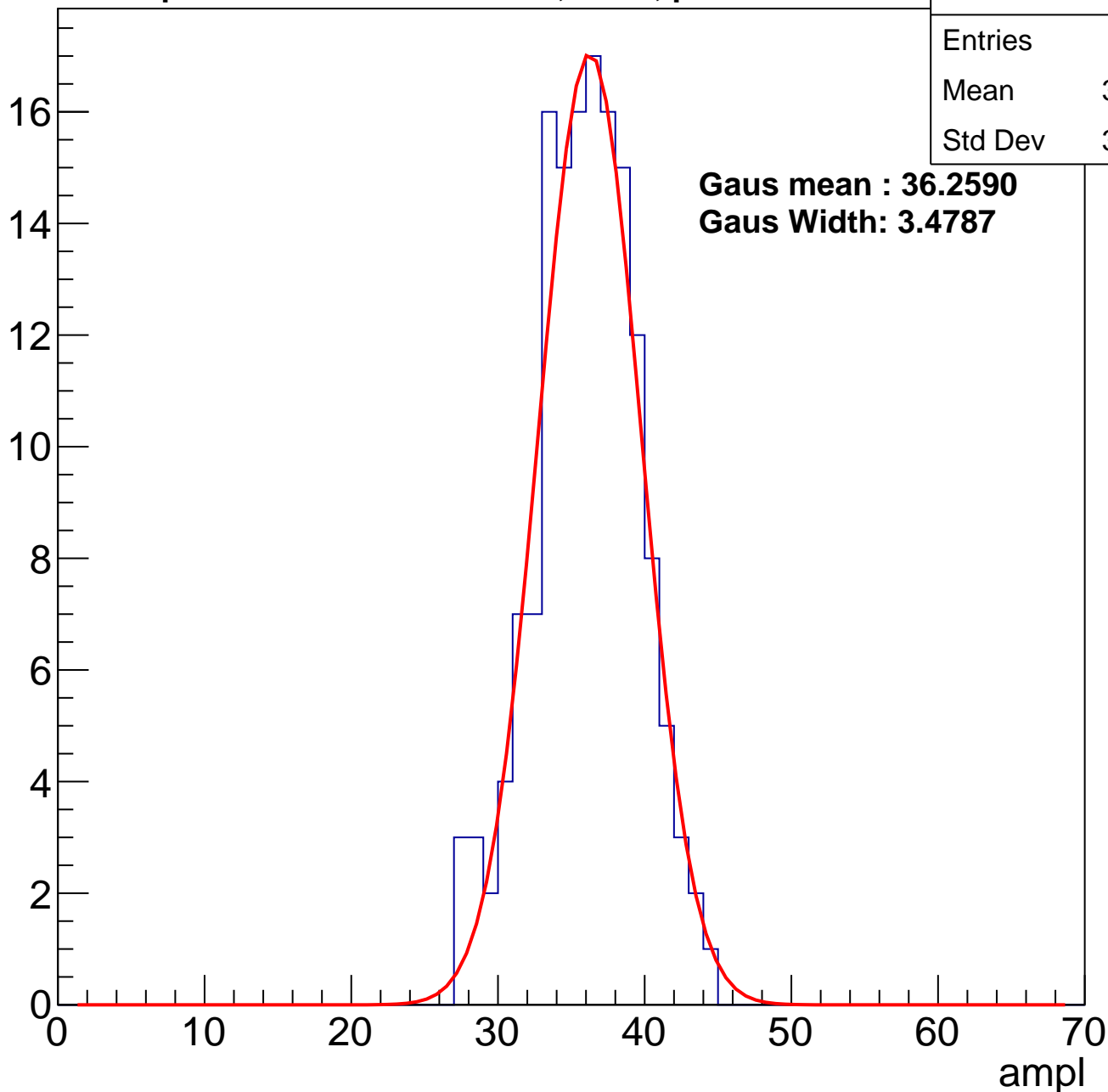
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	35.56
Std Dev	3.499

Gaus mean : 36.2590

Gaus Width: 3.4787

Entry



B0L101S, U20-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	42.77
Std Dev	3.198

Gaus mean : 43.2701

Gaus Width: 3.2894

Entry

14
12
10
8
6
4
2
0

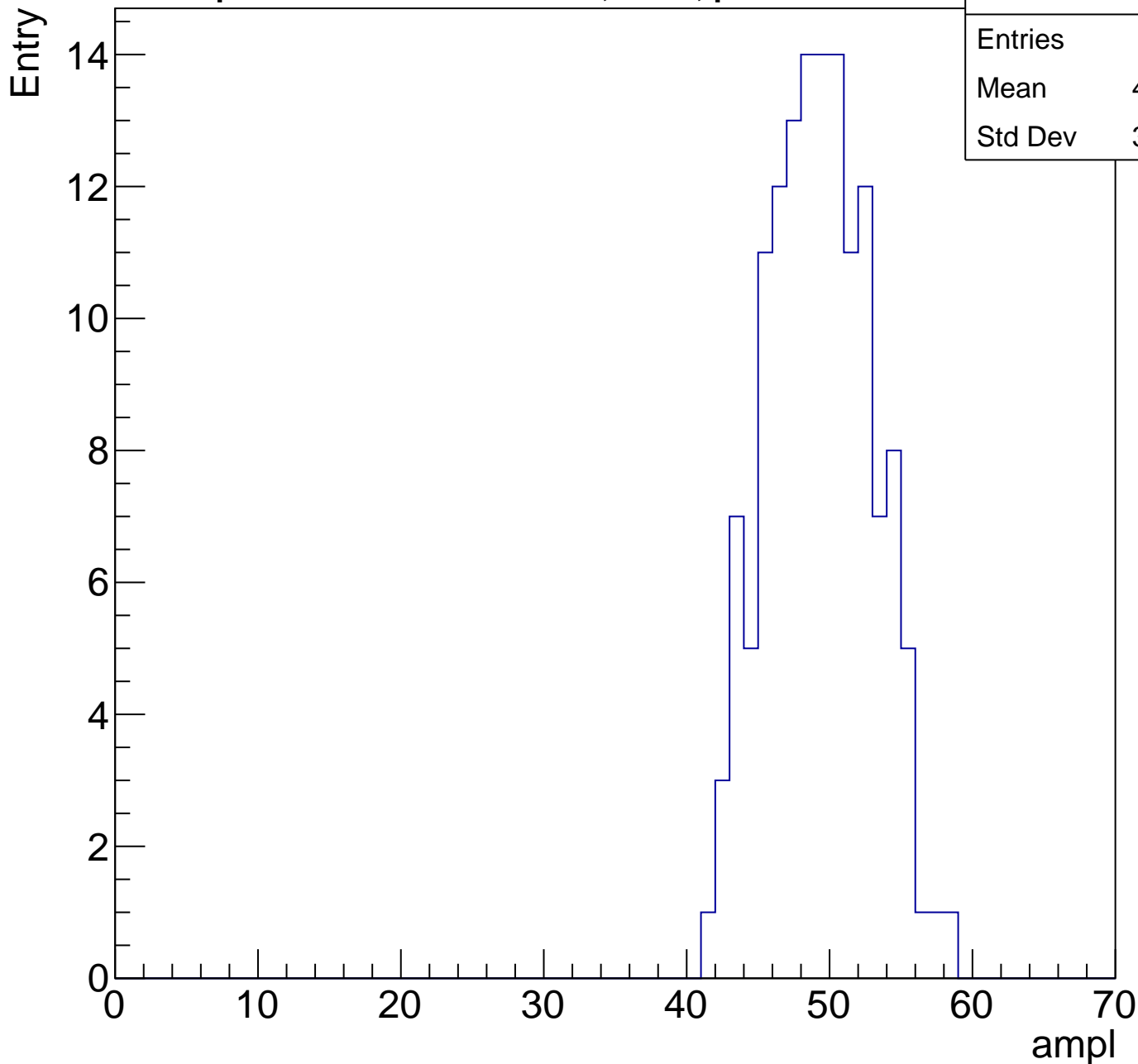
ampl

0 10 20 30 40 50 60 70

B0L101S, U20-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	48.84
Std Dev	3.572

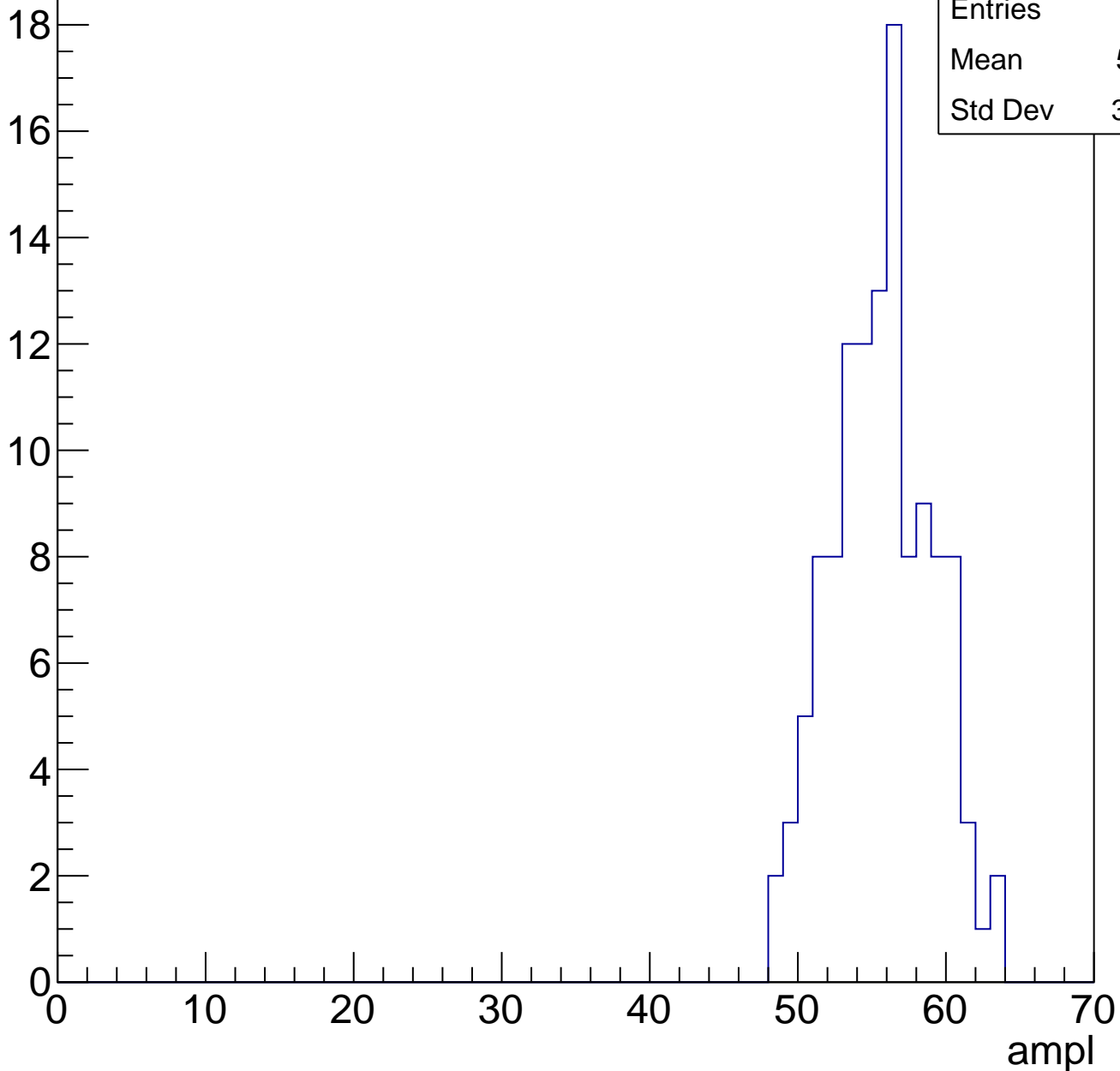


B0L101S, U20-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.21
Std Dev	3.329

Entry



B0L101S, U20-ch75, adc5

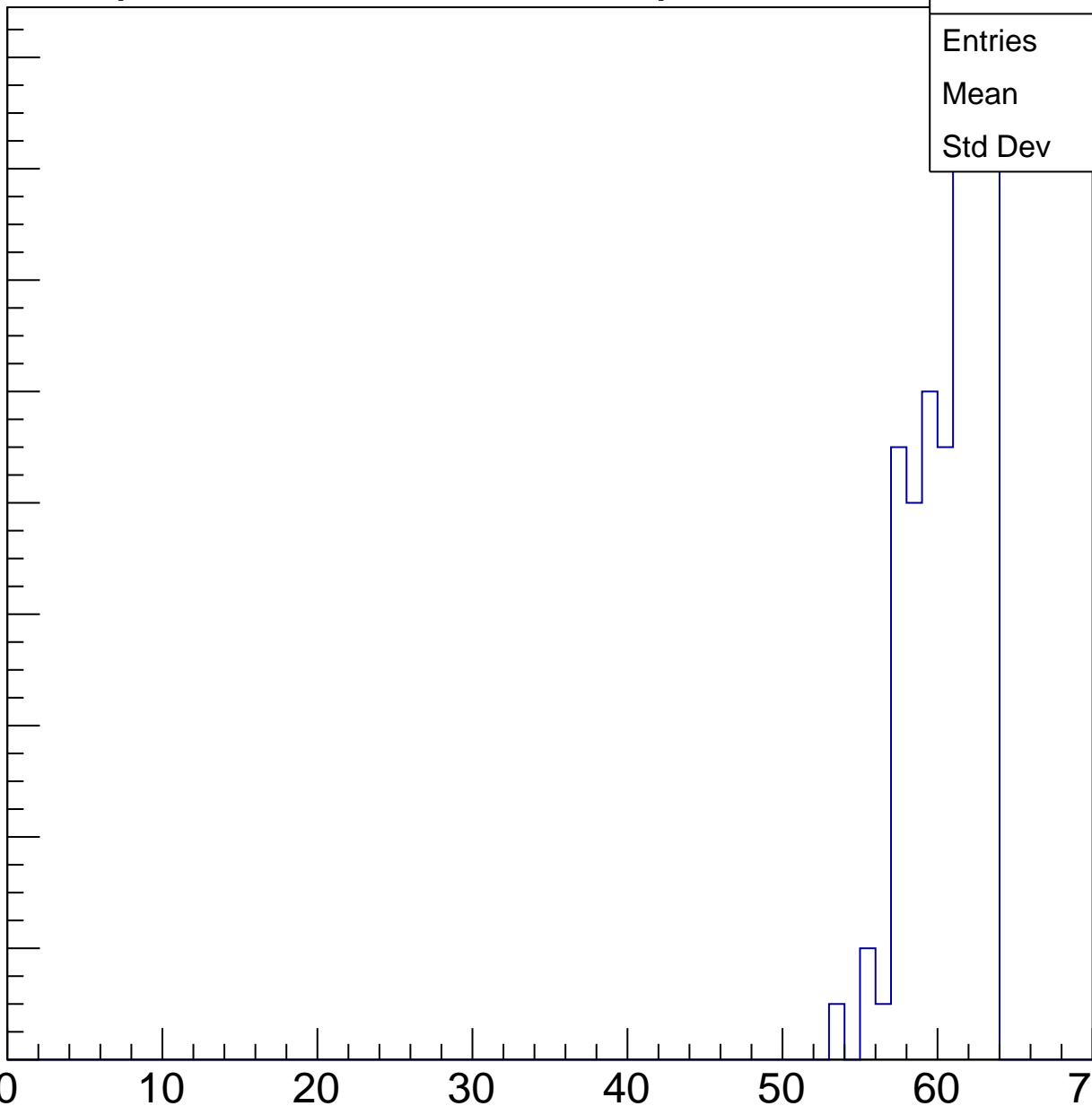
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	60.18
Std Dev	2.247

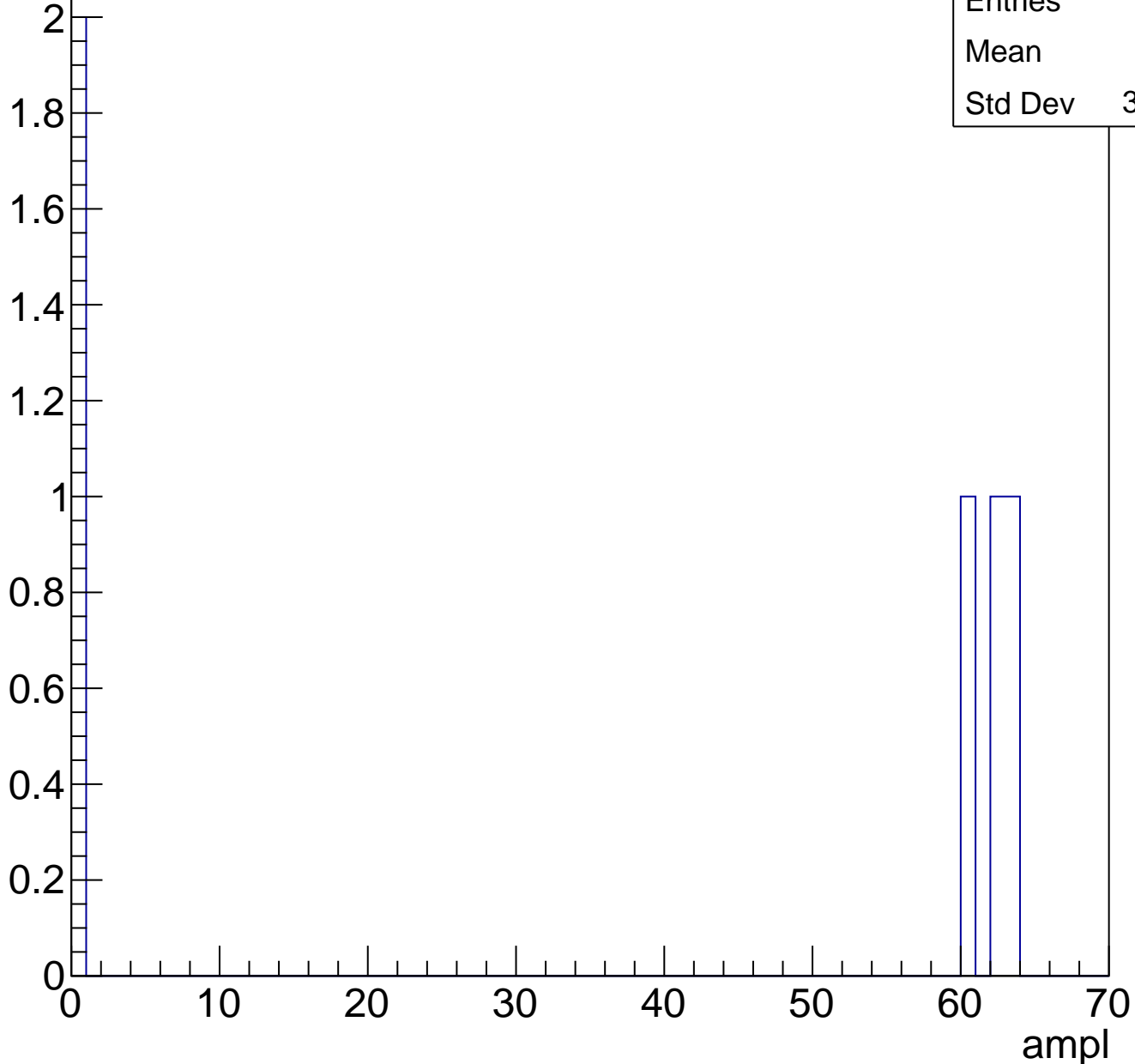
ampl



B0L101S, U20-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	37
Std Dev	30.23

B0L101S, U20-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch76, adc0

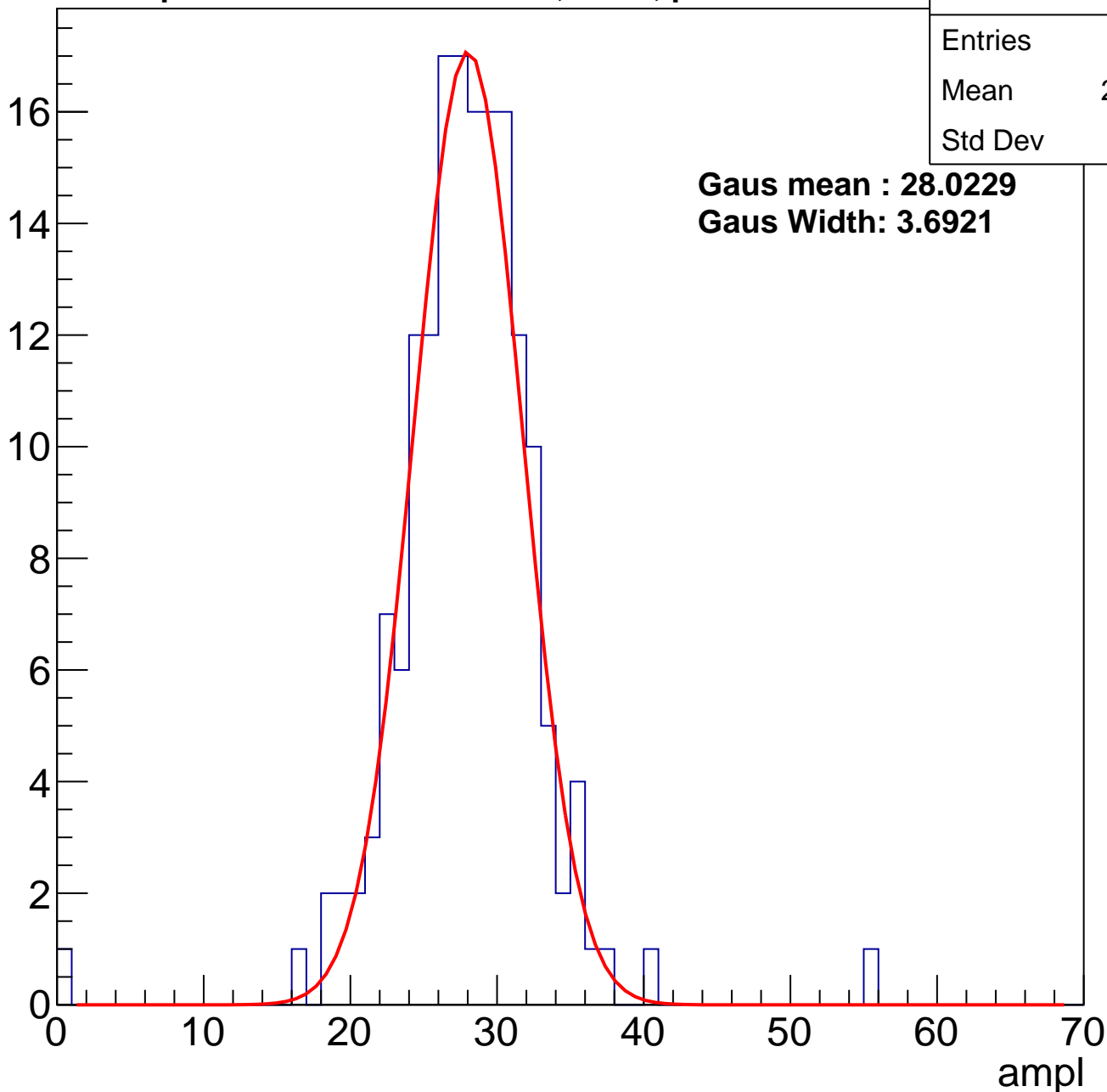
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	27.54
Std Dev	4.94

Gaus mean : 28.0229

Gaus Width: 3.6921

Entry



B0L101S, U20-ch76, adc1

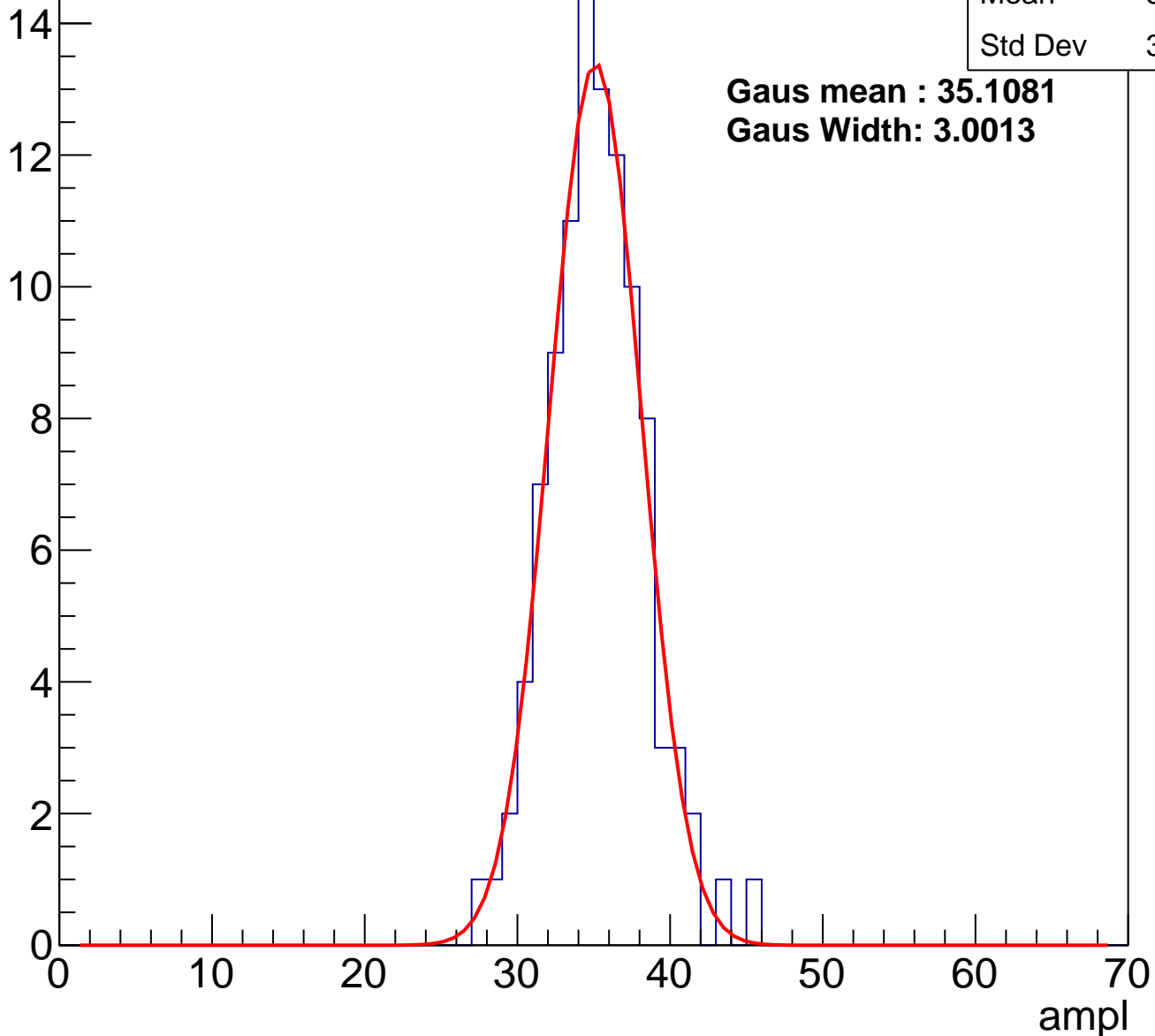
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	34.75
Std Dev	3.137

Gaus mean : 35.1081

Gaus Width: 3.0013

Entry



B0L101S, U20-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

157

Mean

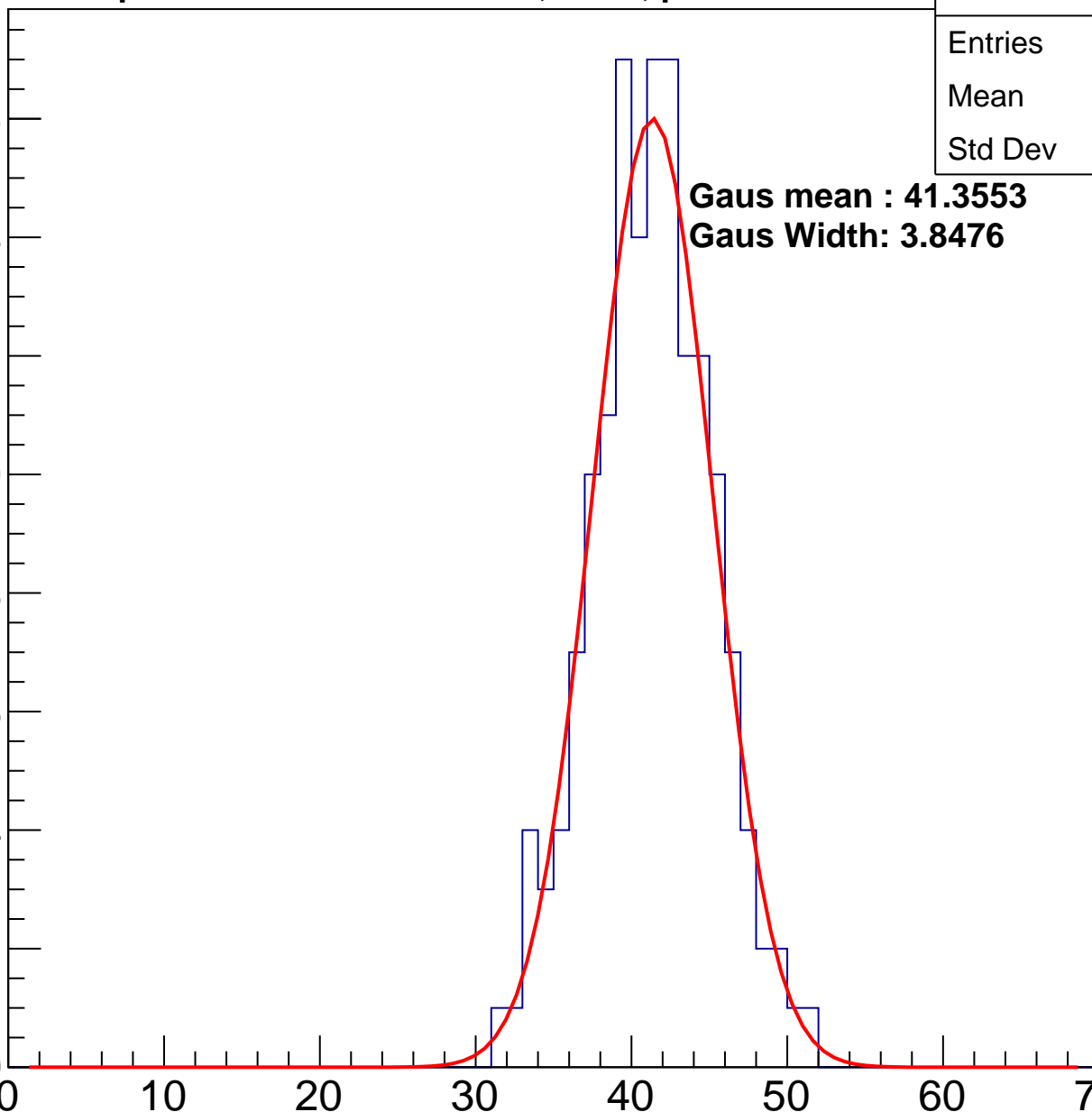
40.83

Std Dev

3.826

Gaus mean : 41.3553

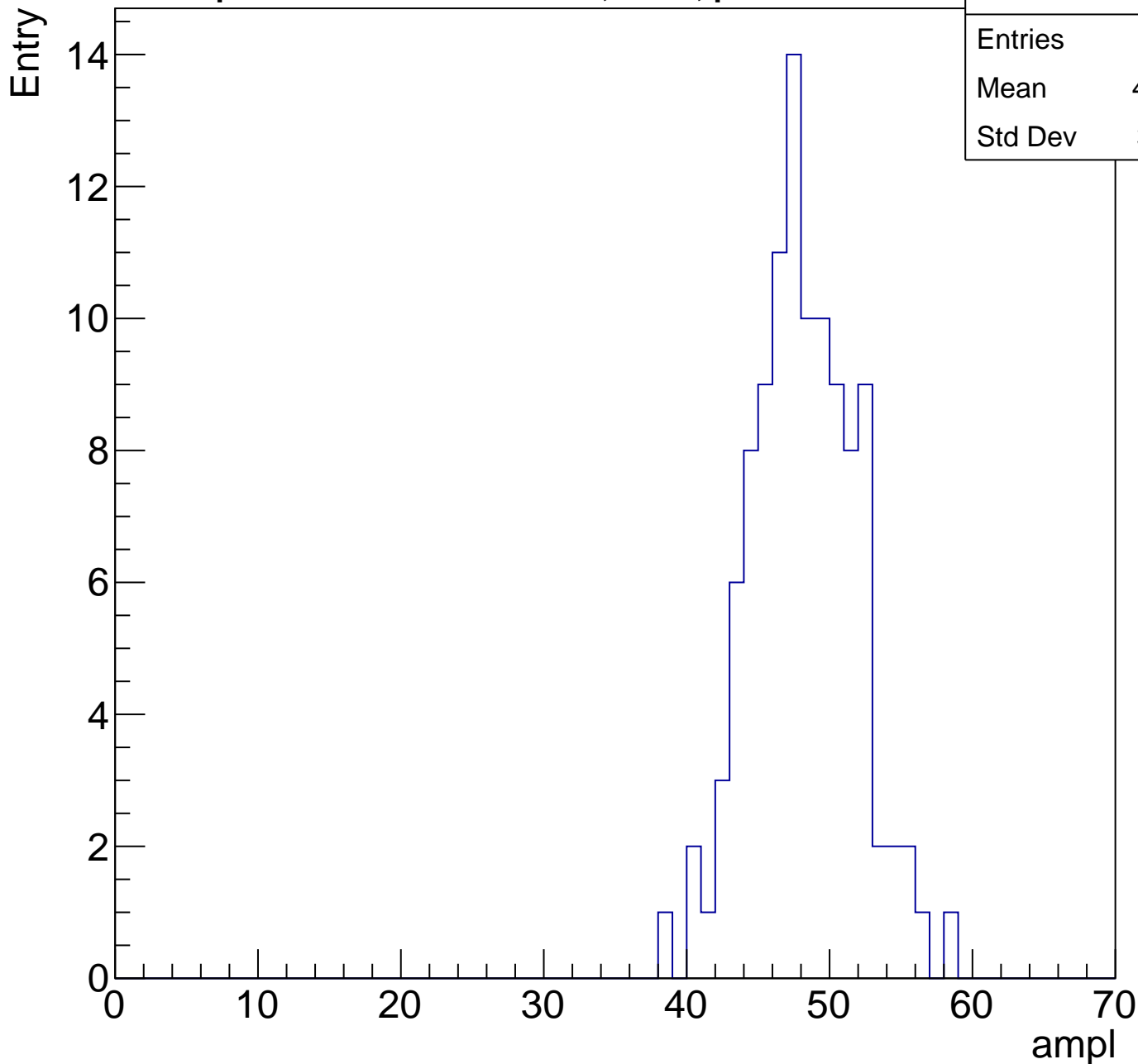
Gaus Width: 3.8476



B0L101S, U20-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

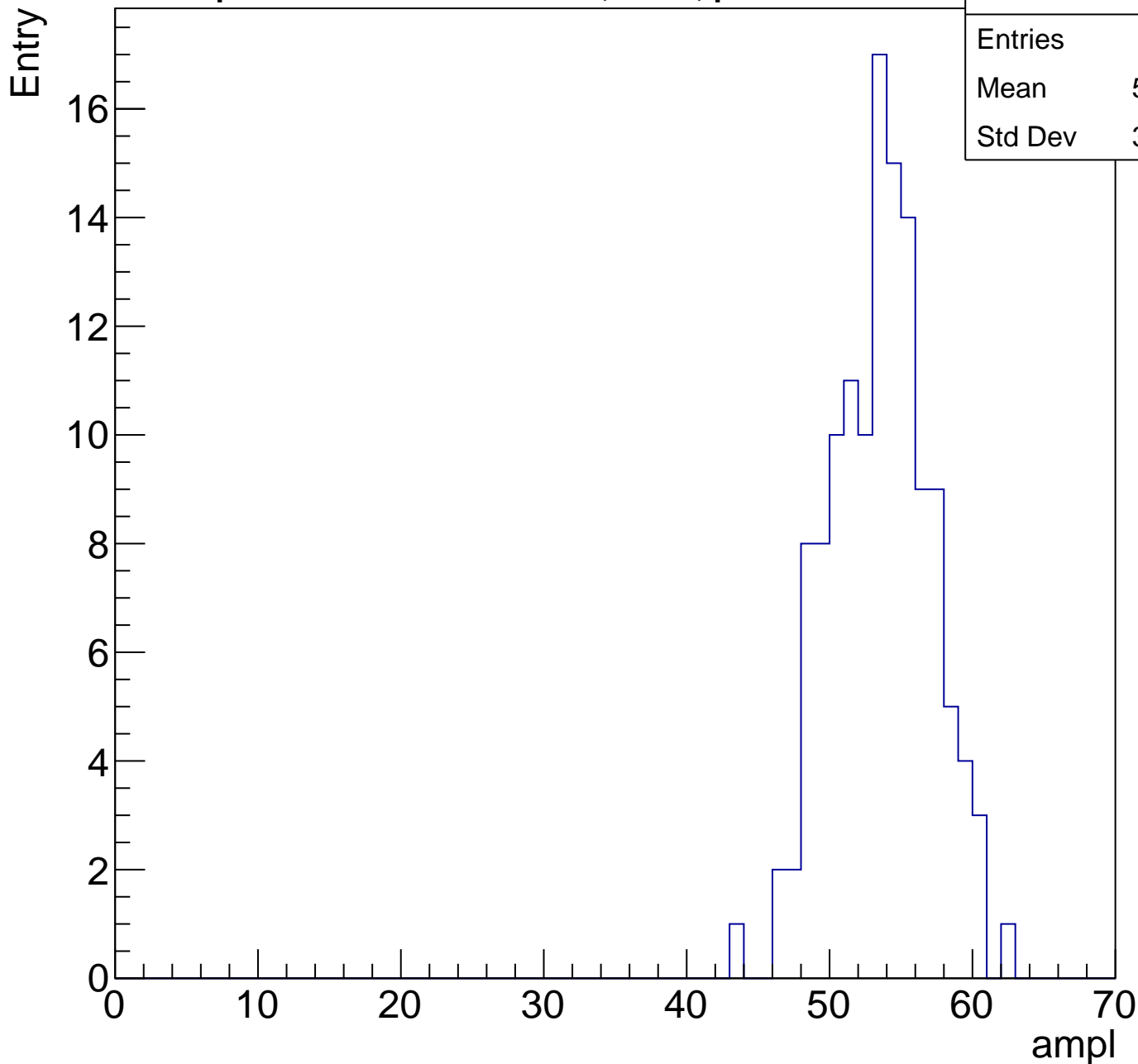
Entries	109
Mean	47.69
Std Dev	3.631



B0L101S, U20-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	53.12
Std Dev	3.448



B0L101S, U20-ch76, adc5

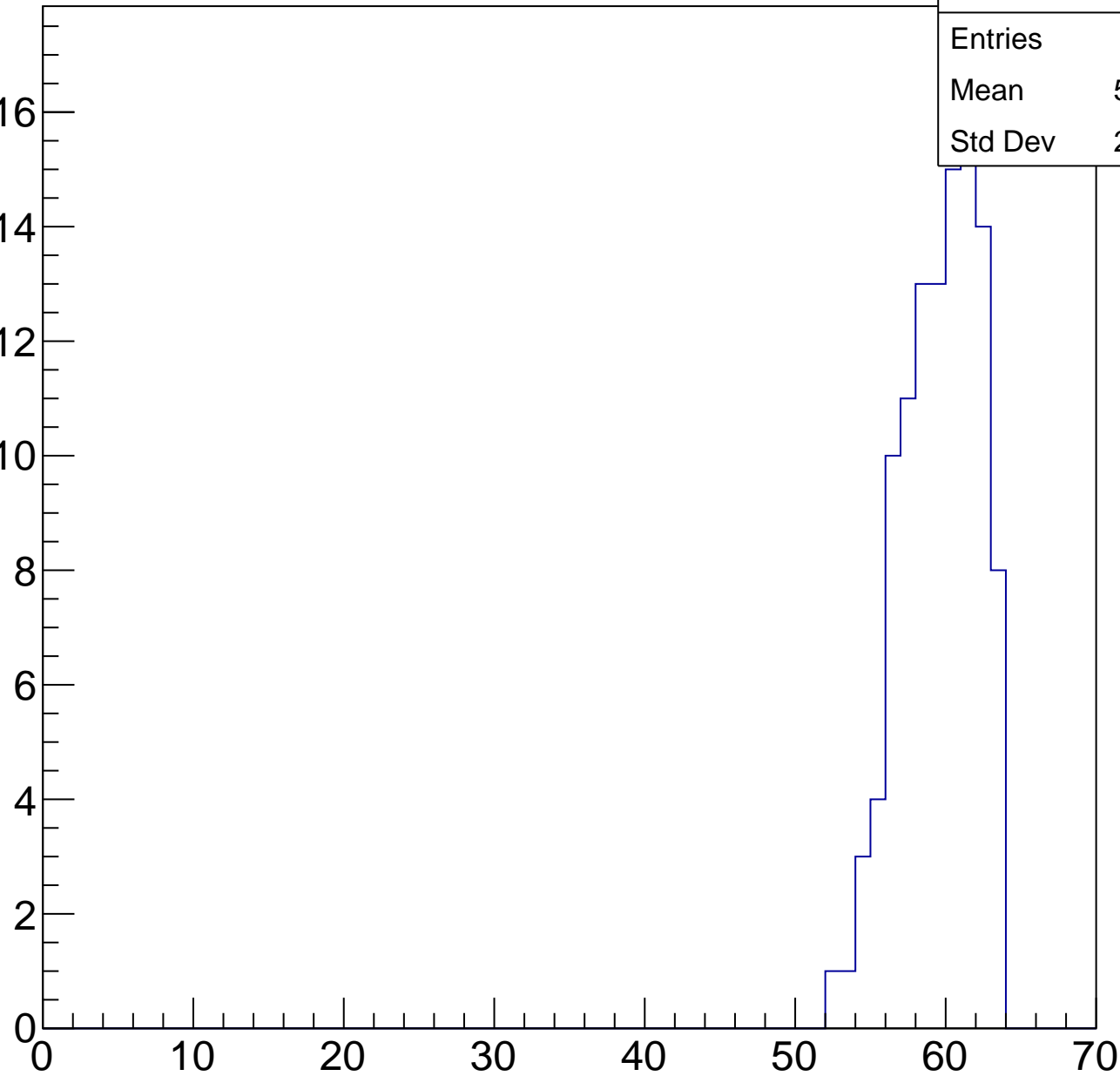
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	59.13
Std Dev	2.534

ampl



B0L101S, U20-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

29

Mean

57.41

Std Dev

15.7

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

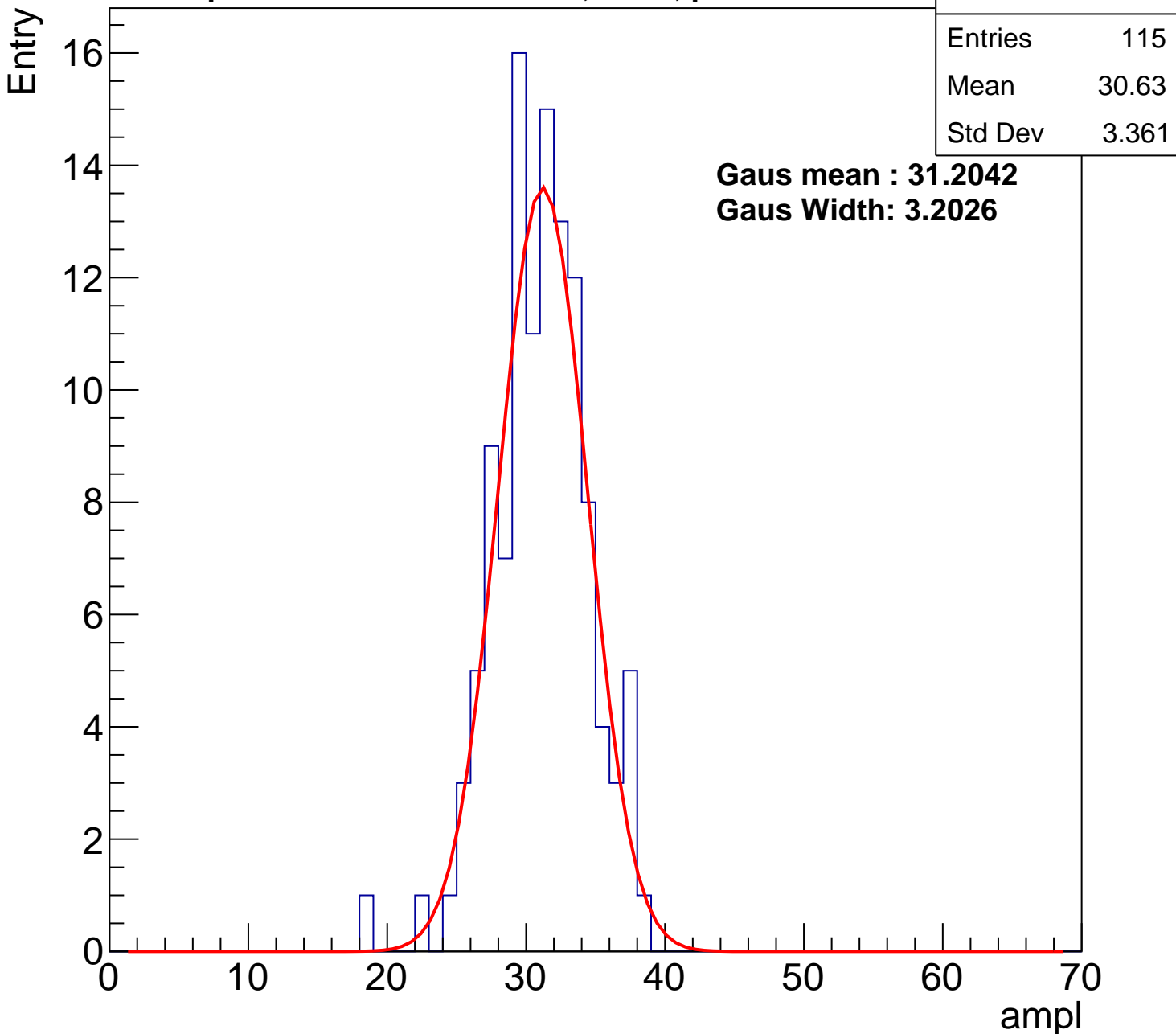
Entries	115
Mean	30.63
Std Dev	3.361

Gaus mean : 31.2042

Gaus Width: 3.2026

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch77, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	37.88
Std Dev	3.787

Gaus mean : 38.1839

Gaus Width: 4.0619

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

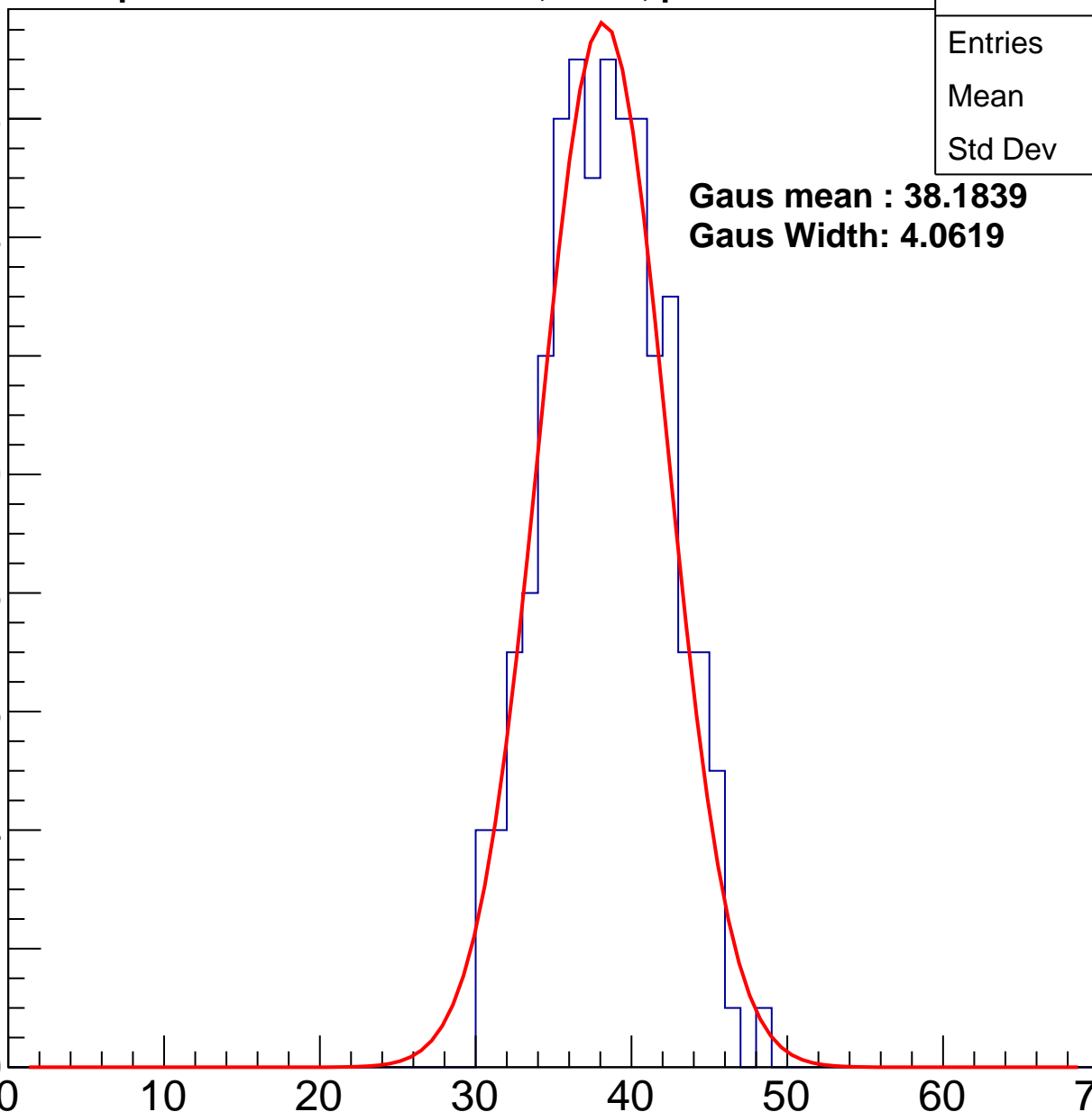
40

50

60

70

ampl



B0L101S, U20-ch77, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	44.95
Std Dev	3.262

Gaus mean : 45.1382

Gaus Width: 3.3457

Entry

12

10

8

6

4

2

0

0

10

20

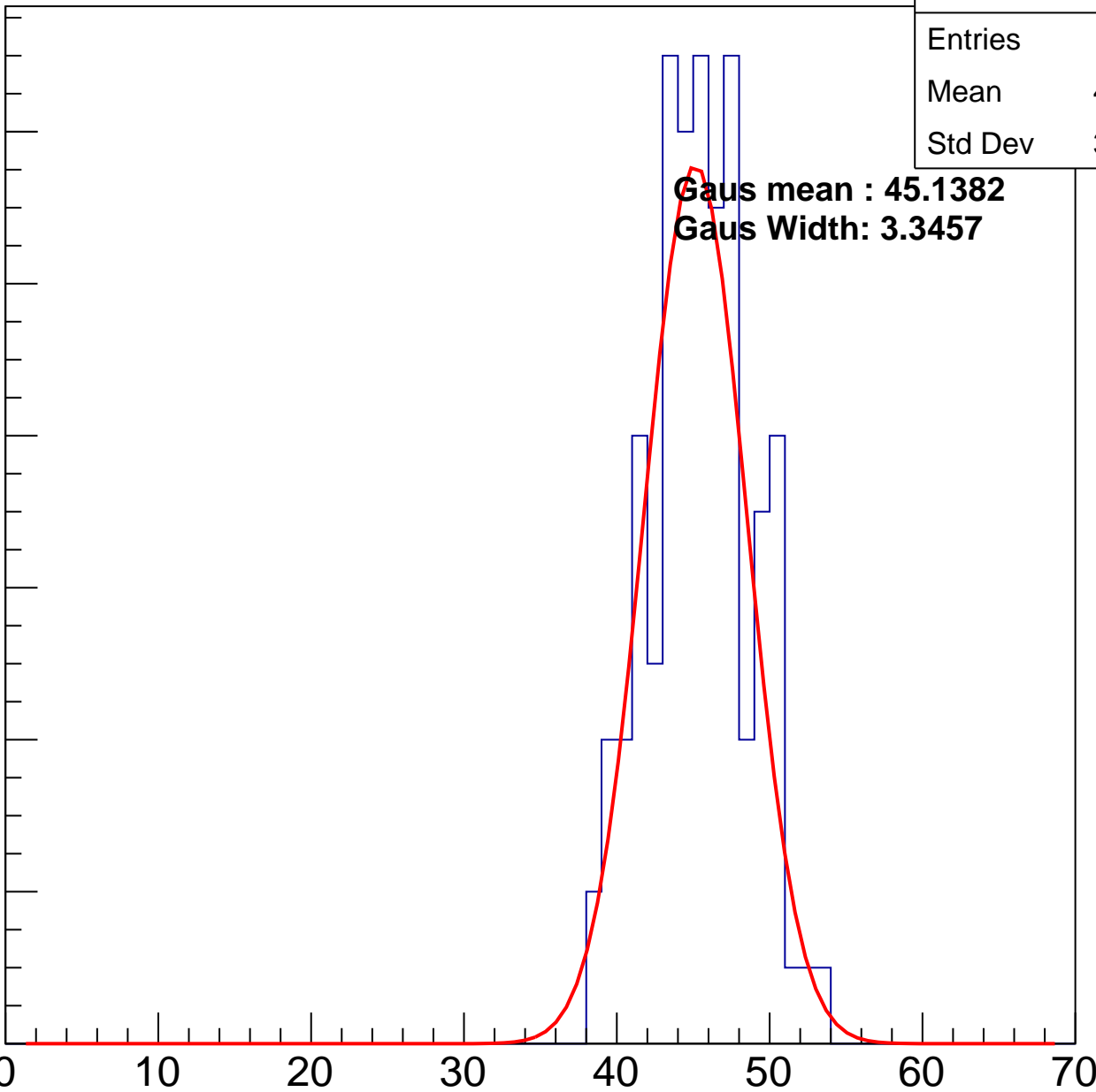
30

40

50

60

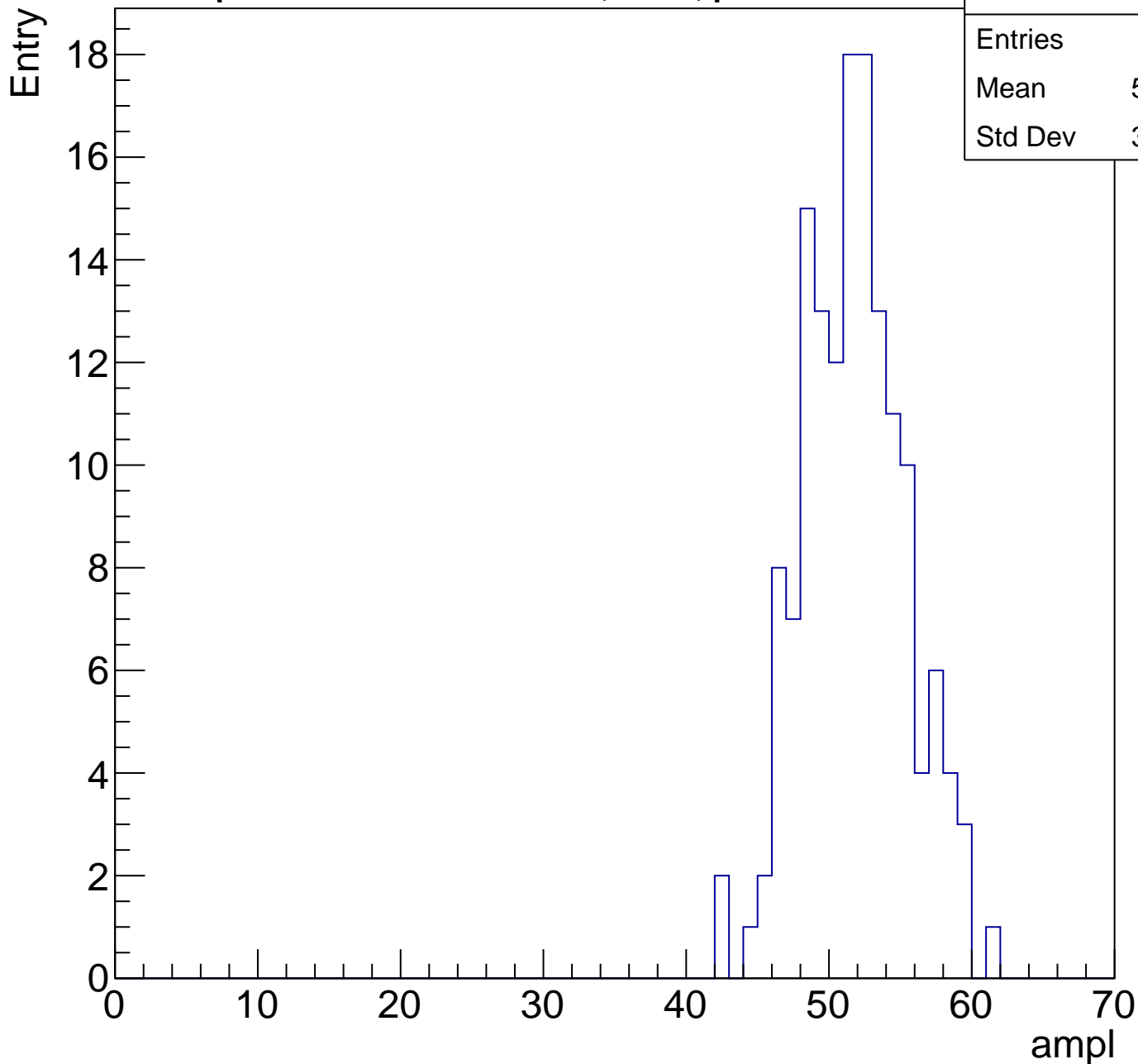
ampl



B0L101S, U20-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	51.32
Std Dev	3.594



B0L101S, U20-ch77, adc4

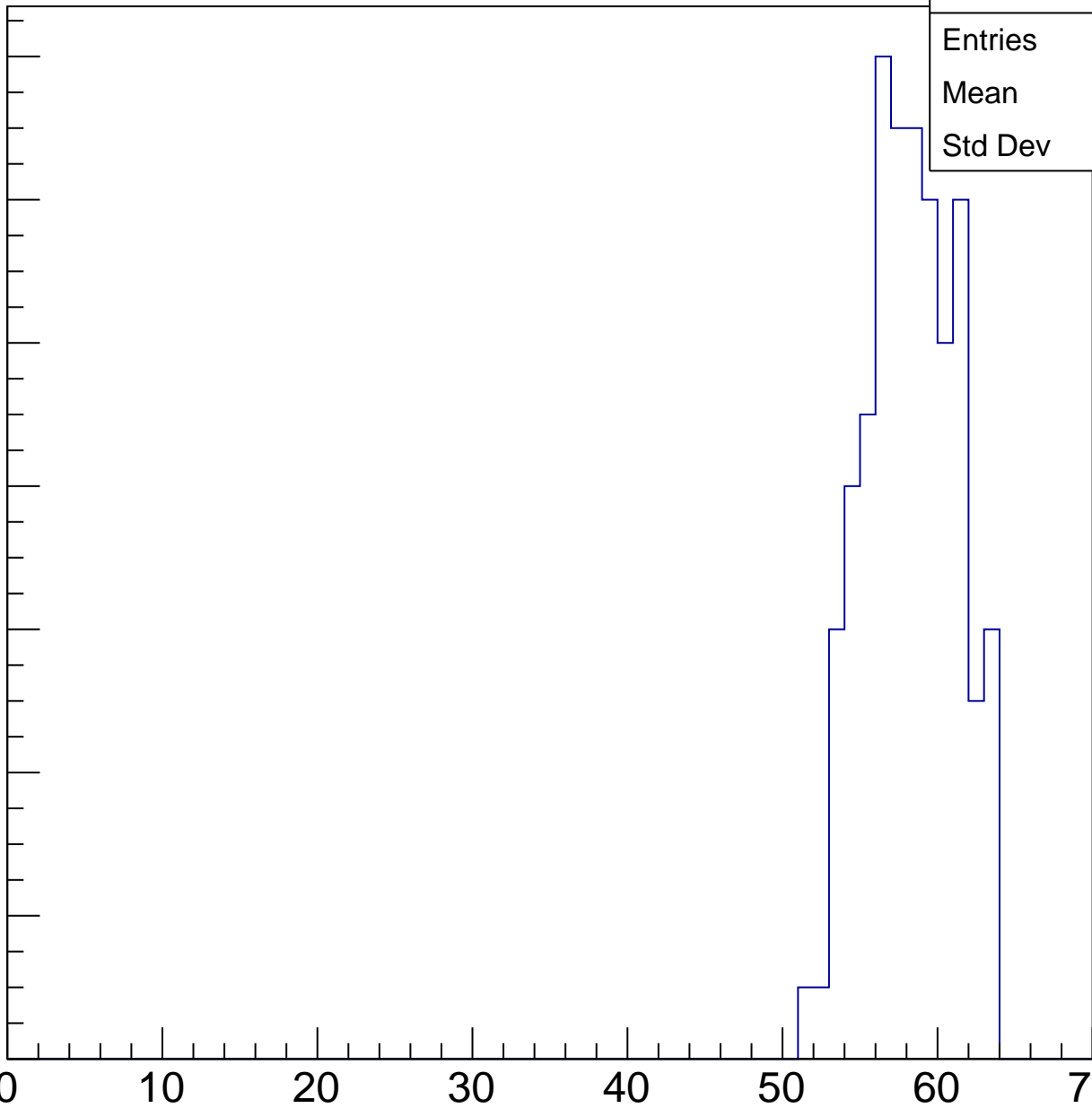
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	57.77
Std Dev	2.856

ampl



B0L101S, U20-ch77, adc5

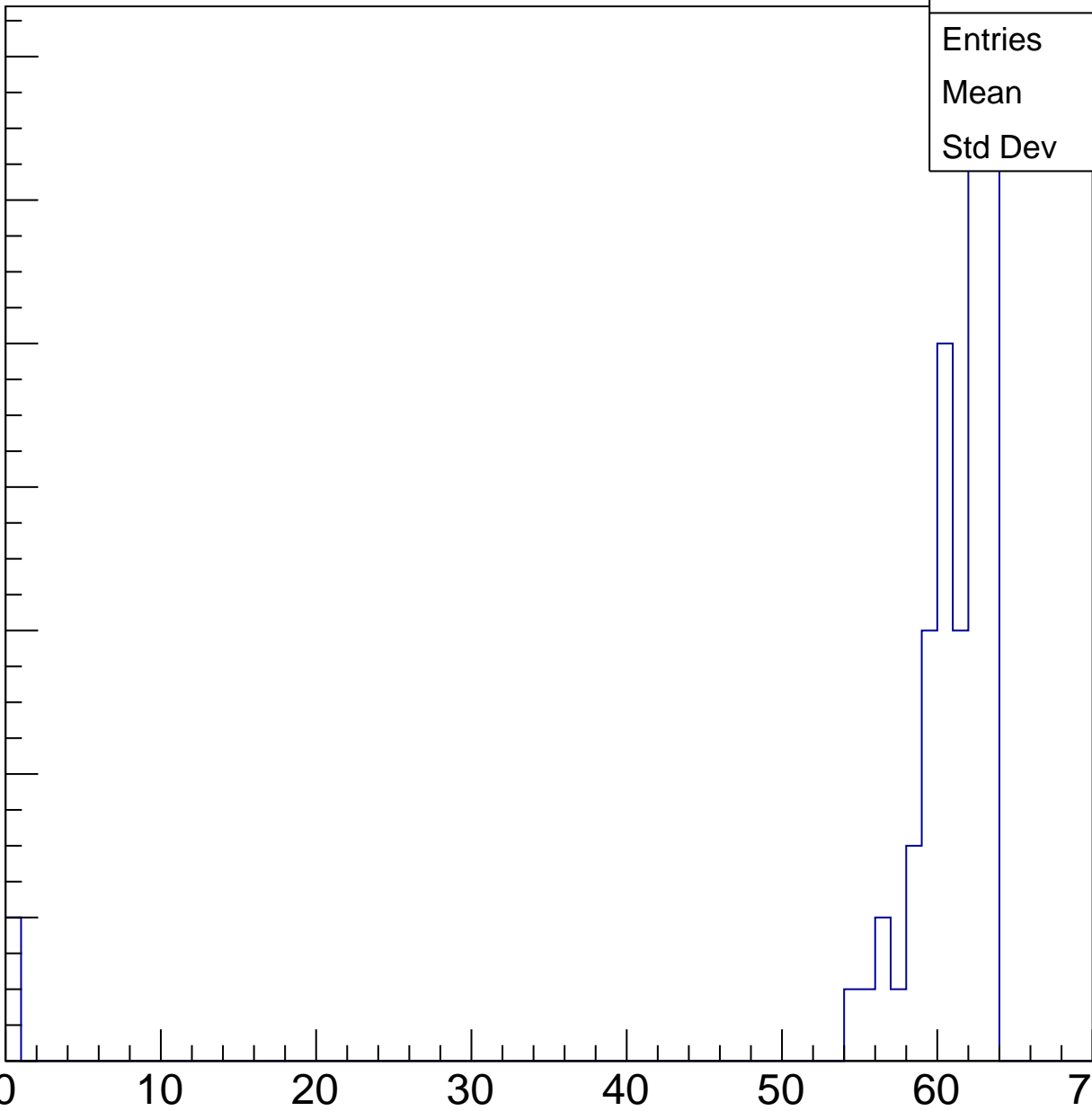
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	58.63
Std Dev	11.19

ampl



B0L101S, U20-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch78, adc0

calib_packv5_042523_0143.root, FC#1, port C1

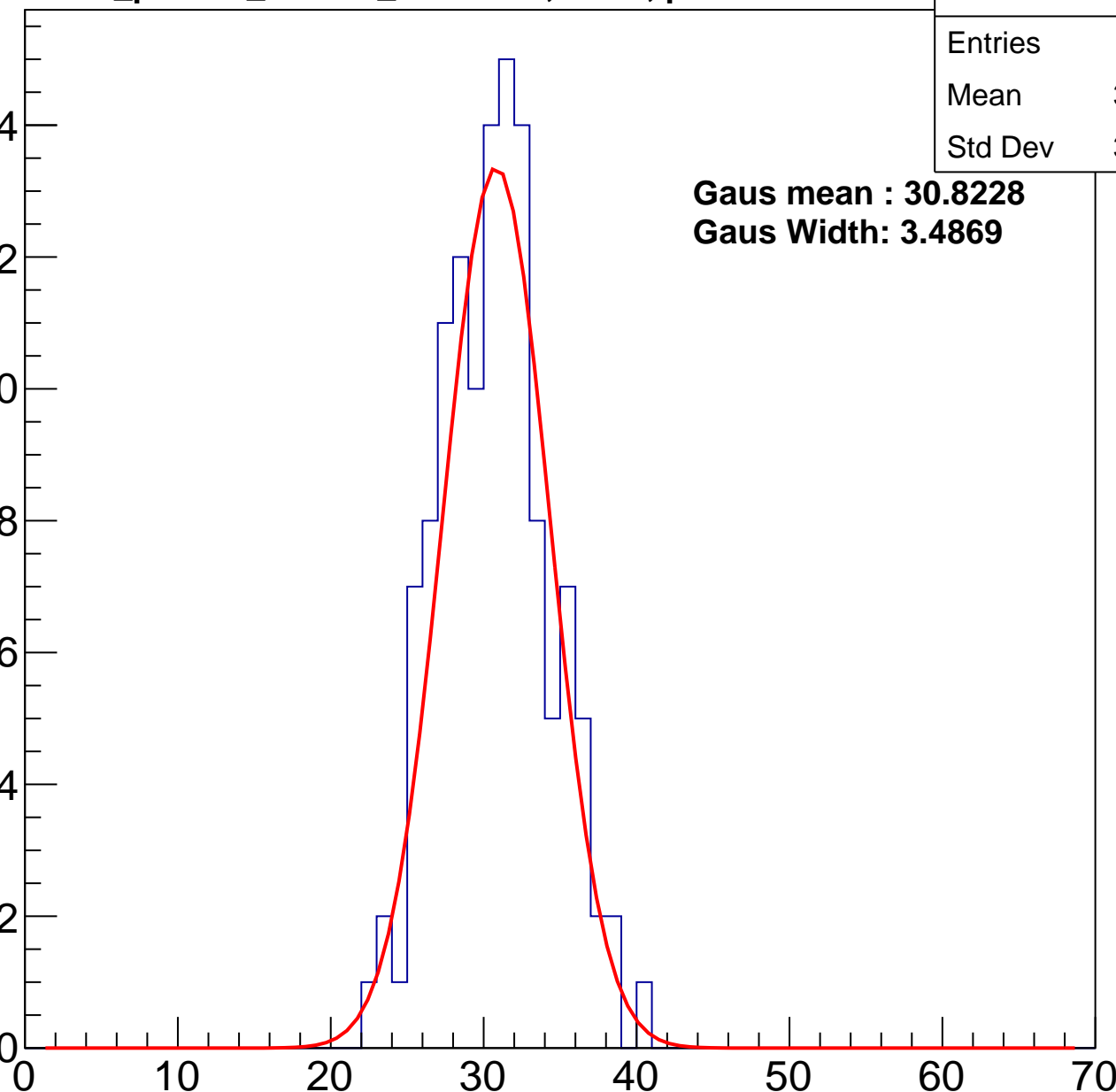
Entry

14
12
10
8
6
4
2
0

Entries	125
Mean	30.24
Std Dev	3.527

Gaus mean : 30.8228

Gaus Width: 3.4869

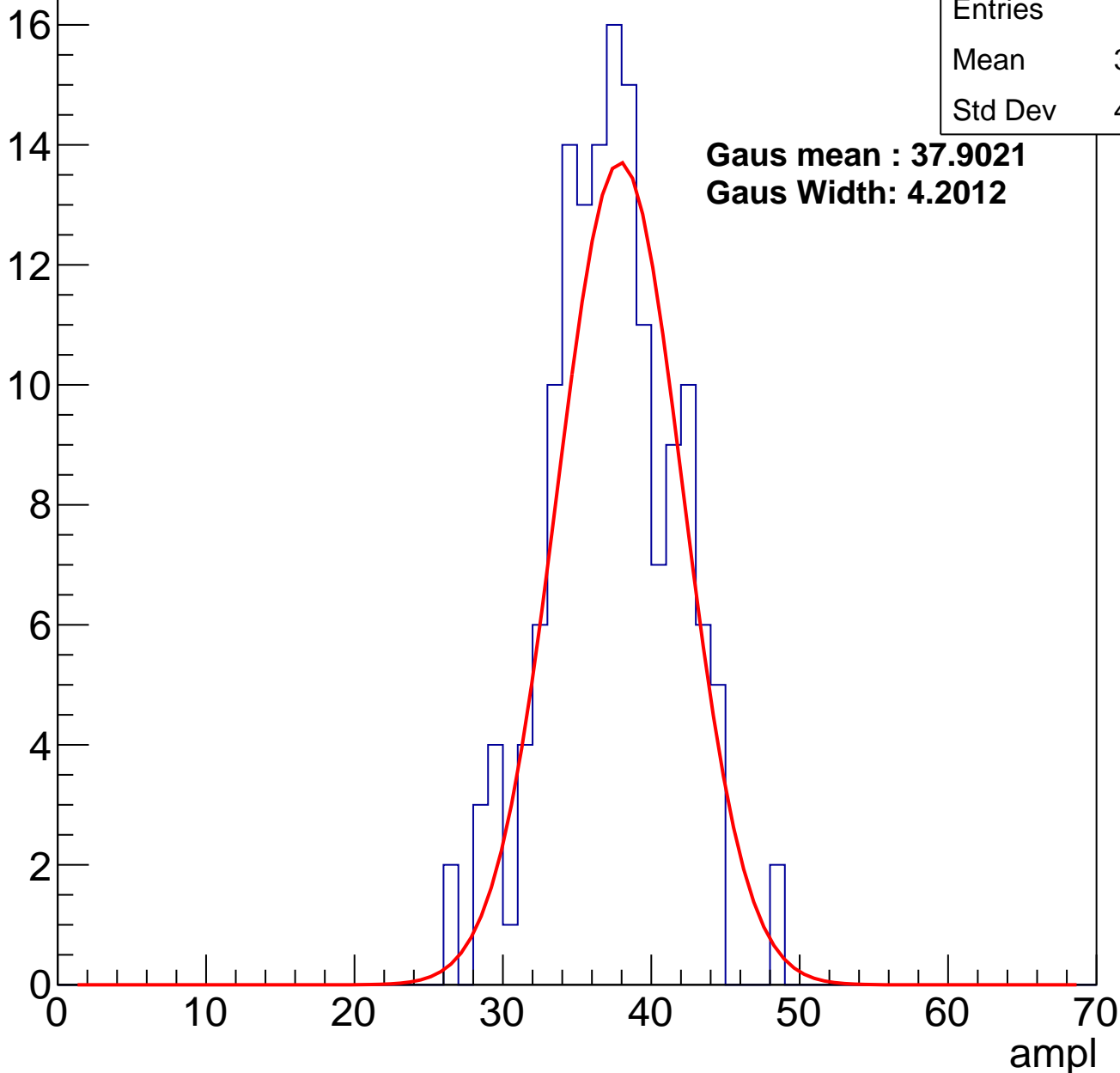


ampl

B0L101S, U20-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

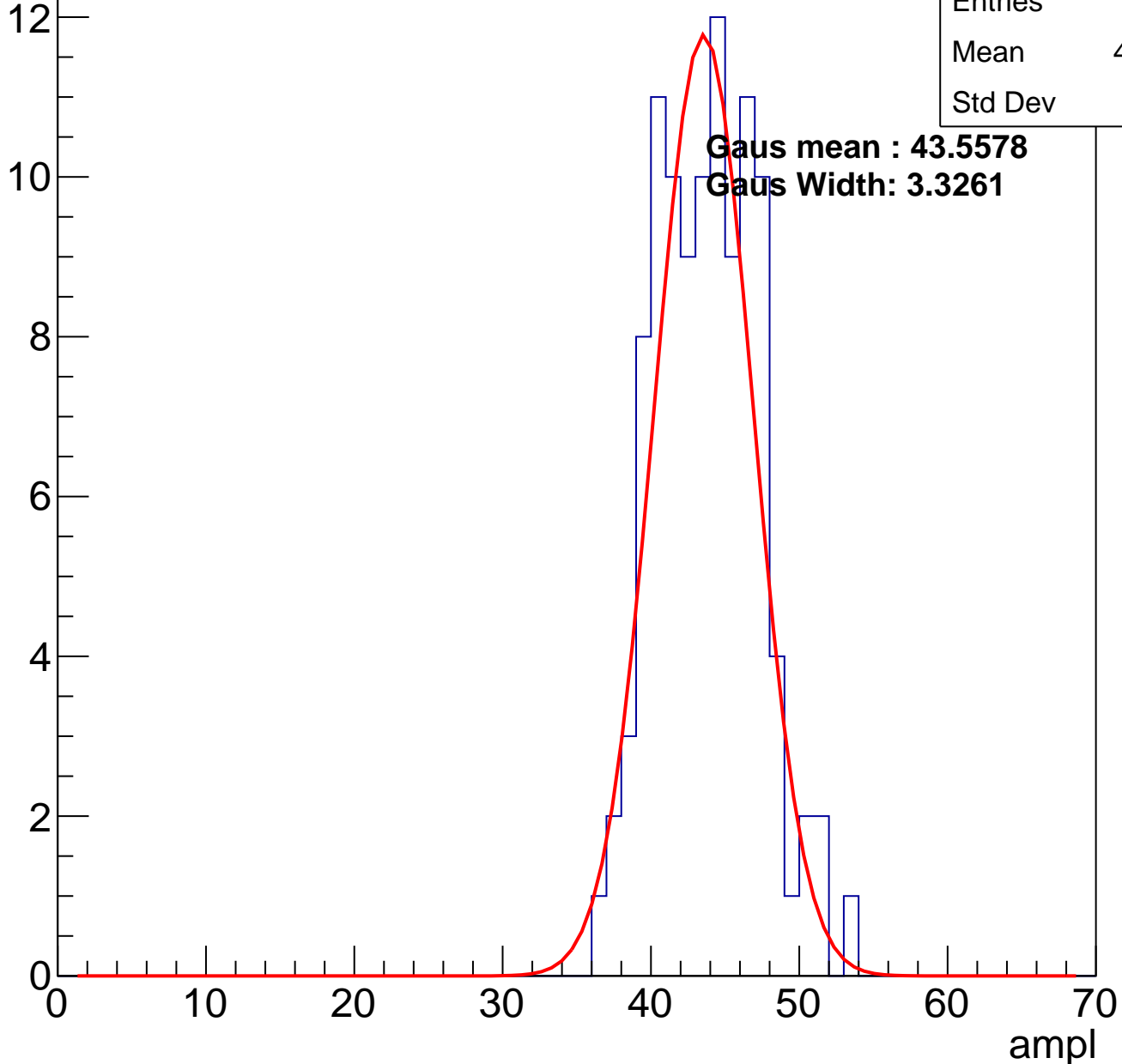
Entry



B0L101S, U20-ch78, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	49.72
Std Dev	3.714

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

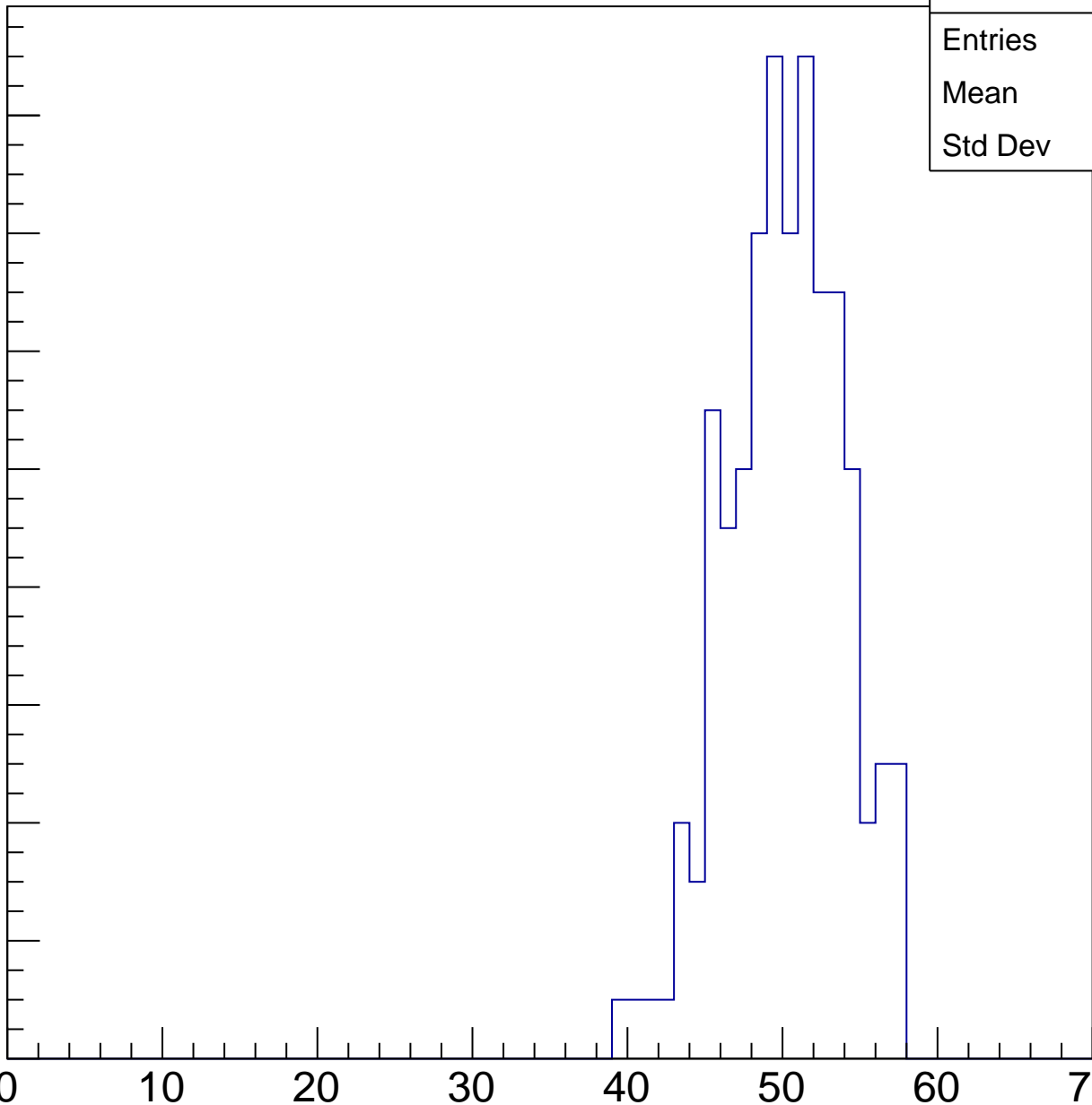
40

50

60

70

ampl



B0L101S, U20-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	56.48
Std Dev	3.174

Entry

12

10

8

6

4

2

0

0

10

20

30

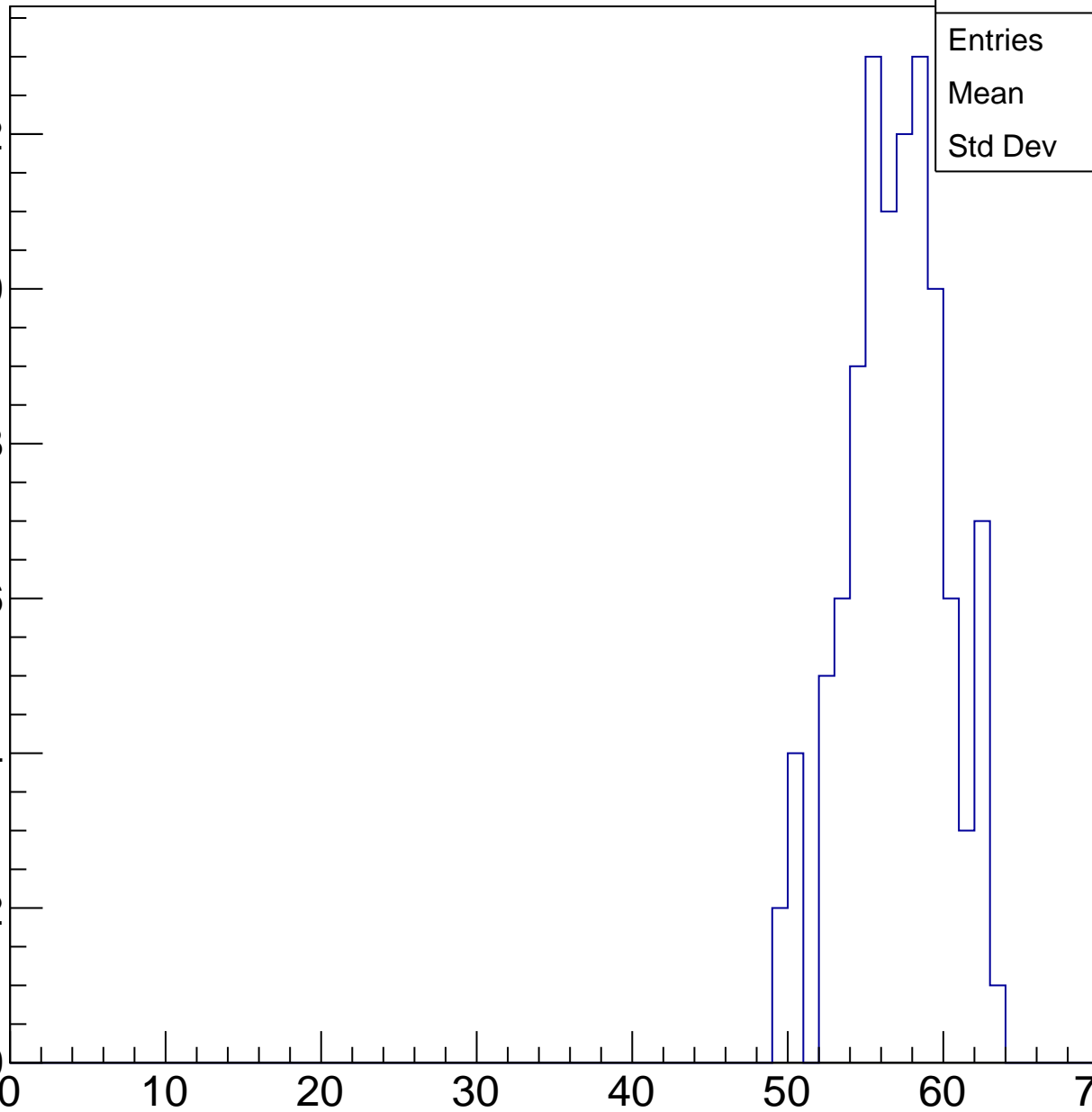
40

50

60

70

ampl



B0L101S, U20-ch78, adc5

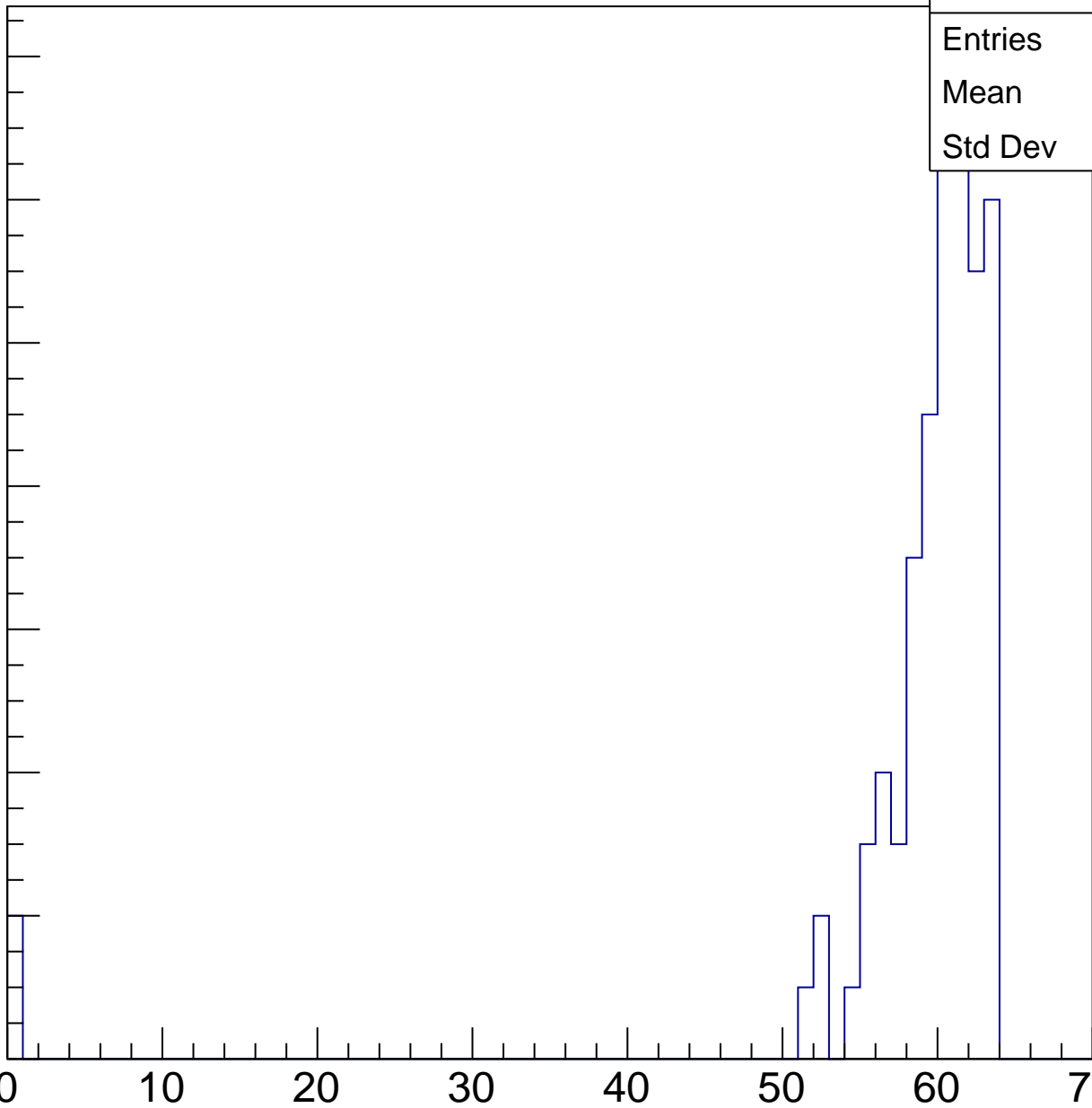
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.29
Std Dev	9.551

ampl

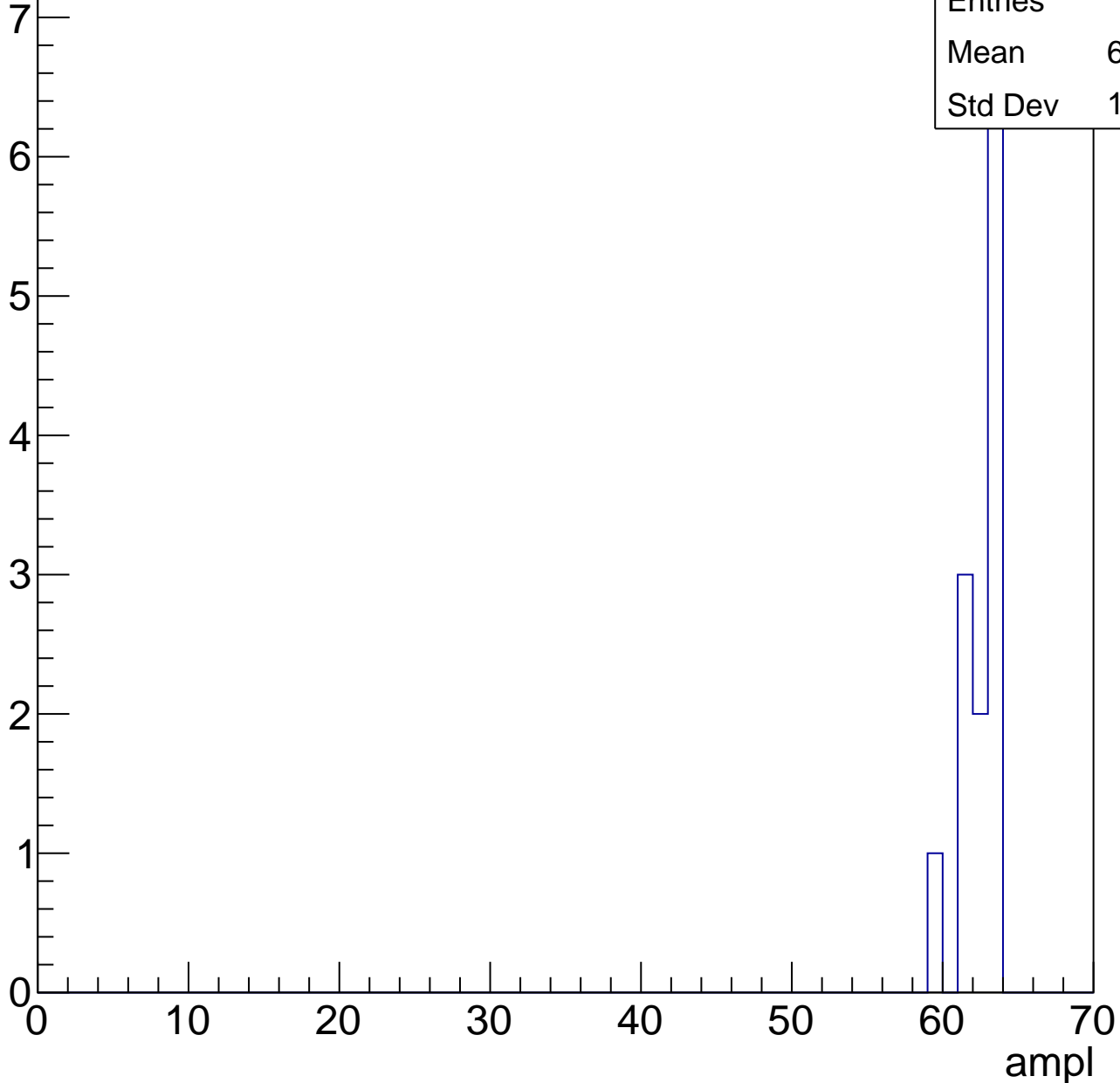


B0L101S, U20-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	62.08
Std Dev	1.206



B0L101S, U20-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	29.5
Std Dev	4.428

Gaus mean : 30.0811

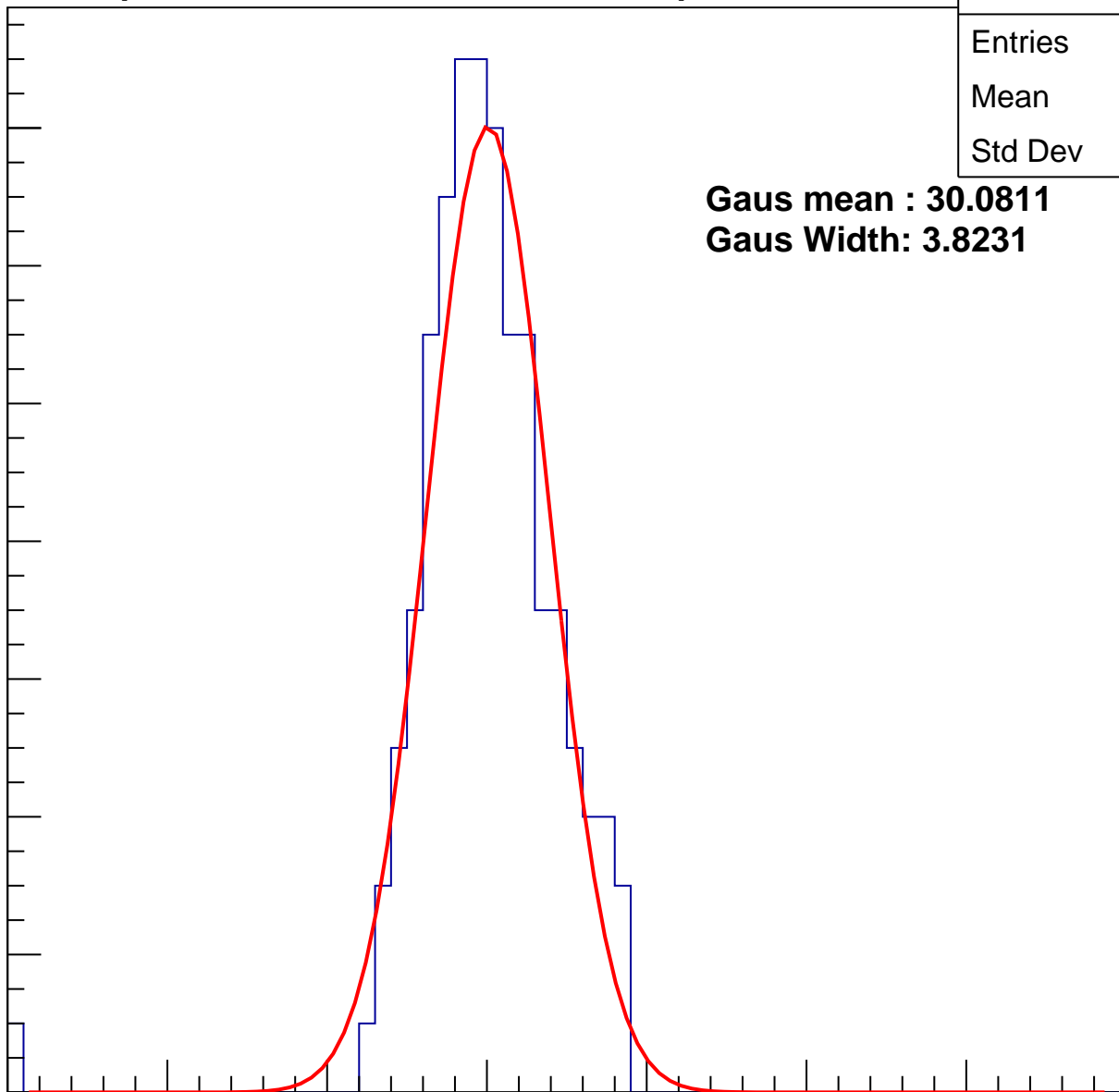
Gaus Width: 3.8231

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



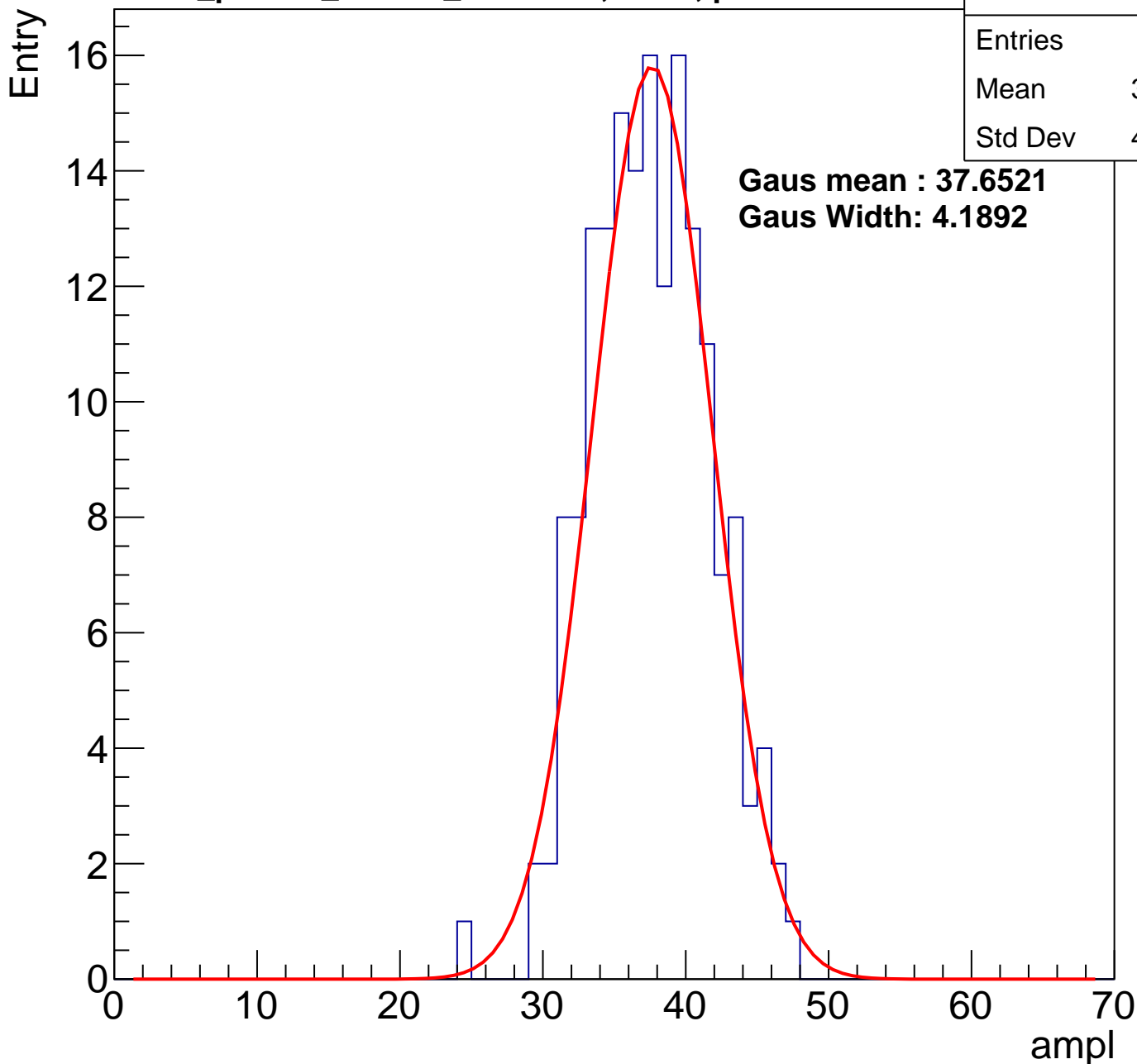
B0L101S, U20-ch79, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	37.15
Std Dev	4.052

Gaus mean : 37.6521

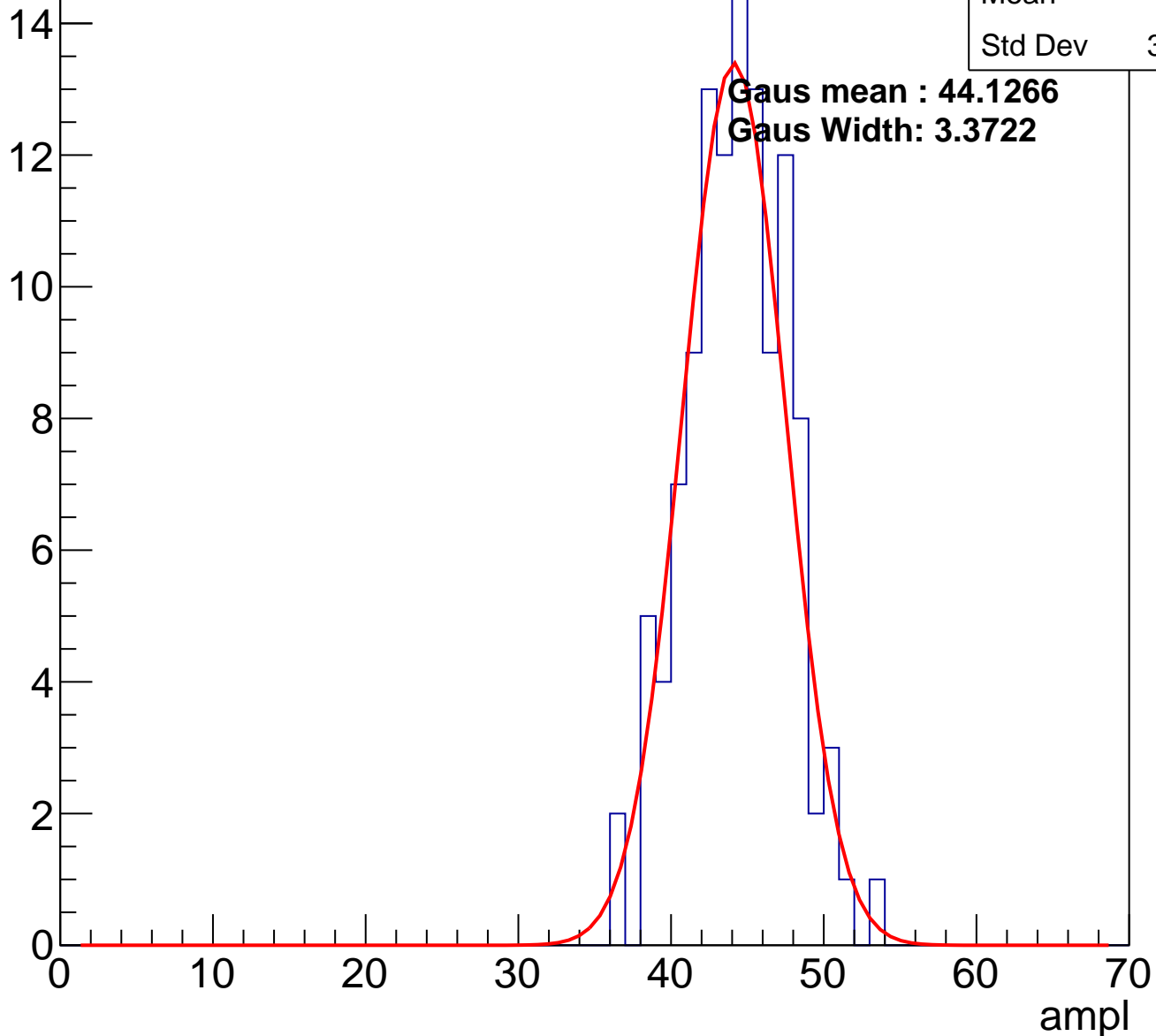
Gaus Width: 4.1892



B0L101S, U20-ch79, adc2

calib_packv5_042523_0143.root, FC#1, port C1

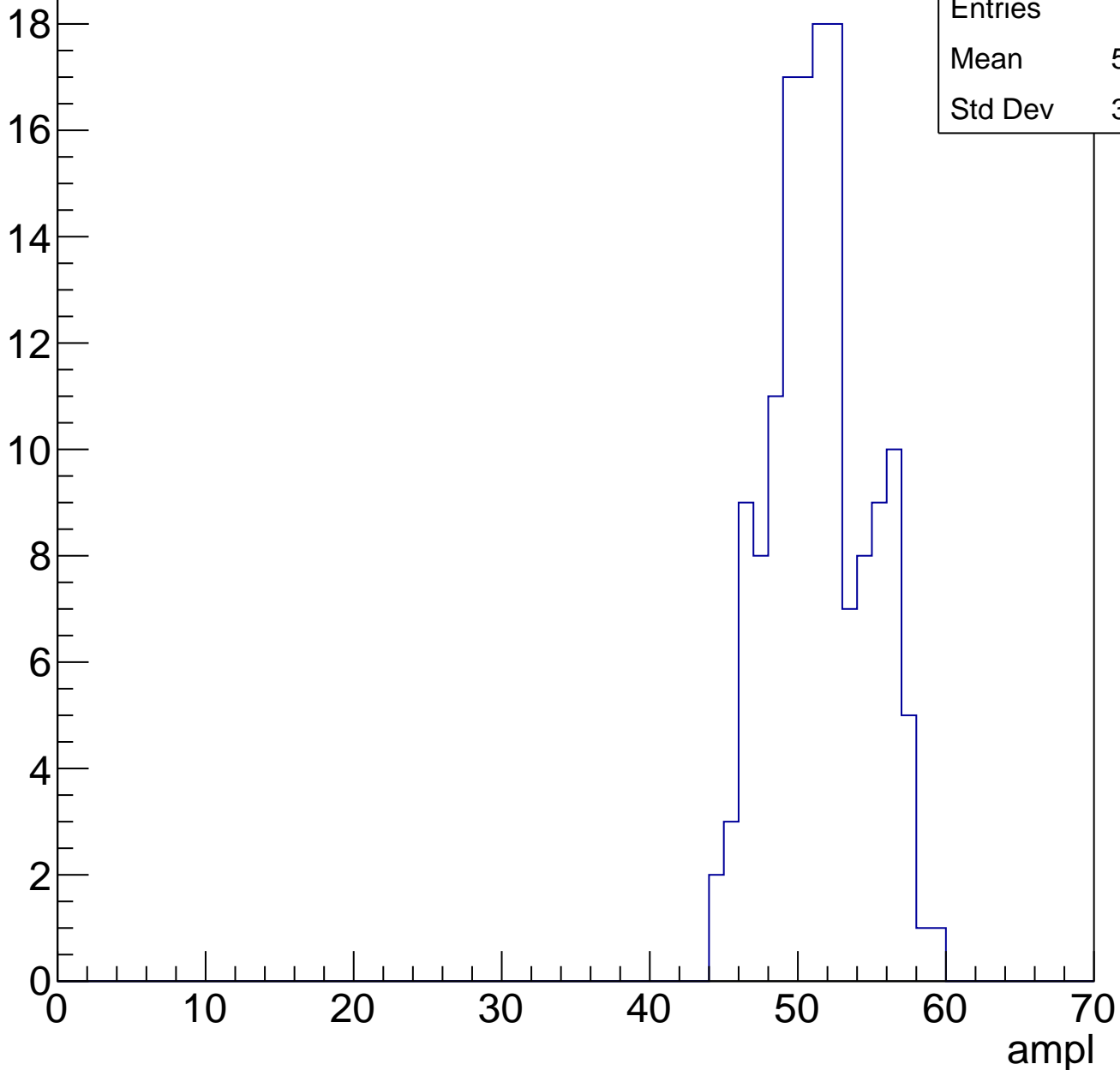
Entry



B0L101S, U20-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

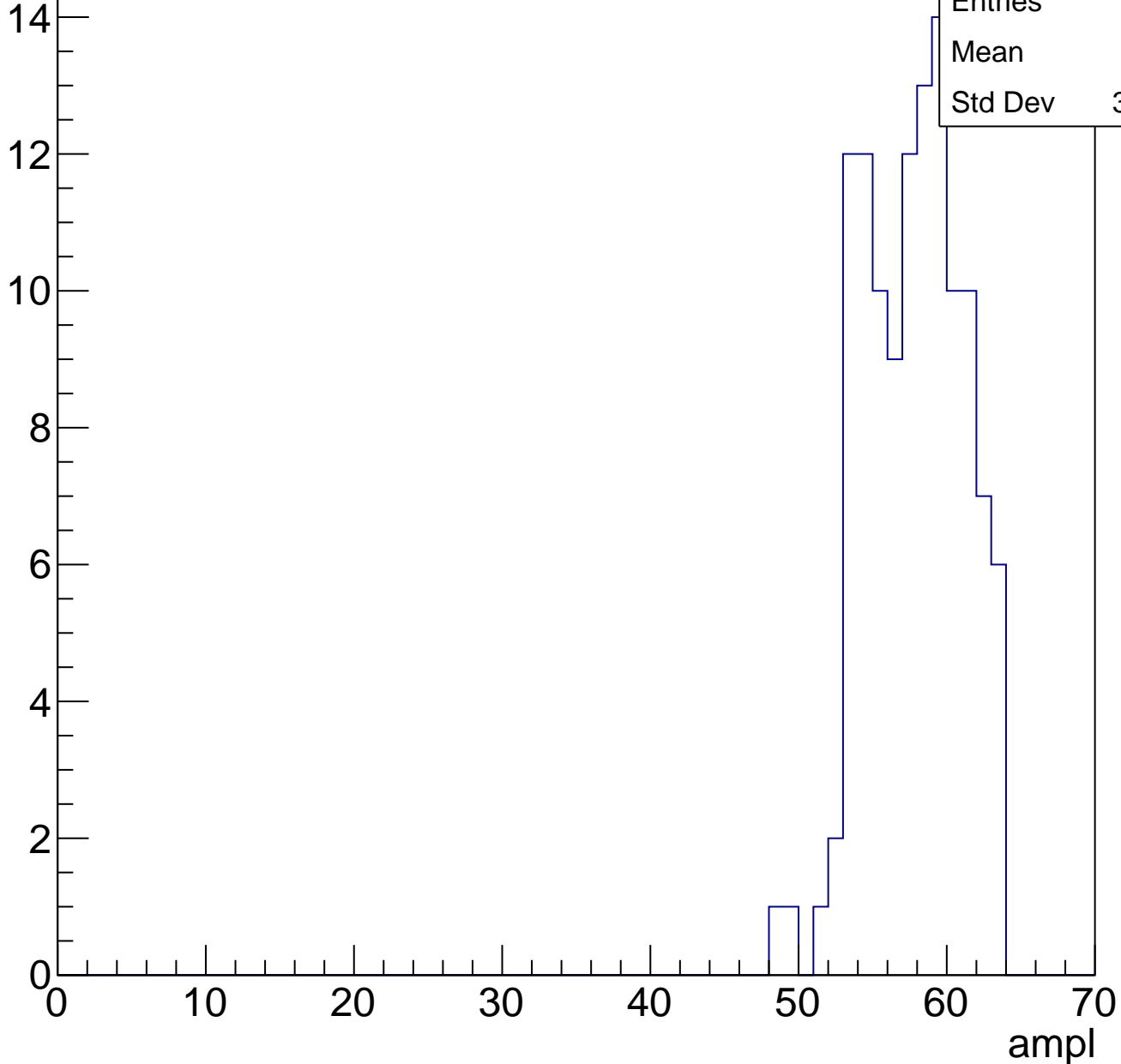
Entry



B0L101S, U20-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch79, adc5

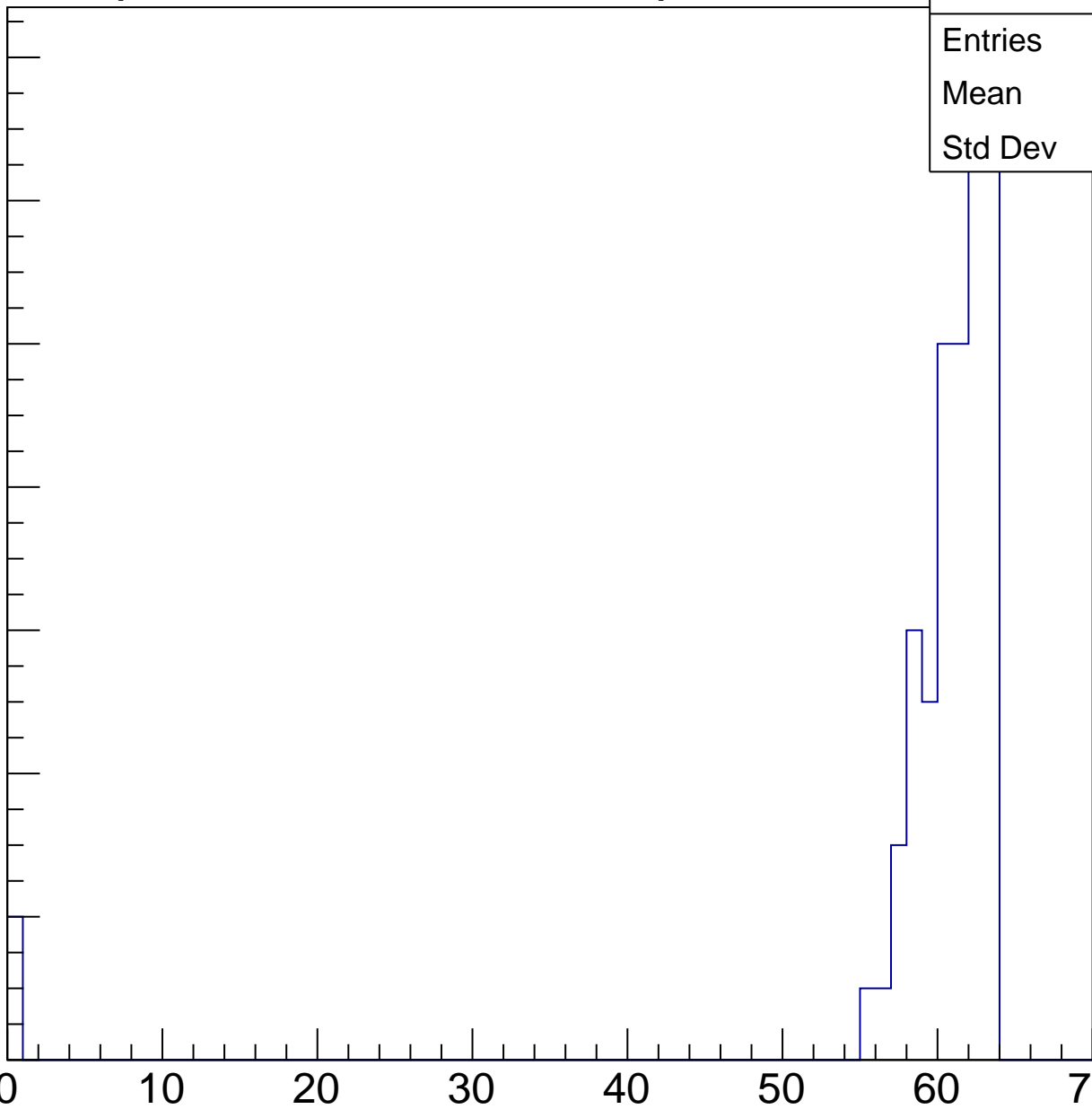
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.82
Std Dev	10.67

ampl



B0L101S, U20-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch80, adc0

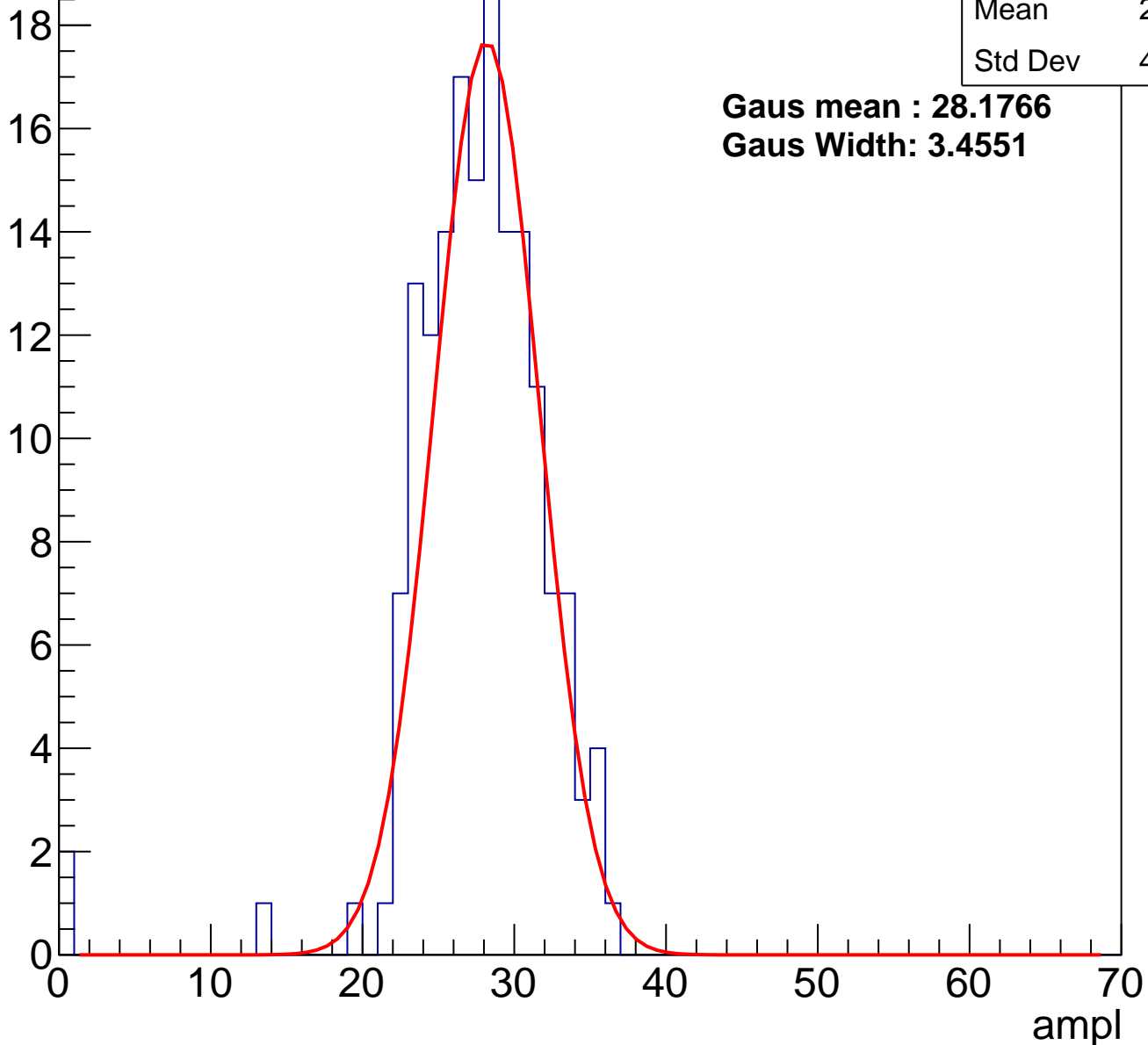
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	27.13
Std Dev	4.702

Gaus mean : 28.1766

Gaus Width: 3.4551

Entry



B0L101S, U20-ch80, adc1

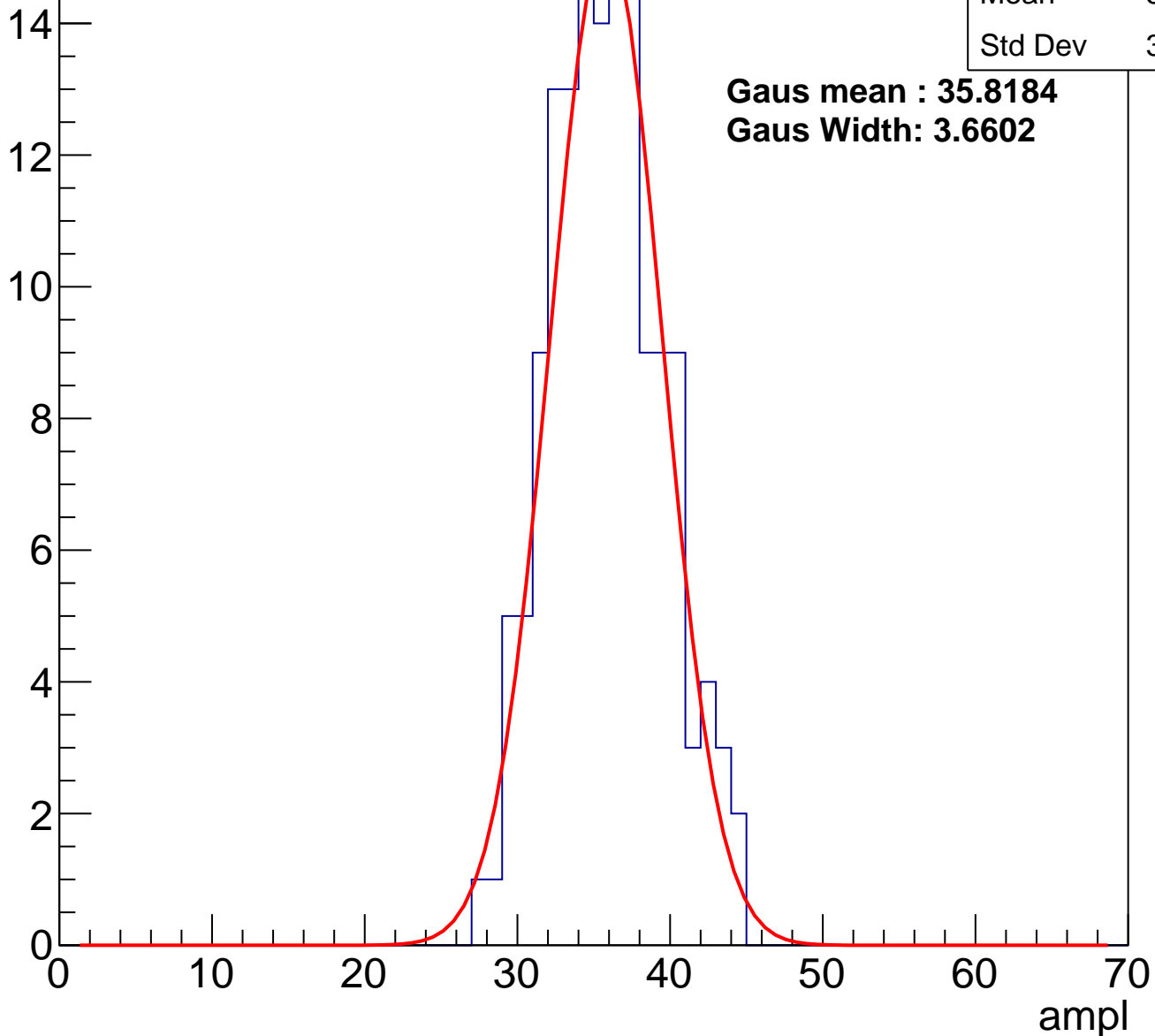
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	35.38
Std Dev	3.628

Gaus mean : 35.8184

Gaus Width: 3.6602

Entry



B0L101S, U20-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

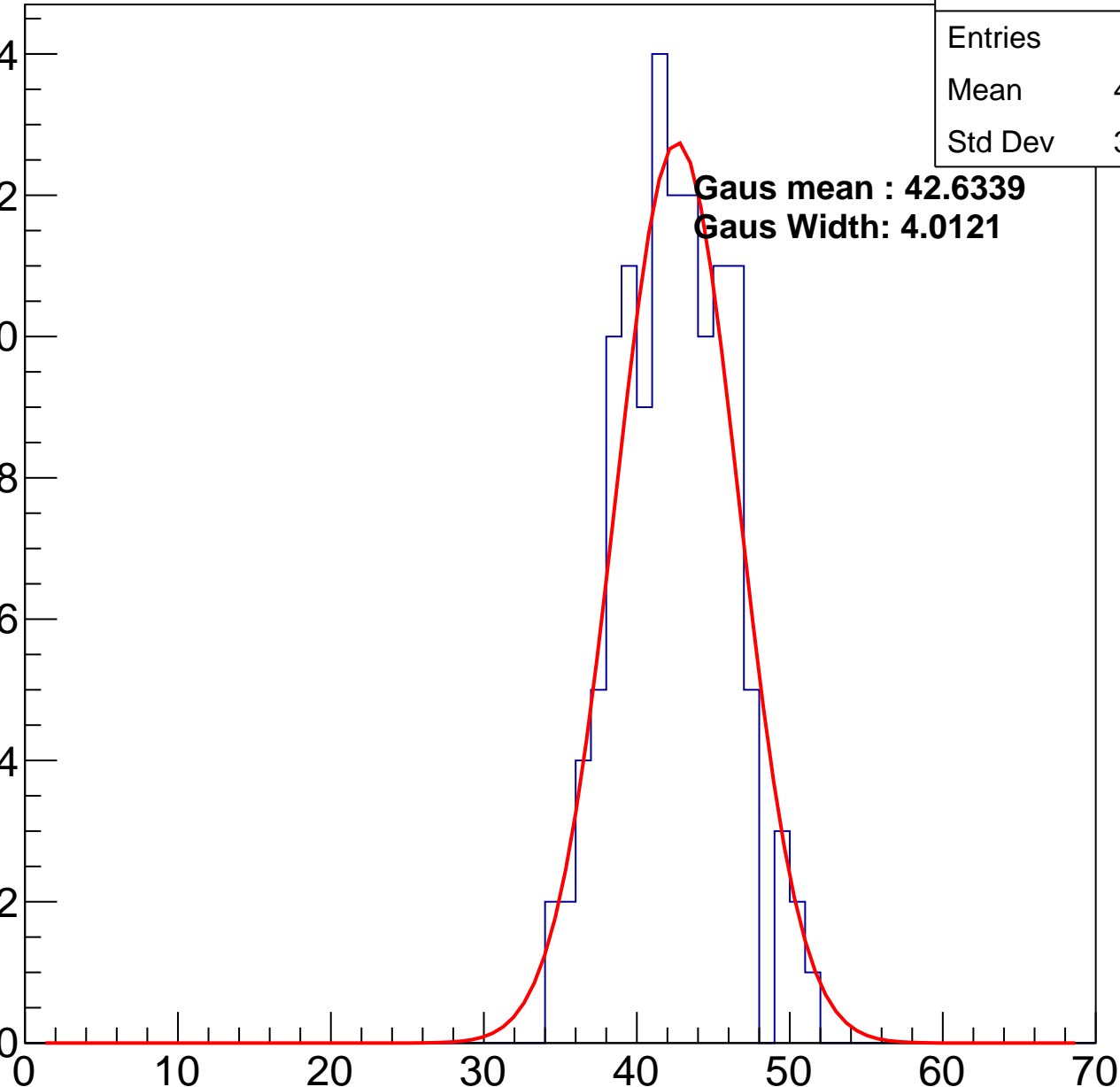
14
12
10
8
6
4
2
0

Entries	124
Mean	41.97
Std Dev	3.605

Gaus mean : 42.6339

Gaus Width: 4.0121

ampl



B0L101S, U20-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

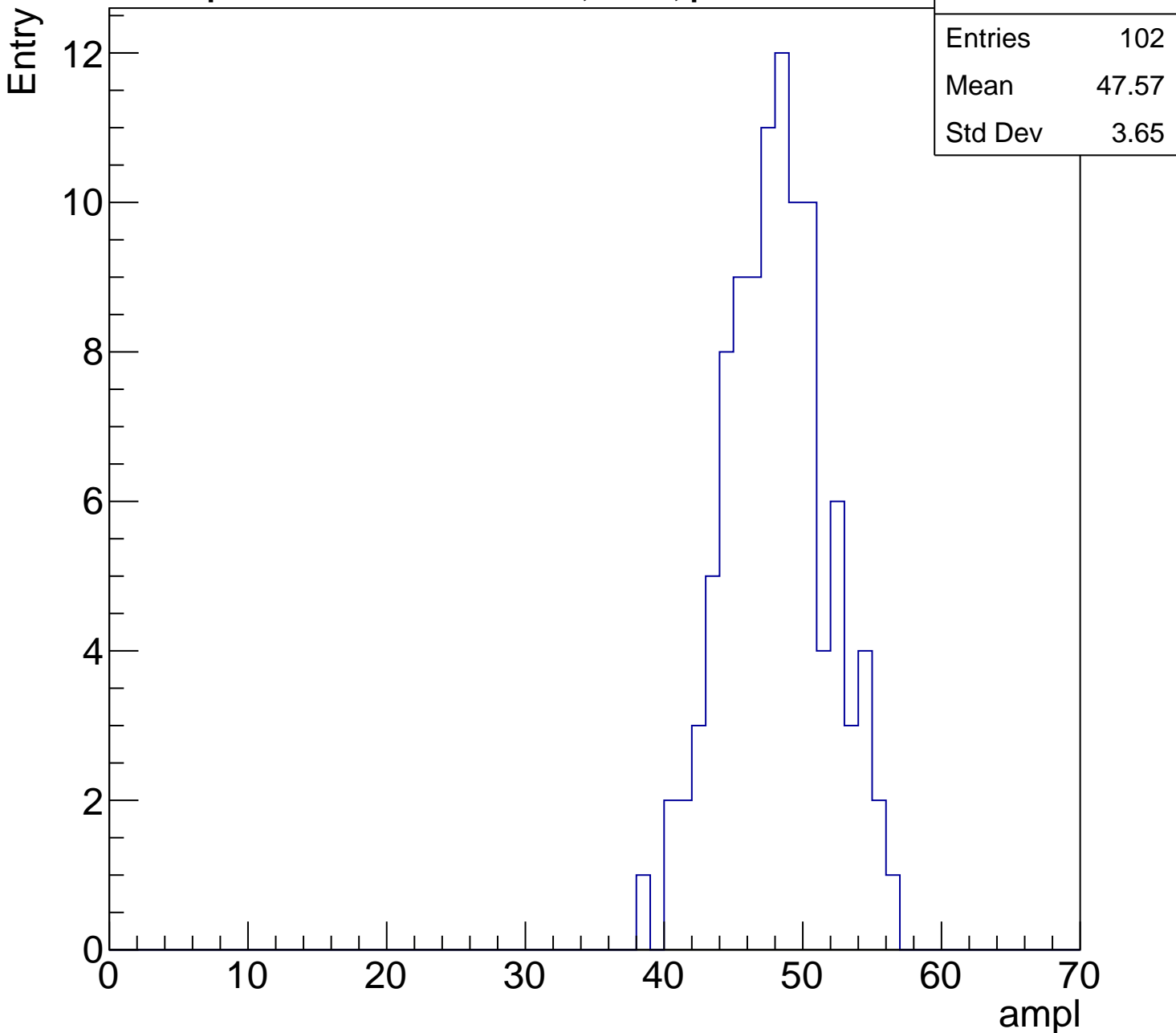
Entries	102
Mean	47.57
Std Dev	3.65

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

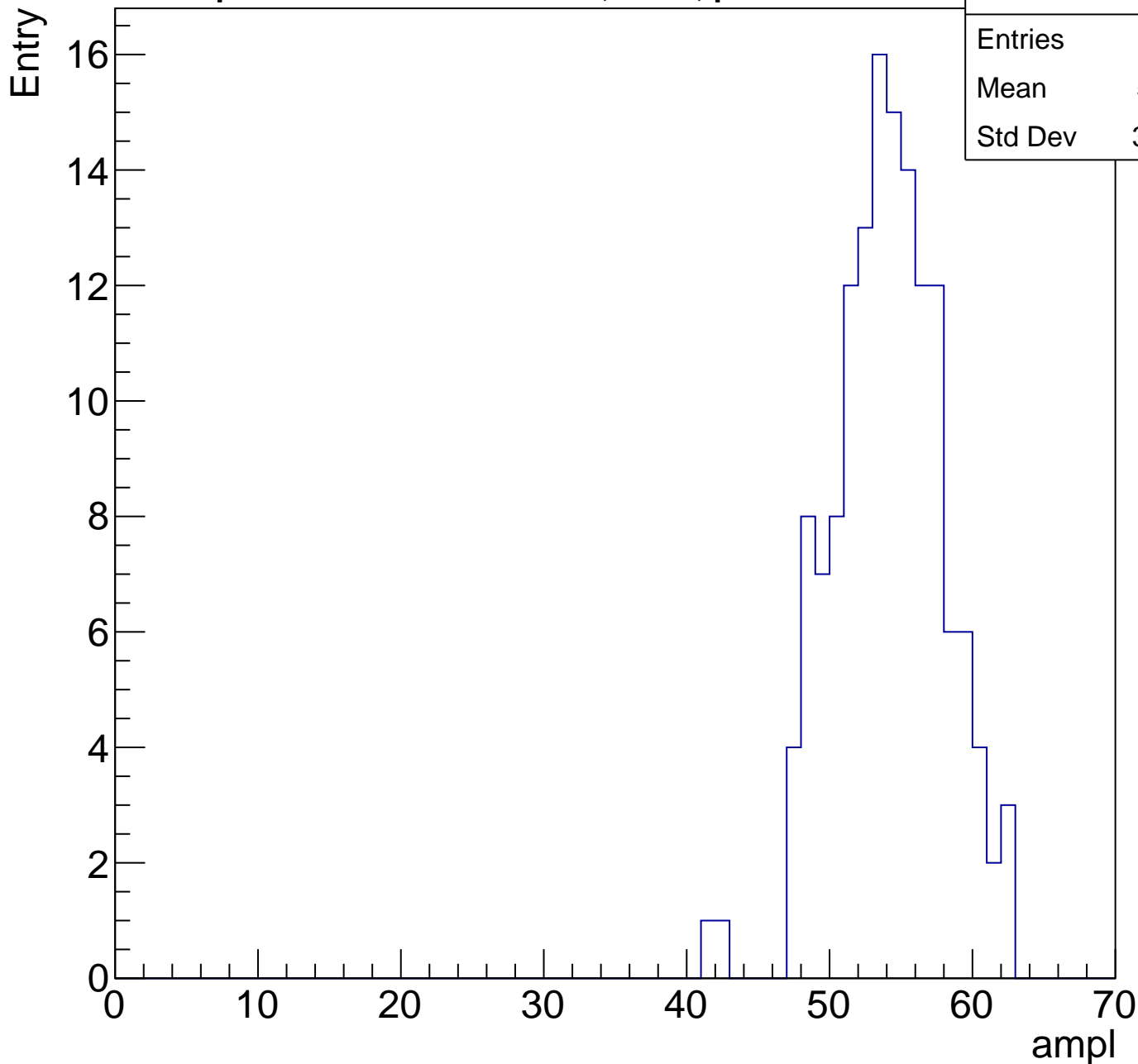
ampl



B0L101S, U20-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	53.61
Std Dev	3.823



B0L101S, U20-ch80, adc5

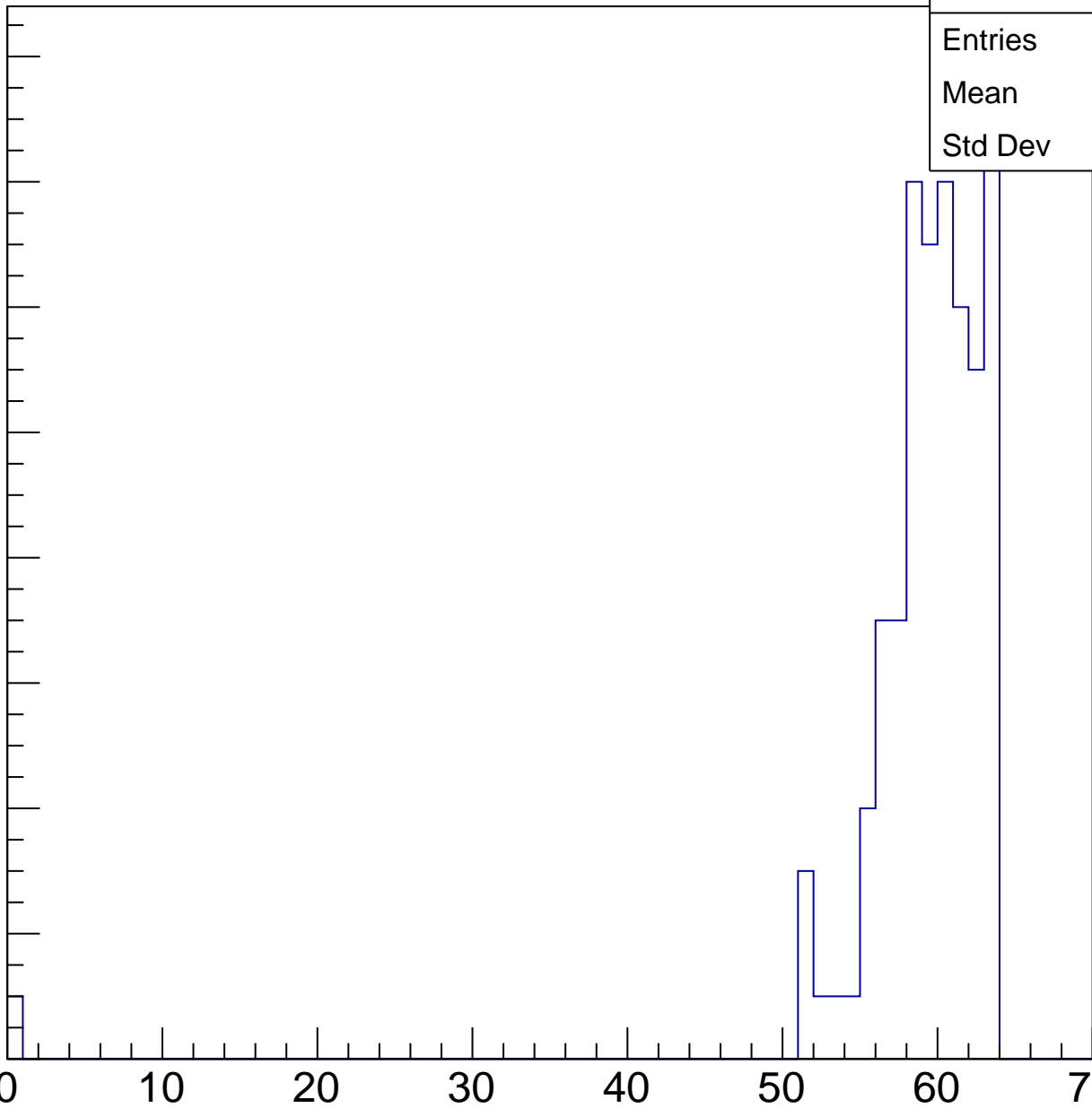
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.7
Std Dev	6.444

ampl

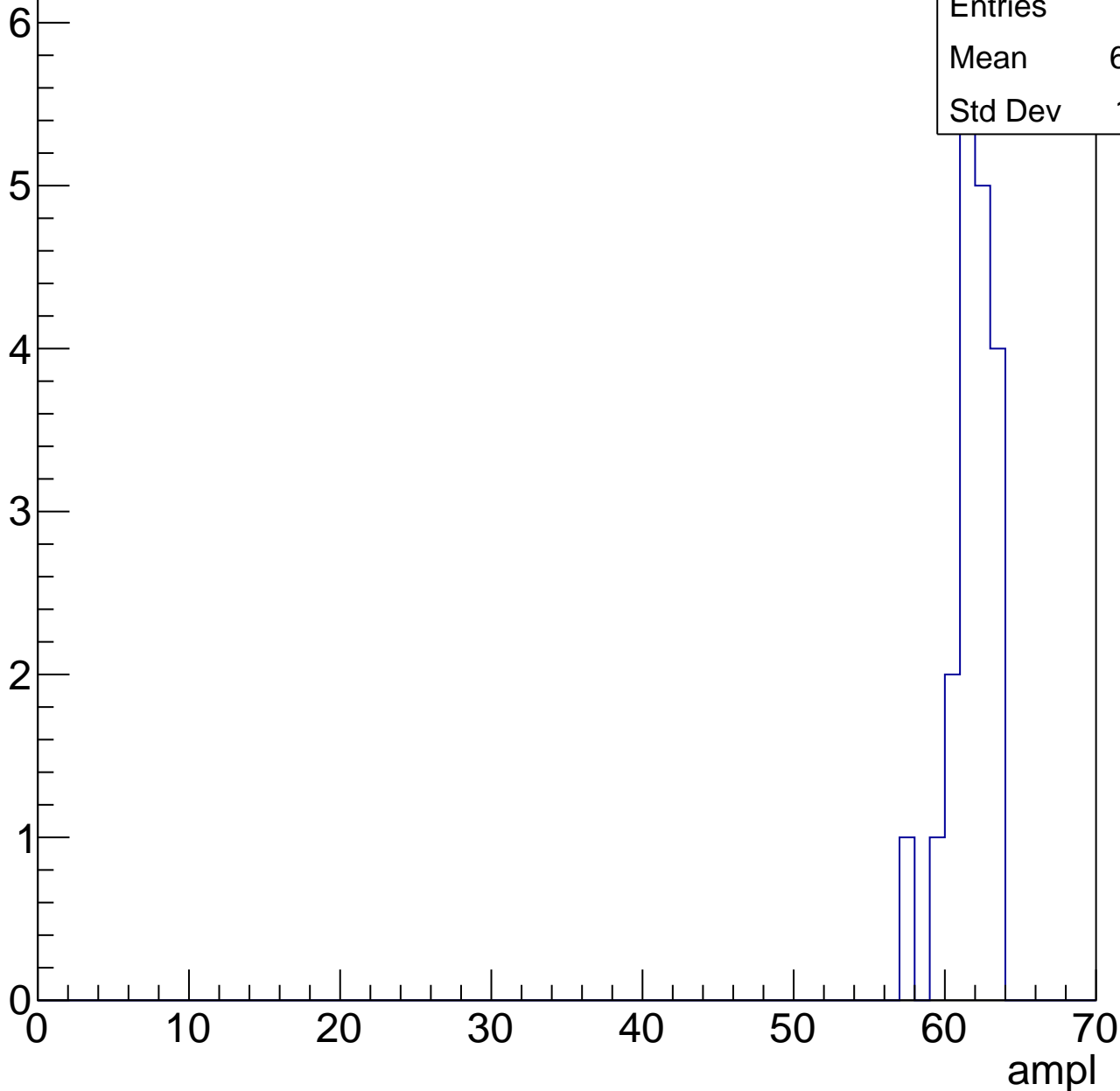


B0L101S, U20-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

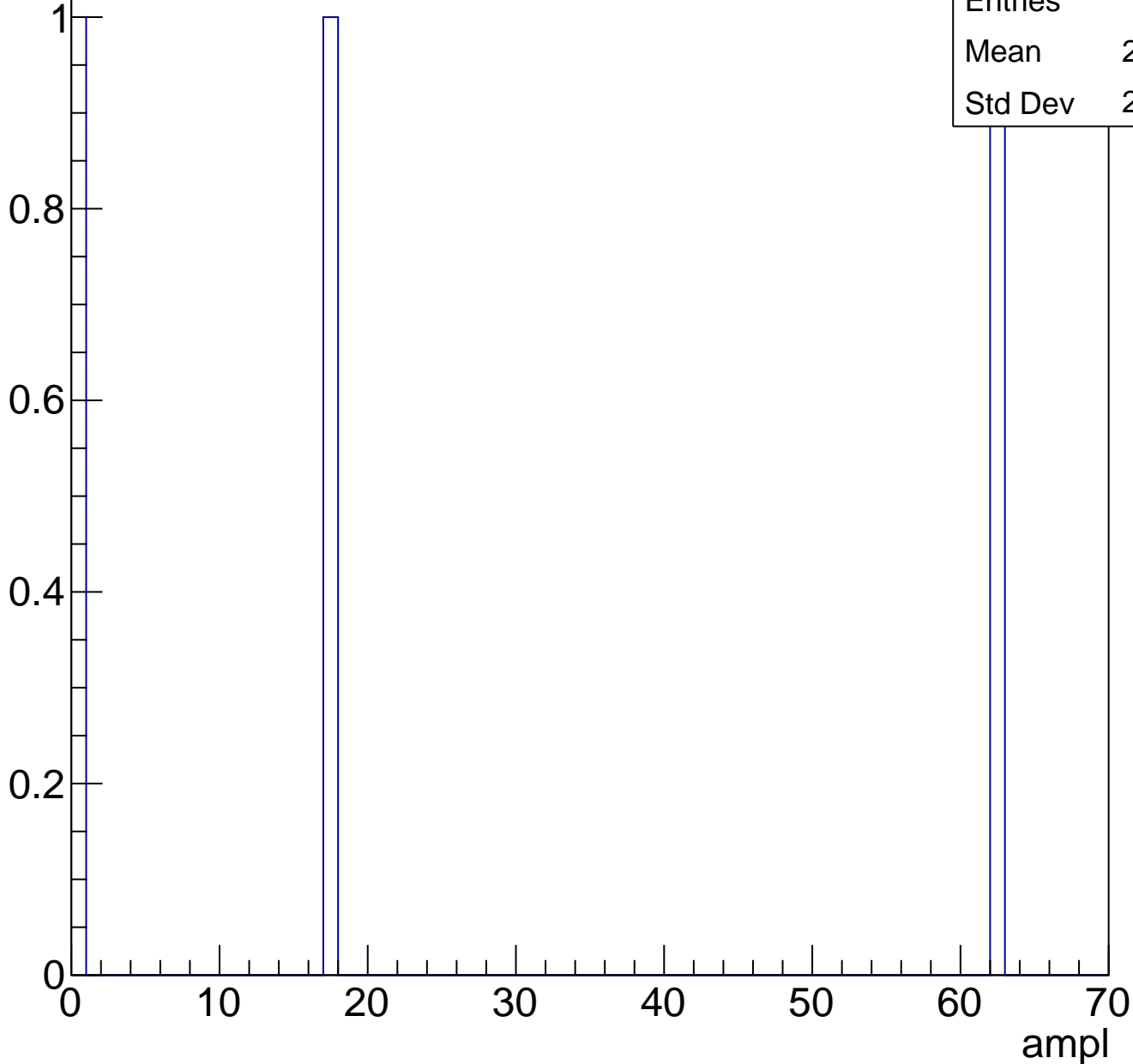
Entries	19
Mean	61.26
Std Dev	1.481



B0L101S, U20-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.33
Std Dev	26.16

B0L101S, U20-ch81, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	29.19
Std Dev	3.275

Gaus mean : 29.6867

Gaus Width: 3.4971

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch81, adc1

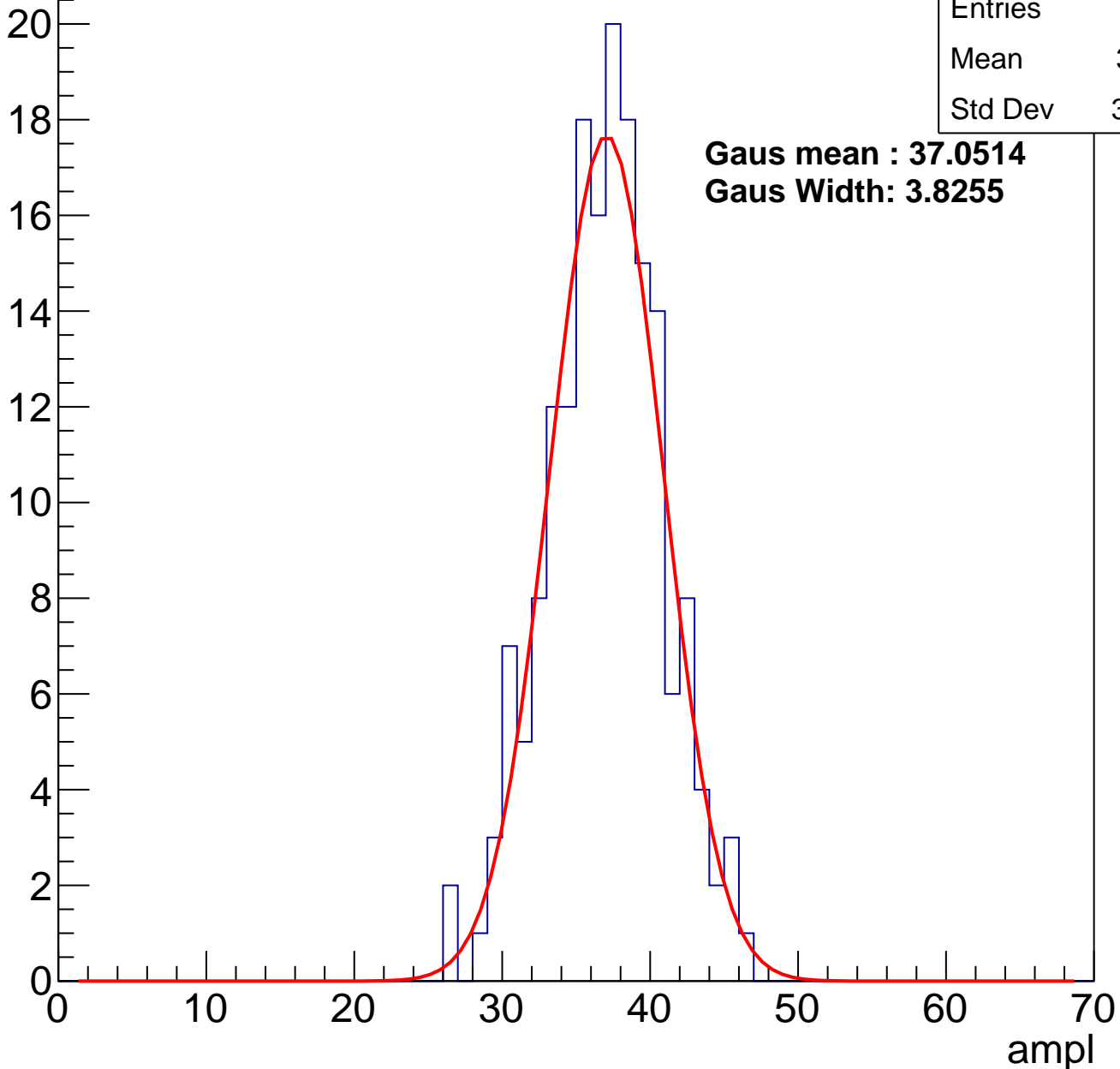
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	36.51
Std Dev	3.854

Gaus mean : 37.0514

Gaus Width: 3.8255

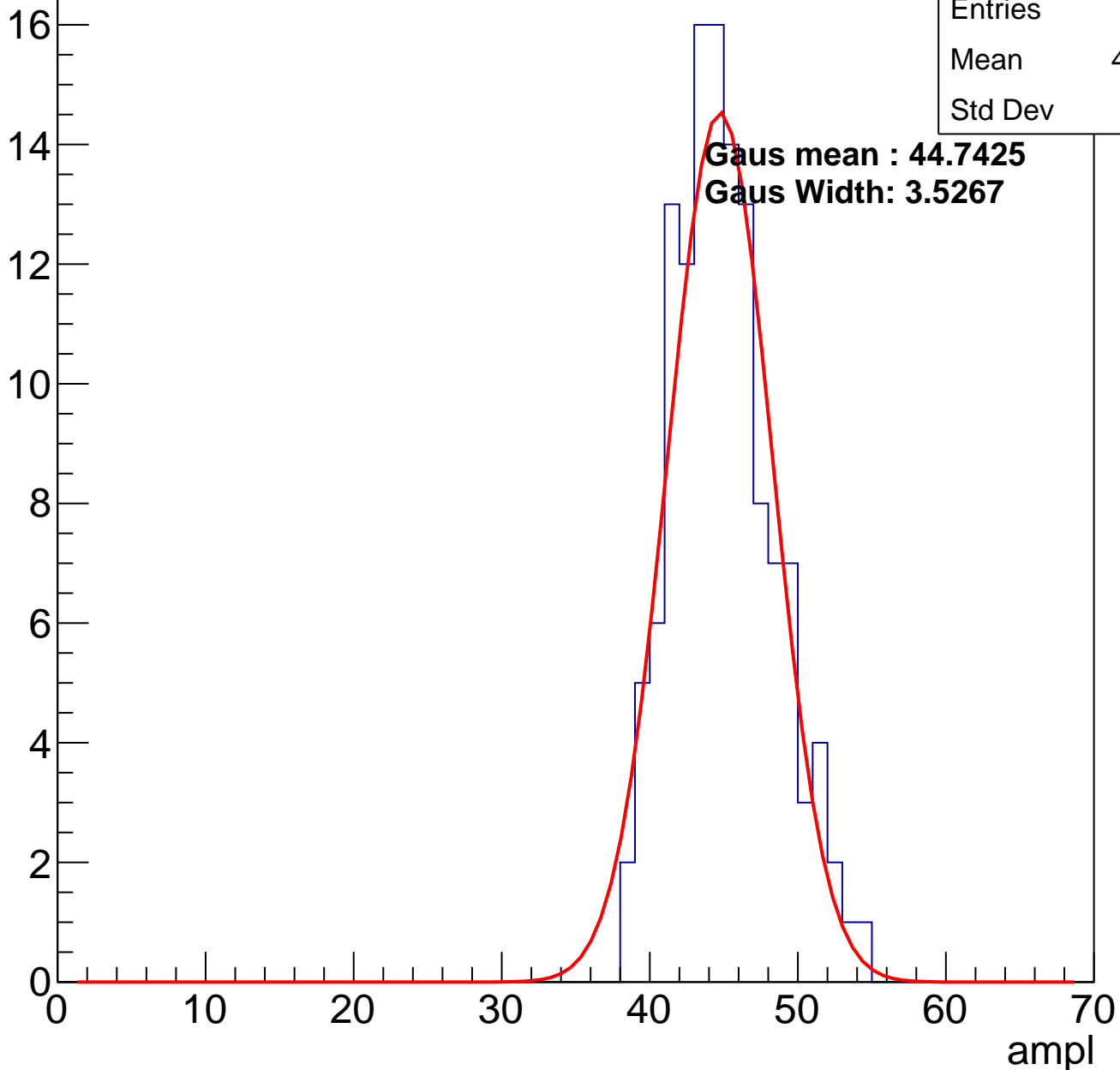
Entry



B0L101S, U20-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

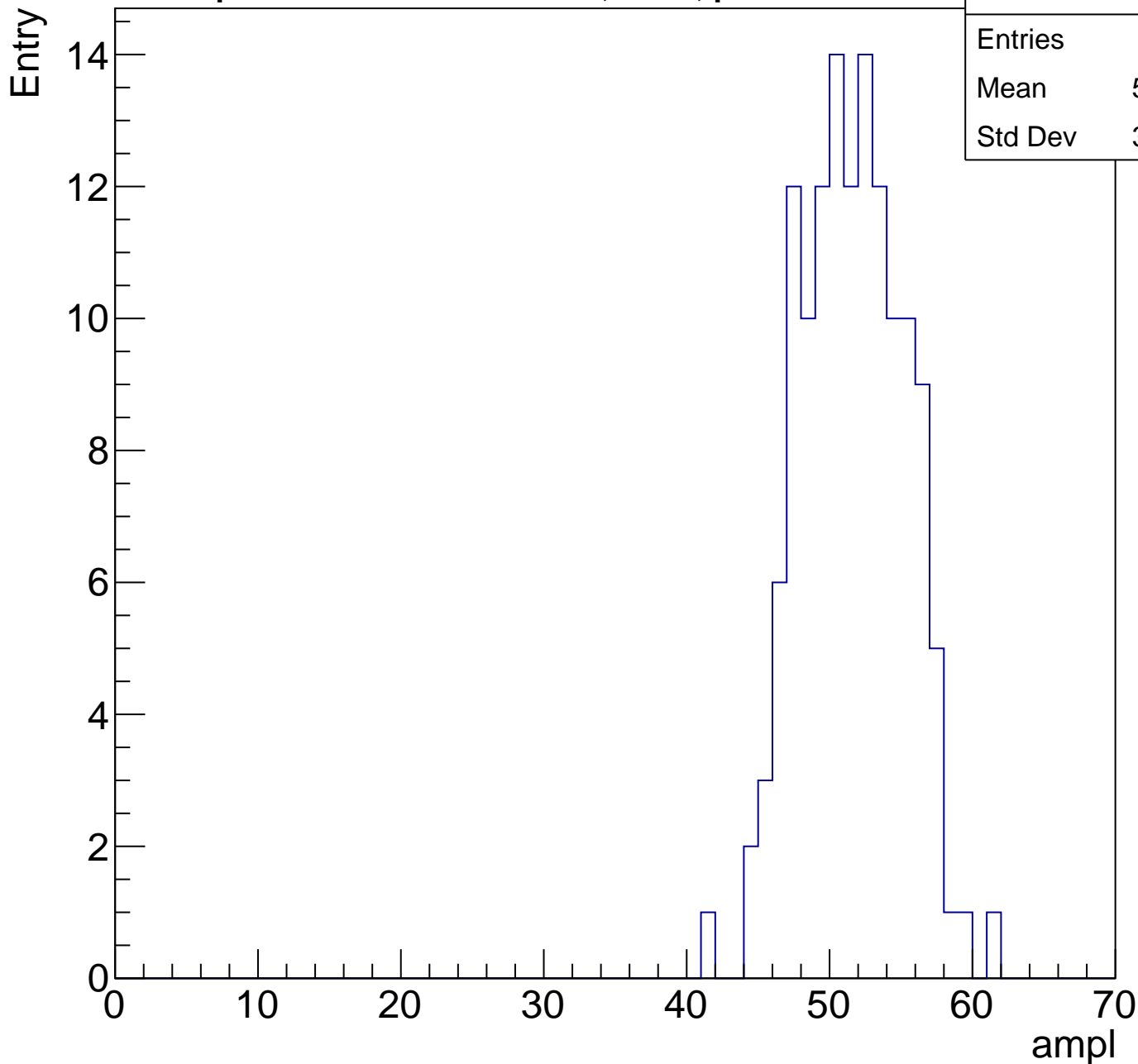
Entry



B0L101S, U20-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

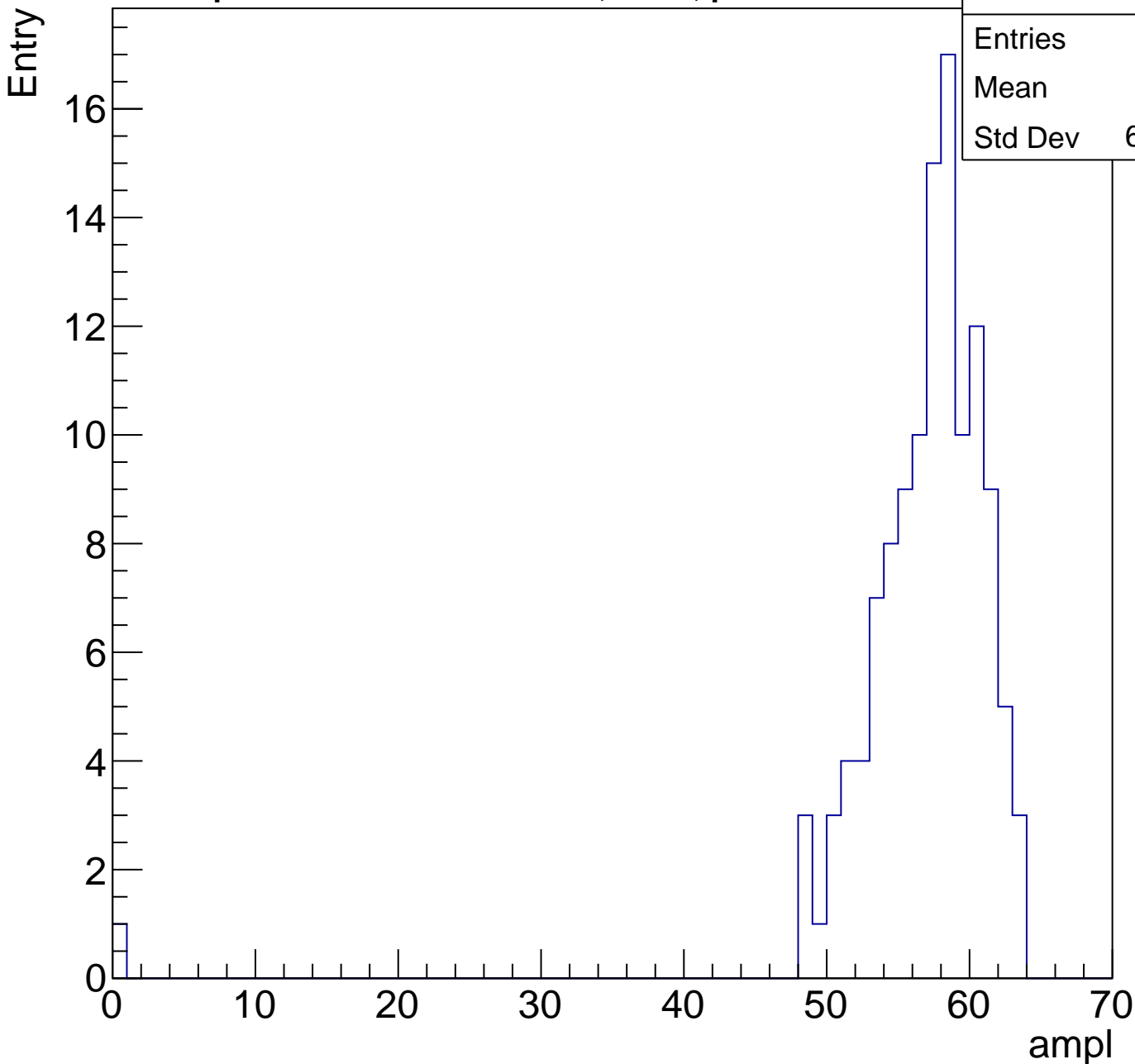
Entries	135
Mean	51.15
Std Dev	3.577



B0L101S, U20-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	56.3
Std Dev	6.205



B0L101S, U20-ch81, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	55
Mean	59.6
Std Dev	8.357

ampl

0

10

20

30

40

50

60

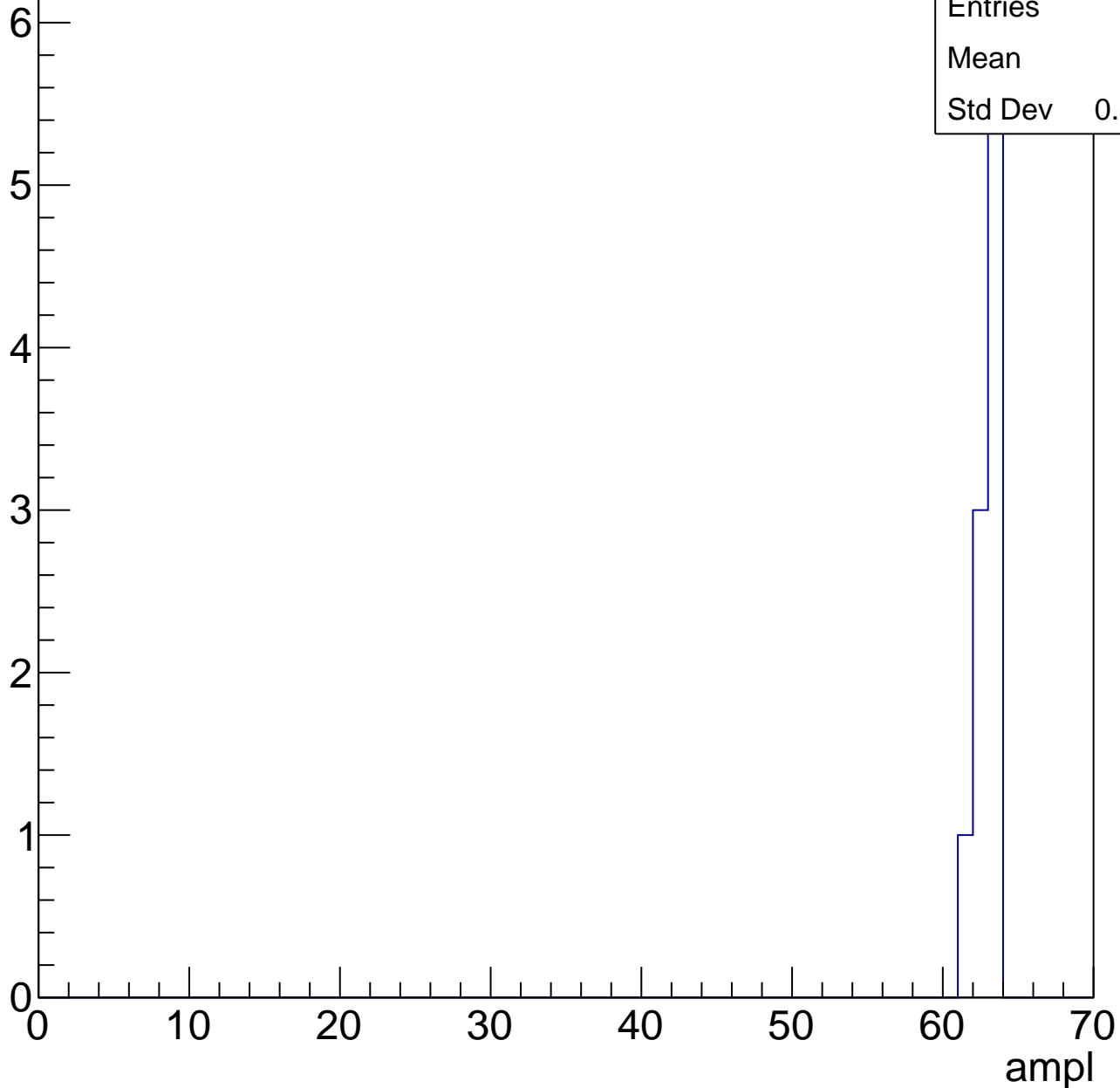
70

B0L101S, U20-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.5
Std Dev	0.6708



B0L101S, U20-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U20-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.03
Std Dev	3.567

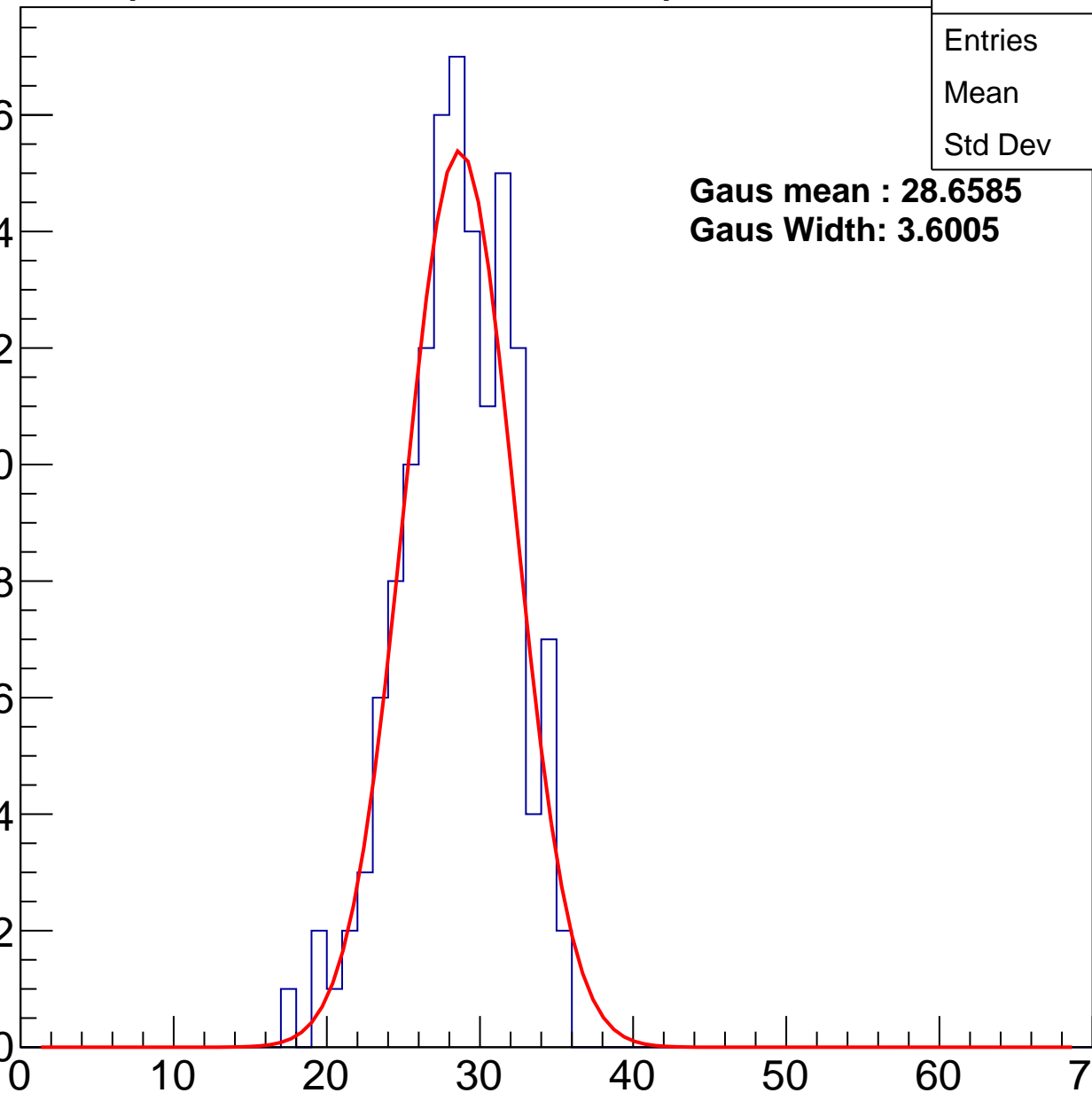
Gaus mean : 28.6585

Gaus Width: 3.6005

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch82, adc1

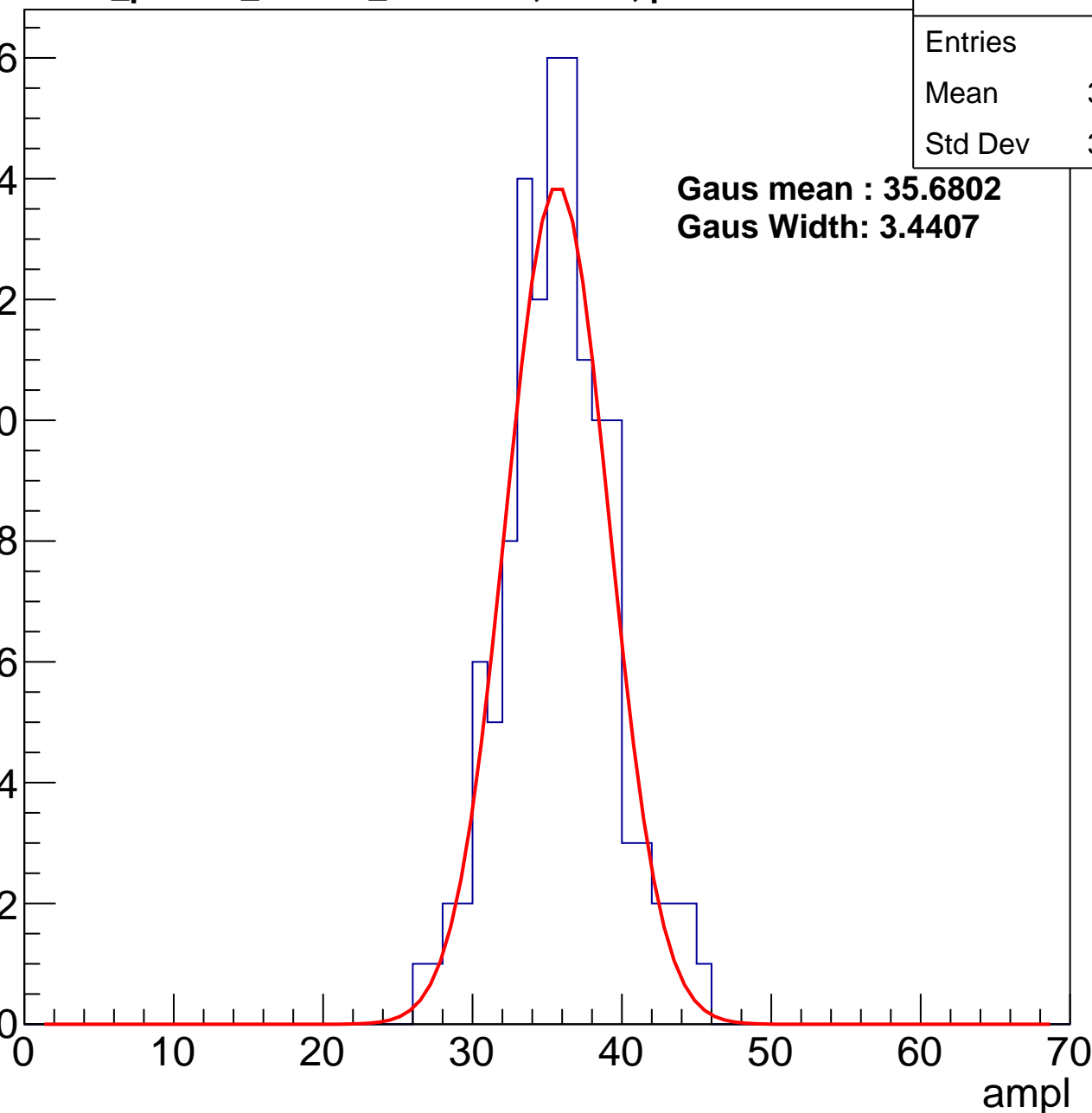
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	35.33
Std Dev	3.607

Gaus mean : 35.6802
Gaus Width: 3.4407



B0L101S, U20-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

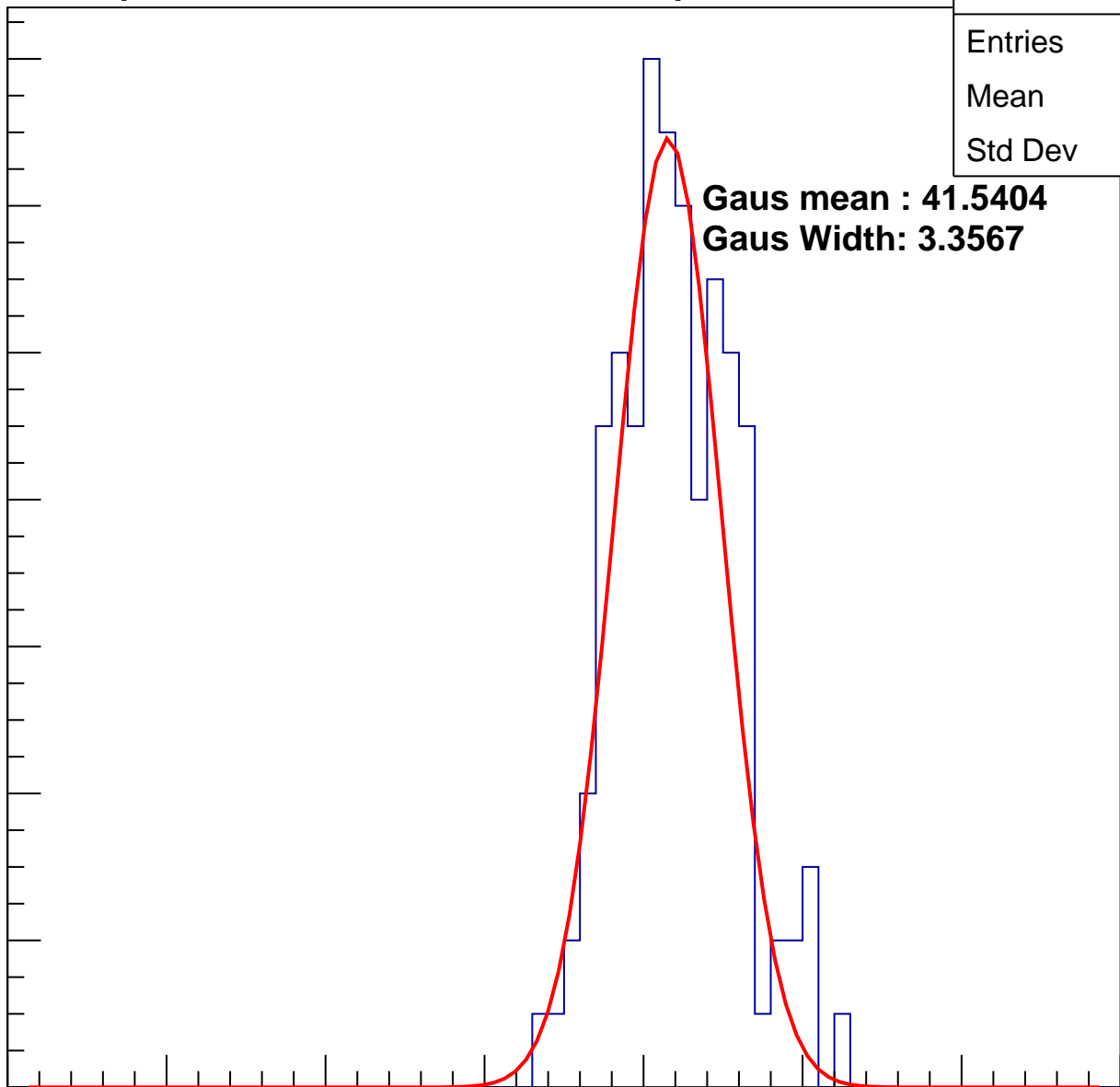
Entries	122
Mean	41.61
Std Dev	3.707

Gaus mean : 41.5404

Gaus Width: 3.3567

ampl

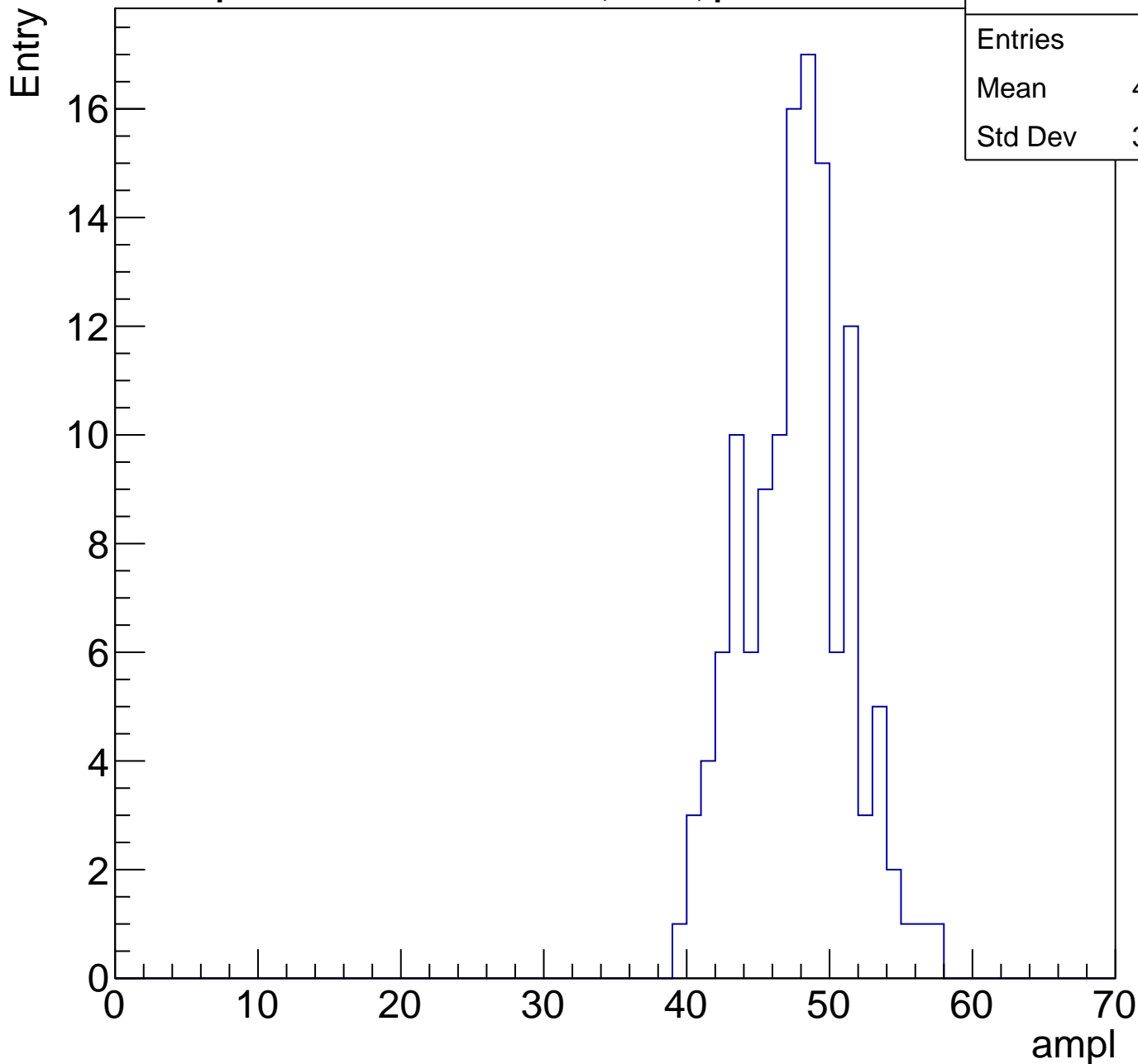
0 10 20 30 40 50 60 70



B0L101S, U20-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	47.23
Std Dev	3.622



B0L101S, U20-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	53.77
Std Dev	3.212

Entry

14
12
10
8
6
4
2
0

0

10

20

30

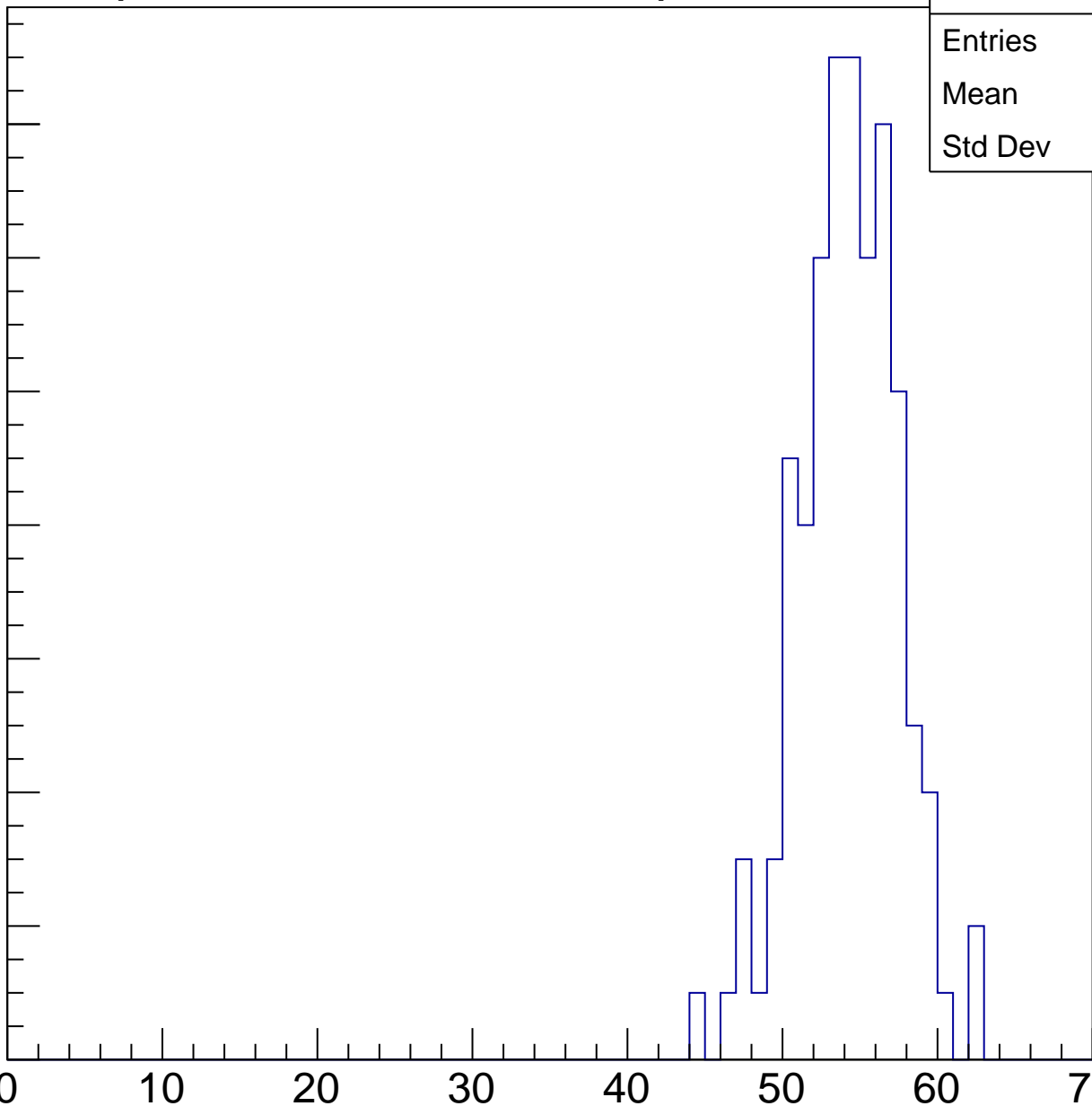
40

50

60

70

ampl



B0L101S, U20-ch82, adc5

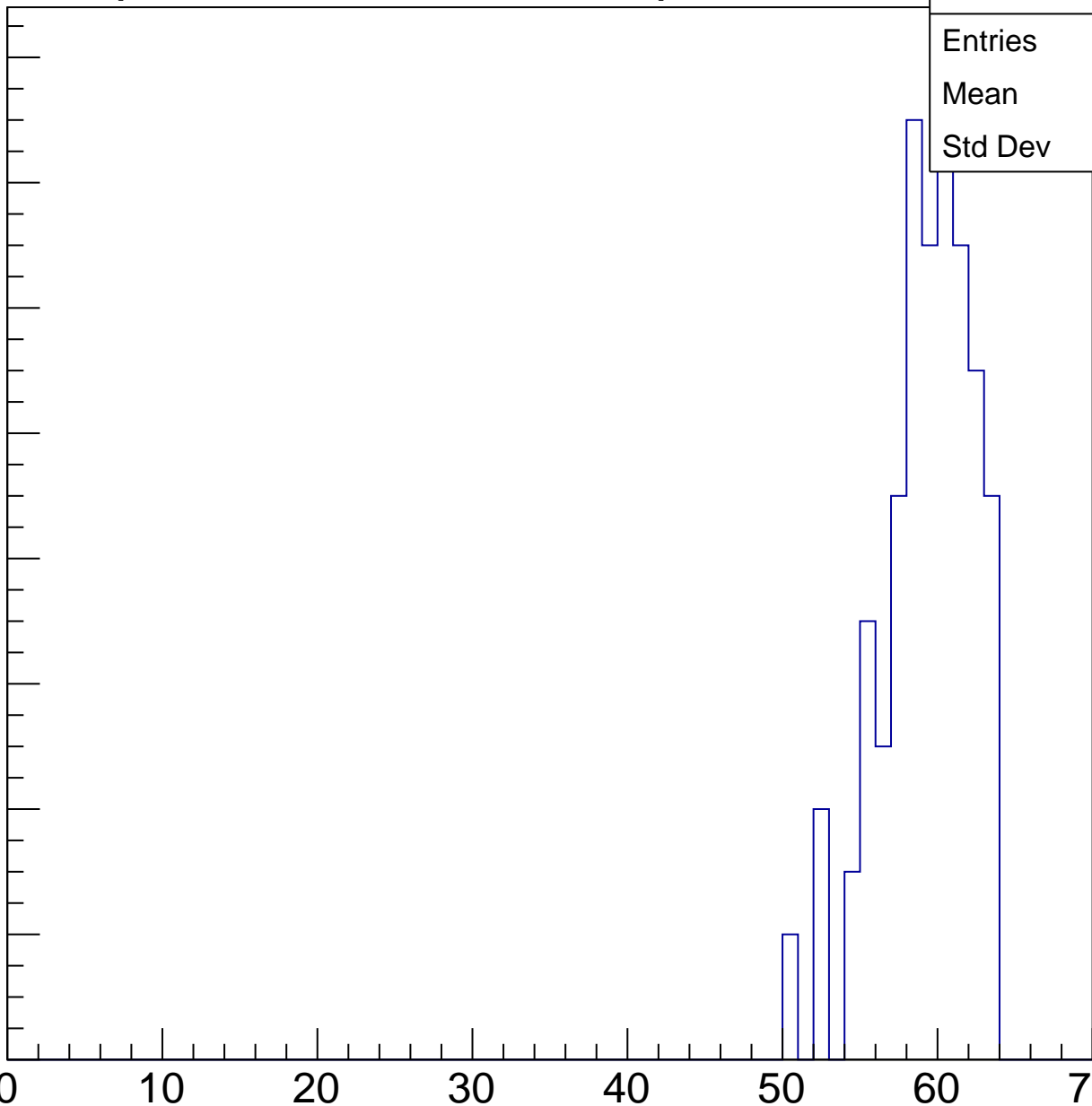
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.76
Std Dev	2.973

ampl



B0L101S, U20-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	33
Mean	57.91
Std Dev	14.76



B0L101S, U20-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch83, adc0

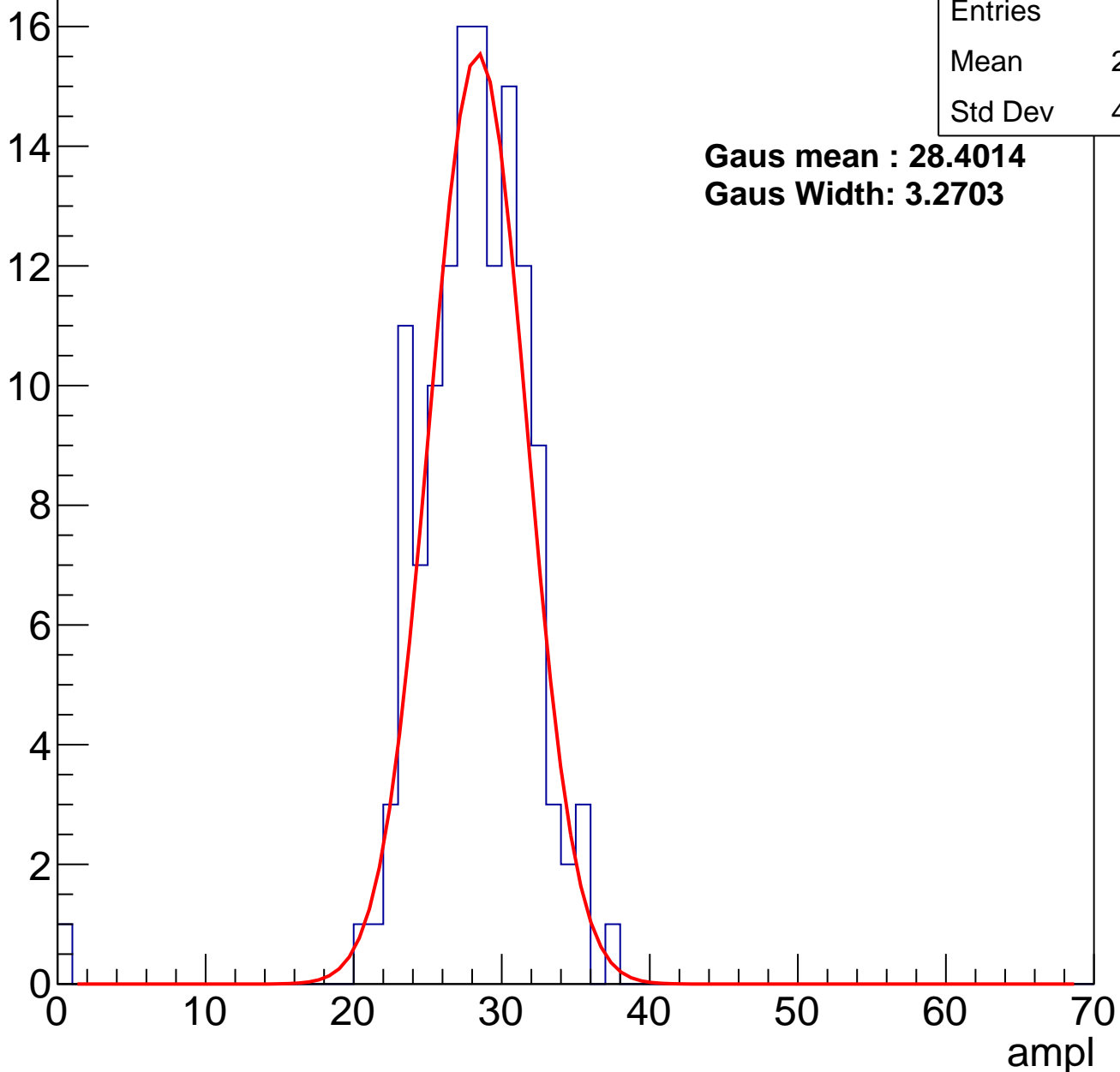
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	27.68
Std Dev	4.065

Gaus mean : 28.4014

Gaus Width: 3.2703

Entry



B0L101S, U20-ch83, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	34.84
Std Dev	4.457

Gaus mean : 35.6314

Gaus Width: 3.2598

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

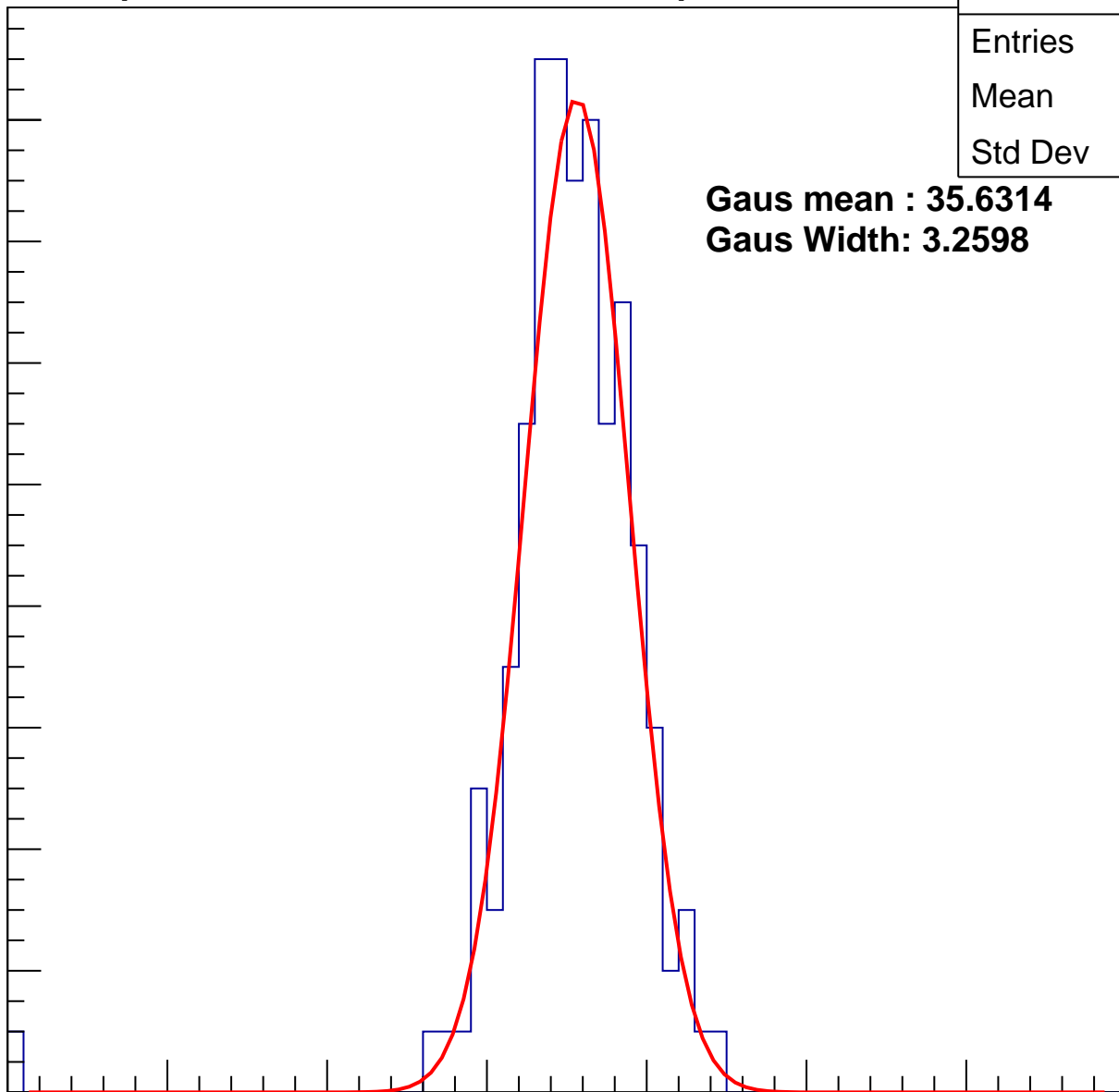
40

50

60

70

ampl



B0L101S, U20-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	41.63
Std Dev	3.624

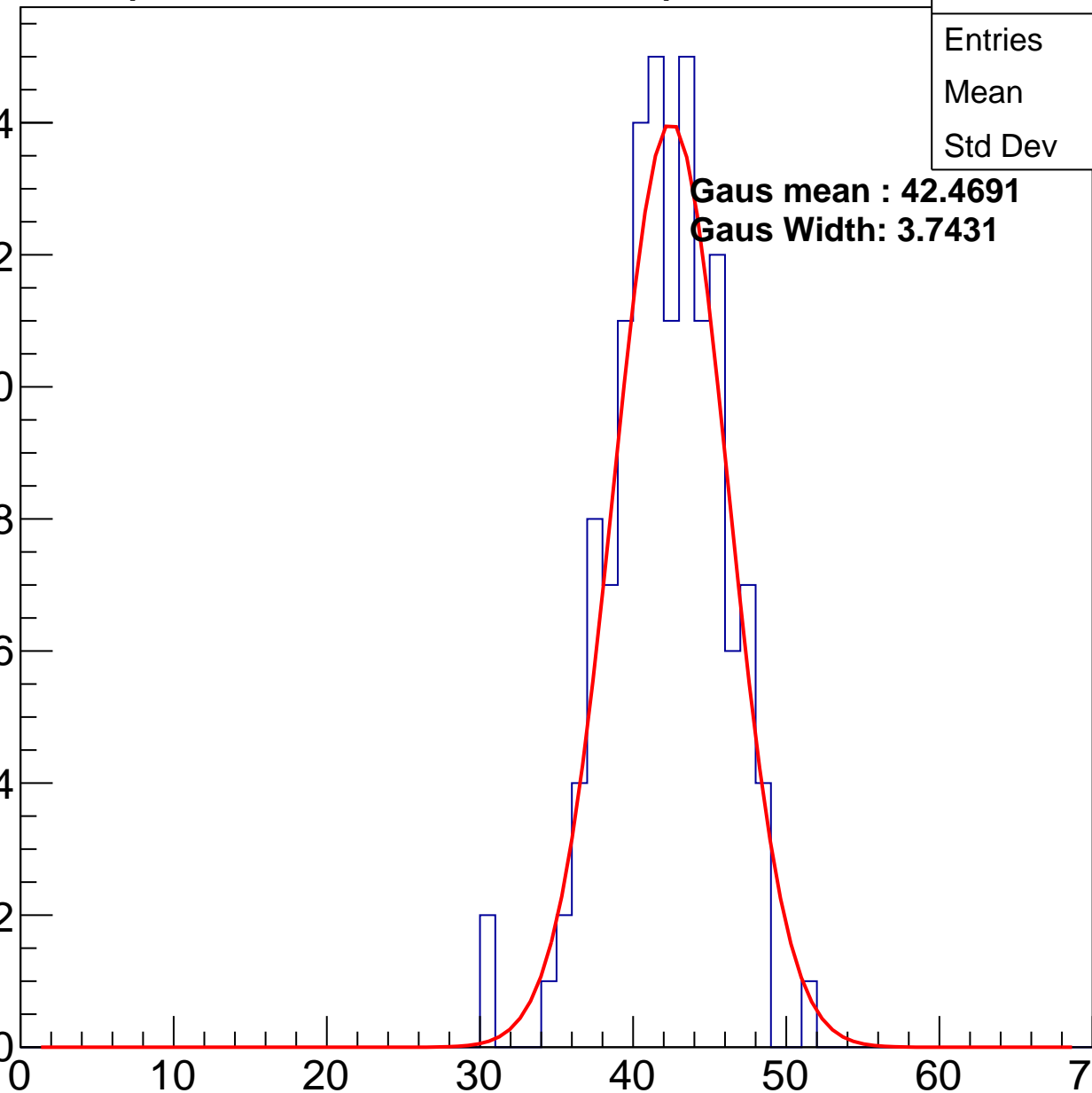
Gaus mean : 42.4691

Gaus Width: 3.7431

Entry

14
12
10
8
6
4
2
0

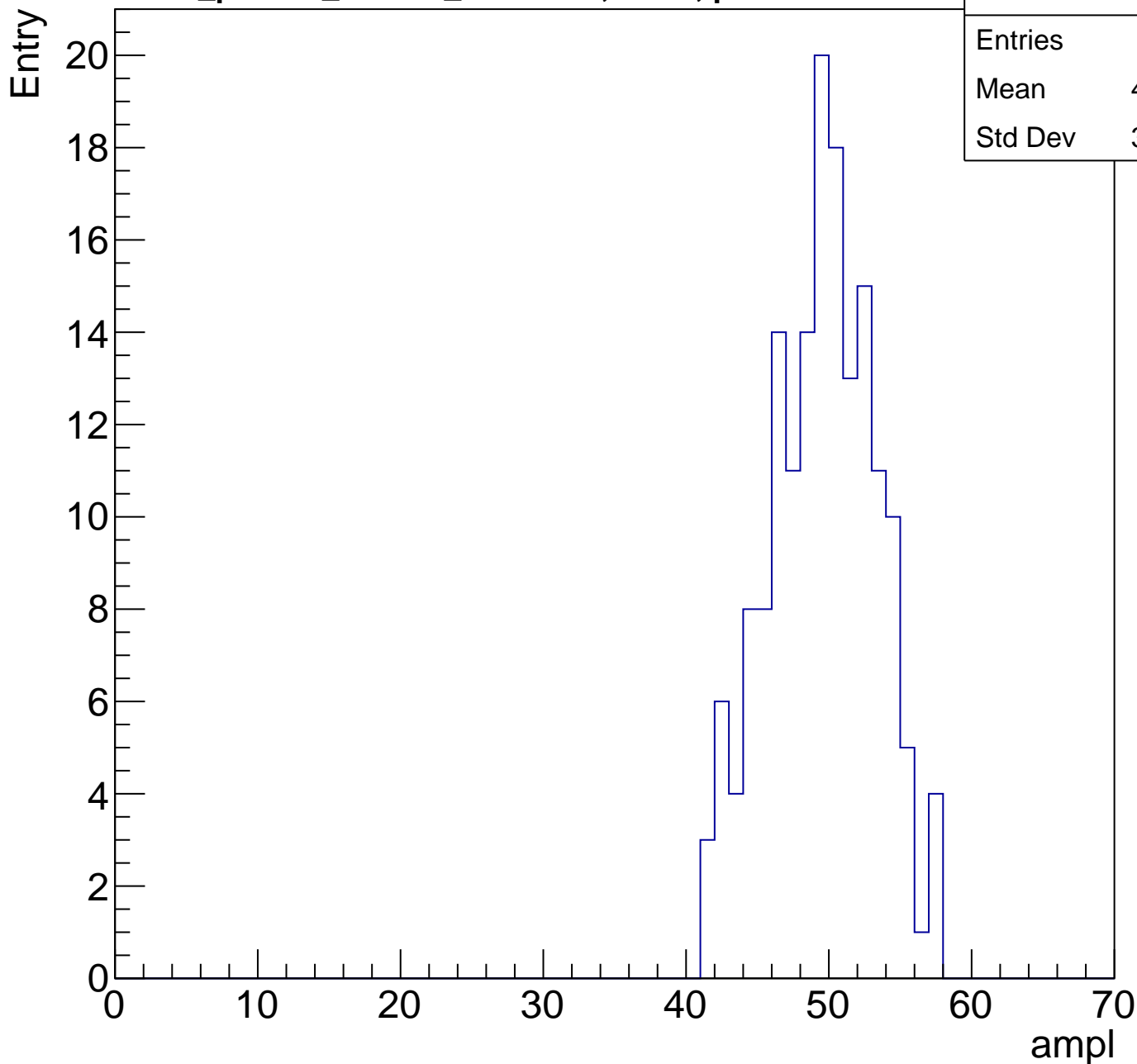
ampl



B0L101S, U20-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	49.07
Std Dev	3.699



B0L101S, U20-ch83, adc4

calib_packv5_042523_0143.root, FC#1, port C1

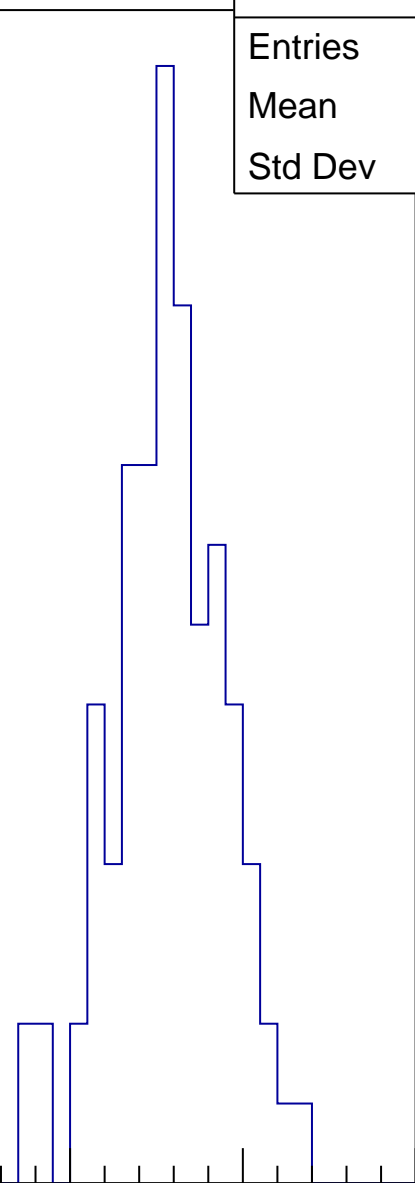
Entries	88
Mean	55.19
Std Dev	3.271

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch83, adc5

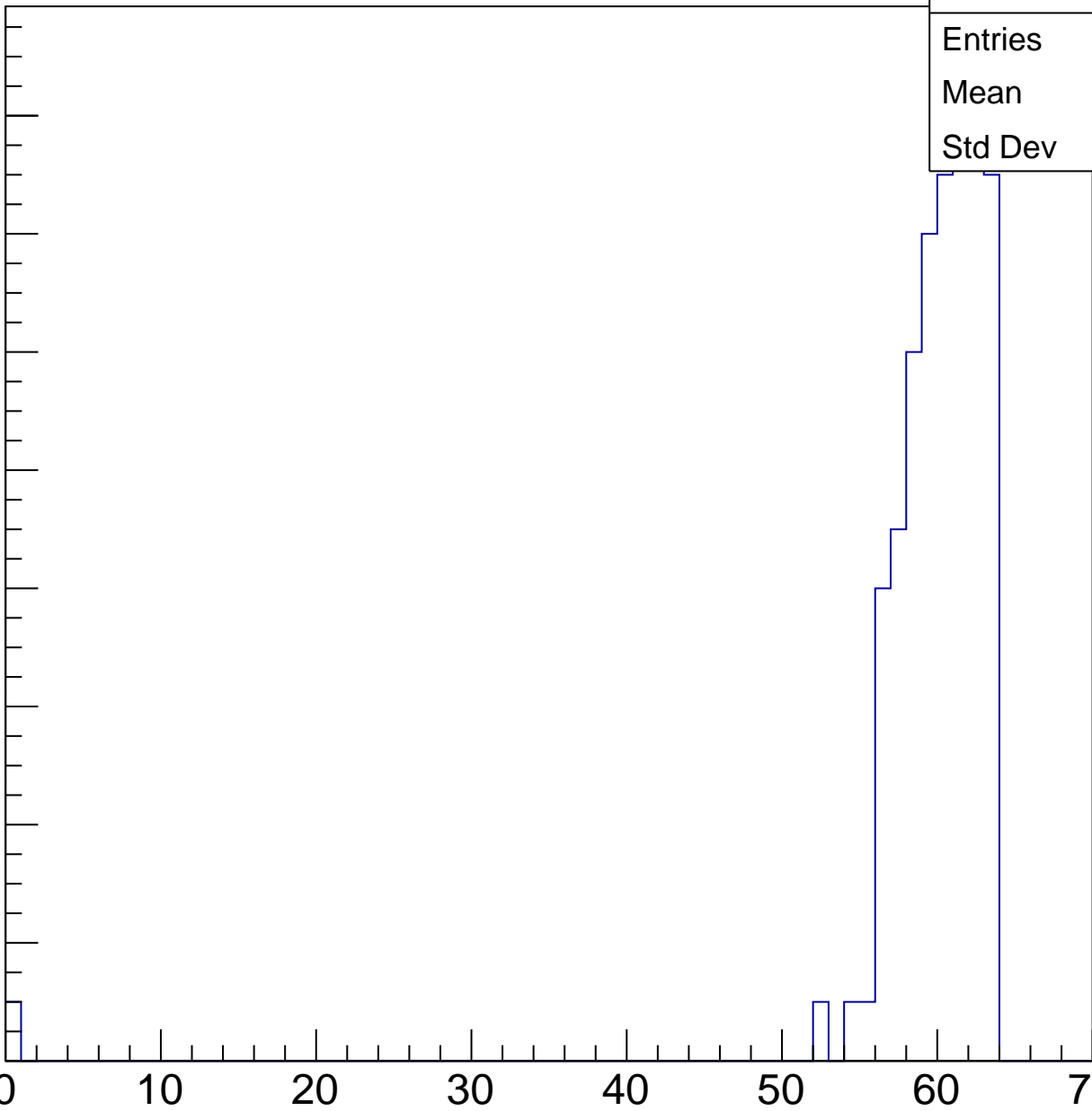
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	59.28
Std Dev	6.121

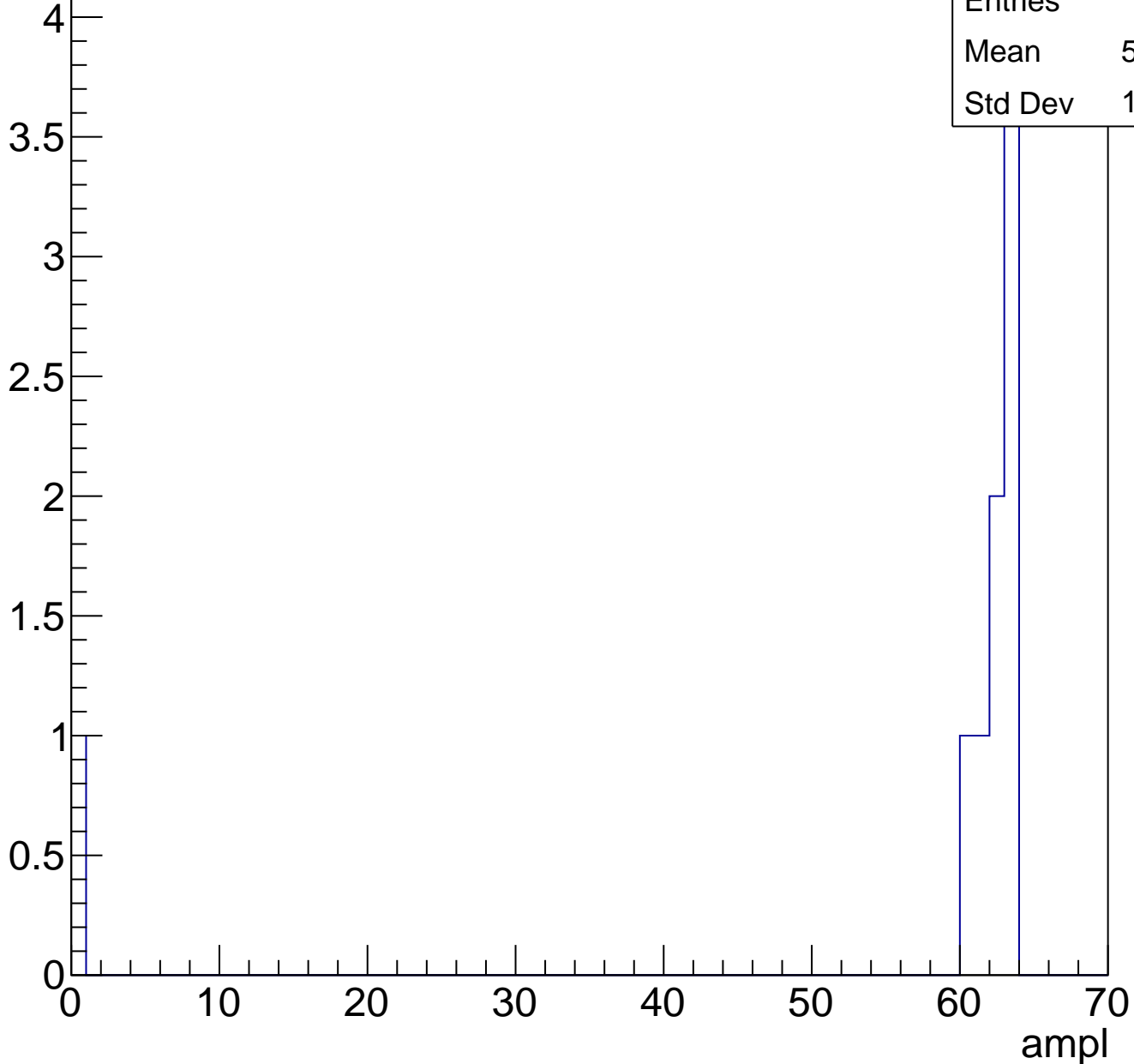
ampl



B0L101S, U20-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	28.39
Std Dev	3.776

Gaus mean : 28.5809

Gaus Width: 4.2524

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U20-ch84, adc1

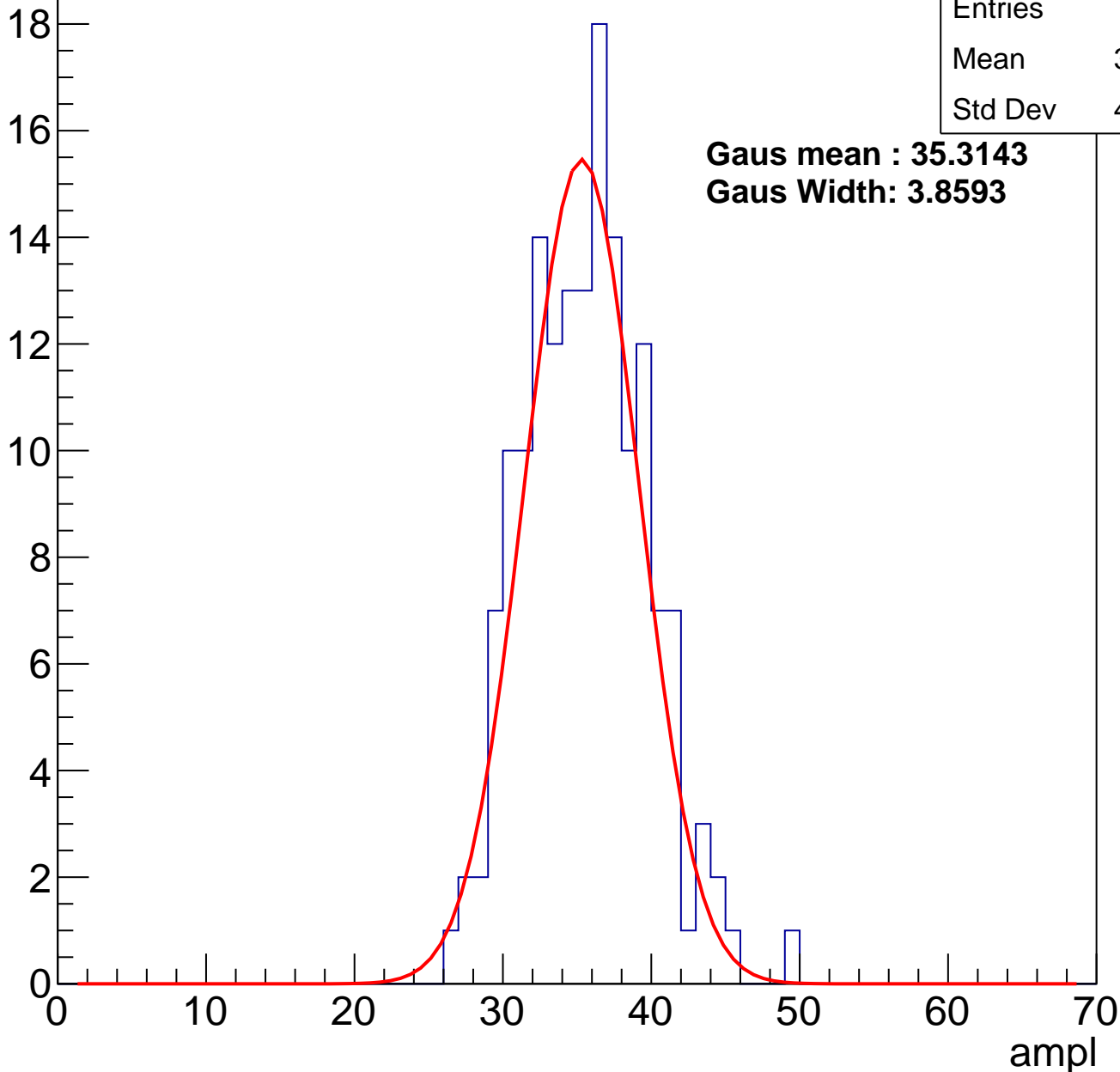
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	35.15
Std Dev	4.058

Gaus mean : 35.3143

Gaus Width: 3.8593

Entry



B0L101S, U20-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

132

Mean

42.07

Std Dev

3.656

Gaus mean : 42.6569

Gaus Width: 3.5594

0

10

20

30

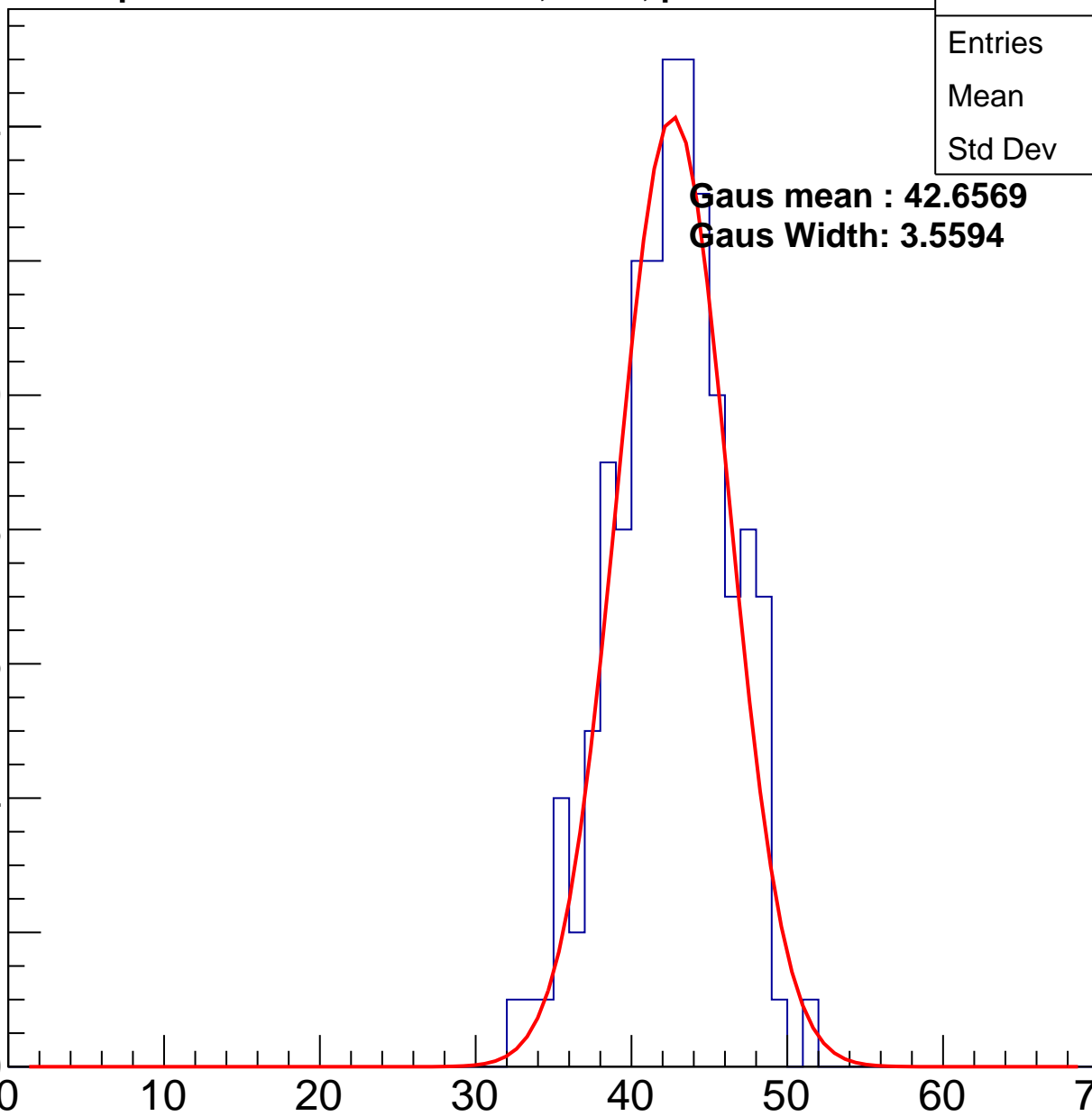
40

50

60

70

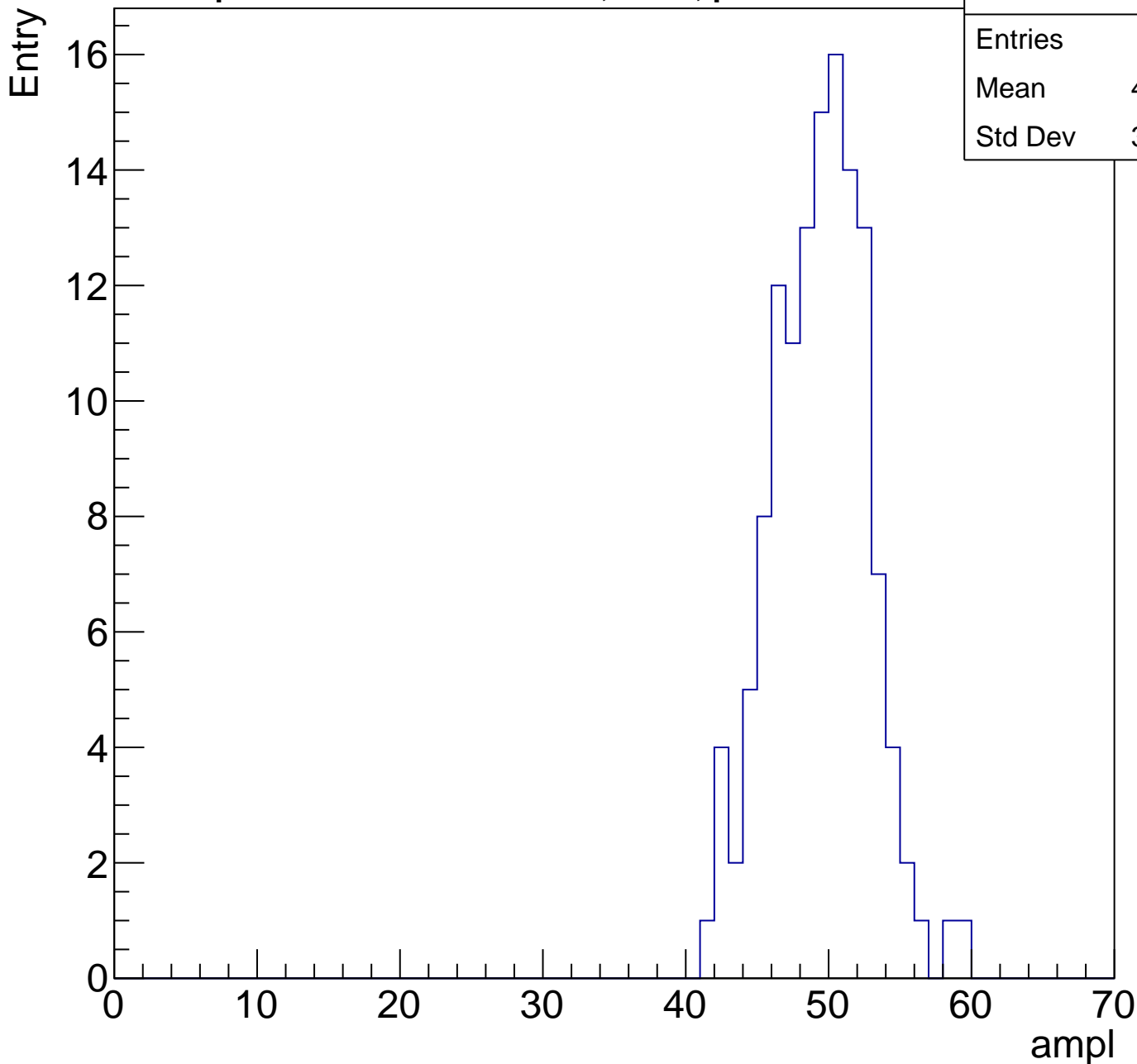
ampl



B0L101S, U20-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	48.95
Std Dev	3.343



B0L101S, U20-ch84, adc4

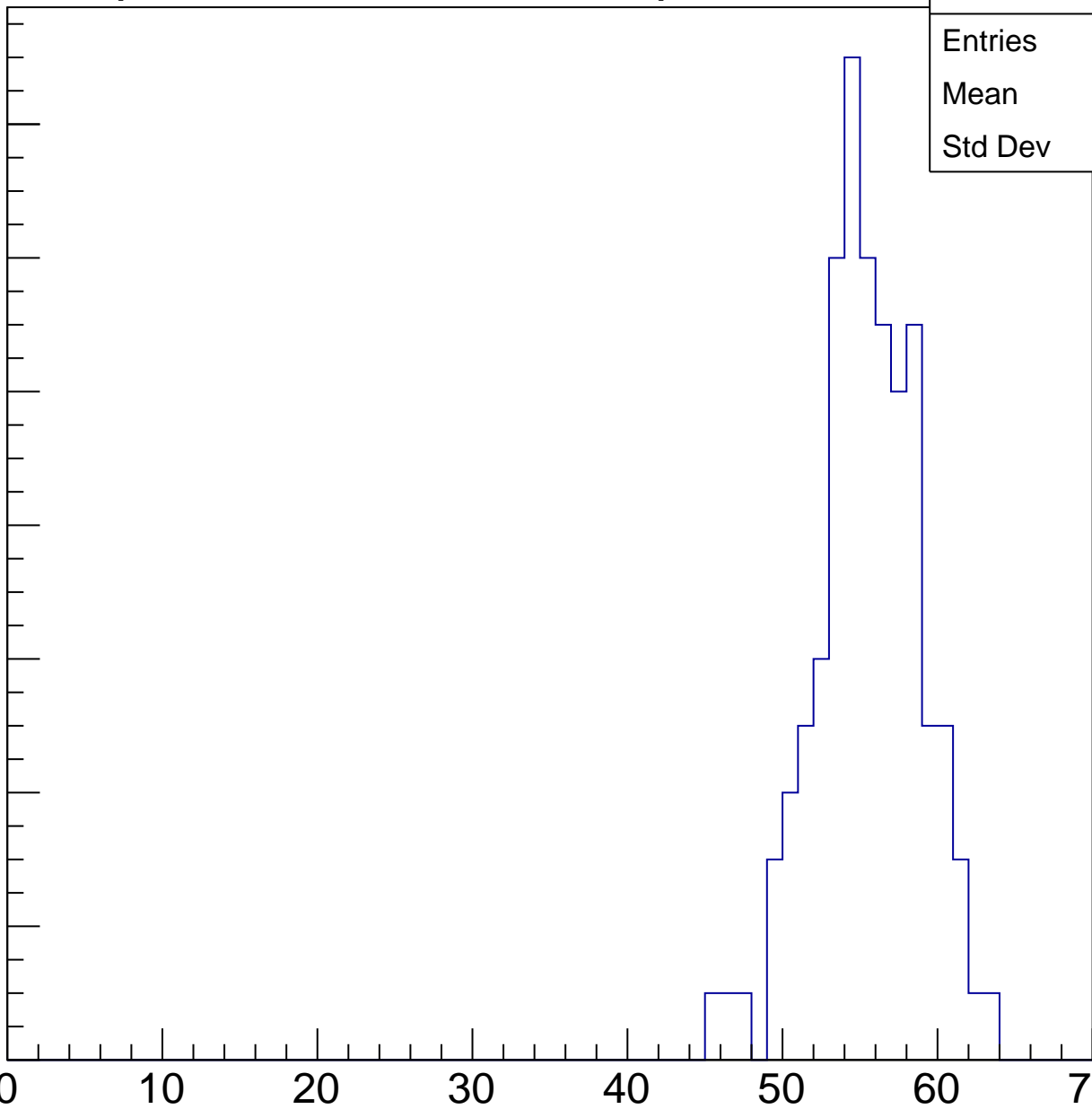
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	55
Std Dev	3.385

ampl



B0L101S, U20-ch84, adc5

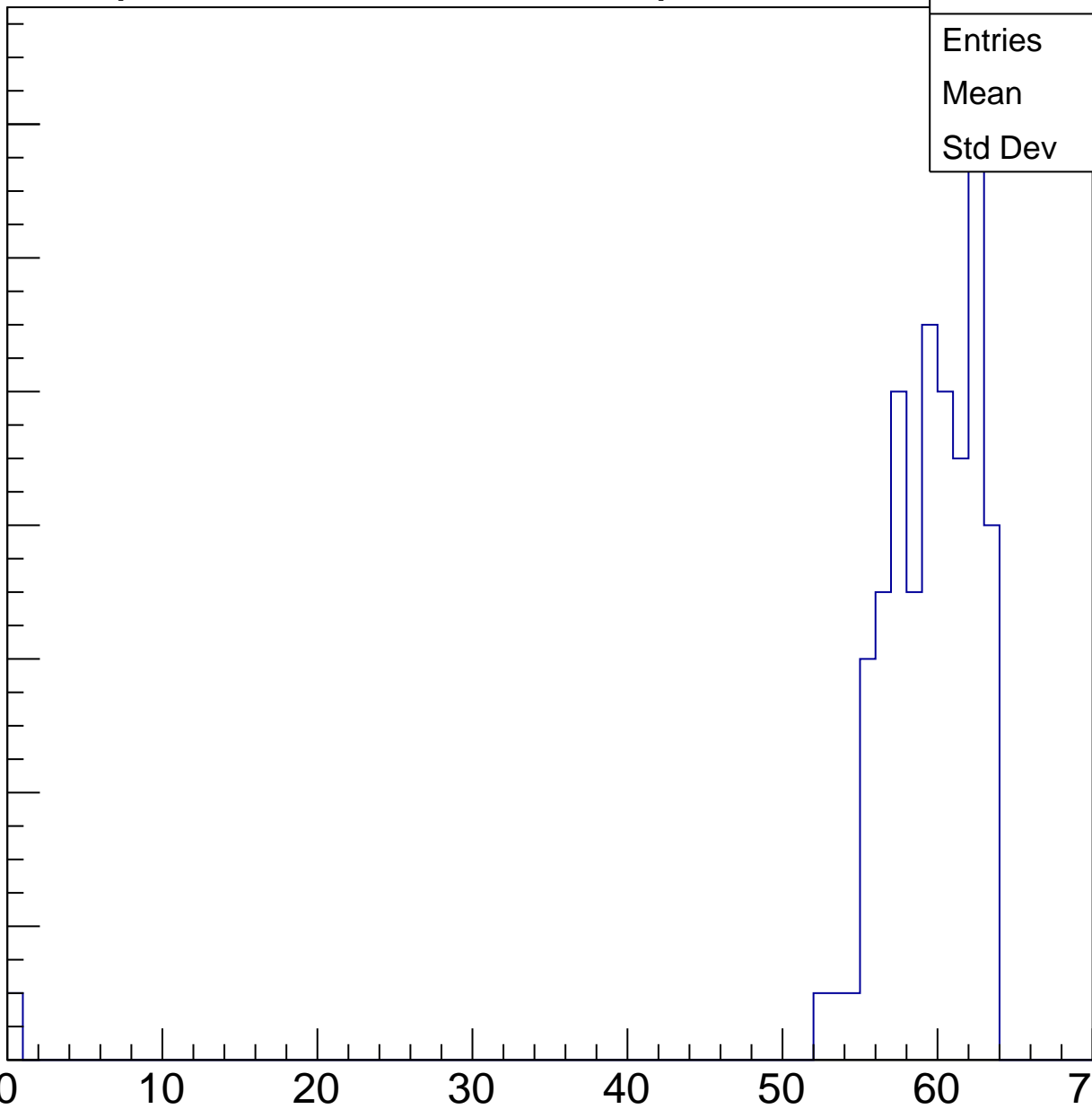
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	58.49
Std Dev	6.851

ampl

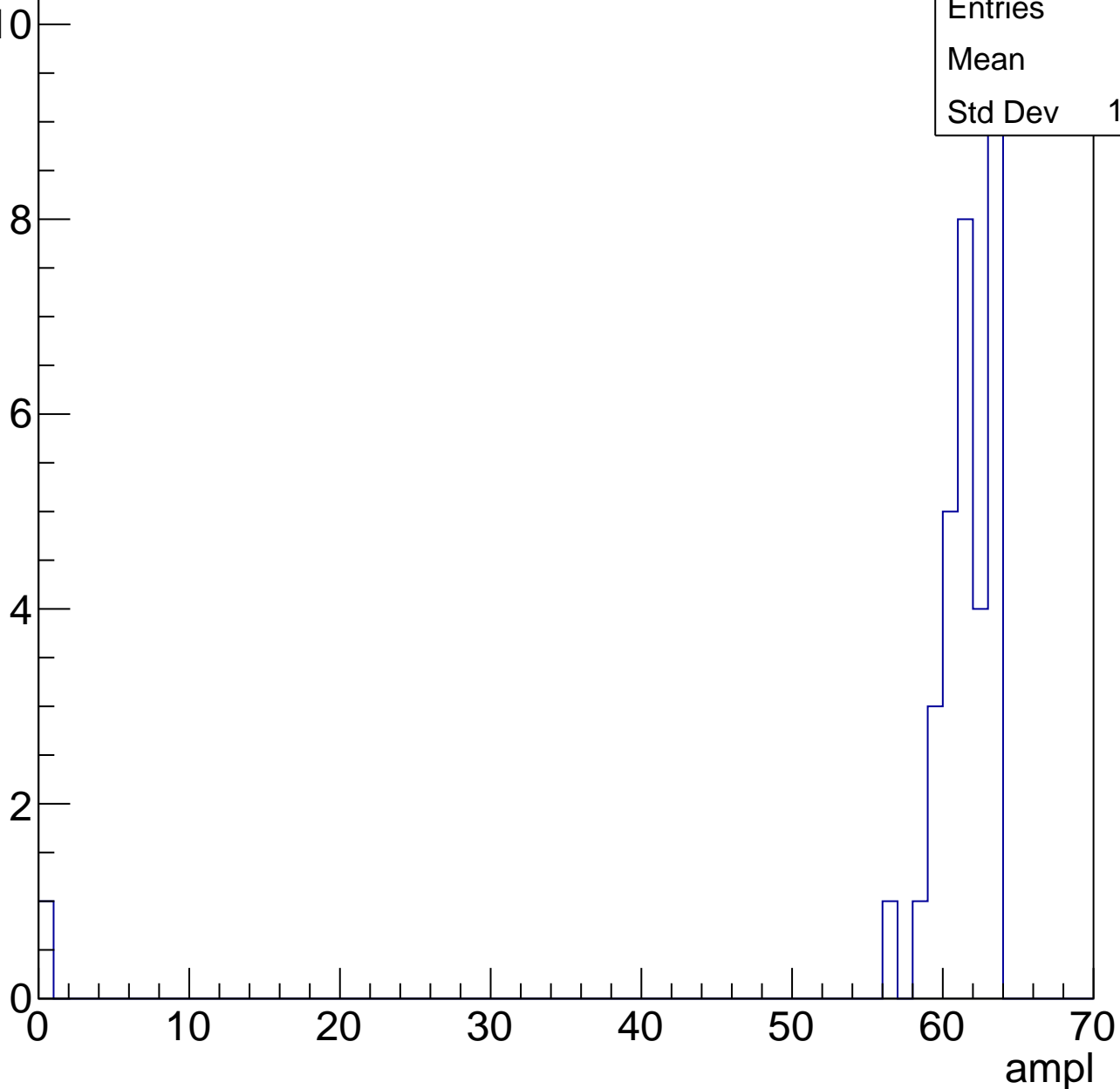


B0L101S, U20-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	33
Mean	59.3
Std Dev	10.62



B0L101S, U20-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch85, adc0

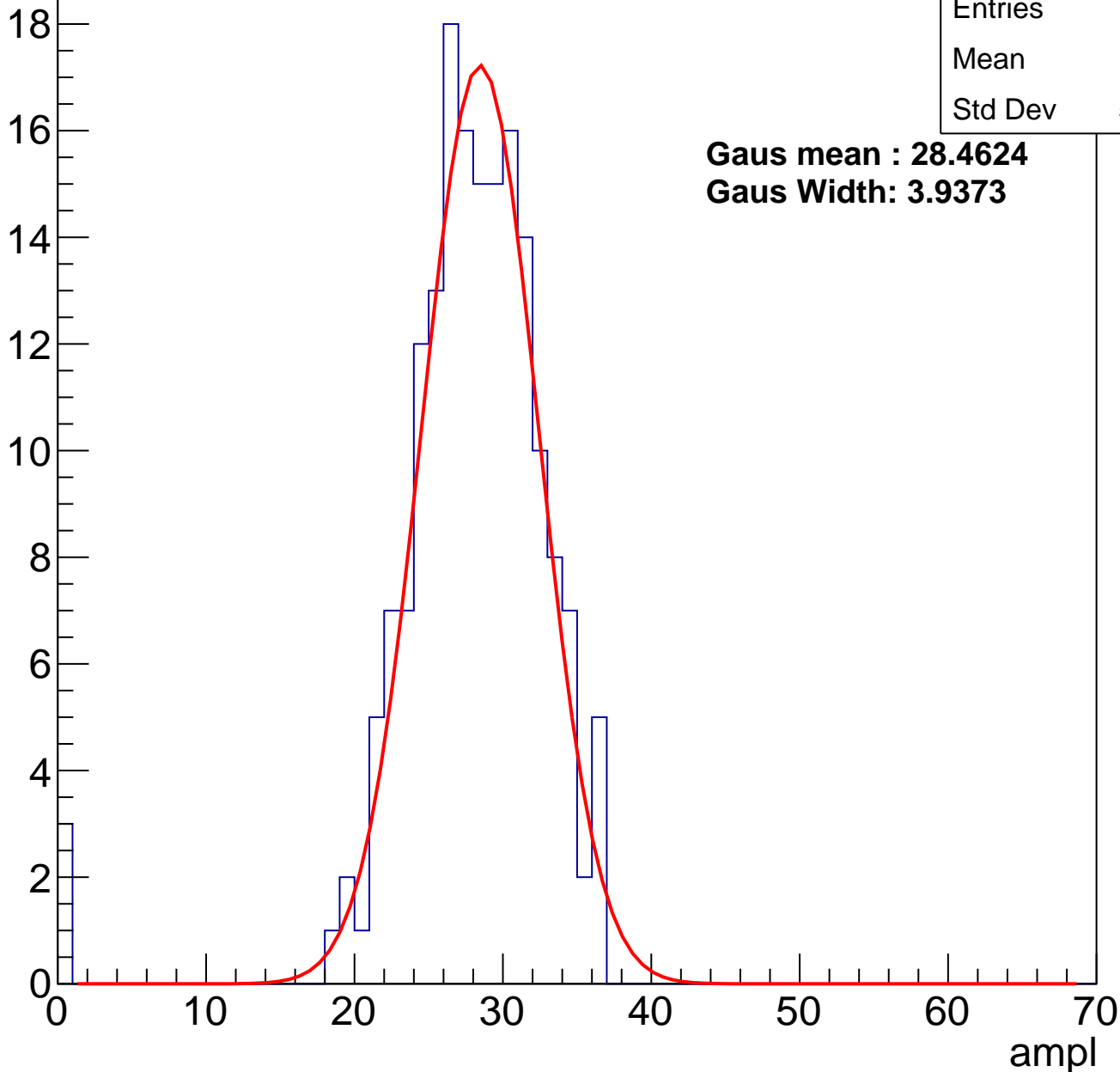
calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	27.4
Std Dev	5.271

Gaus mean : 28.4624

Gaus Width: 3.9373

Entry



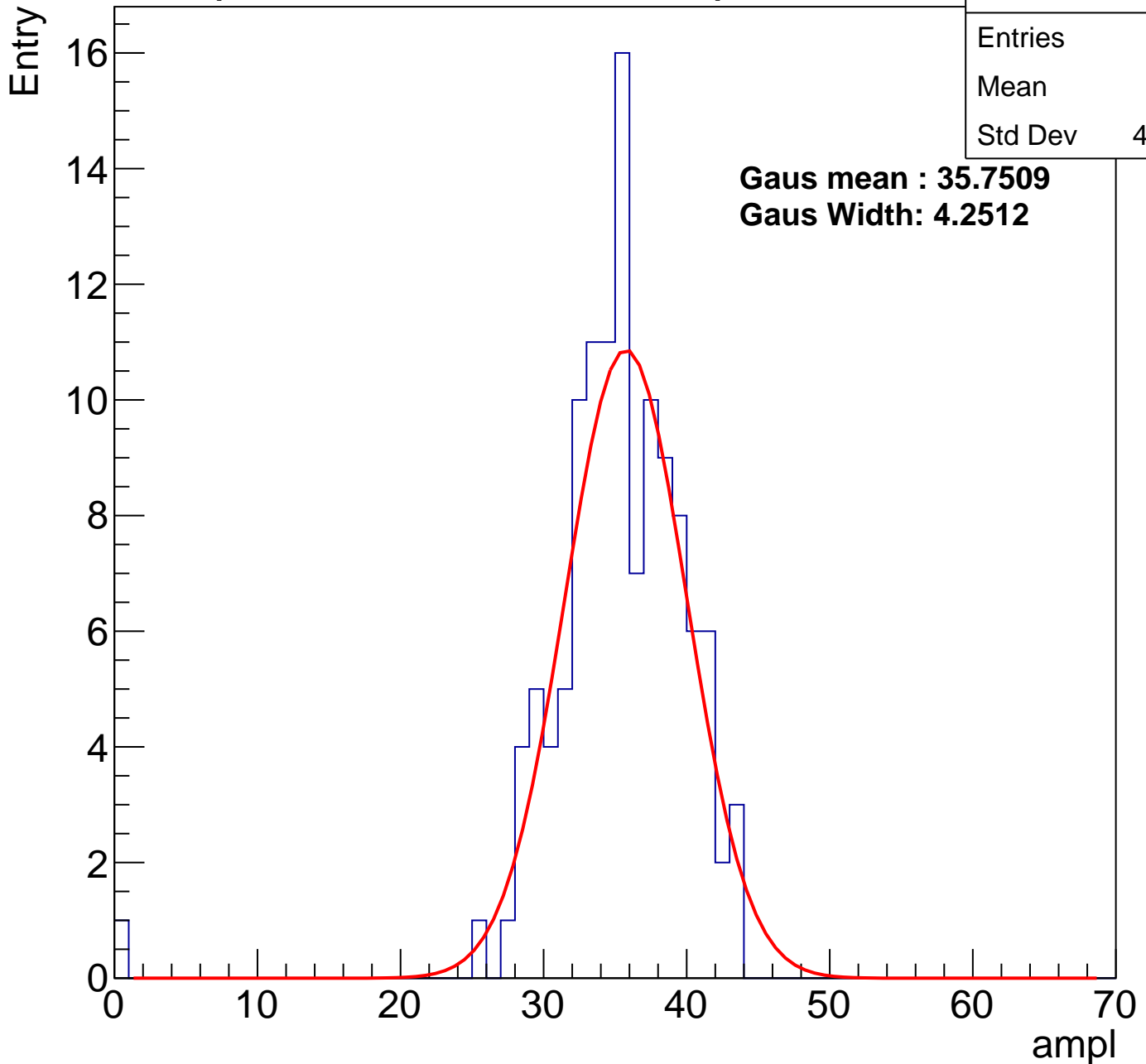
B0L101S, U20-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	34.8
Std Dev	4.998

Gaus mean : 35.7509

Gaus Width: 4.2512



B0L101S, U20-ch85, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	41.76
Std Dev	3.829

Gaus mean : 42.5285

Gaus Width: 4.0541

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

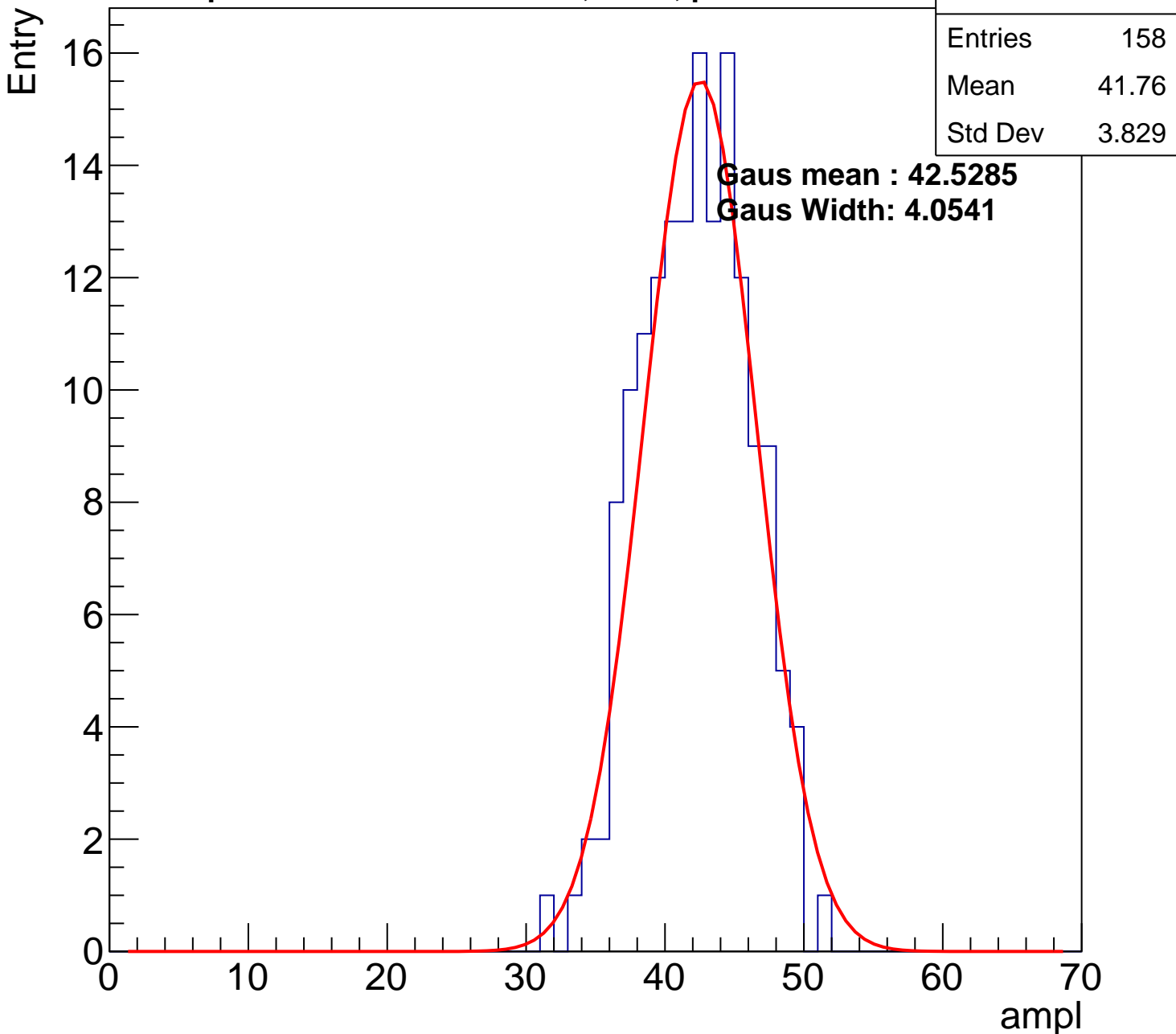
40

50

60

70

ampl



B0L101S, U20-ch85, adc3

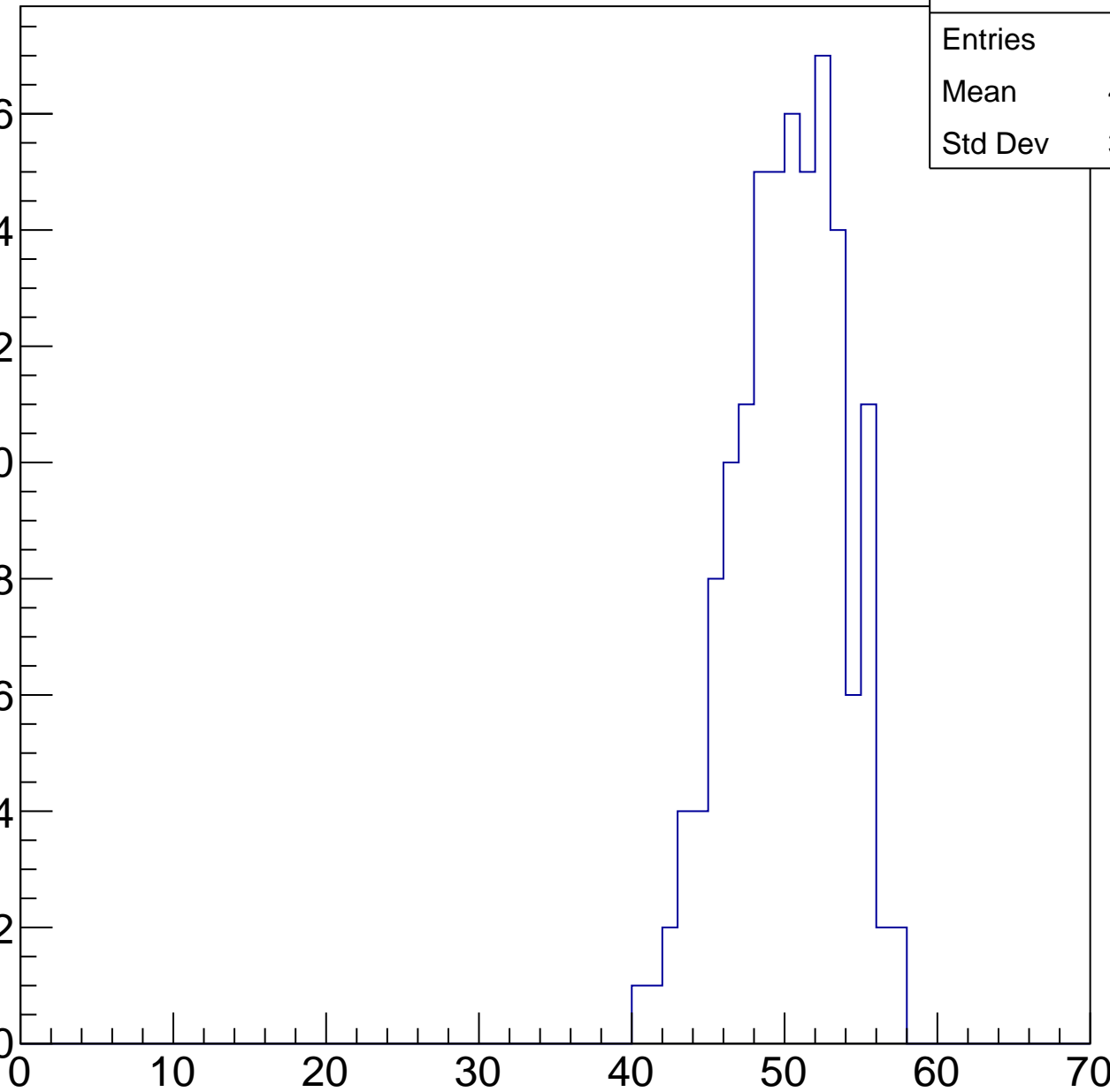
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	49.68
Std Dev	3.529

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch85, adc4

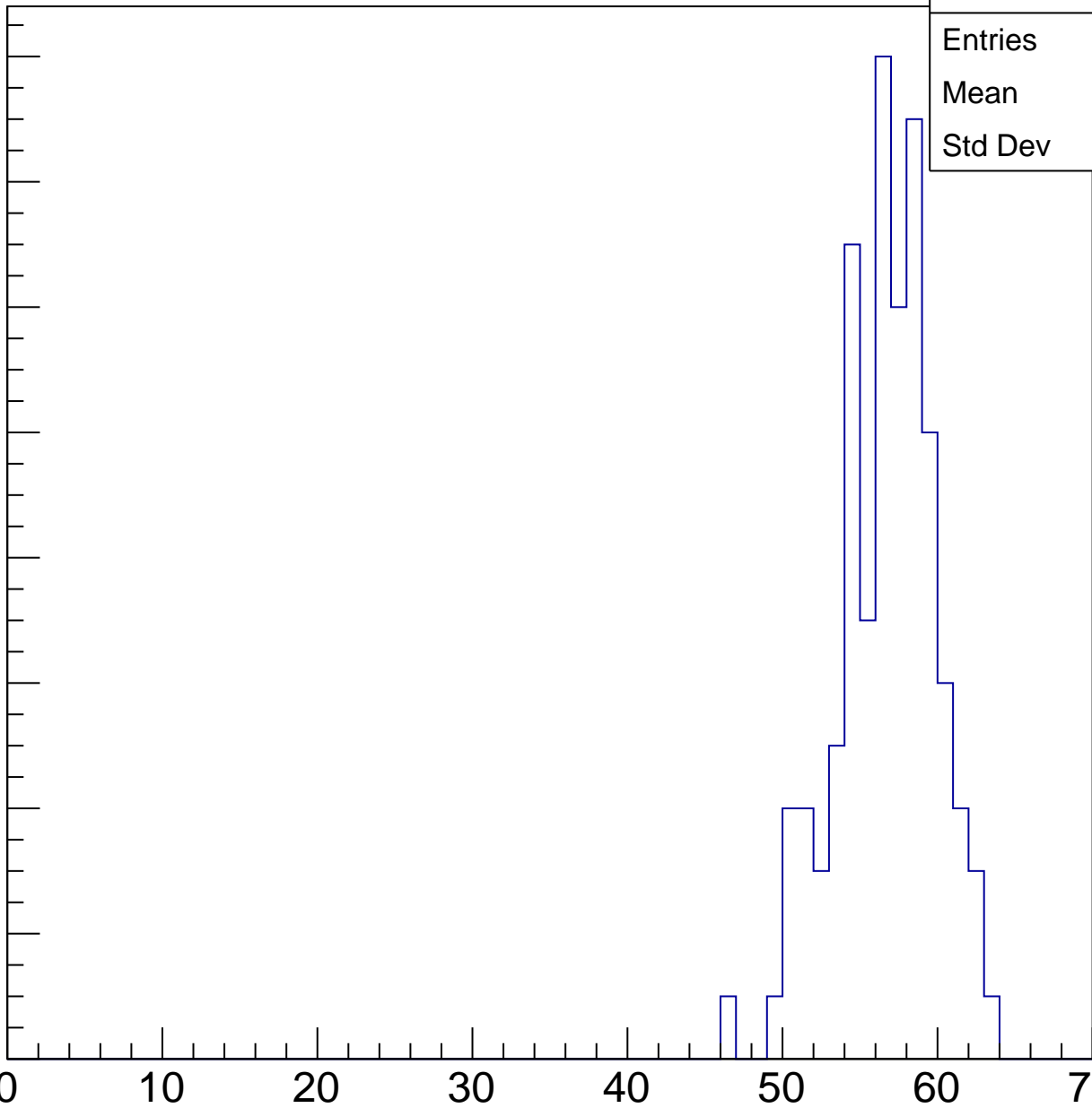
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	56.19
Std Dev	3.172

ampl



B0L101S, U20-ch85, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

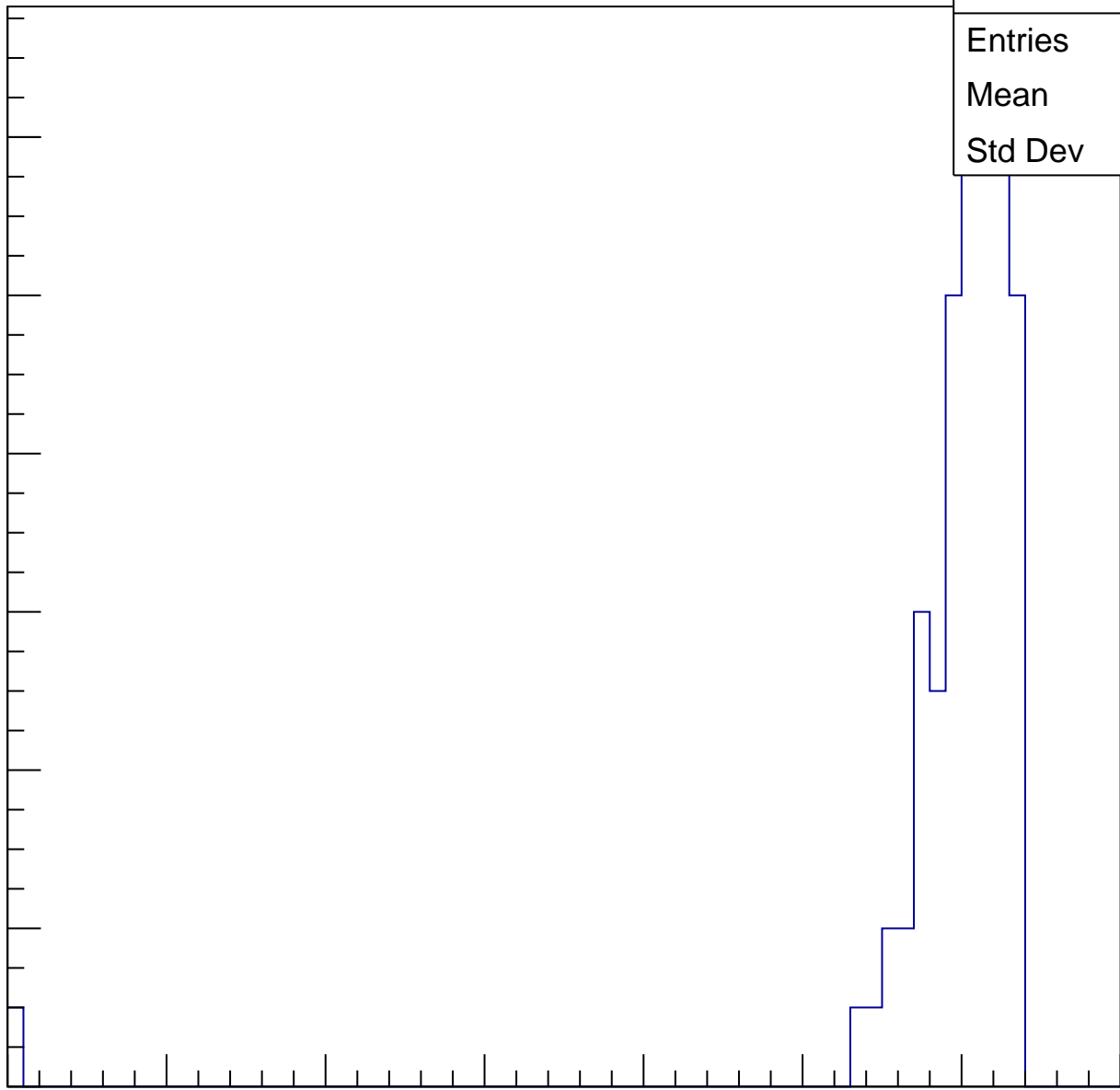
2

0

Entries	76
Mean	59.18
Std Dev	7.211

ampl

0 10 20 30 40 50 60 70

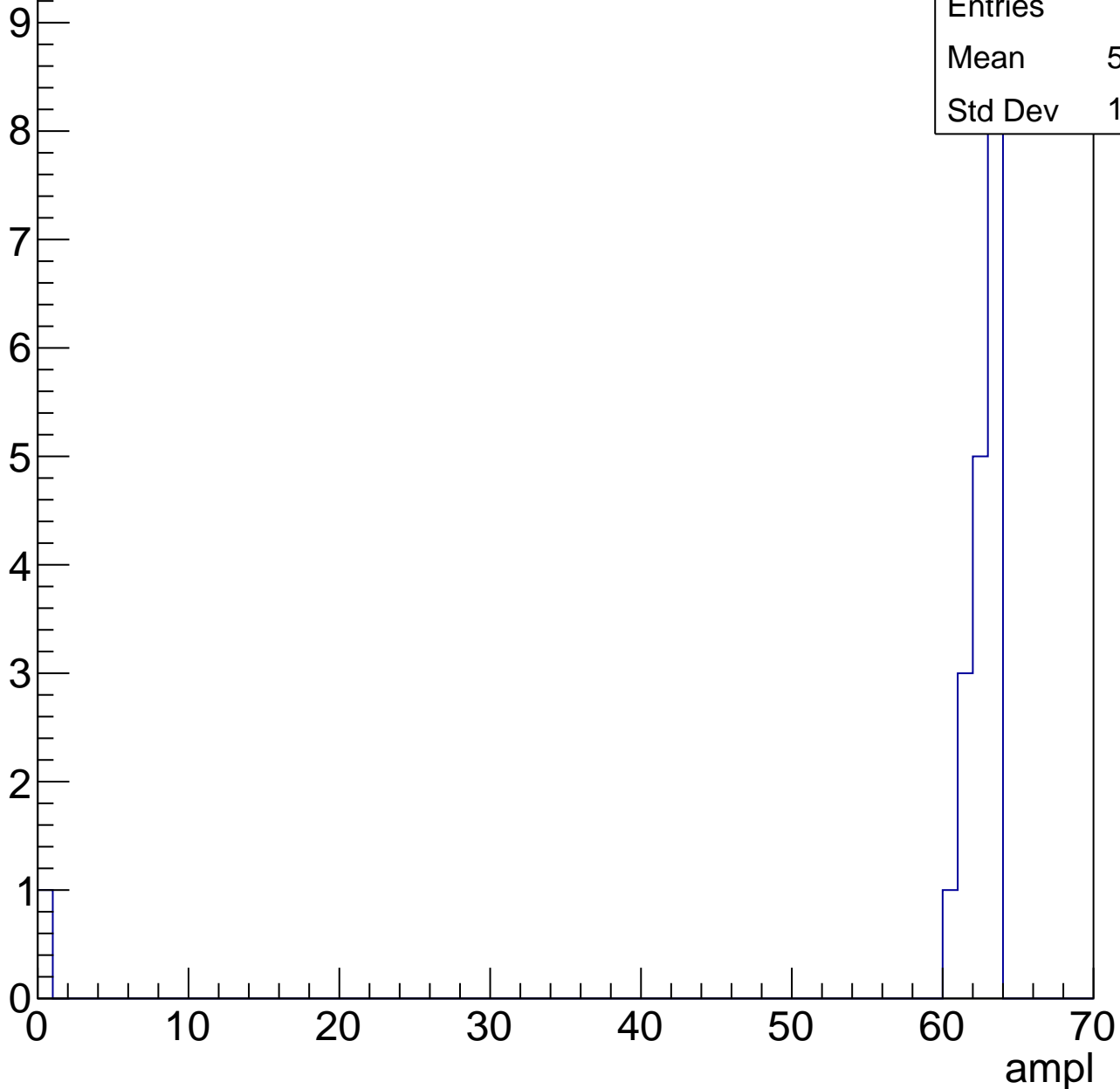


B0L101S, U20-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	58.95
Std Dev	13.92



B0L101S, U20-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch86, adc0

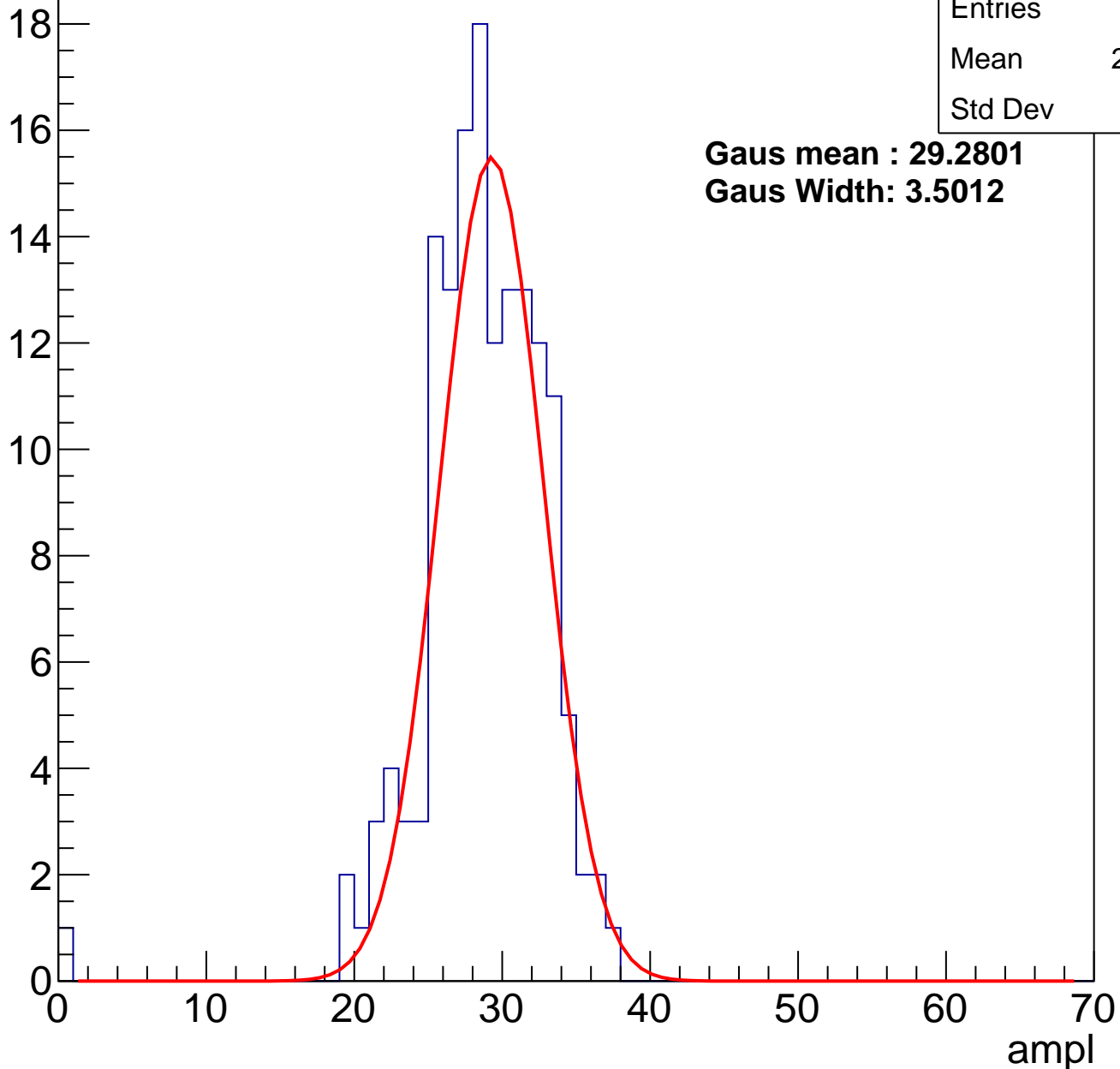
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	28.26
Std Dev	4.29

Gaus mean : 29.2801

Gaus Width: 3.5012

Entry



B0L101S, U20-ch86, adc1

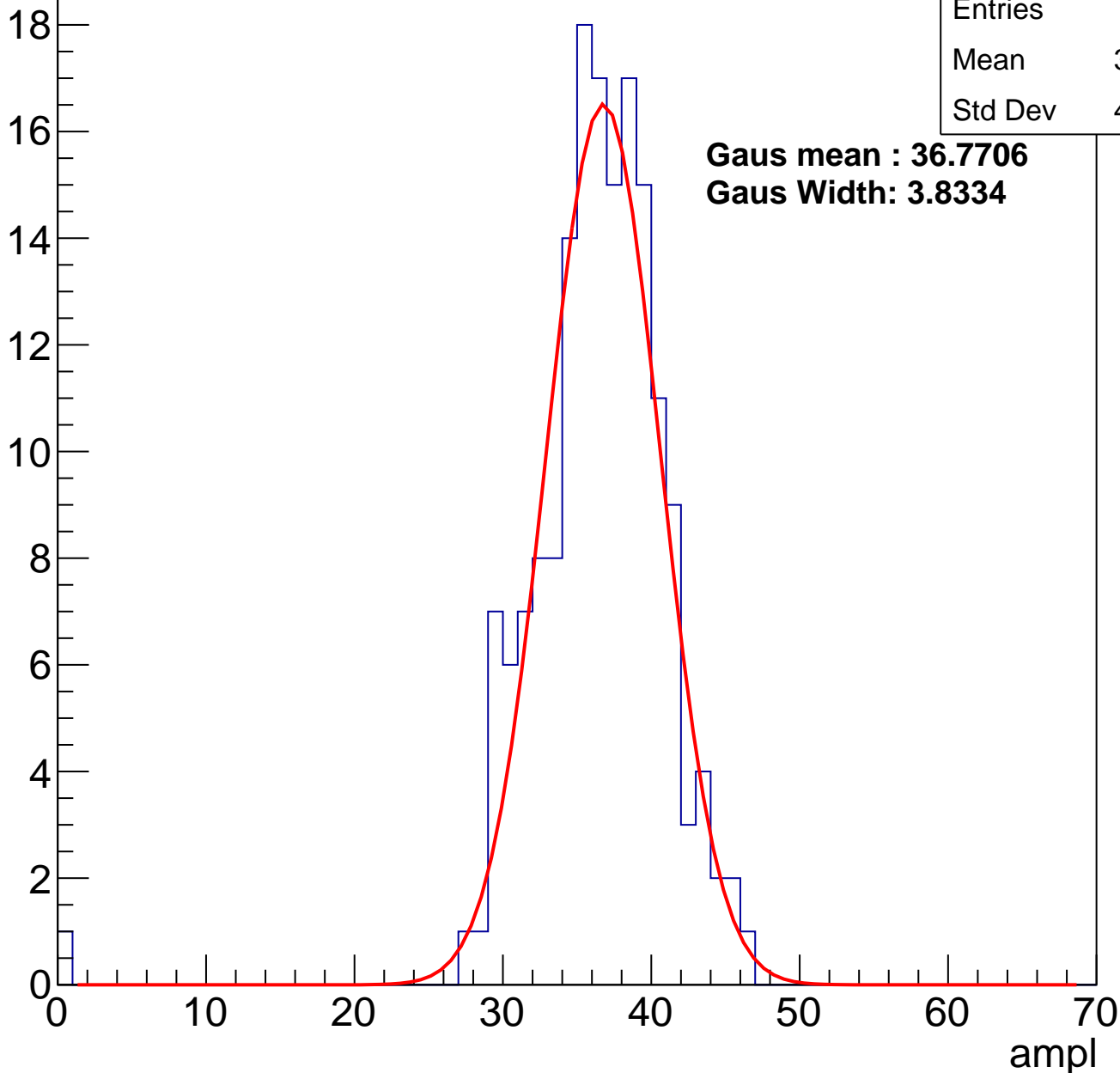
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	35.99
Std Dev	4.746

Gaus mean : 36.7706

Gaus Width: 3.8334

Entry



B0L101S, U20-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

125

Mean

43.5

Std Dev

3.643

Gaus mean : 43.9269

Gaus Width: 3.5141

0

10

20

30

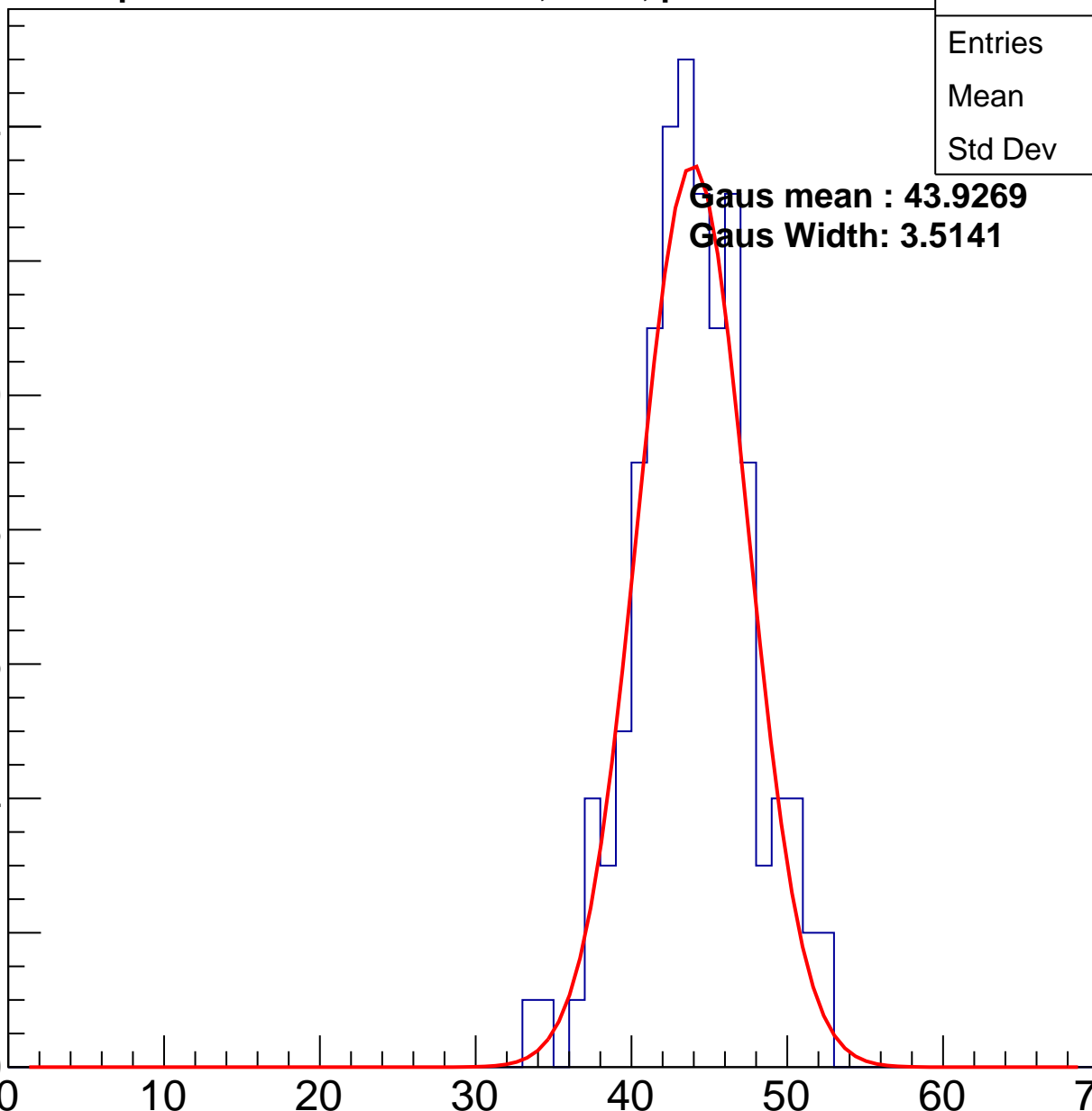
40

50

60

70

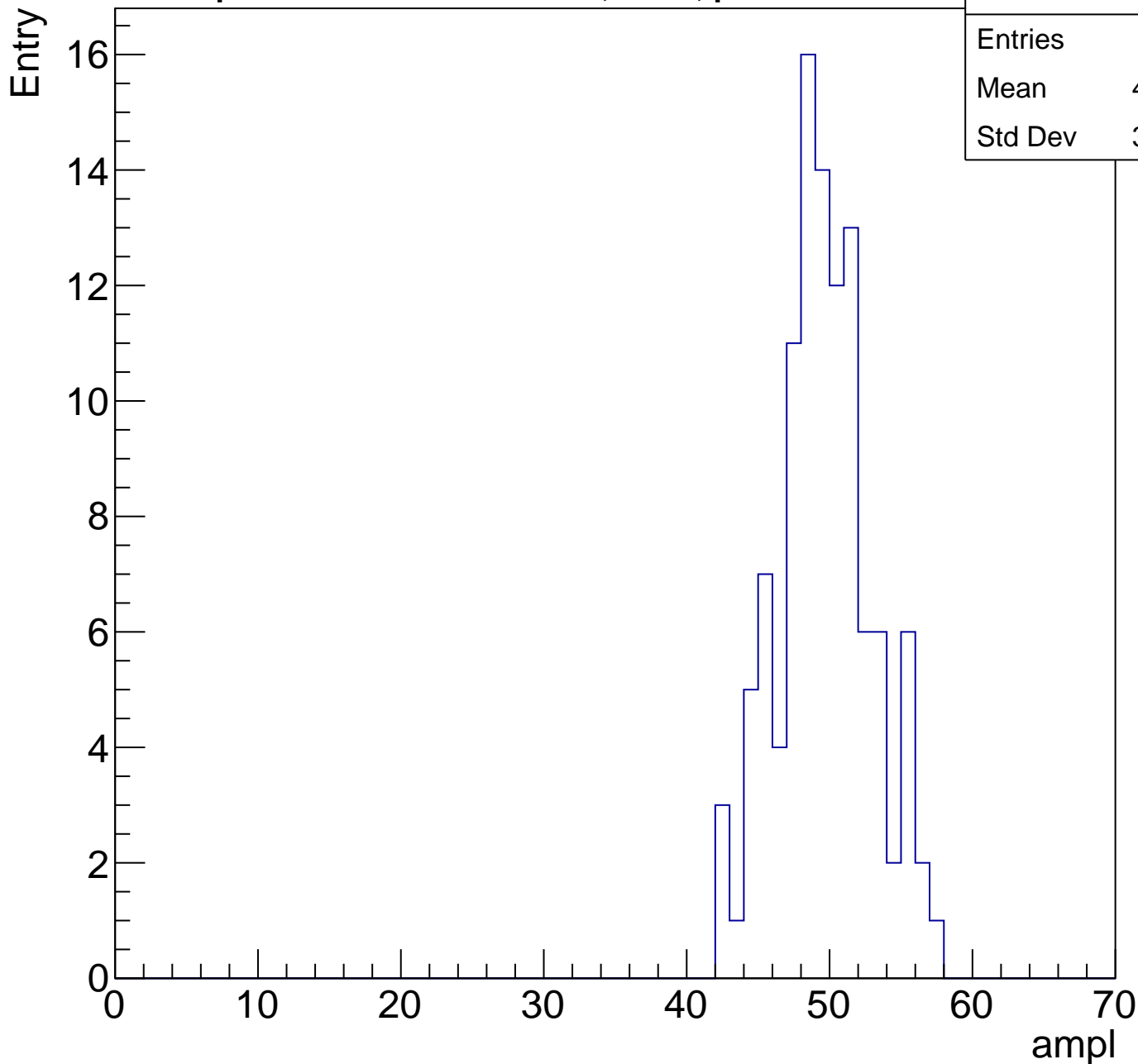
ampl



B0L101S, U20-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

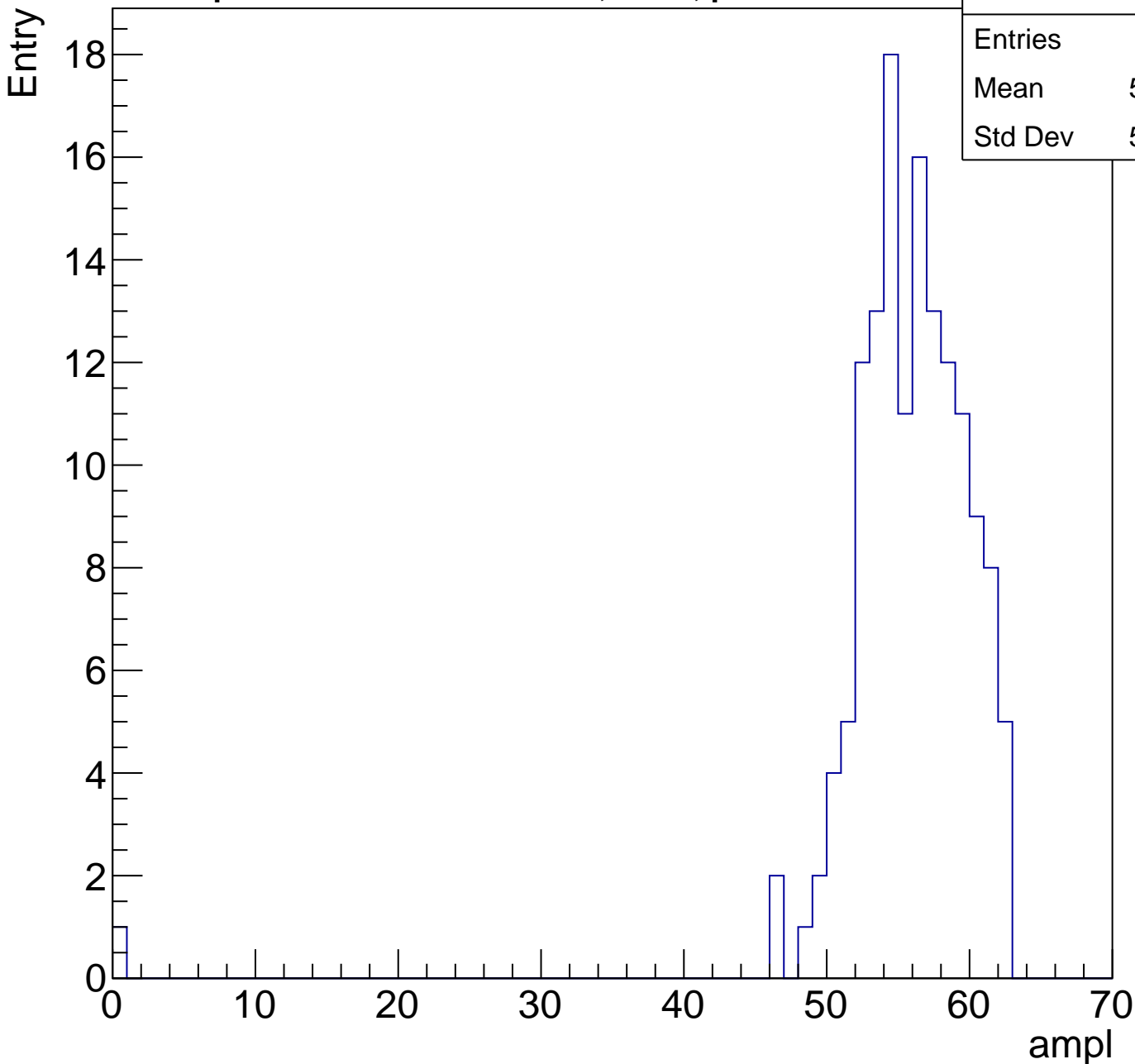
Entries	109
Mean	49.17
Std Dev	3.255



B0L101S, U20-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	55.27
Std Dev	5.773



B0L101S, U20-ch86, adc5

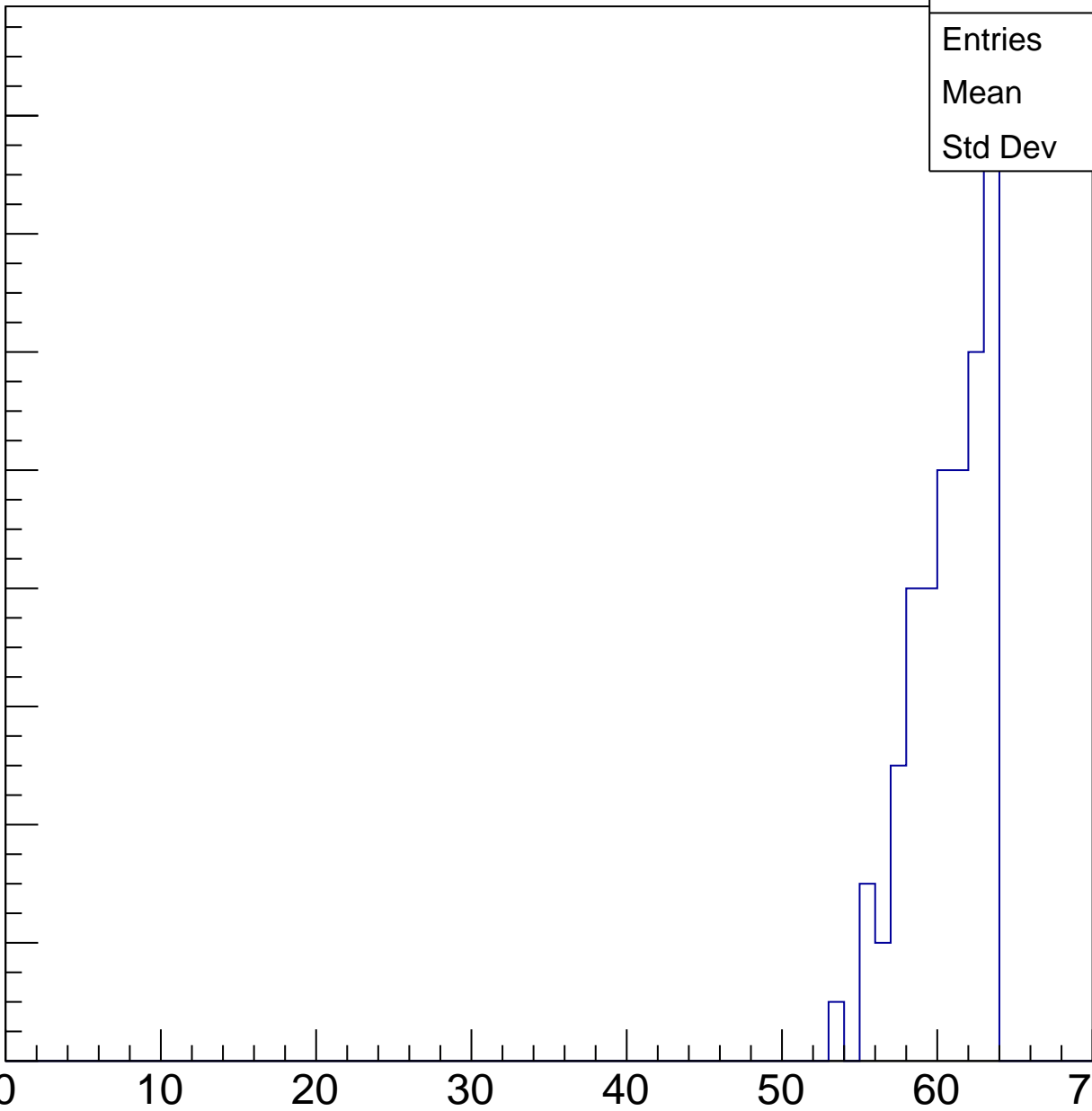
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	60.21
Std Dev	2.43

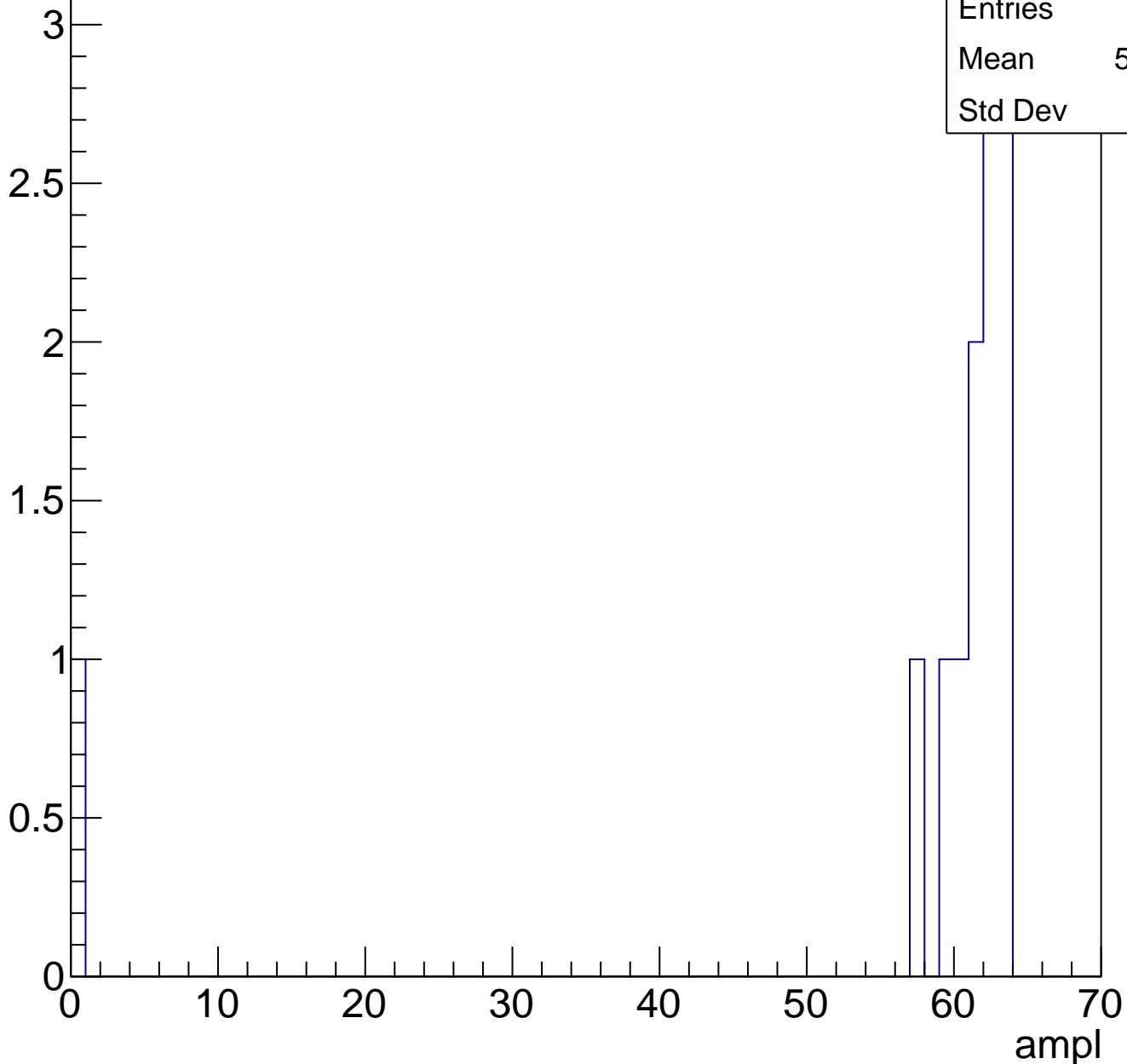
ampl



B0L101S, U20-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch87, adc0

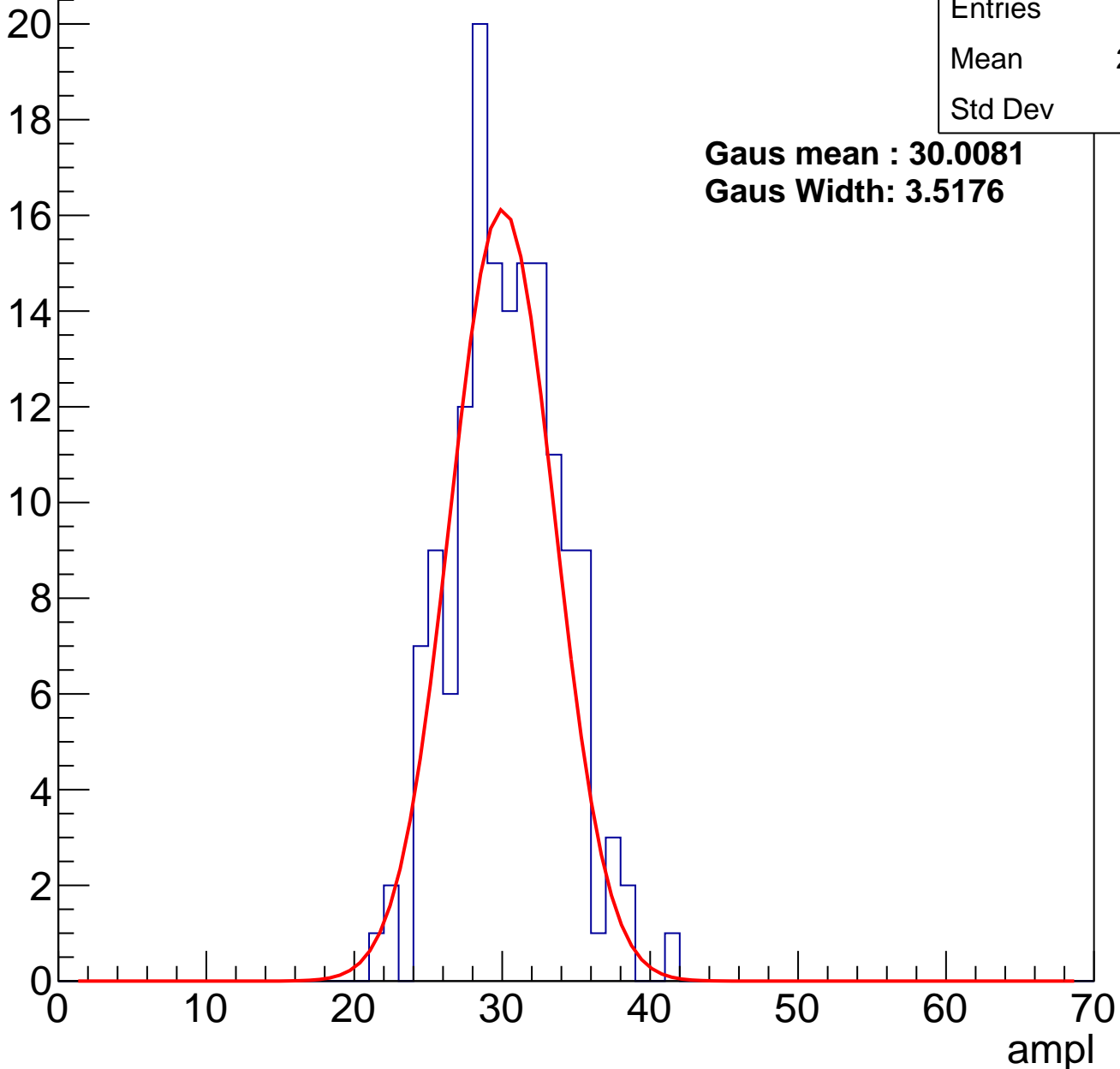
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.91
Std Dev	3.59

Gaus mean : 30.0081

Gaus Width: 3.5176

Entry



B0L101S, U20-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	36.19
Std Dev	4.581

Gaus mean : 37.0589

Gaus Width: 3.3343

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

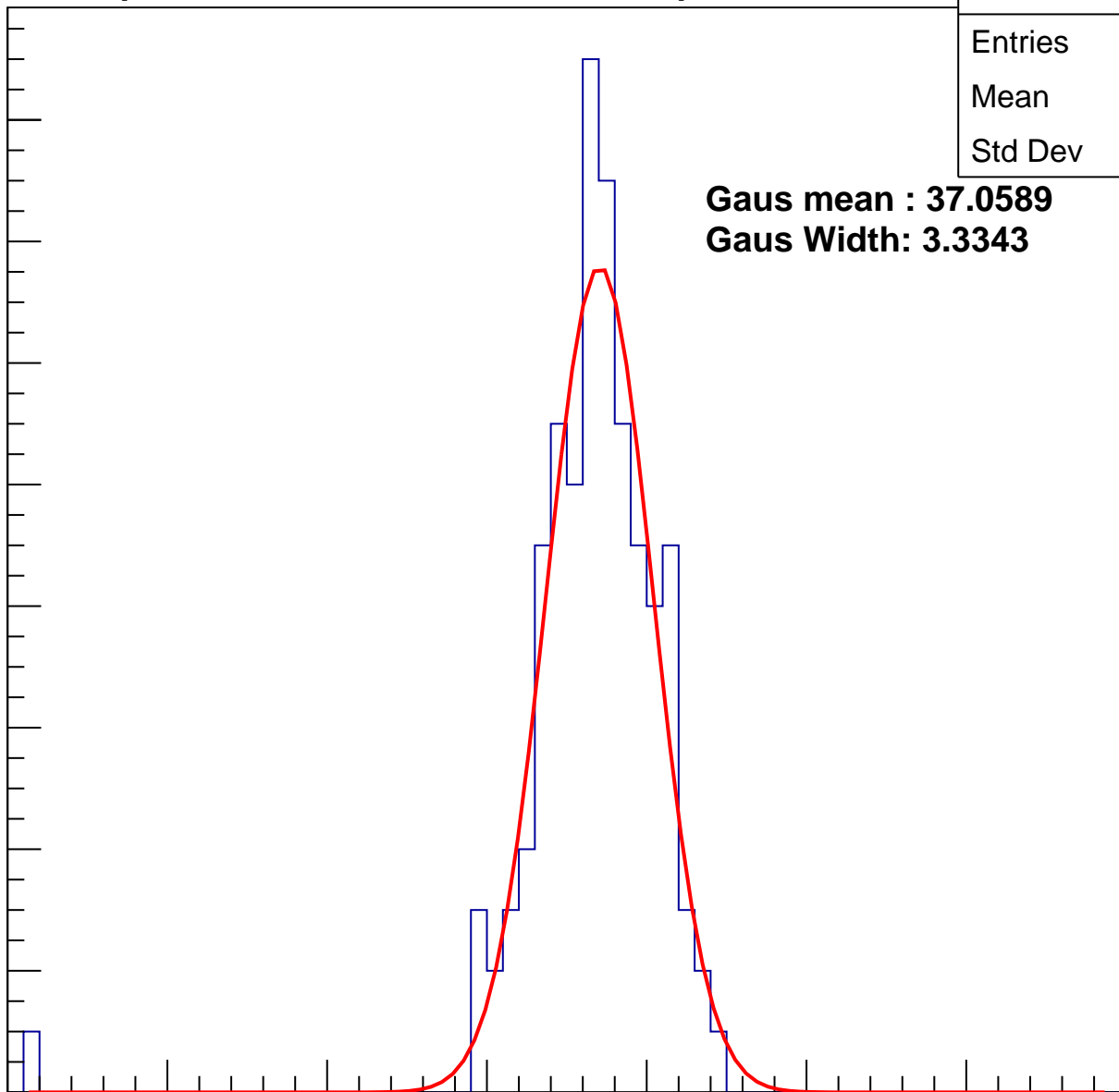
40

50

60

70

ampl



B0L101S, U20-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

141

Mean

43.14

Std Dev

3.43

Gaus mean : 43.6974

Gaus Width: 3.4595

0

10

20

30

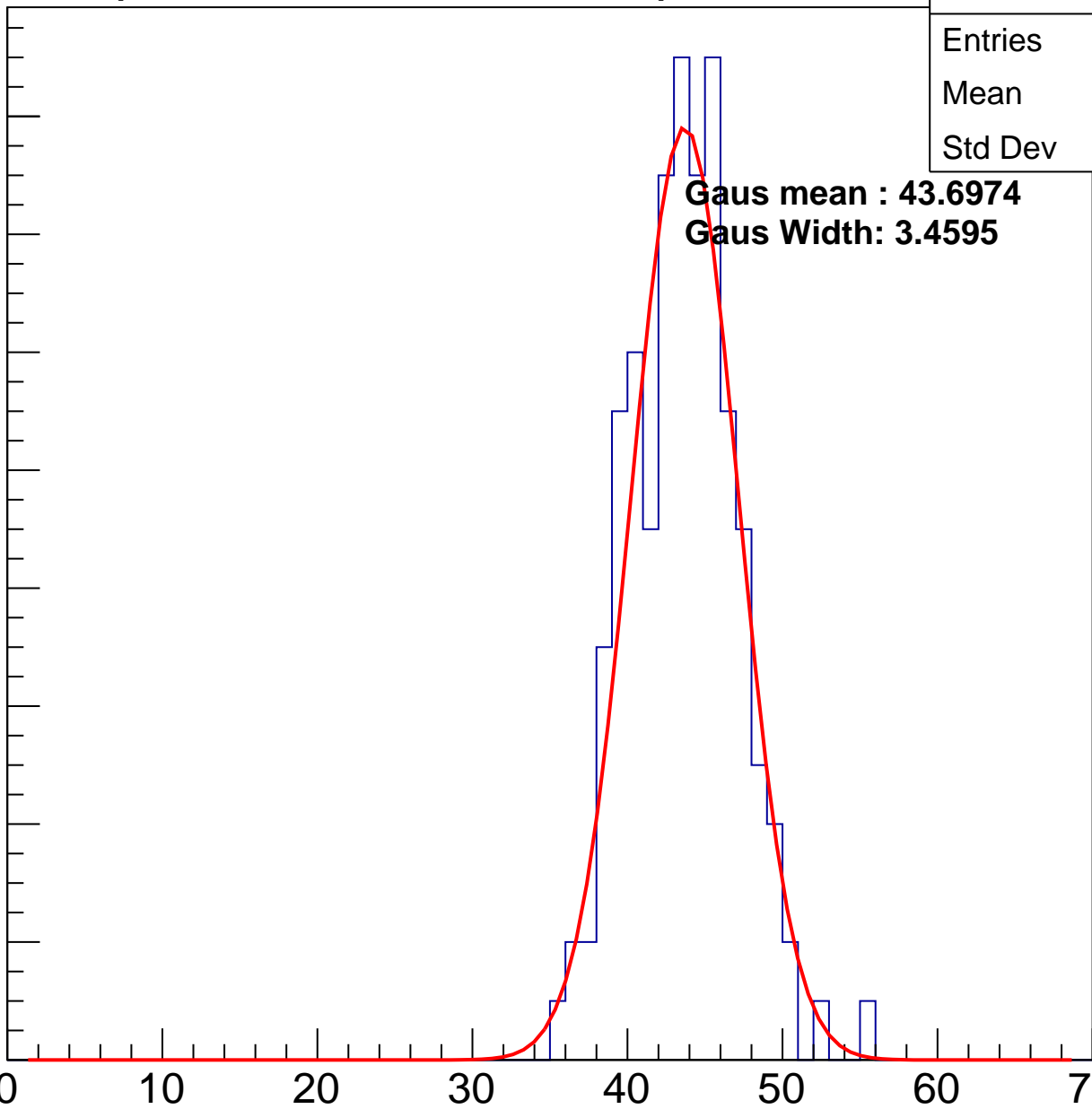
40

50

60

70

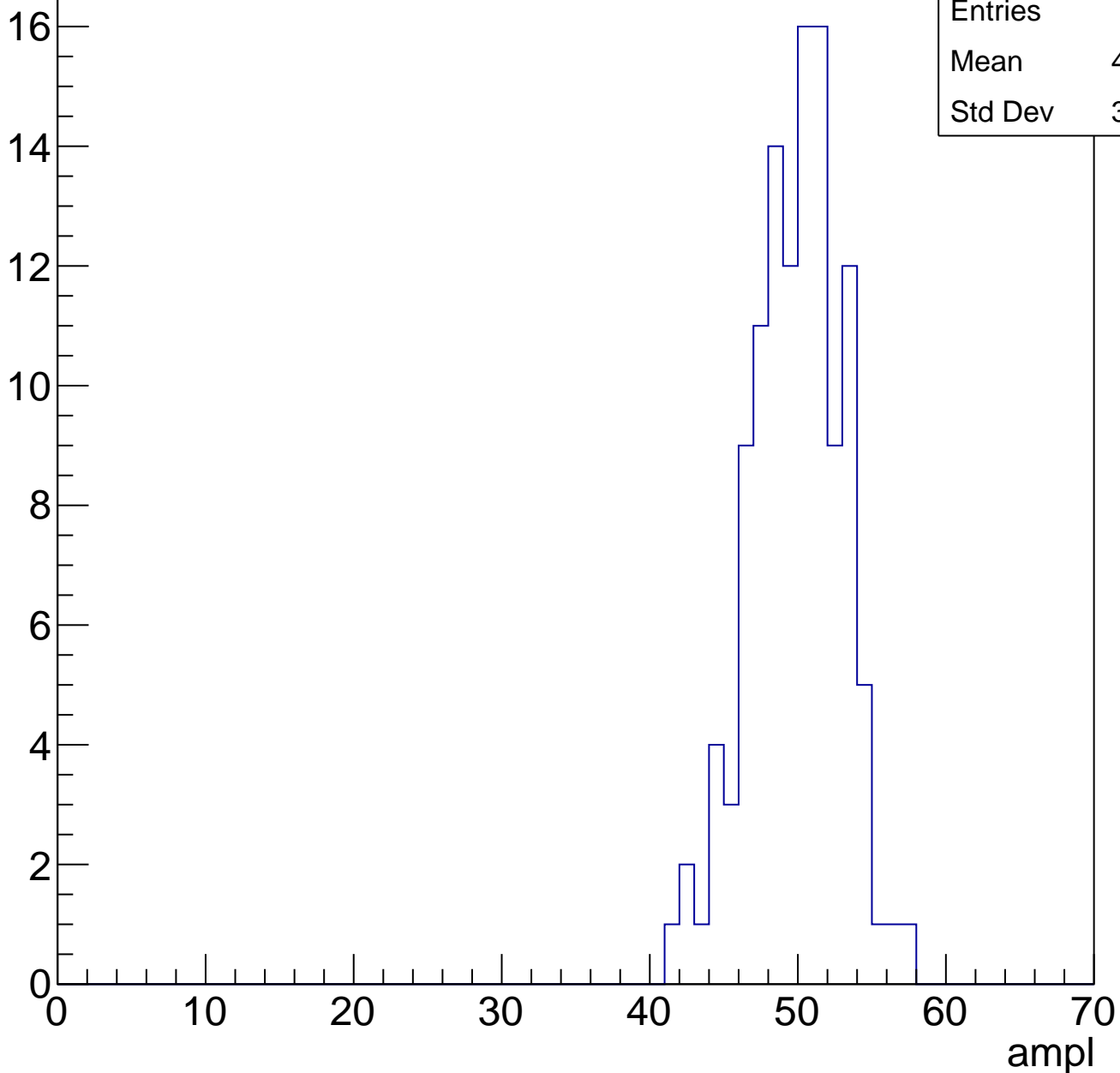
ampl



B0L101S, U20-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch87, adc4

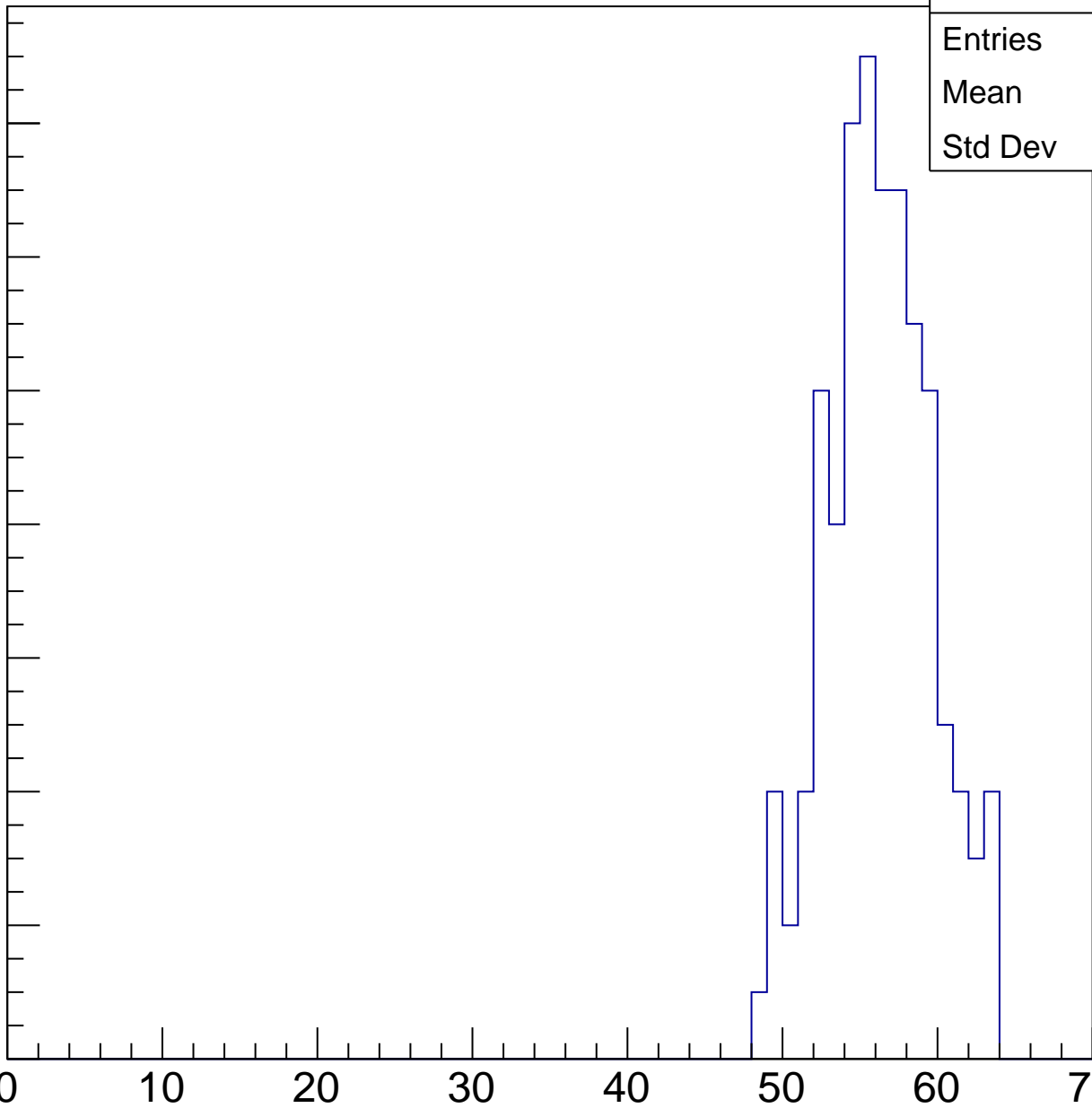
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	55.8
Std Dev	3.354

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch87, adc5

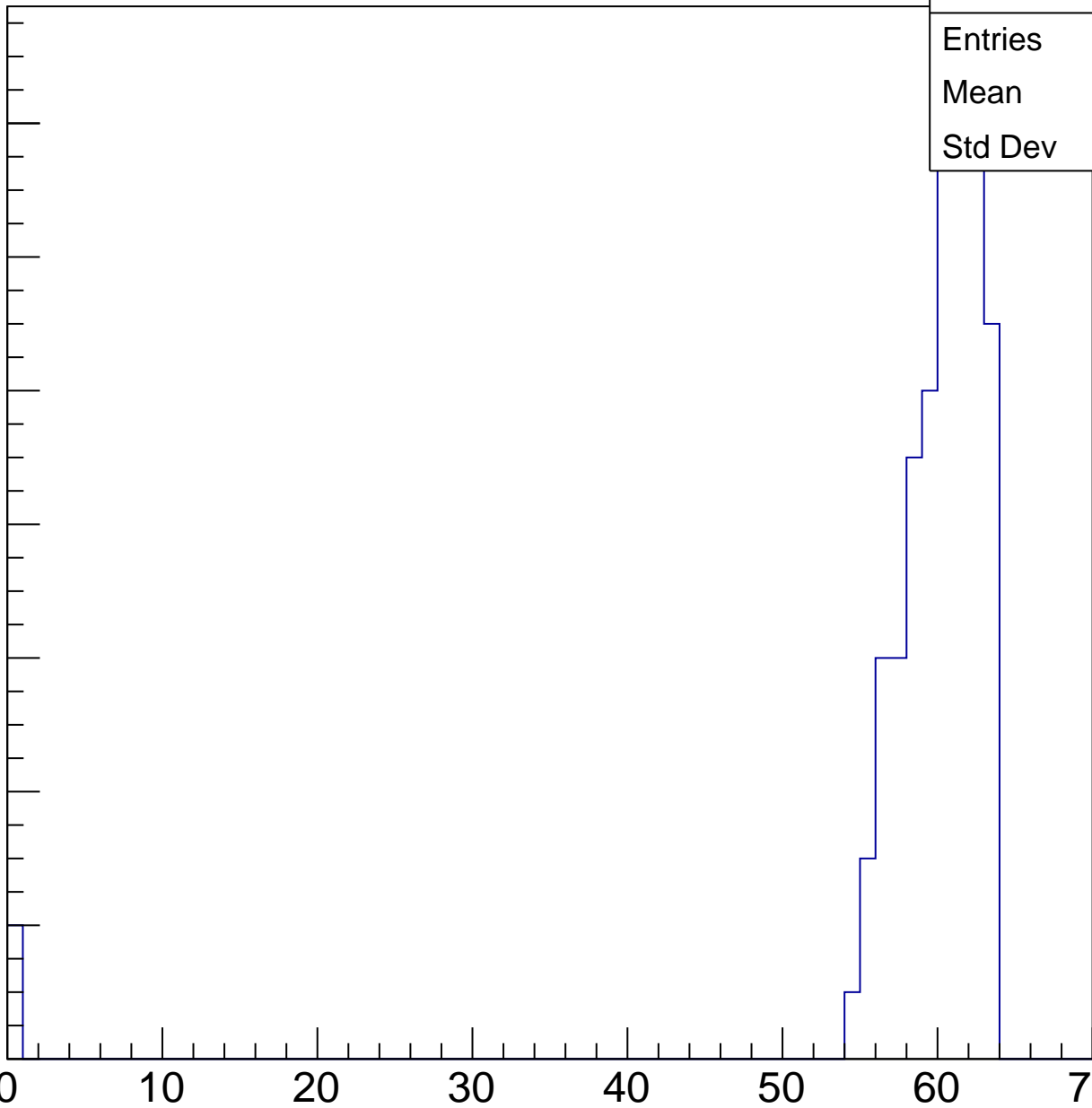
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.52
Std Dev	9.064

ampl

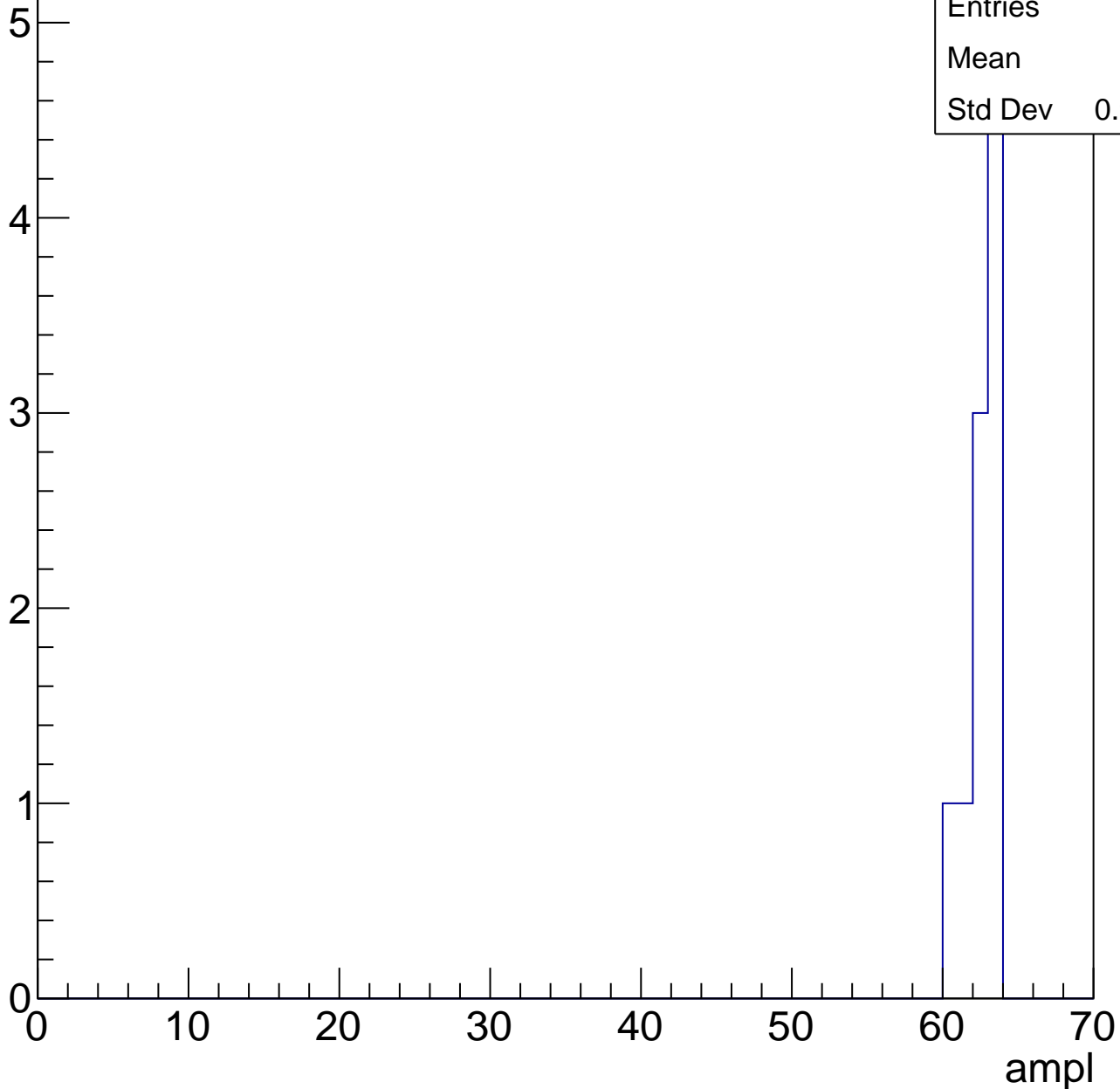


B0L101S, U20-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B0L101S, U20-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U20-ch88, adc0

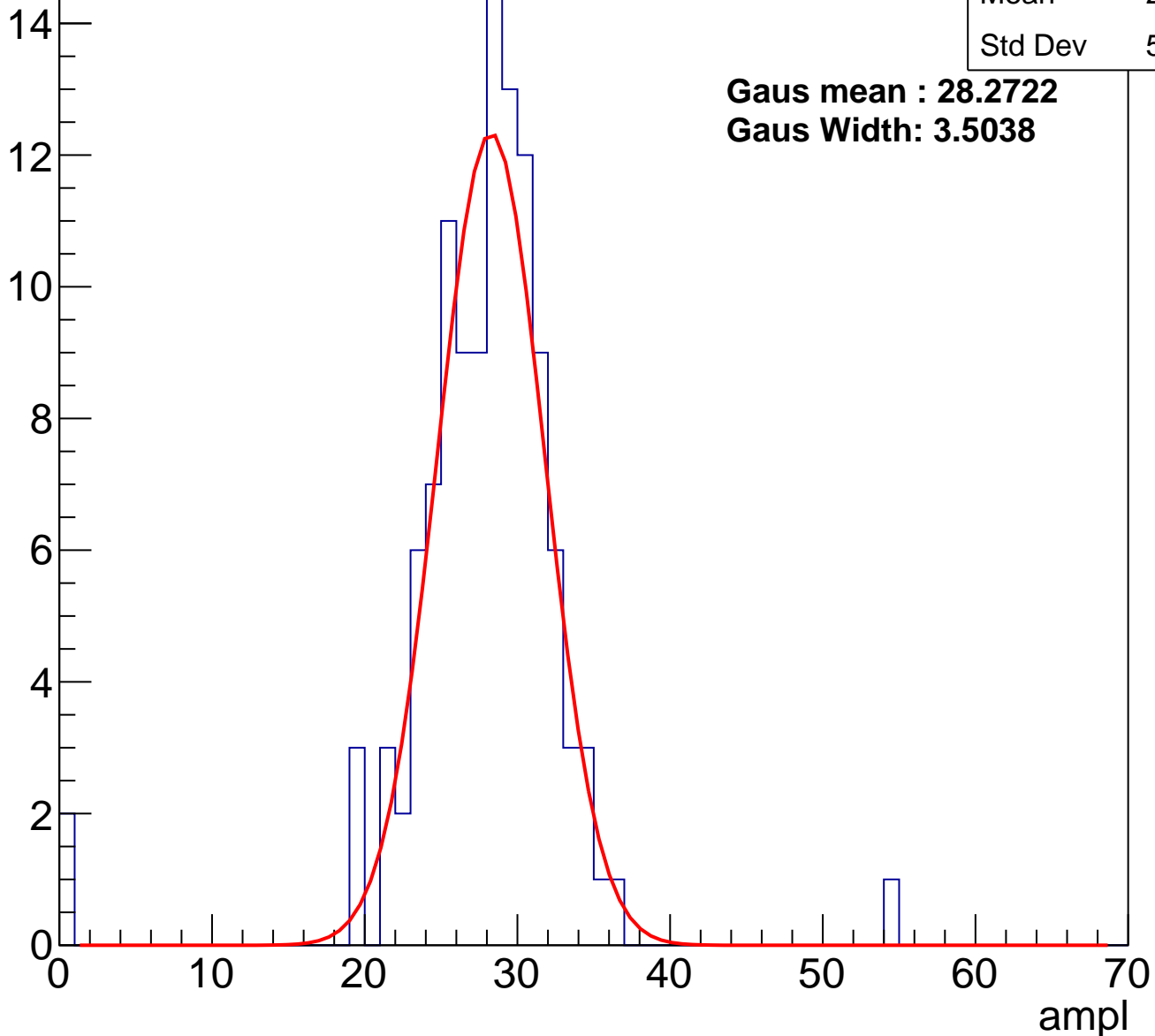
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	27.38
Std Dev	5.572

Gaus mean : 28.2722

Gaus Width: 3.5038

Entry



B0L101S, U20-ch88, adc1

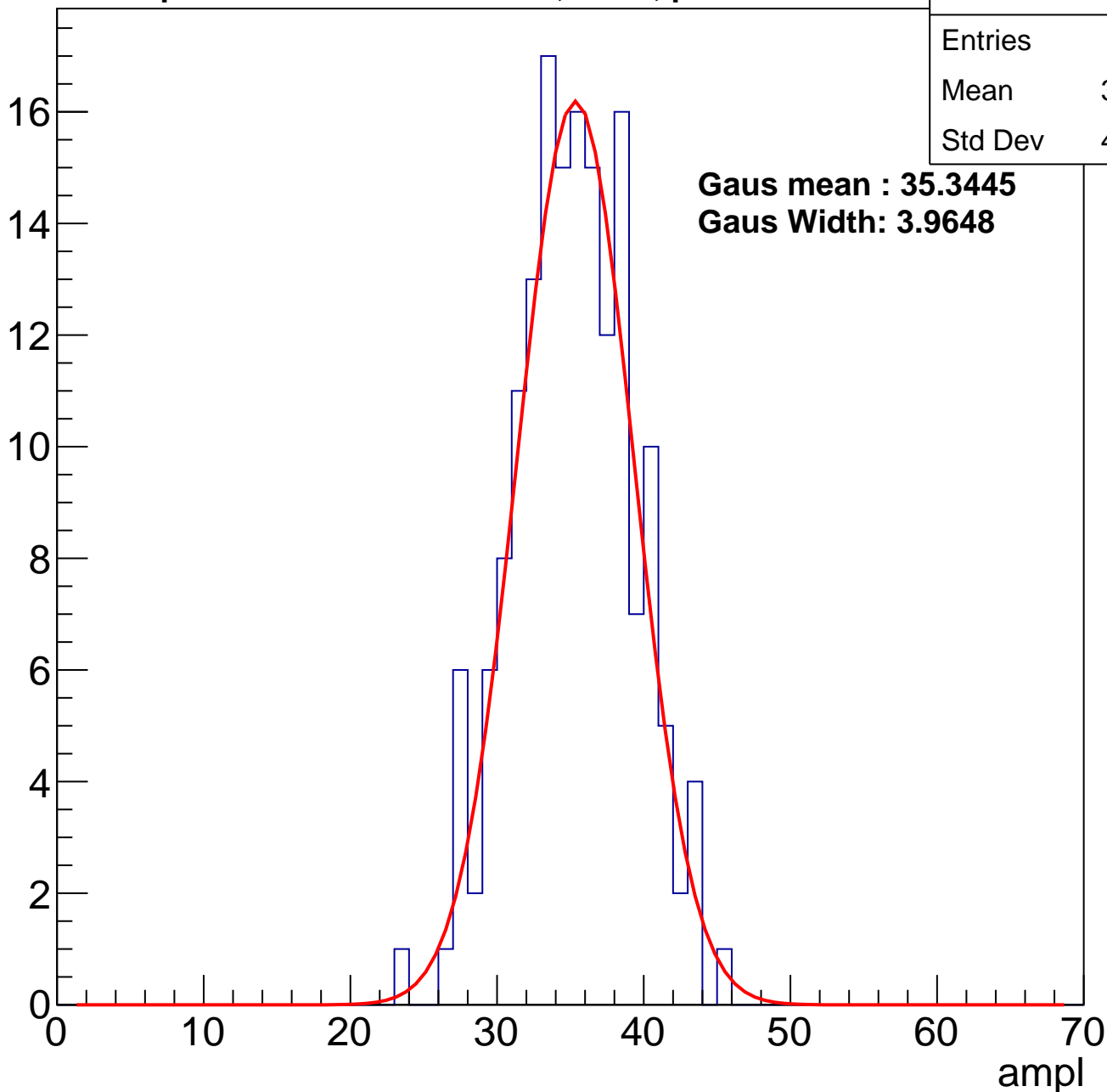
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	34.76
Std Dev	4.003

Gaus mean : 35.3445

Gaus Width: 3.9648

Entry



B0L101S, U20-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

100

Mean

41.04

Std Dev

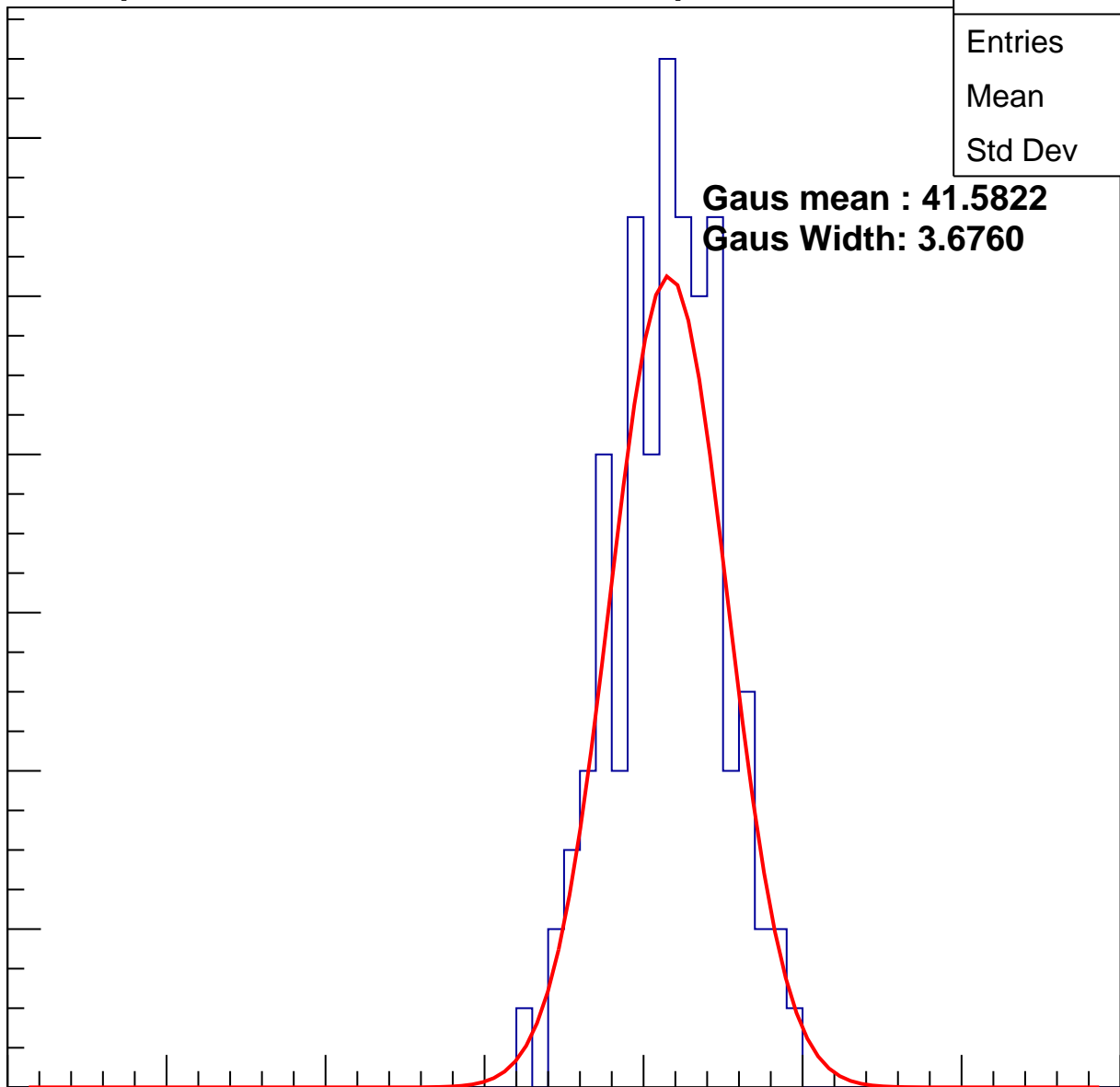
3.429

Gaus mean : 41.5822

Gaus Width: 3.6760

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

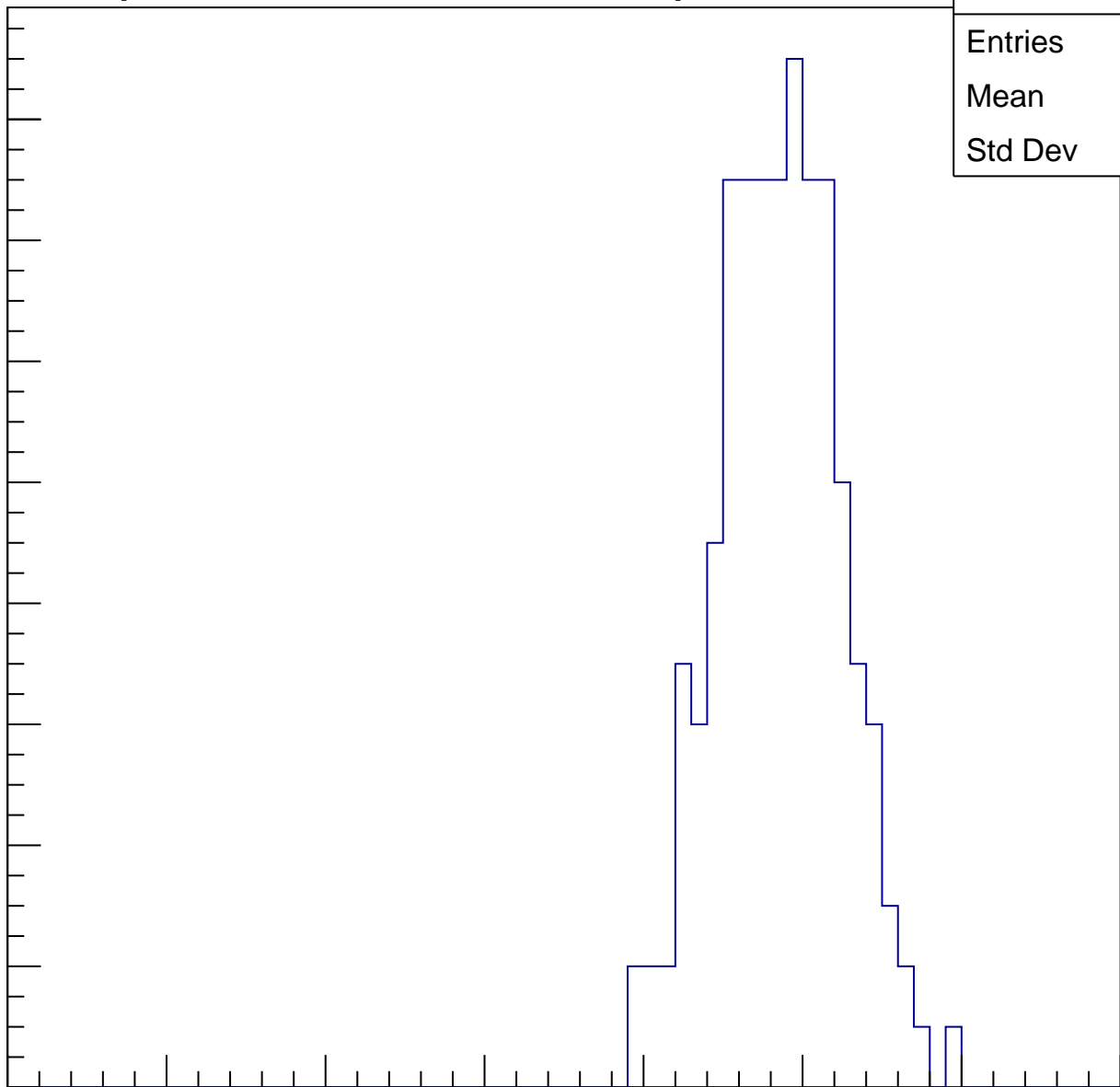
Entries	165
Mean	48.08
Std Dev	3.8

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch88, adc4

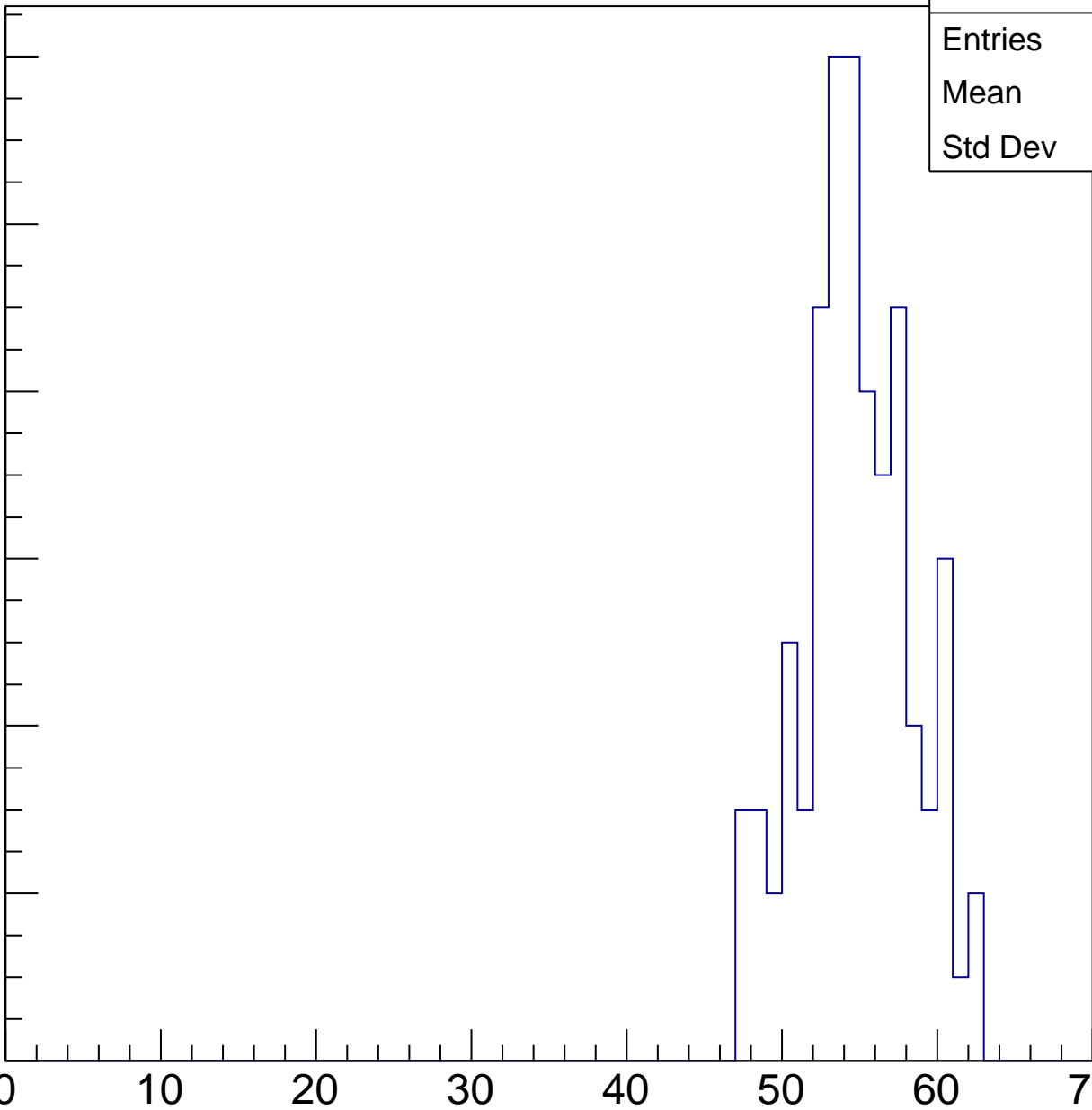
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	89
Mean	54.35
Std Dev	3.513

ampl



B0L101S, U20-ch88, adc5

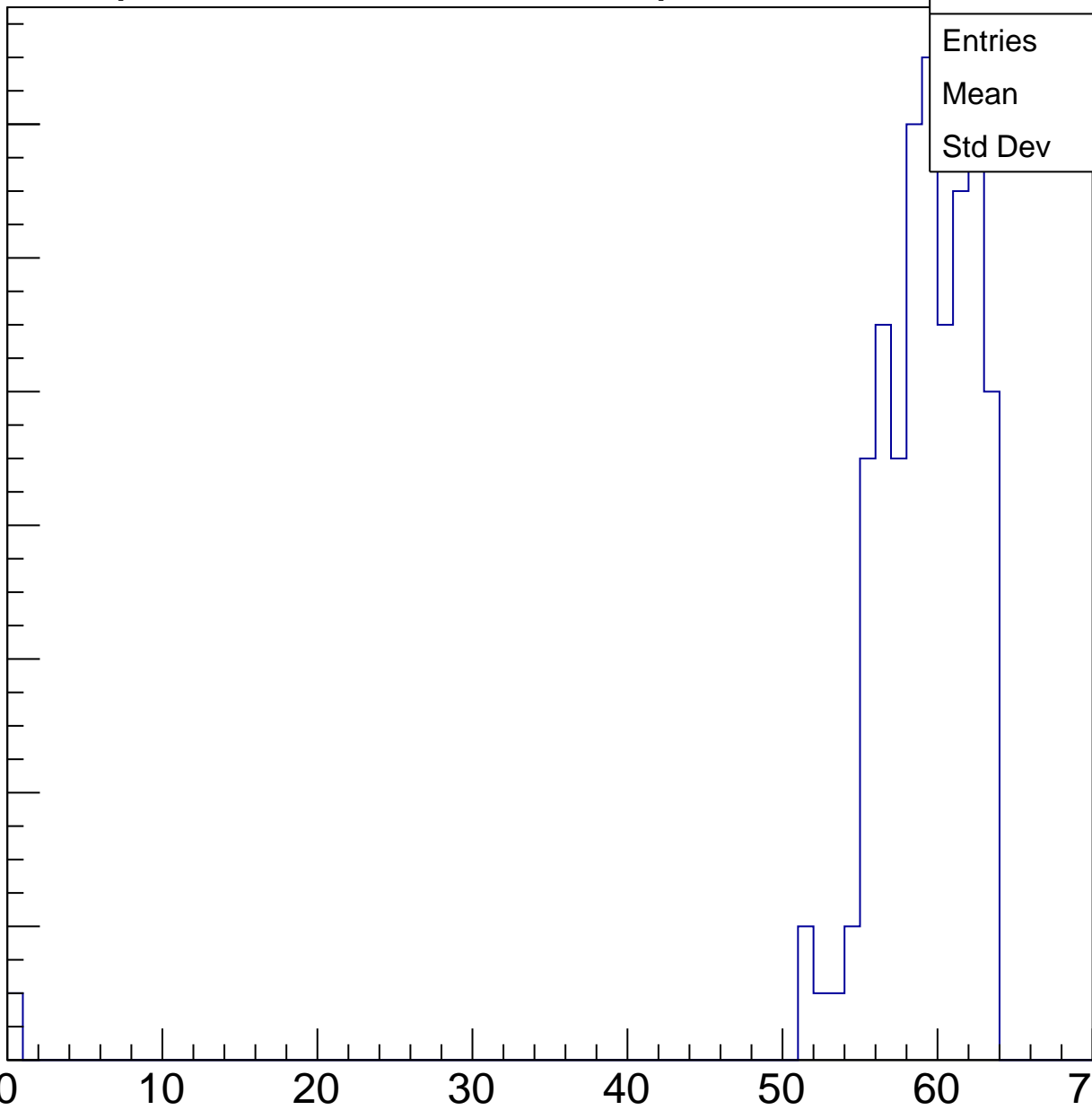
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	58.29
Std Dev	6.19

ampl

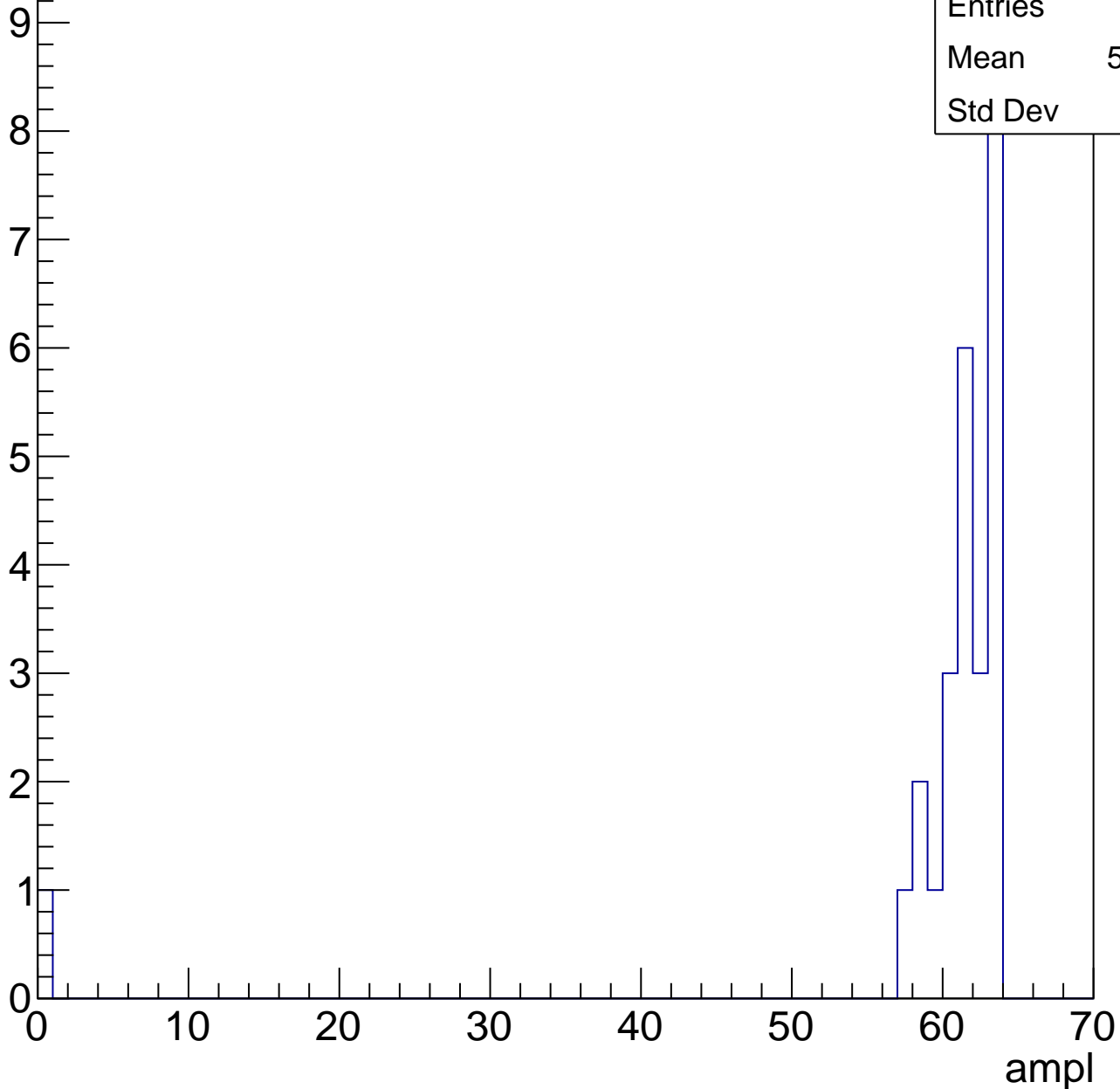


B0L101S, U20-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

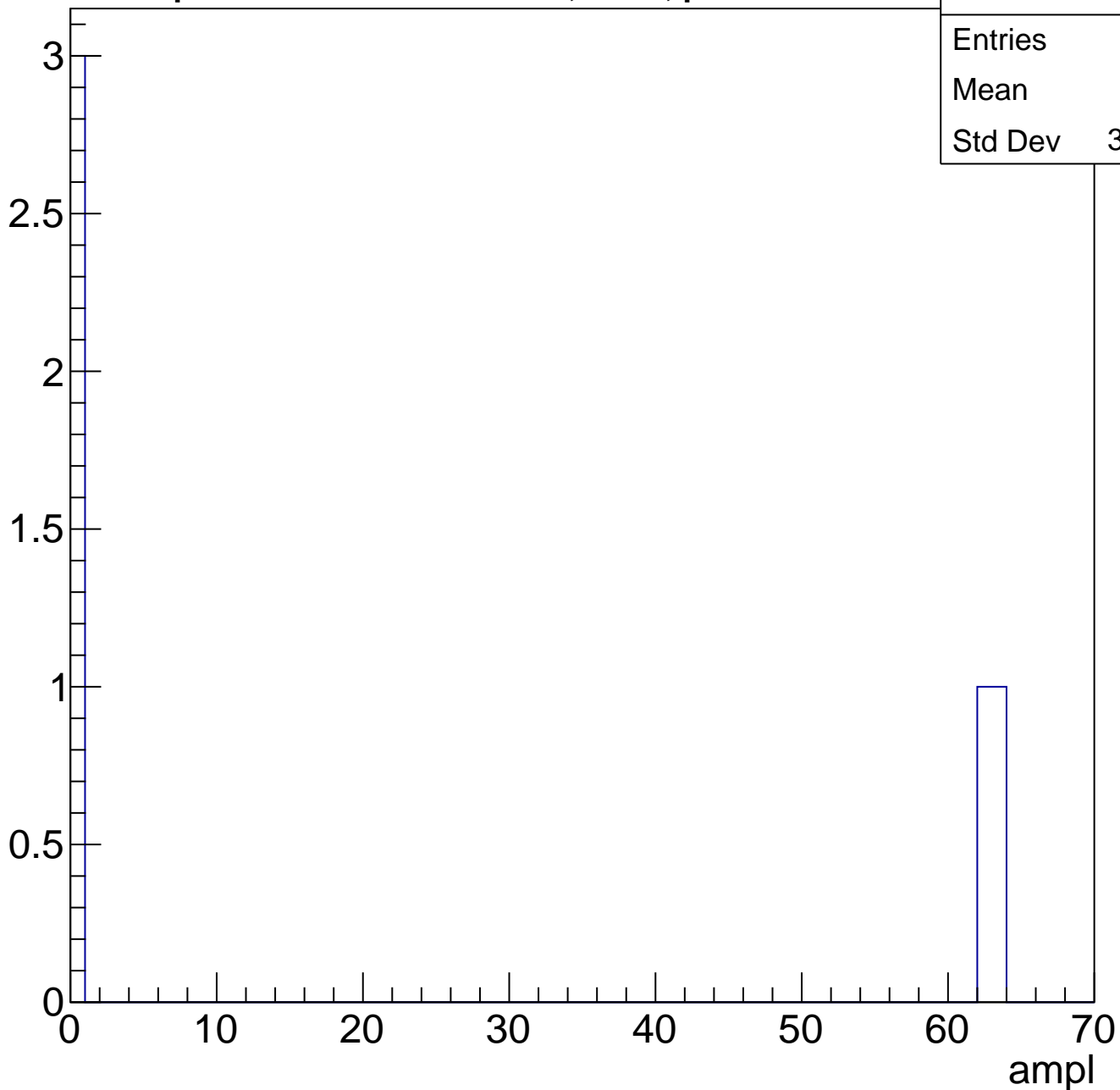
Entries	26
Mean	58.88
Std Dev	11.9



B0L101S, U20-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch89, adc0

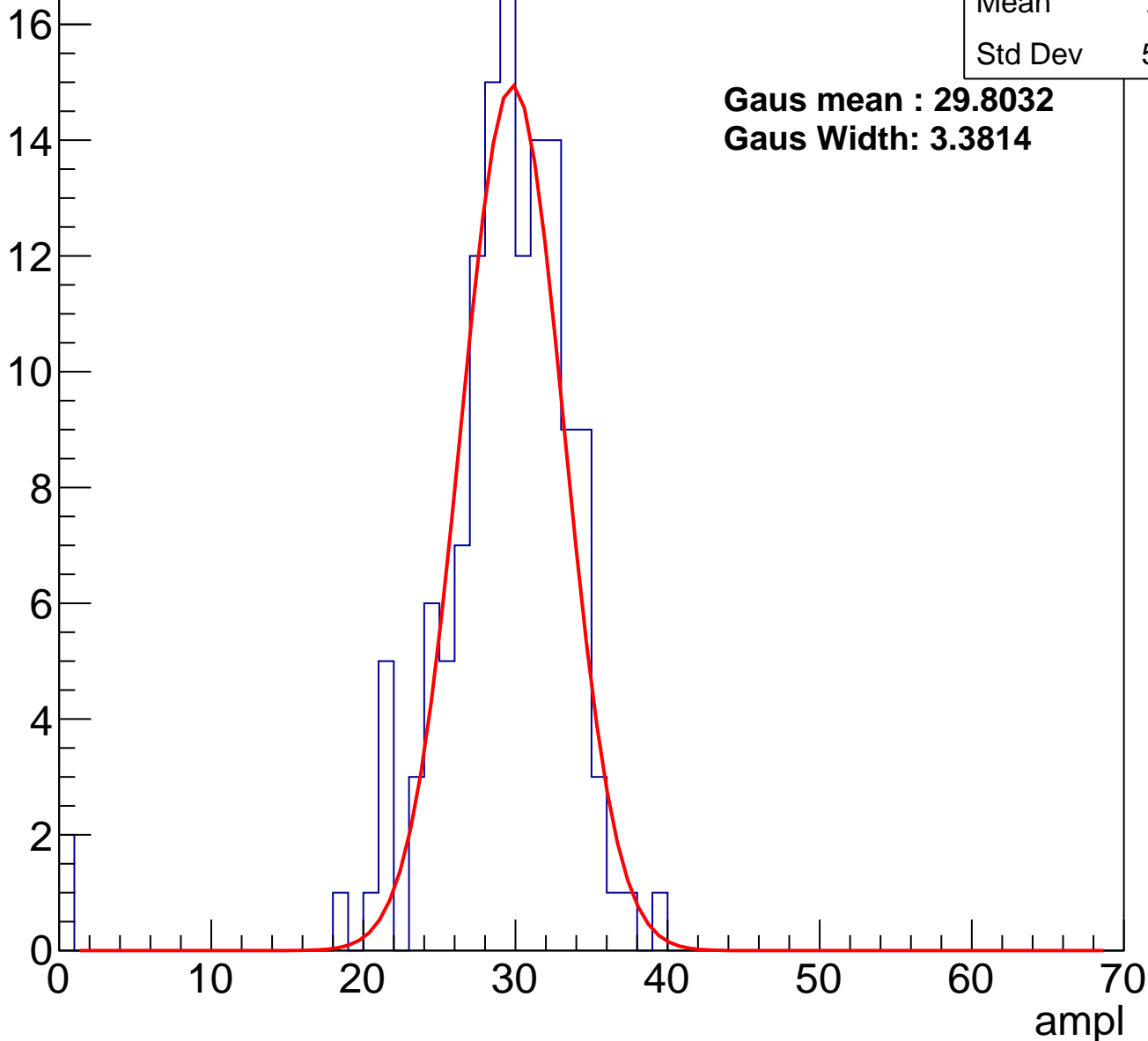
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	138
Mean	28.71
Std Dev	5.065

Gaus mean : 29.8032

Gaus Width: 3.3814



B0L101S, U20-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

36.4

Std Dev

3.724

Gaus mean : 36.7113

Gaus Width: 3.4814

0

10

20

30

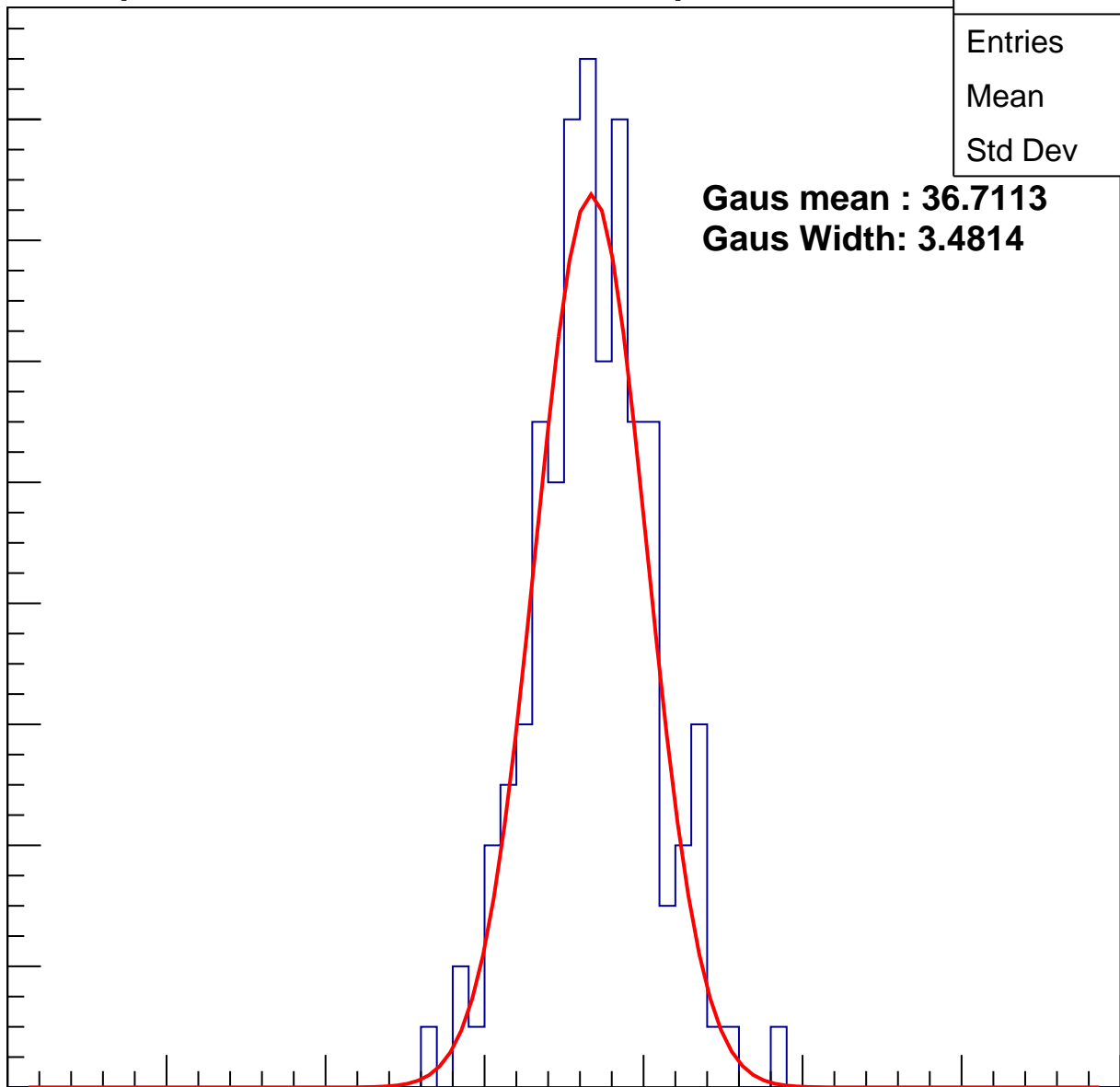
40

50

60

70

ampl



B0L101S, U20-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	42.83
Std Dev	3.389

Gaus mean : 43.1979

Gaus Width: 3.3367

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

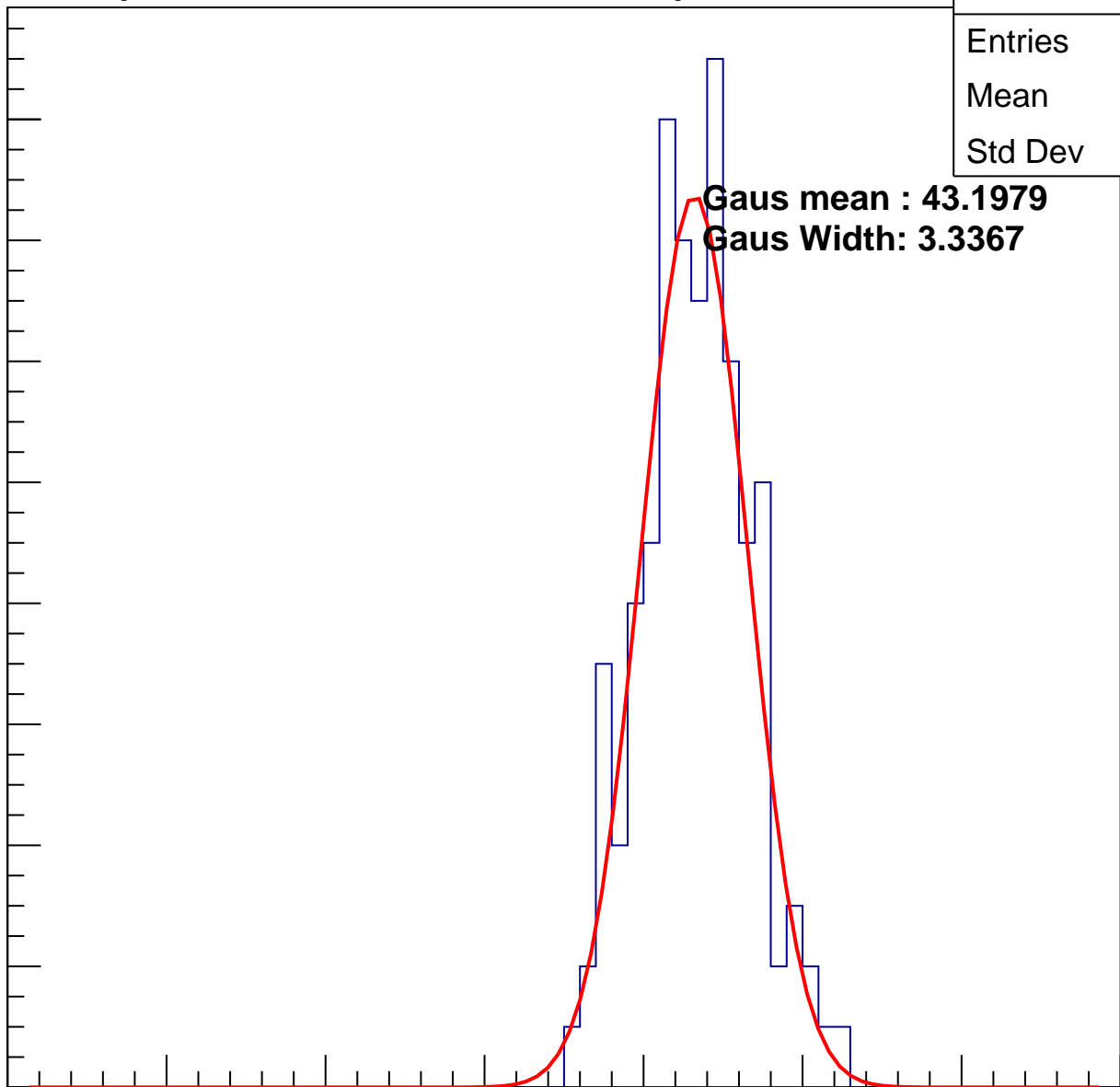
40

50

60

70

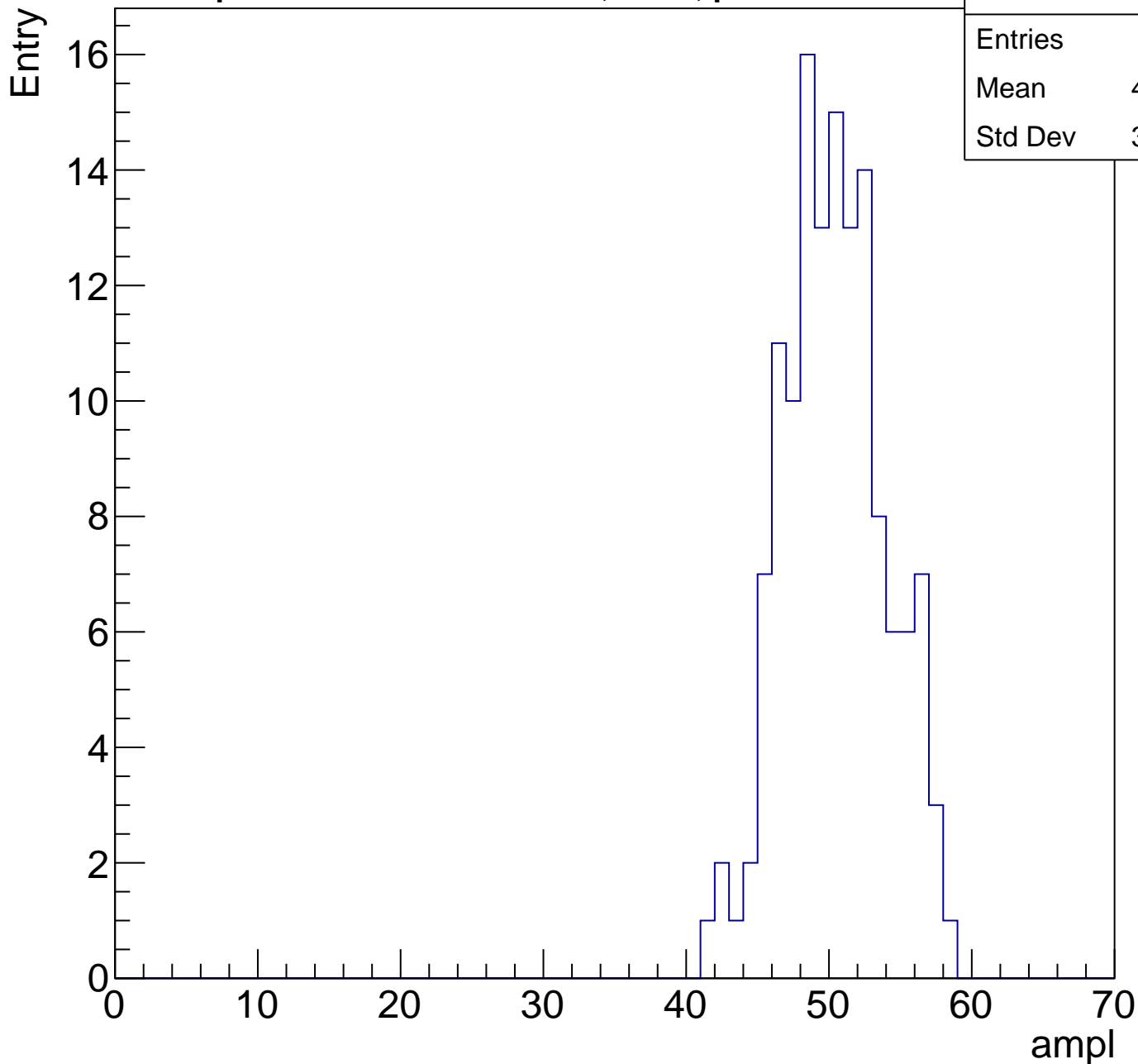
ampl



B0L101S, U20-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	49.94
Std Dev	3.533

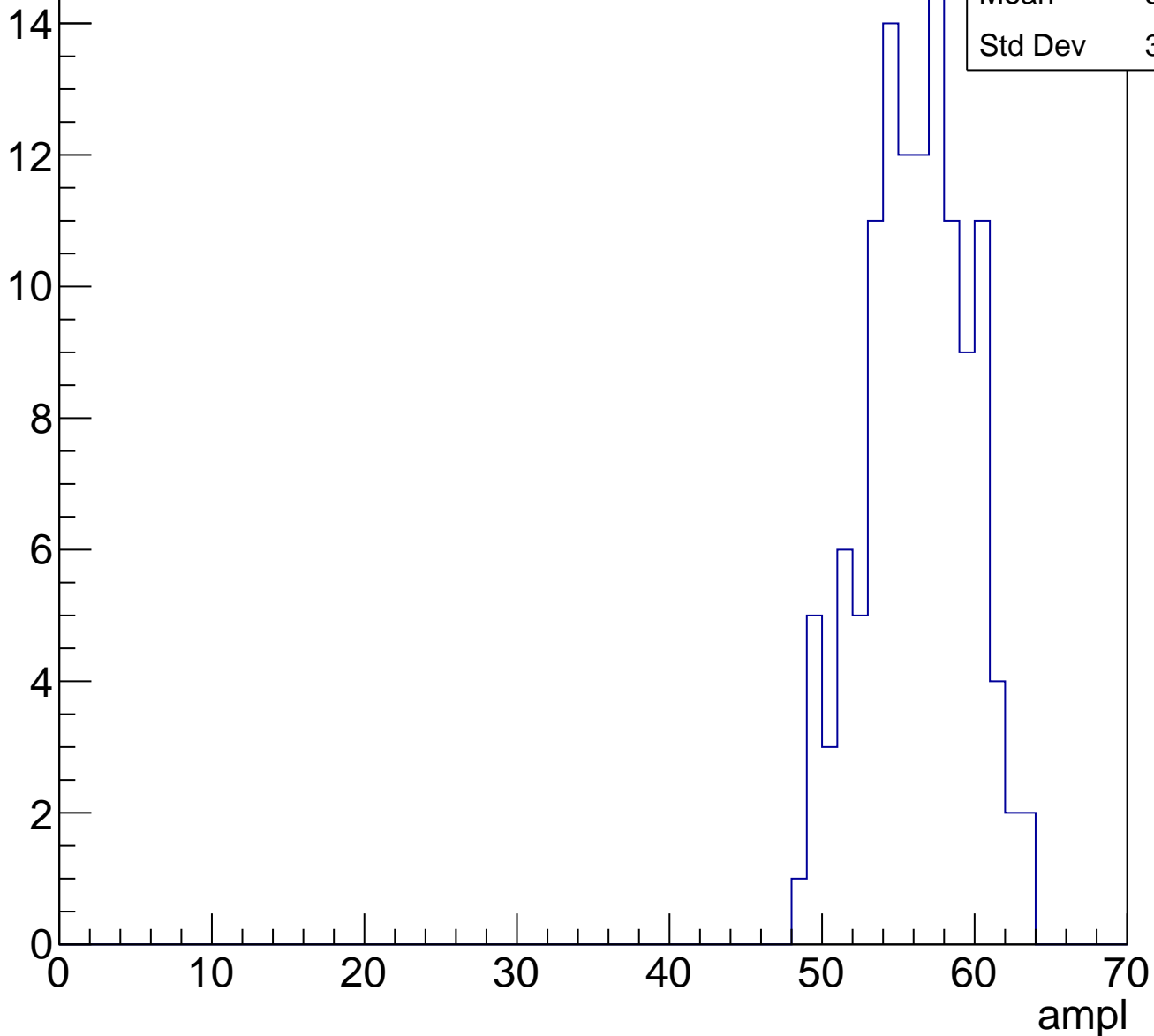


B0L101S, U20-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	55.76
Std Dev	3.365

Entry



B0L101S, U20-ch89, adc5

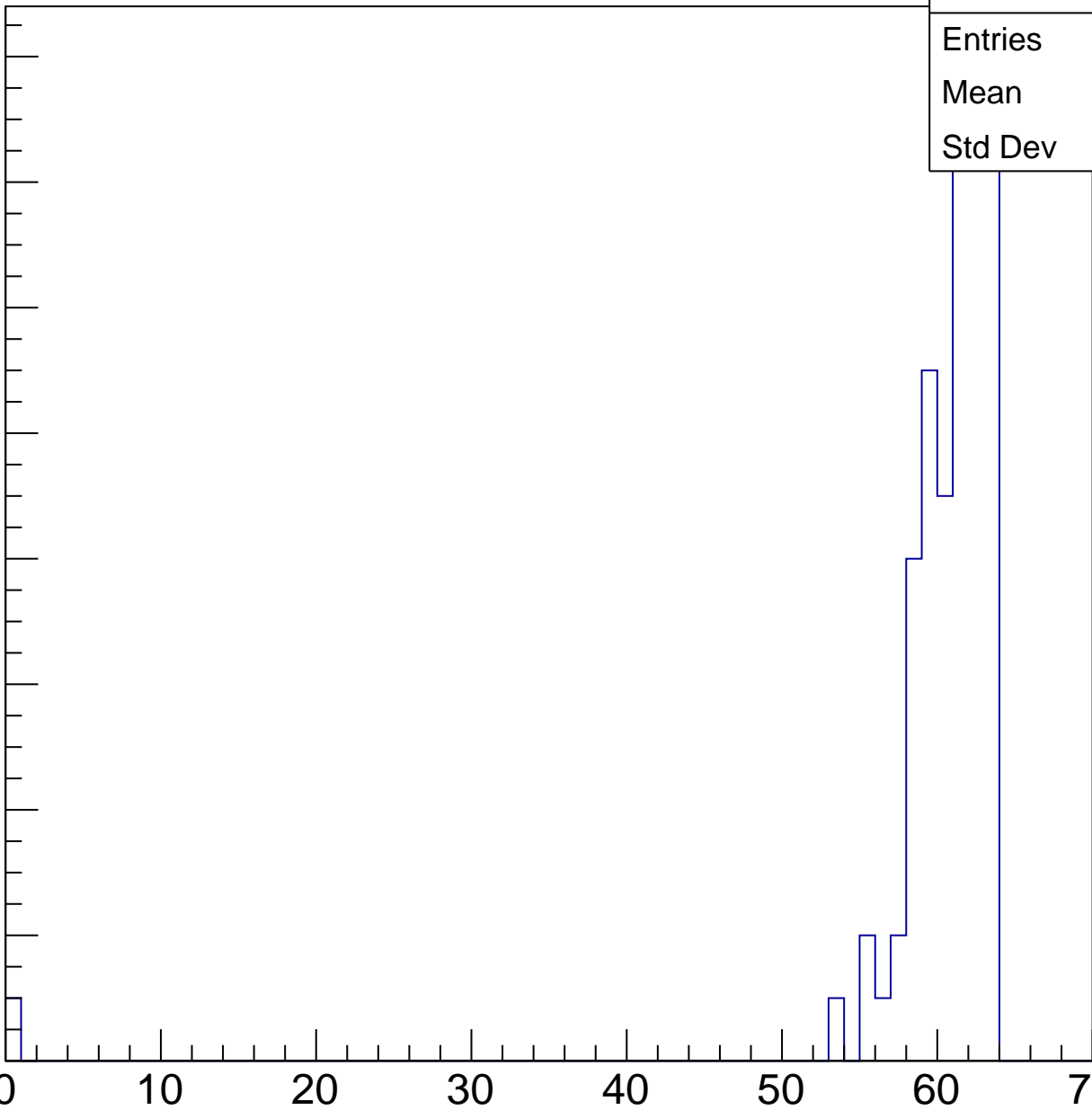
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.78
Std Dev	6.939

ampl



B0L101S, U20-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	29.48
Std Dev	5.245

Gaus mean : 29.9401

Gaus Width: 3.7515

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

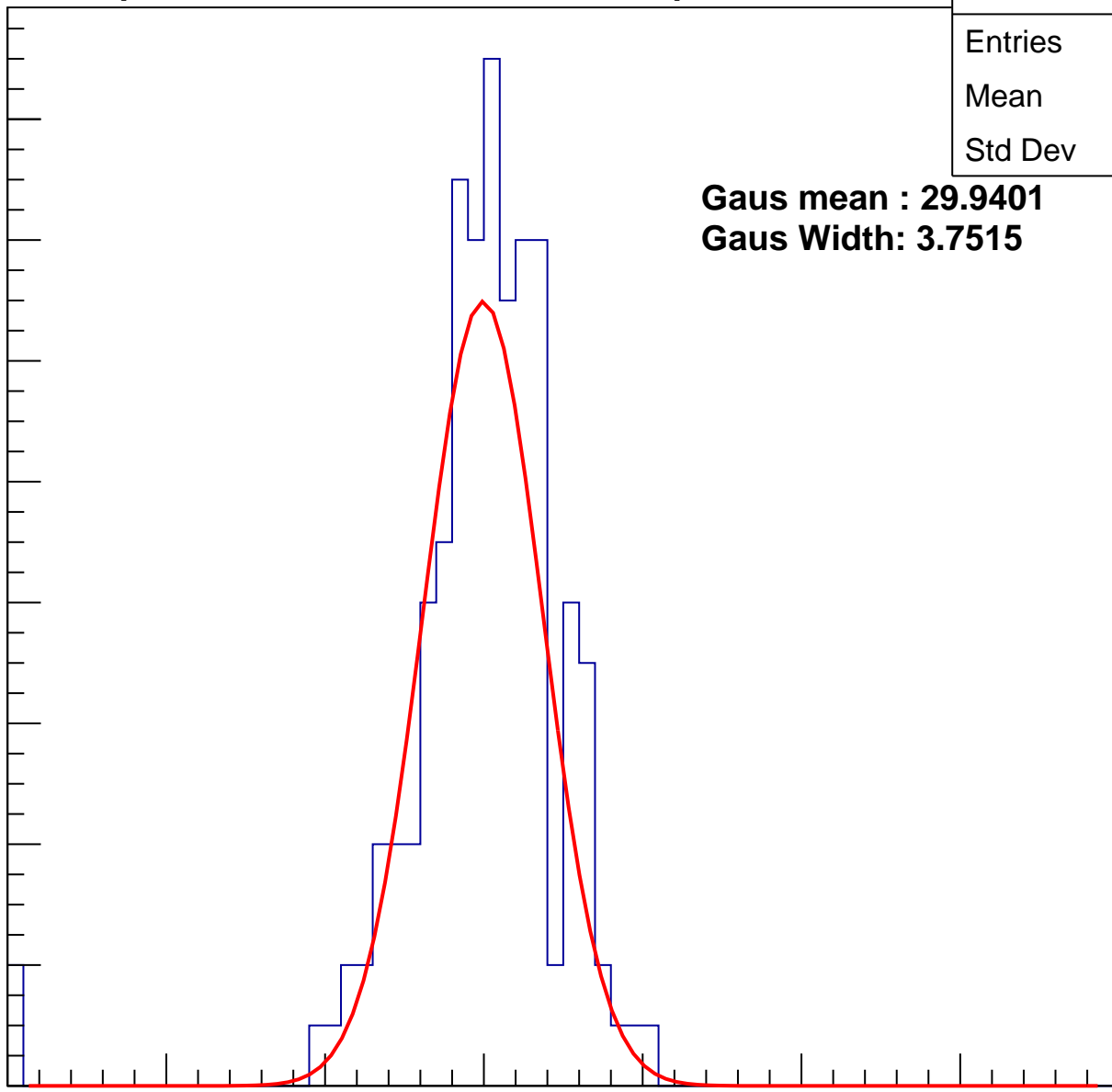
40

50

60

70

ampl



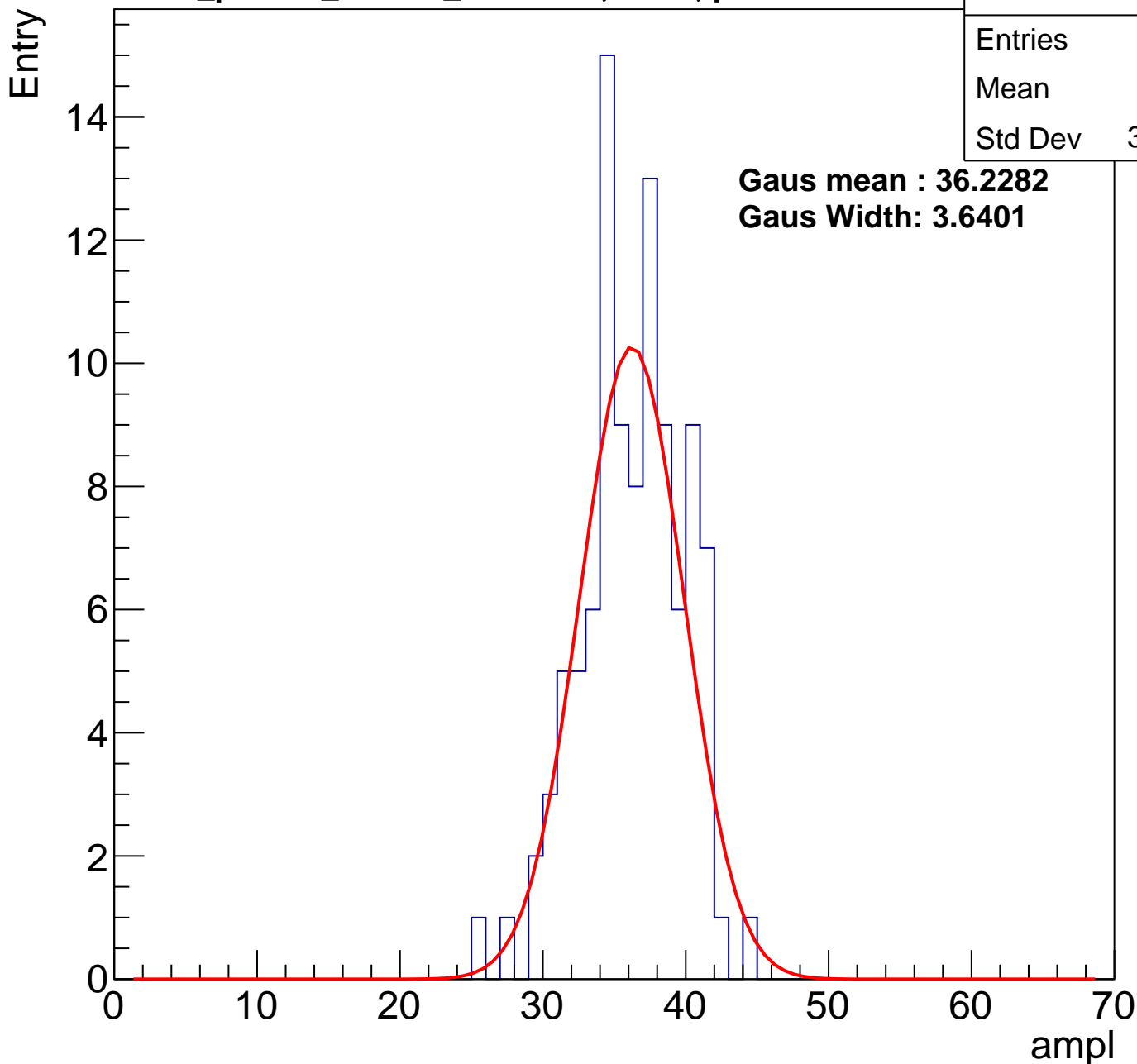
B0L101S, U20-ch90, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	35.8
Std Dev	3.524

Gaus mean : 36.2282

Gaus Width: 3.6401



B0L101S, U20-ch90, adc2

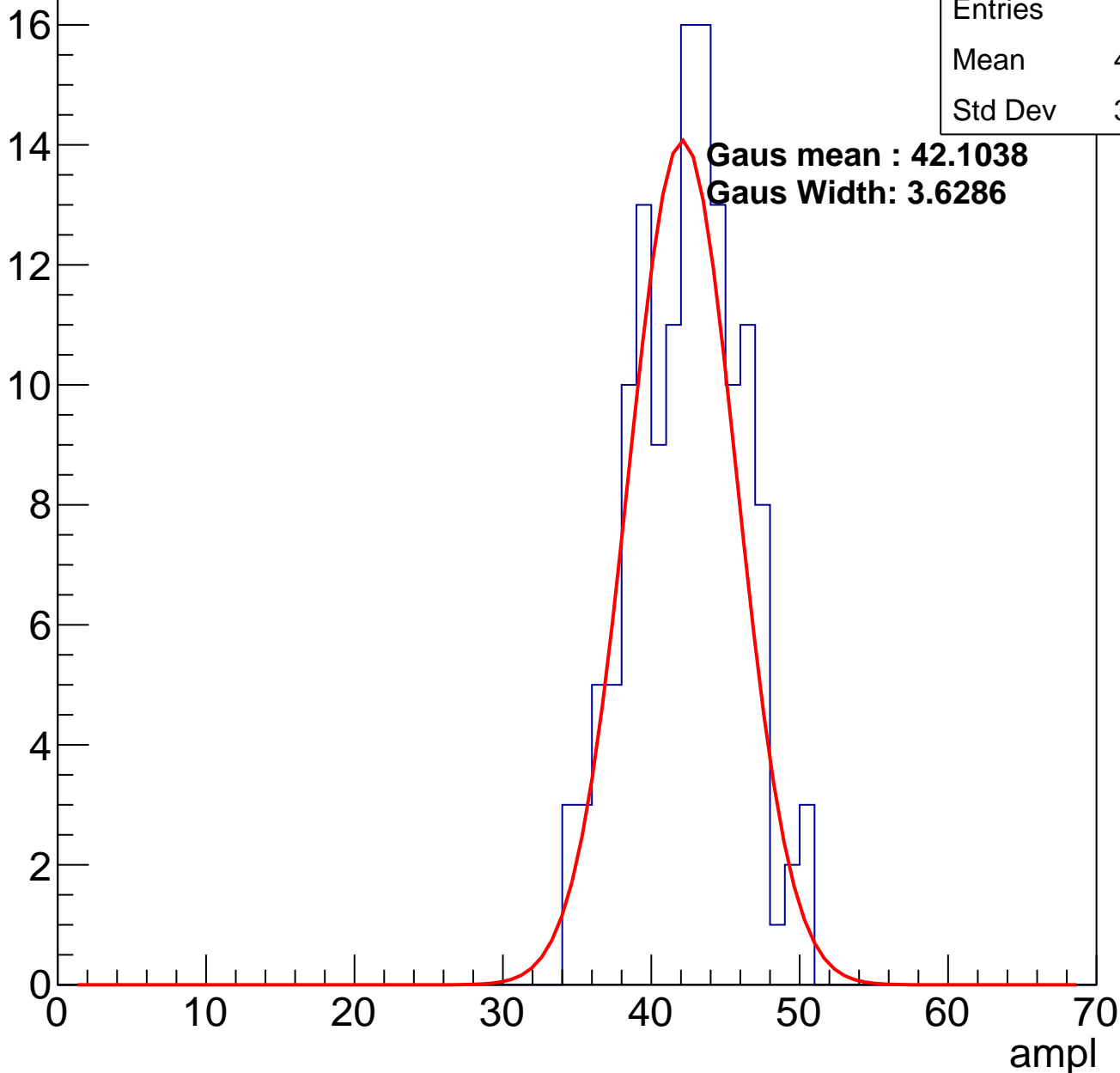
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	41.94
Std Dev	3.626

Gaus mean : 42.1038

Gaus Width: 3.6286

Entry



B0L101S, U20-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

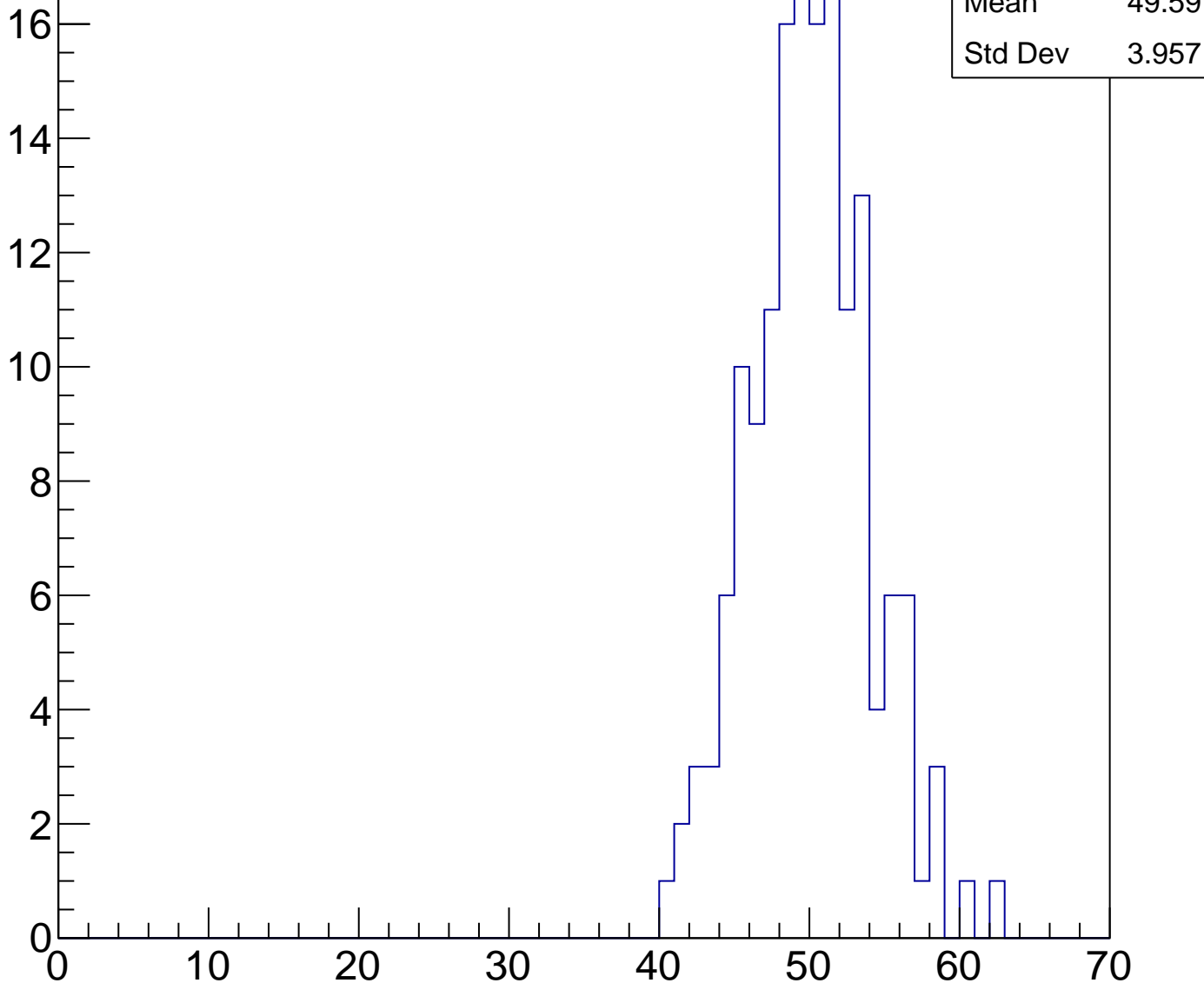
157

Mean

49.59

Std Dev

3.957



B0L101S, U20-ch90, adc4

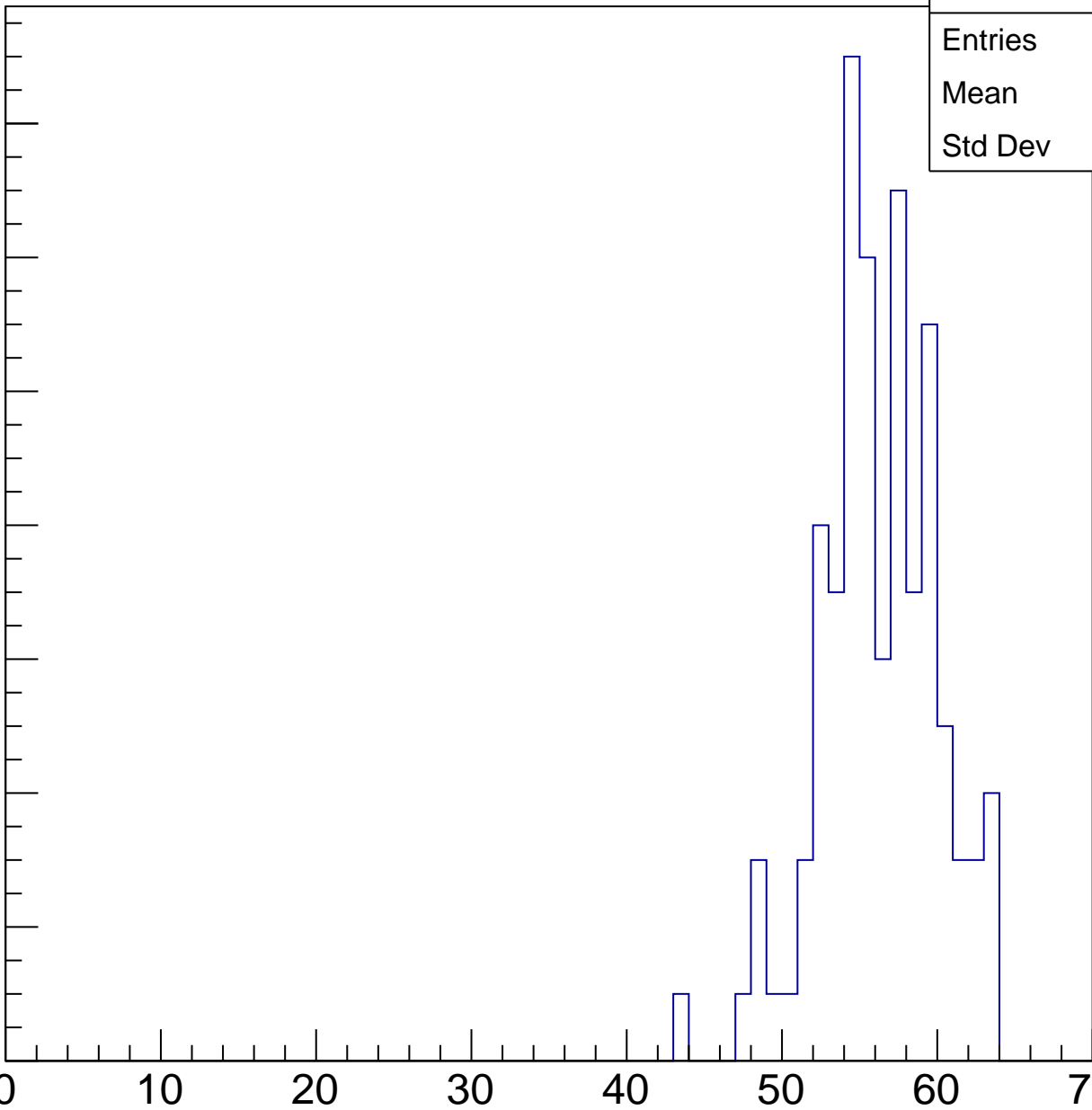
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	55.73
Std Dev	3.742

ampl



B0L101S, U20-ch90, adc5

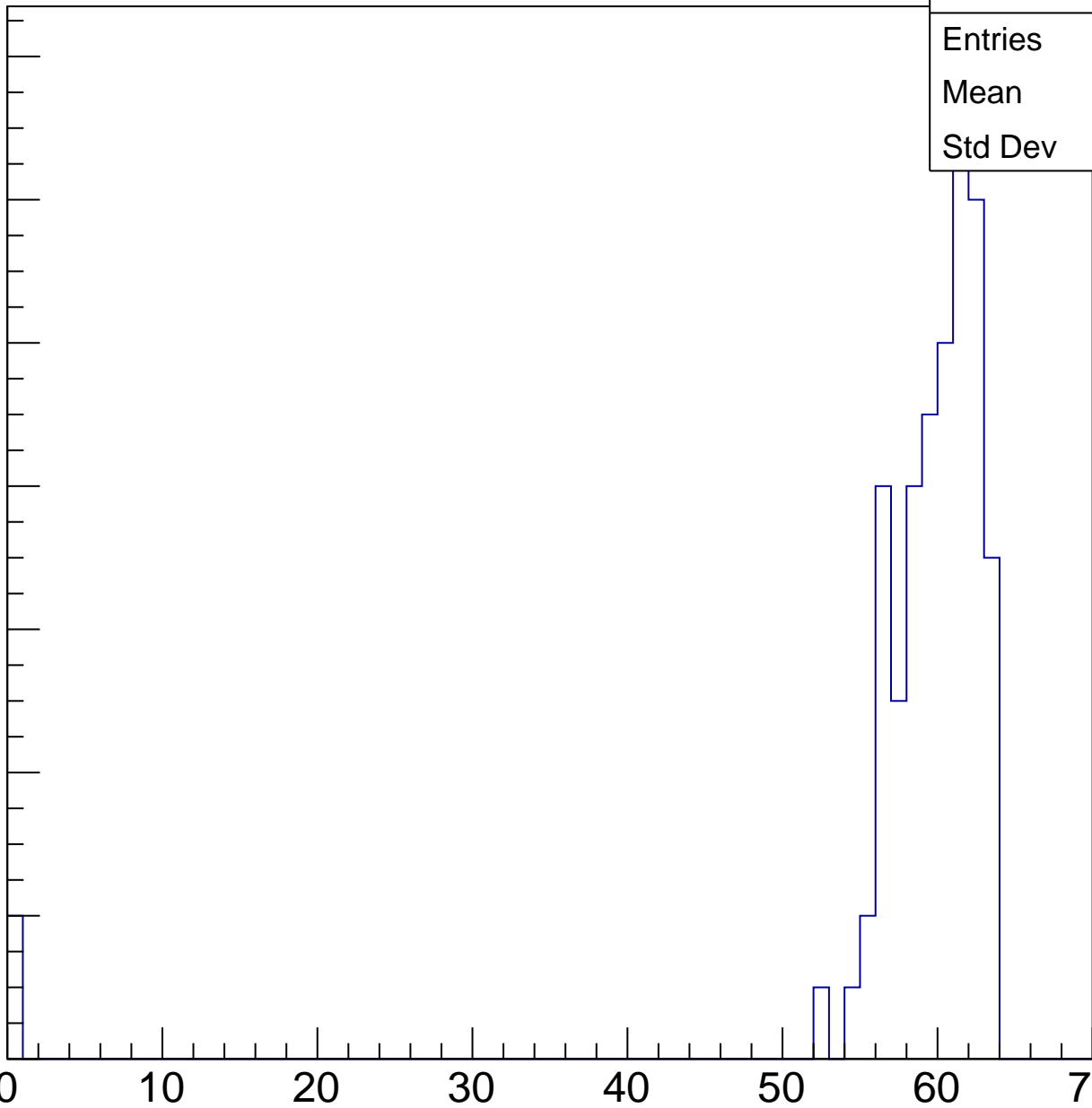
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	58.01
Std Dev	9.663

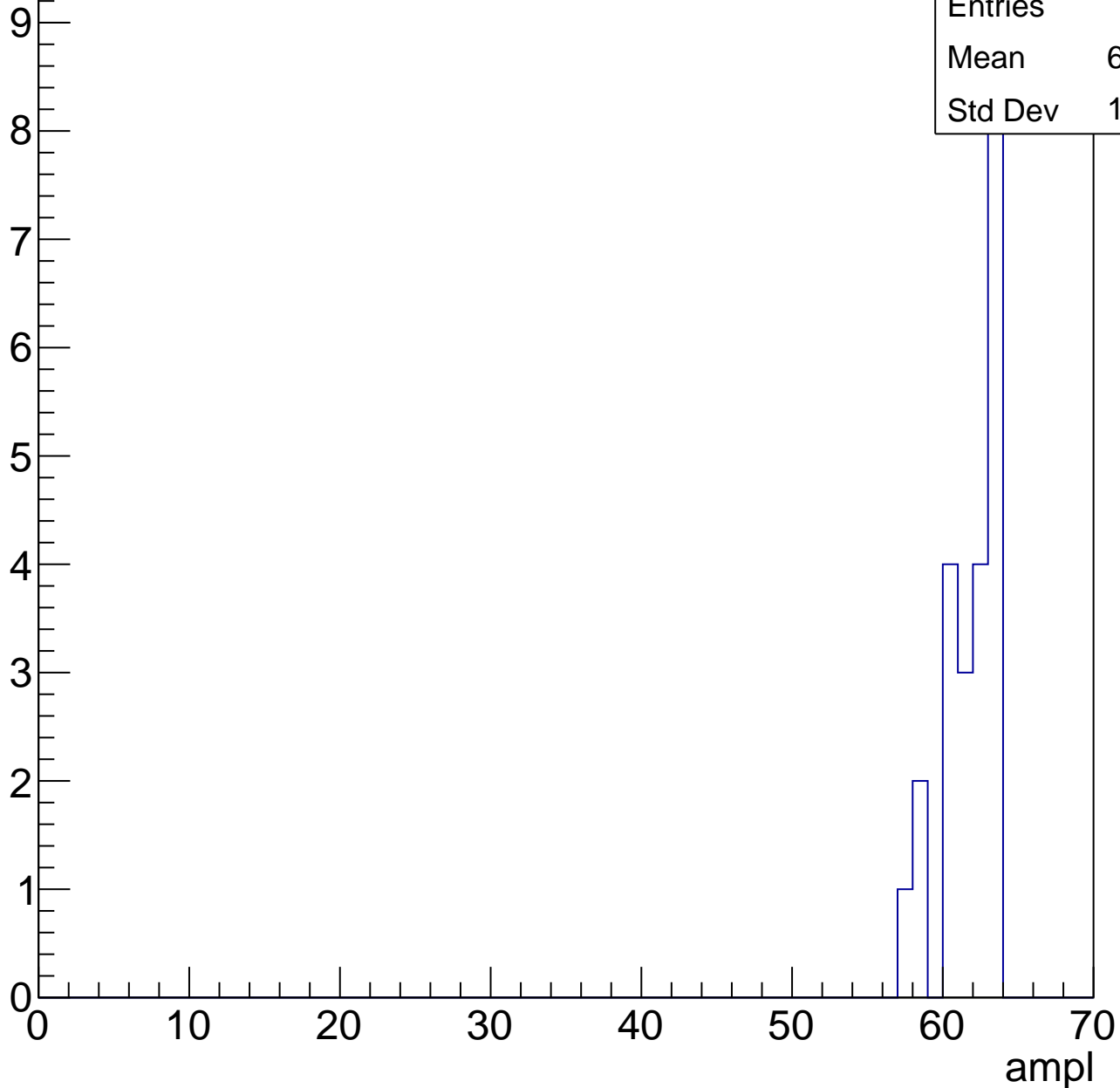
ampl



B0L101S, U20-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch91, adc0

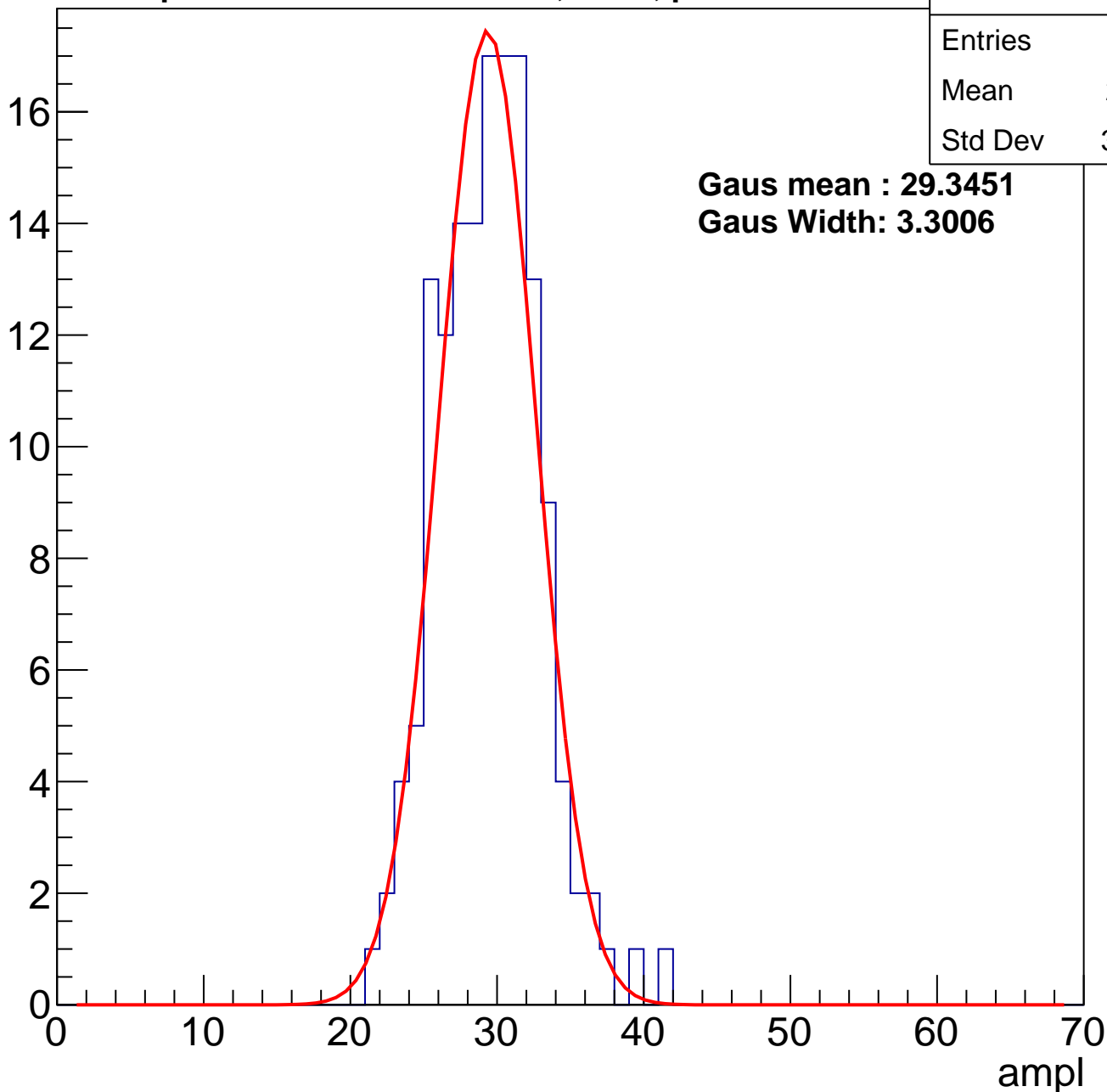
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.01
Std Dev	3.418

Gaus mean : 29.3451

Gaus Width: 3.3006

Entry



B0L101S, U20-ch91, adc1

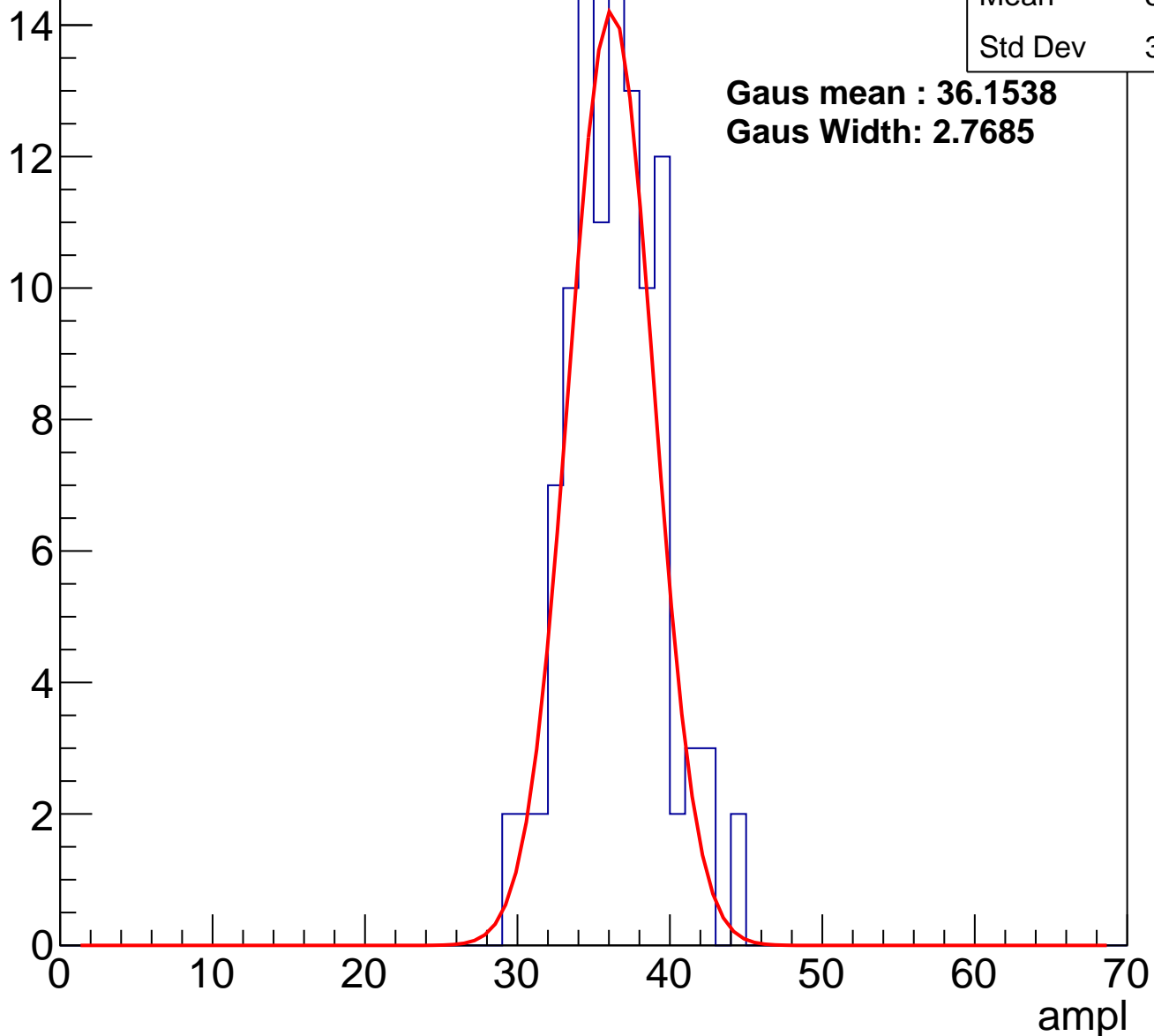
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	35.92
Std Dev	3.023

Gaus mean : 36.1538

Gaus Width: 2.7685

Entry



B0L101S, U20-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	42.45
Std Dev	3.935

Gaus mean : 42.9815

Gaus Width: 3.8005

Entry

16

14

12

10

8

6

4

2

0

0

10

20

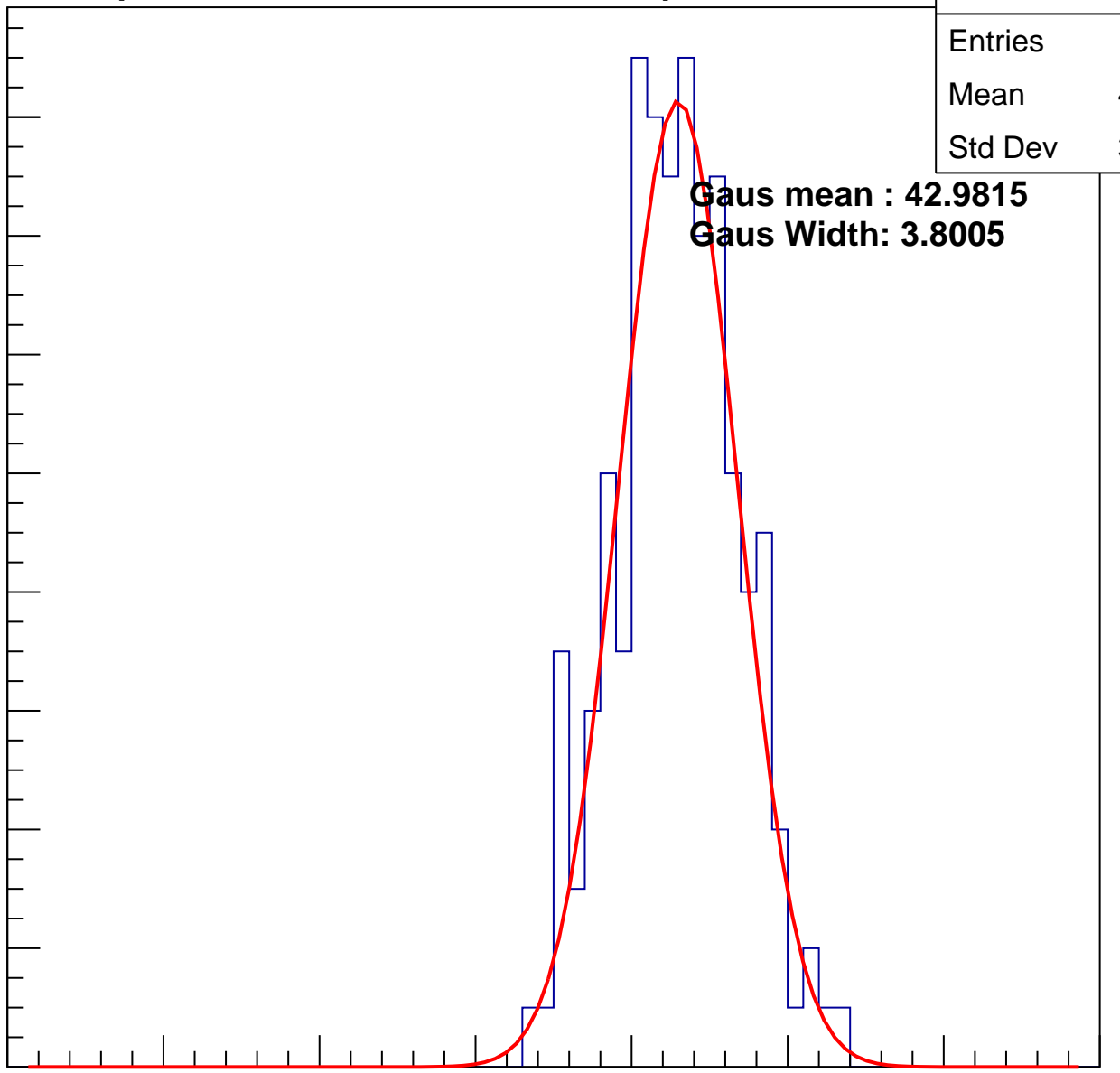
30

40

50

60

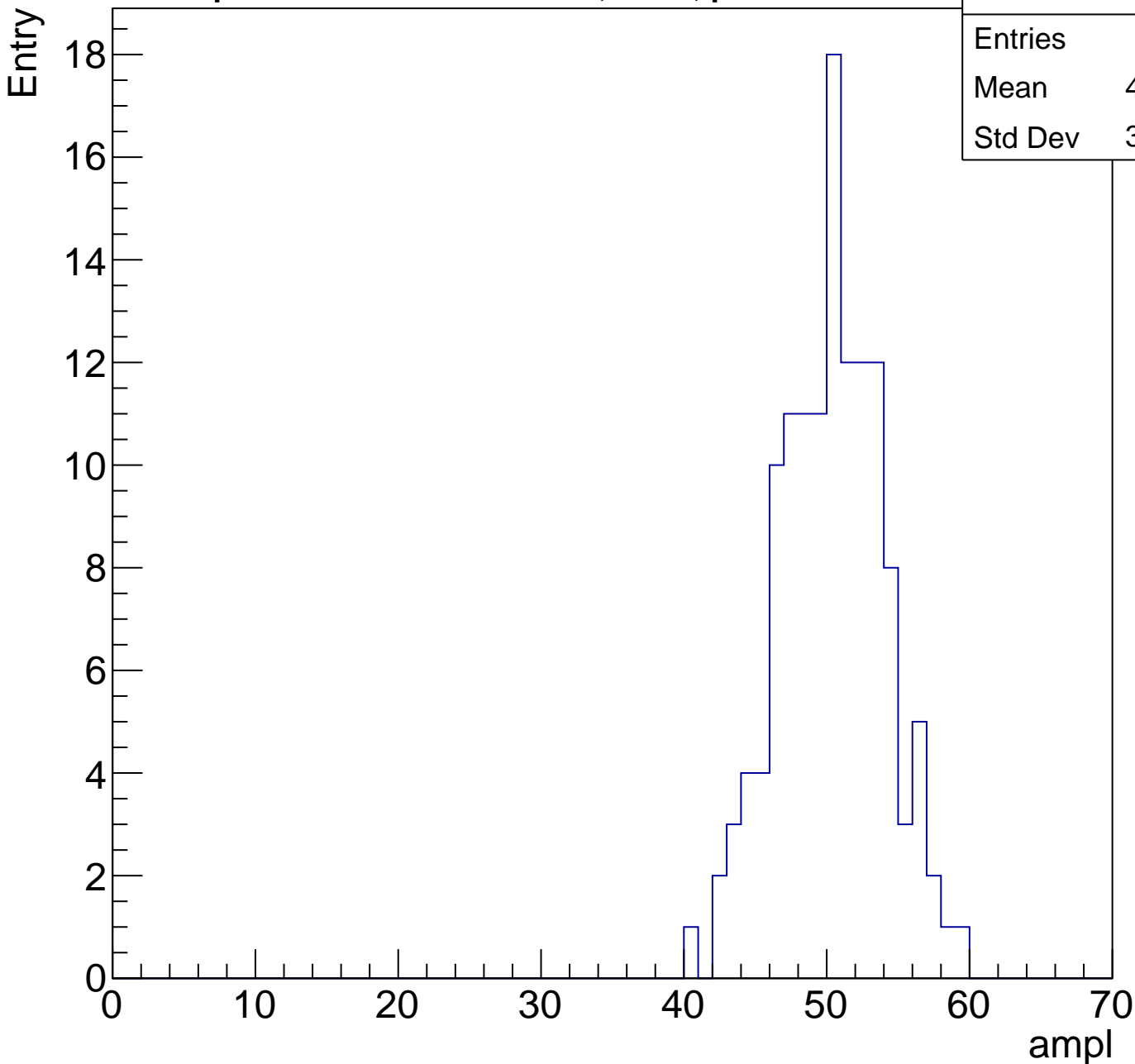
ampl



B0L101S, U20-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	49.87
Std Dev	3.629



B0L101S, U20-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

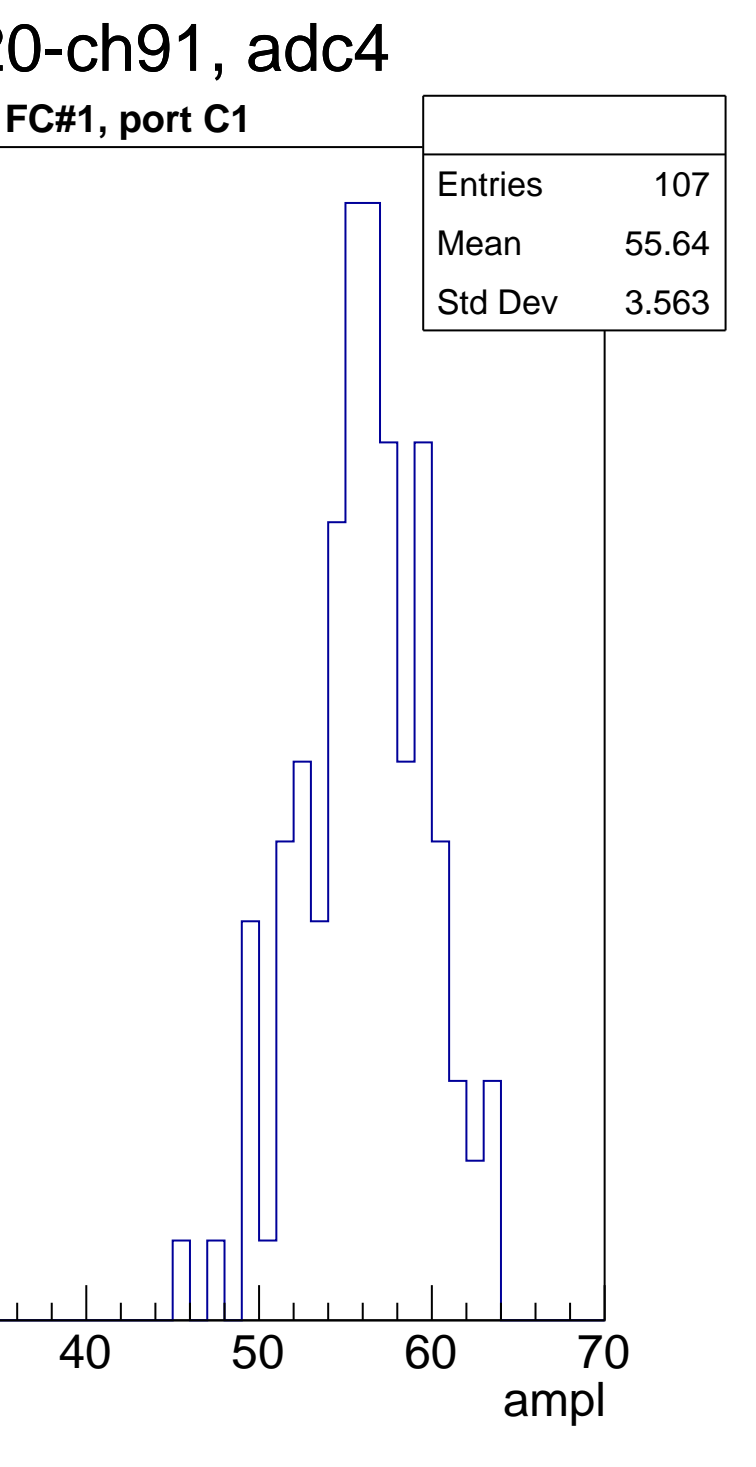
Entries	107
Mean	55.64
Std Dev	3.563

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch91, adc5

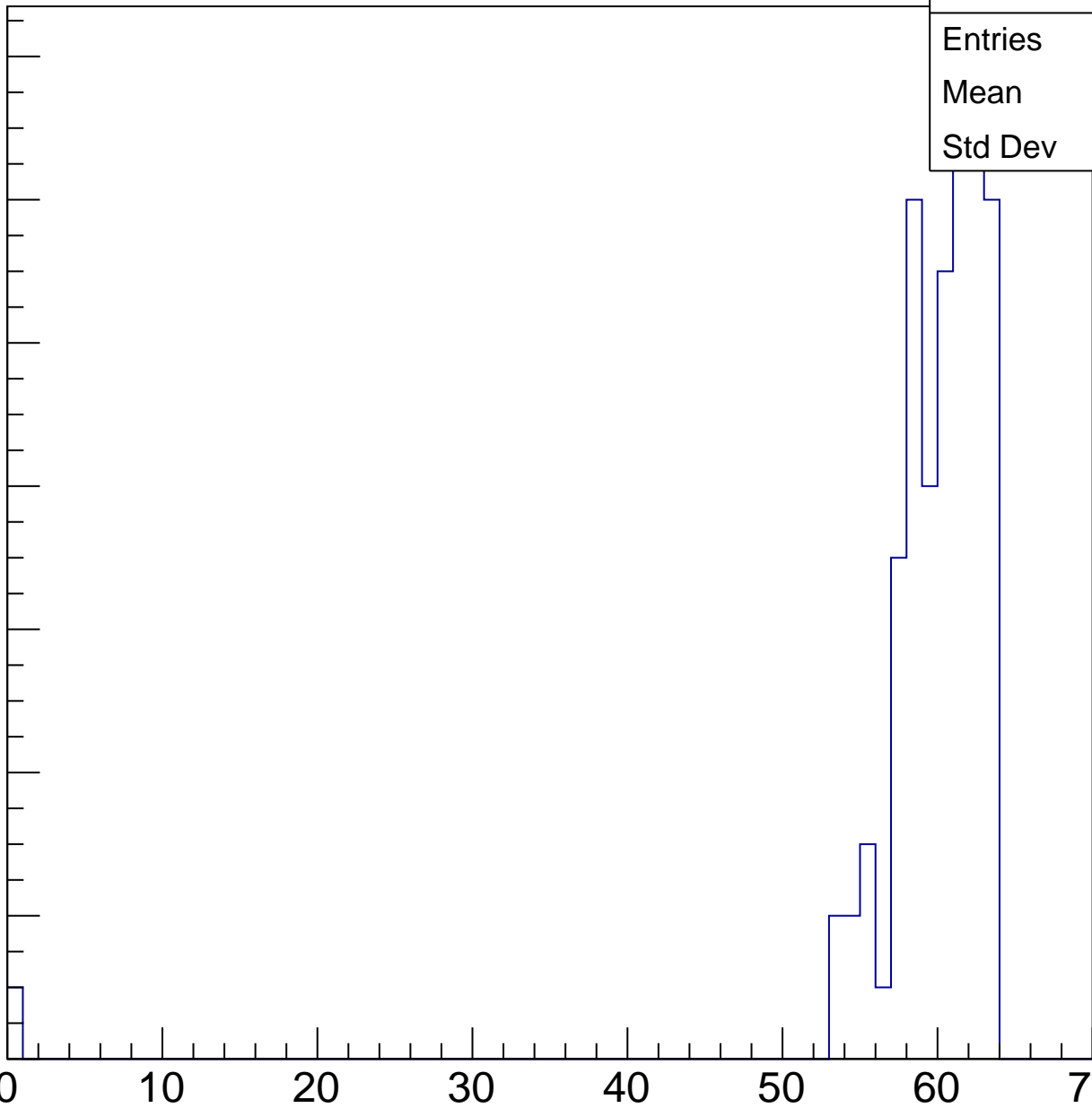
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	59.08
Std Dev	6.852

ampl

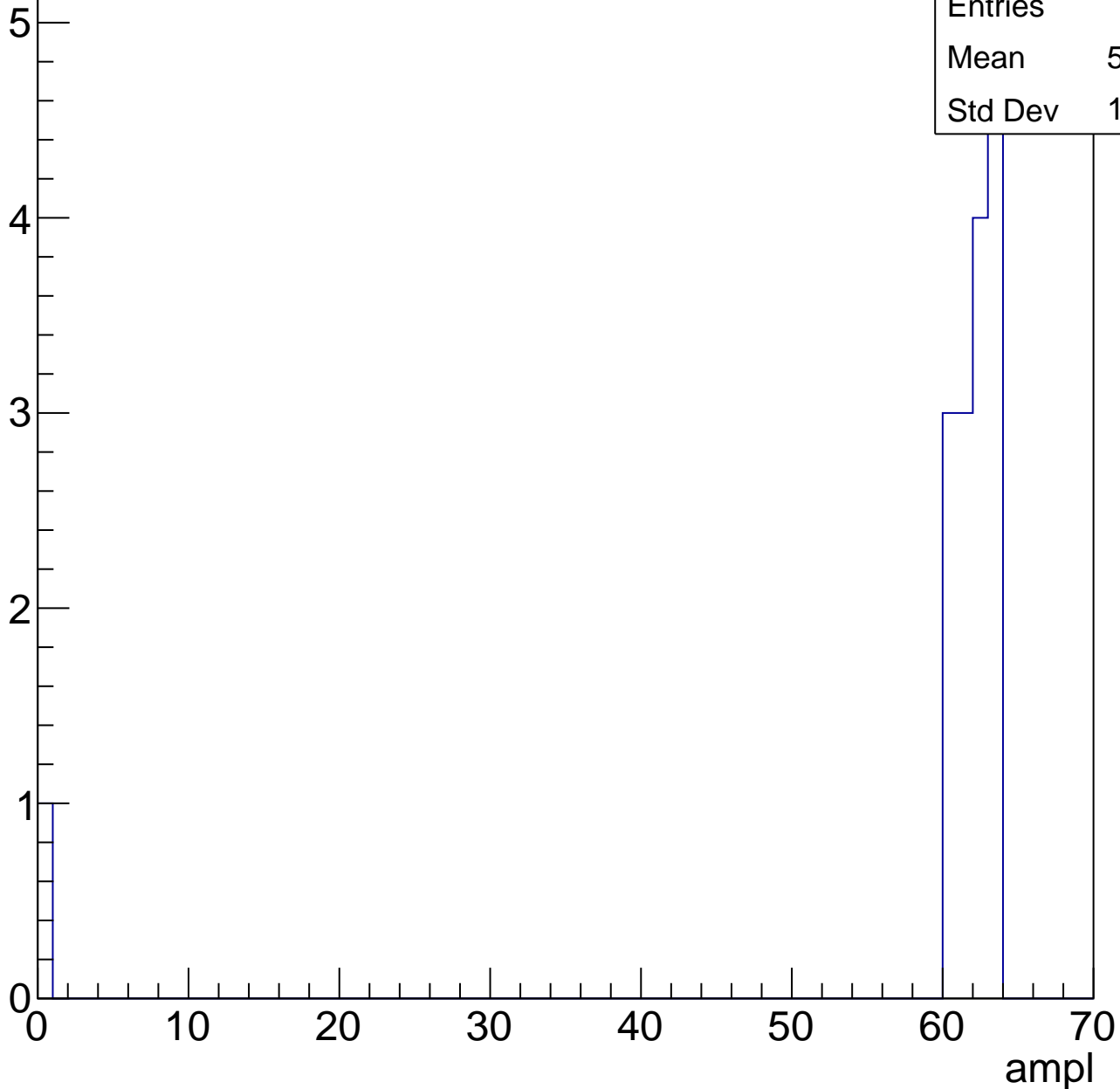


B0L101S, U20-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	57.88
Std Dev	14.98



B0L101S, U20-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



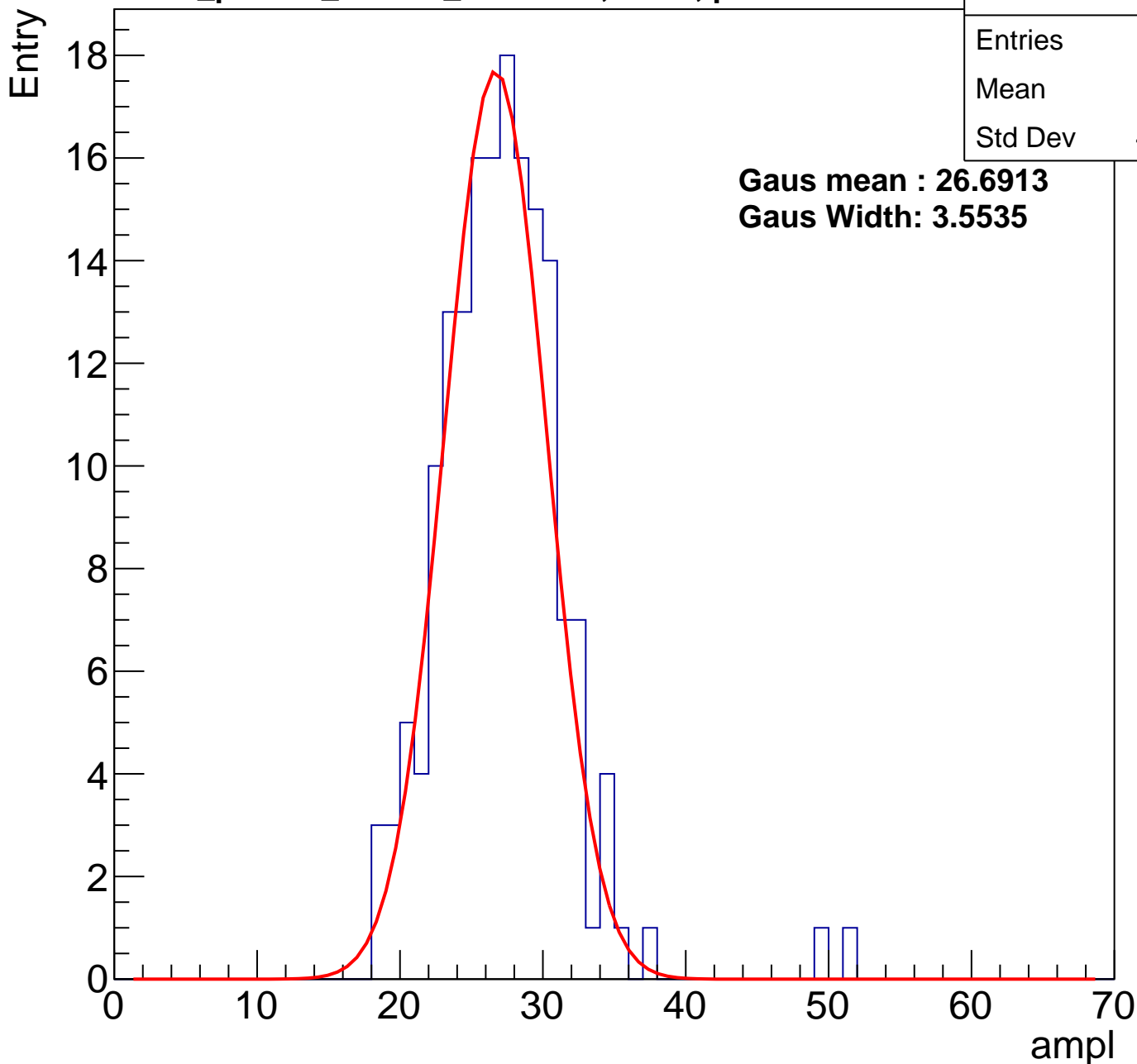
B0L101S, U20-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	26.7
Std Dev	4.491

Gaus mean : 26.6913

Gaus Width: 3.5535



B0L101S, U20-ch92, adc1

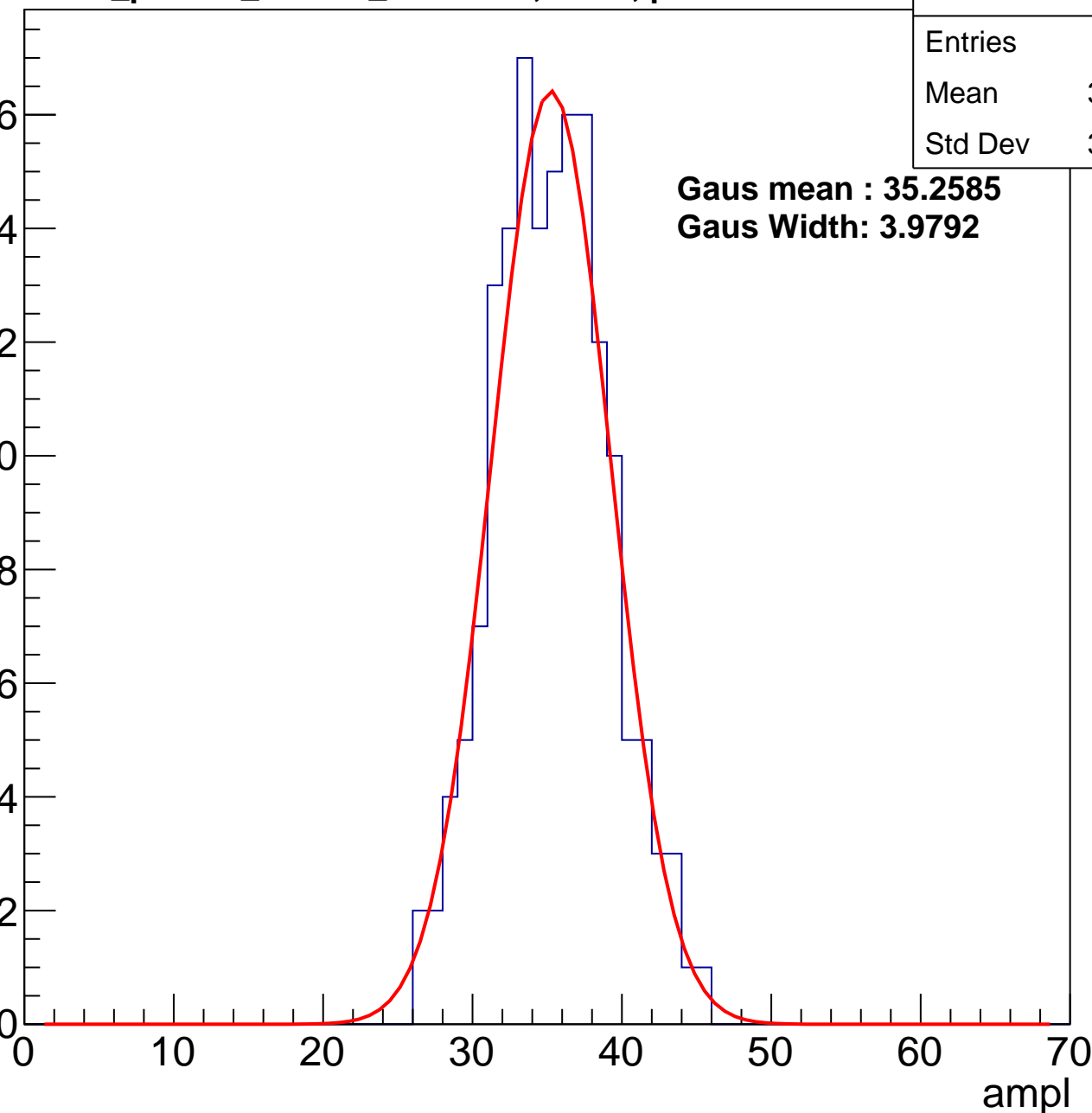
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	165
Mean	34.84
Std Dev	3.834

Gaus mean : 35.2585
Gaus Width: 3.9792



B0L101S, U20-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

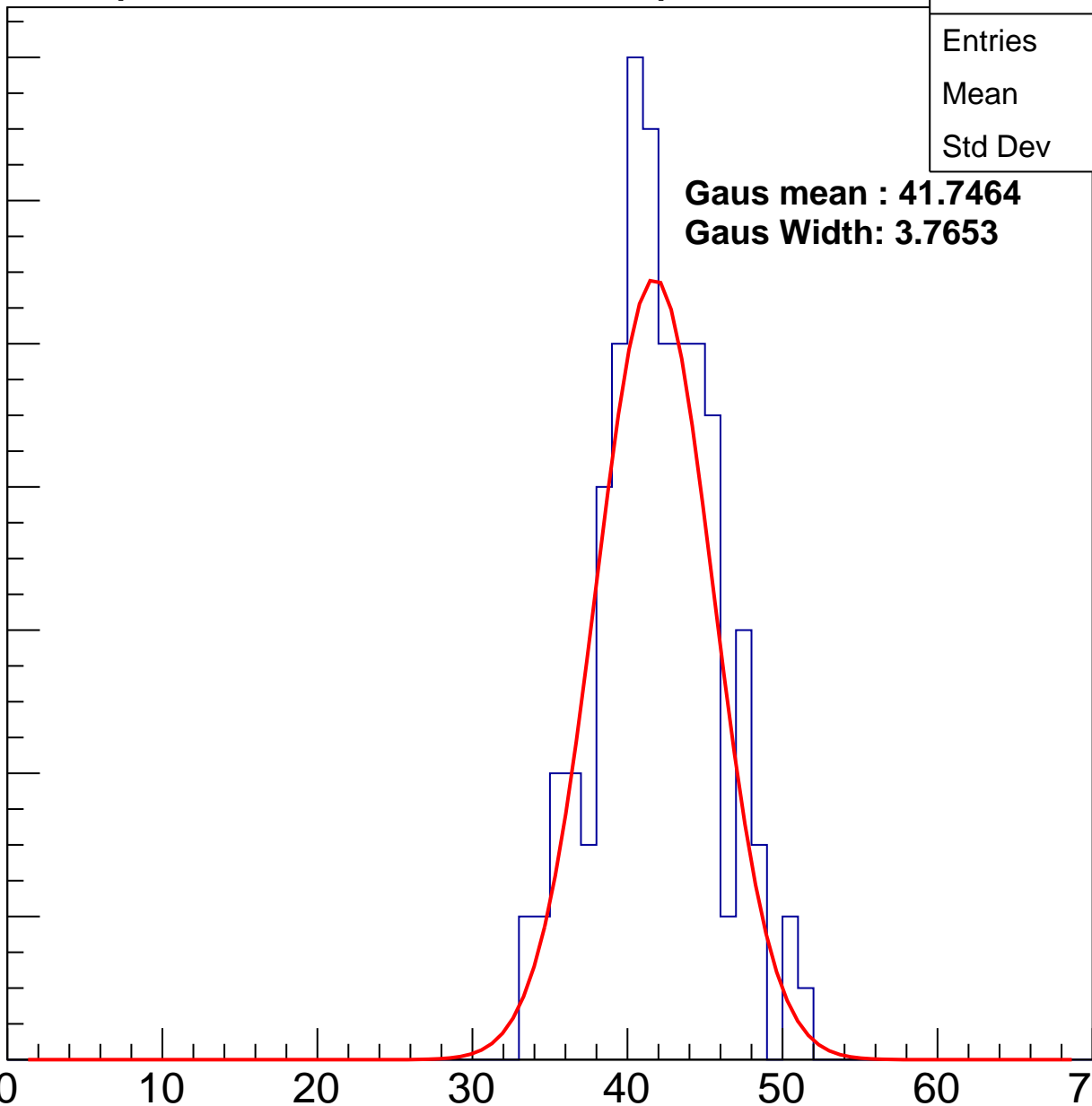
Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	41.42
Std Dev	3.743

Gaus mean : 41.7464
Gaus Width: 3.7653

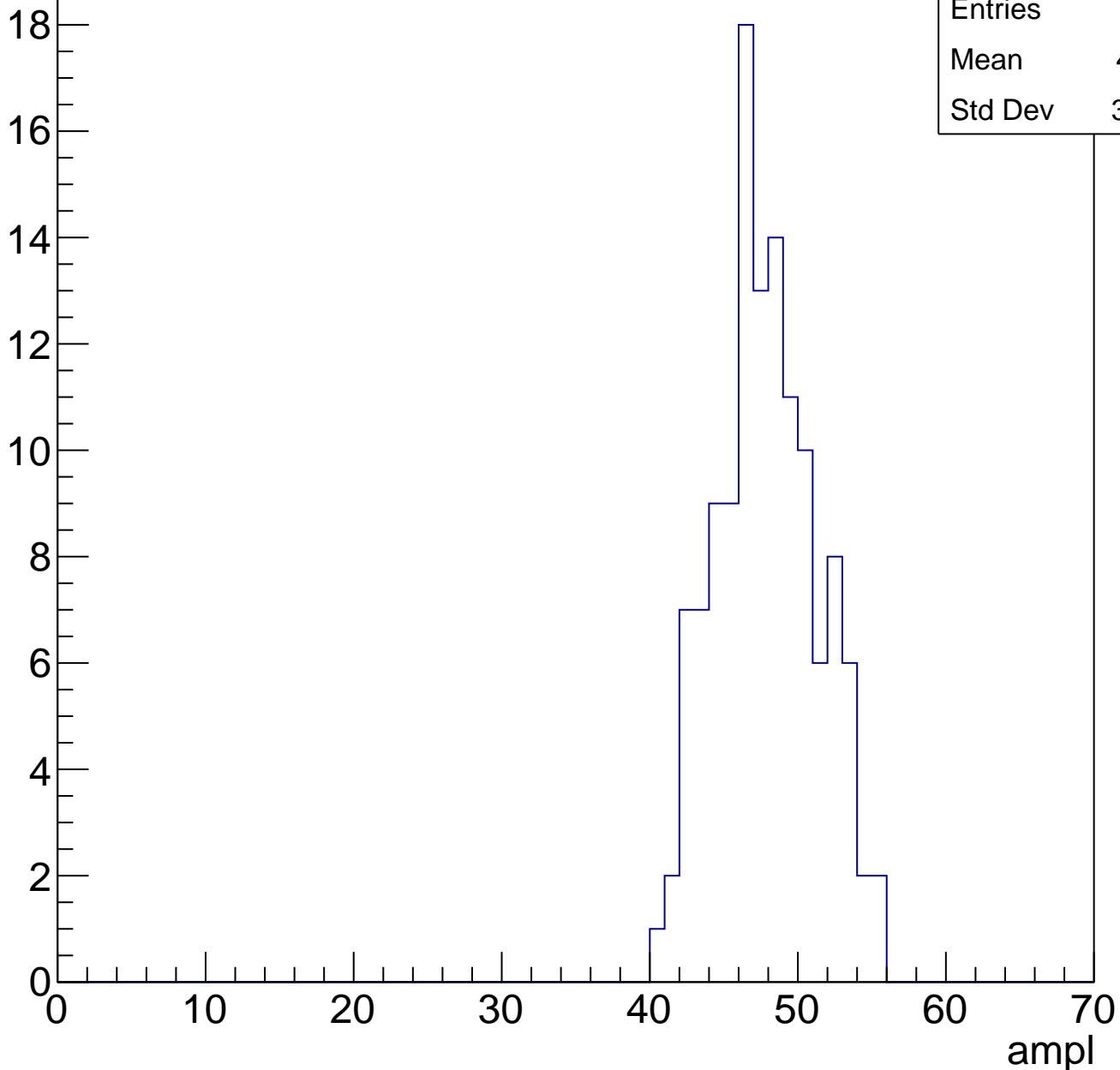
0 10 20 30 40 50 60 70
ampl



B0L101S, U20-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



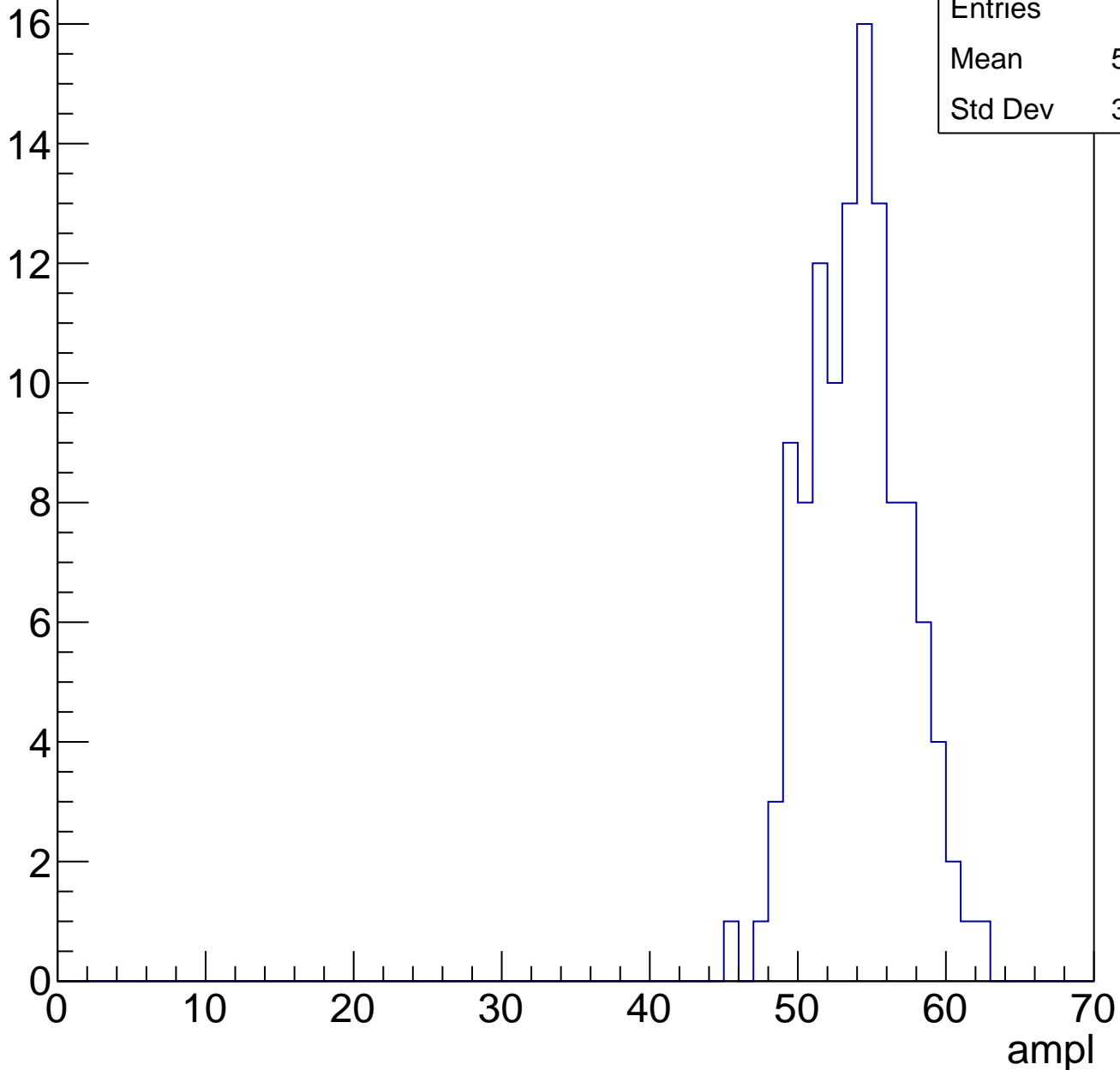
Entries	125
Mean	47.41
Std Dev	3.356

B0L101S, U20-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	53.52
Std Dev	3.247

Entry



B0L101S, U20-ch92, adc5

calib_packv5_042523_0143.root, FC#1, port C1

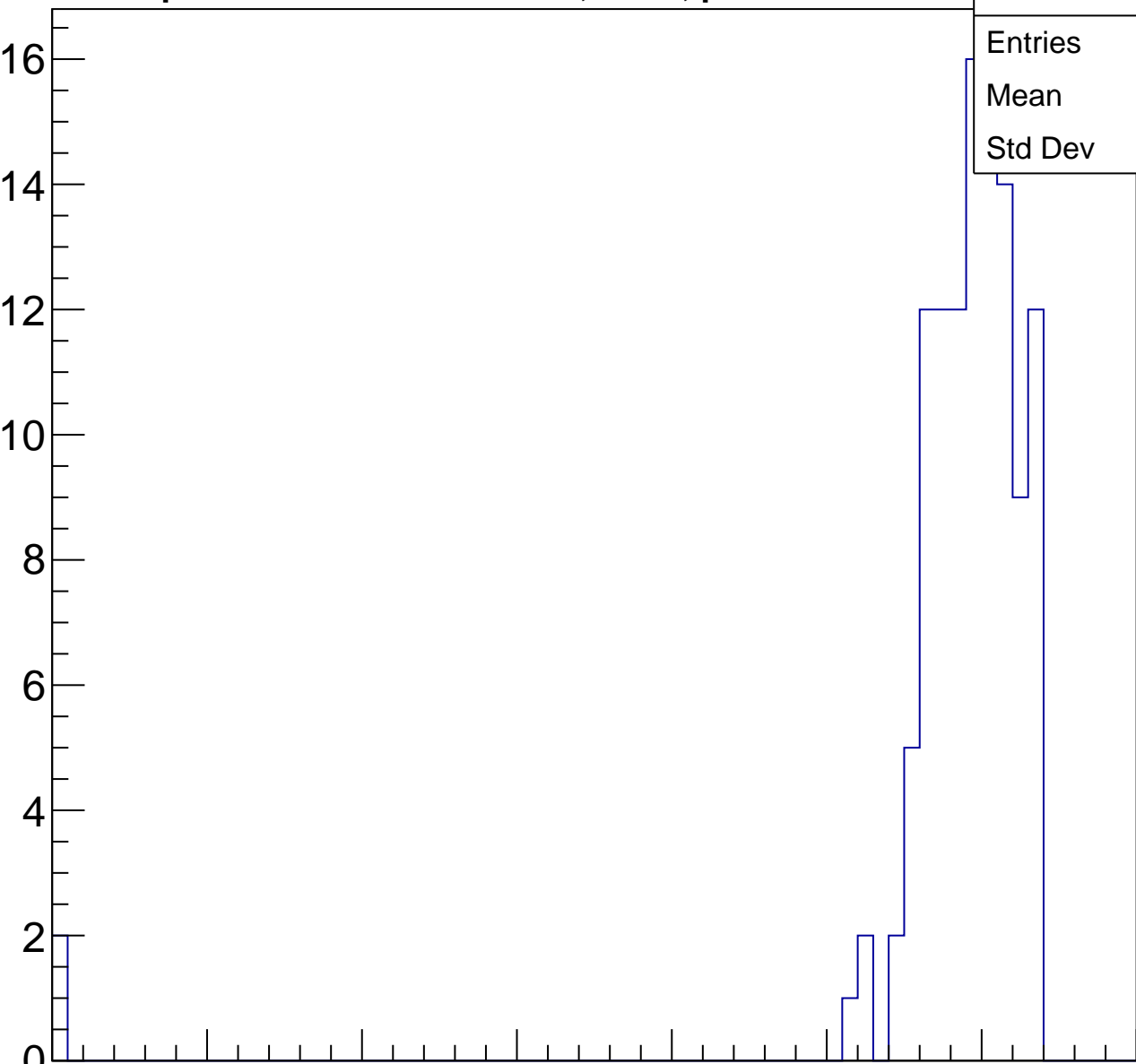
Entries	115
Mean	57.93
Std Dev	8.148

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

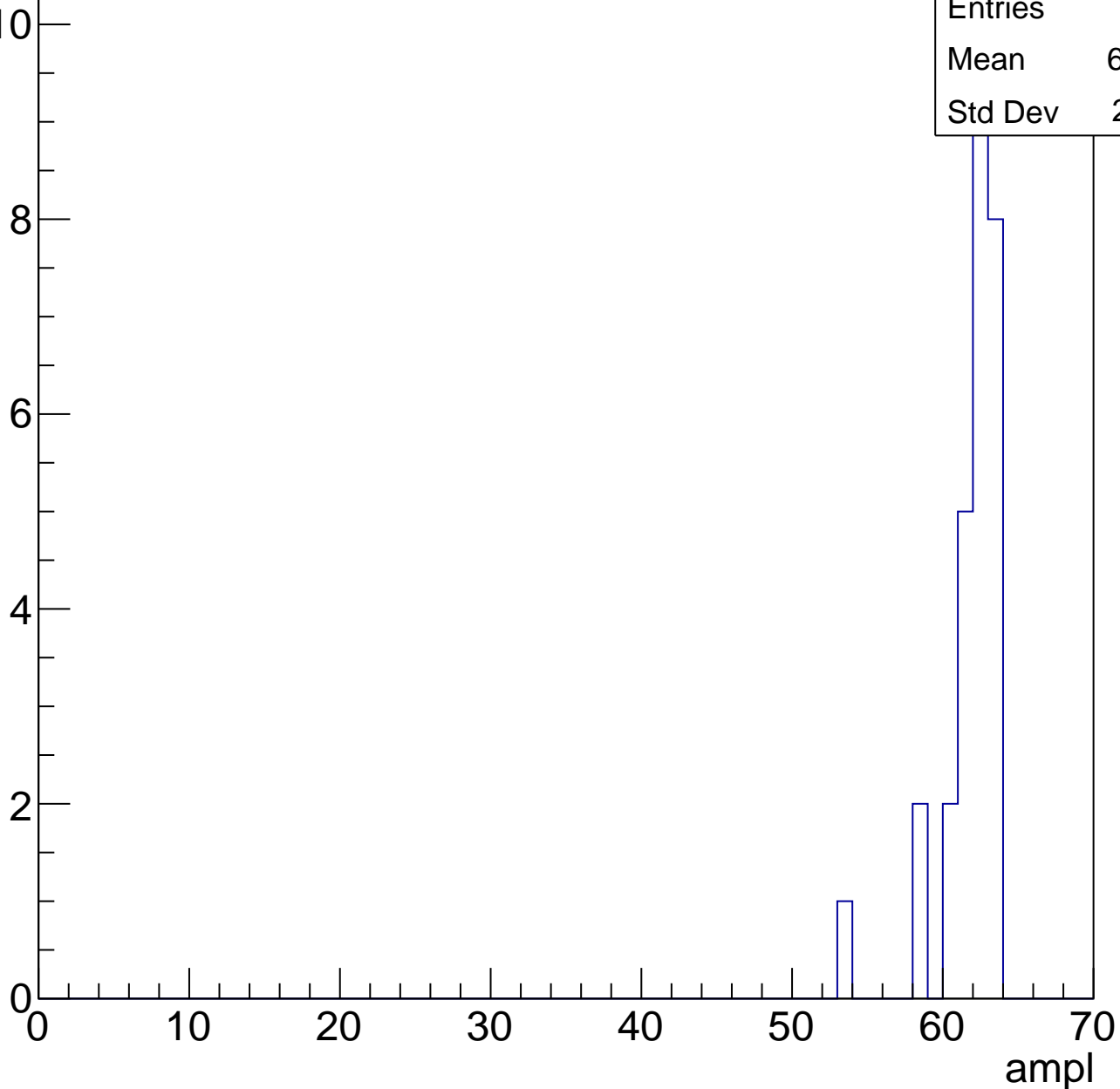


B0L101S, U20-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	61.36
Std Dev	2.091



B0L101S, U20-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

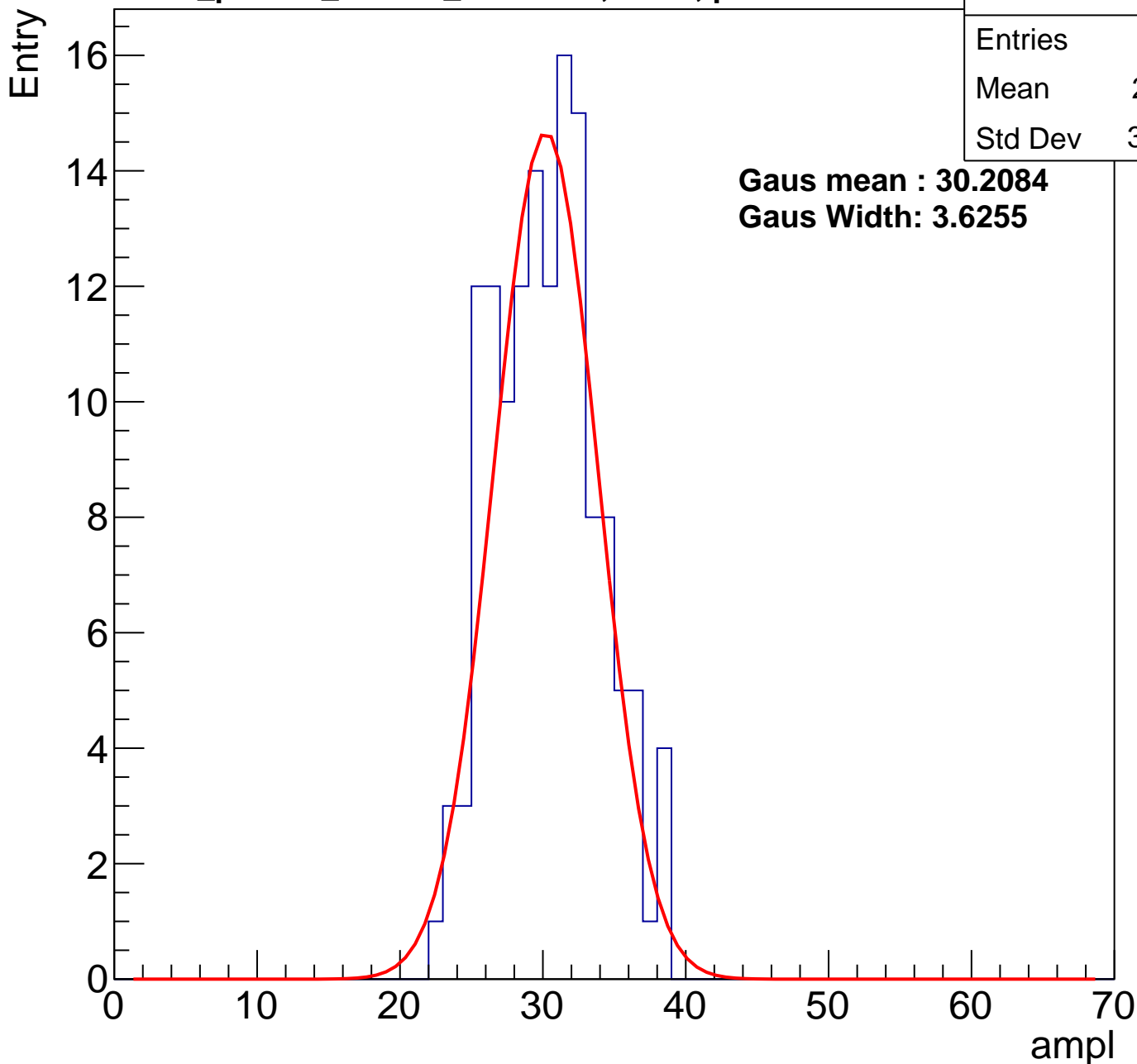
B0L101S, U20-ch93, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	29.81
Std Dev	3.616

Gaus mean : 30.2084

Gaus Width: 3.6255



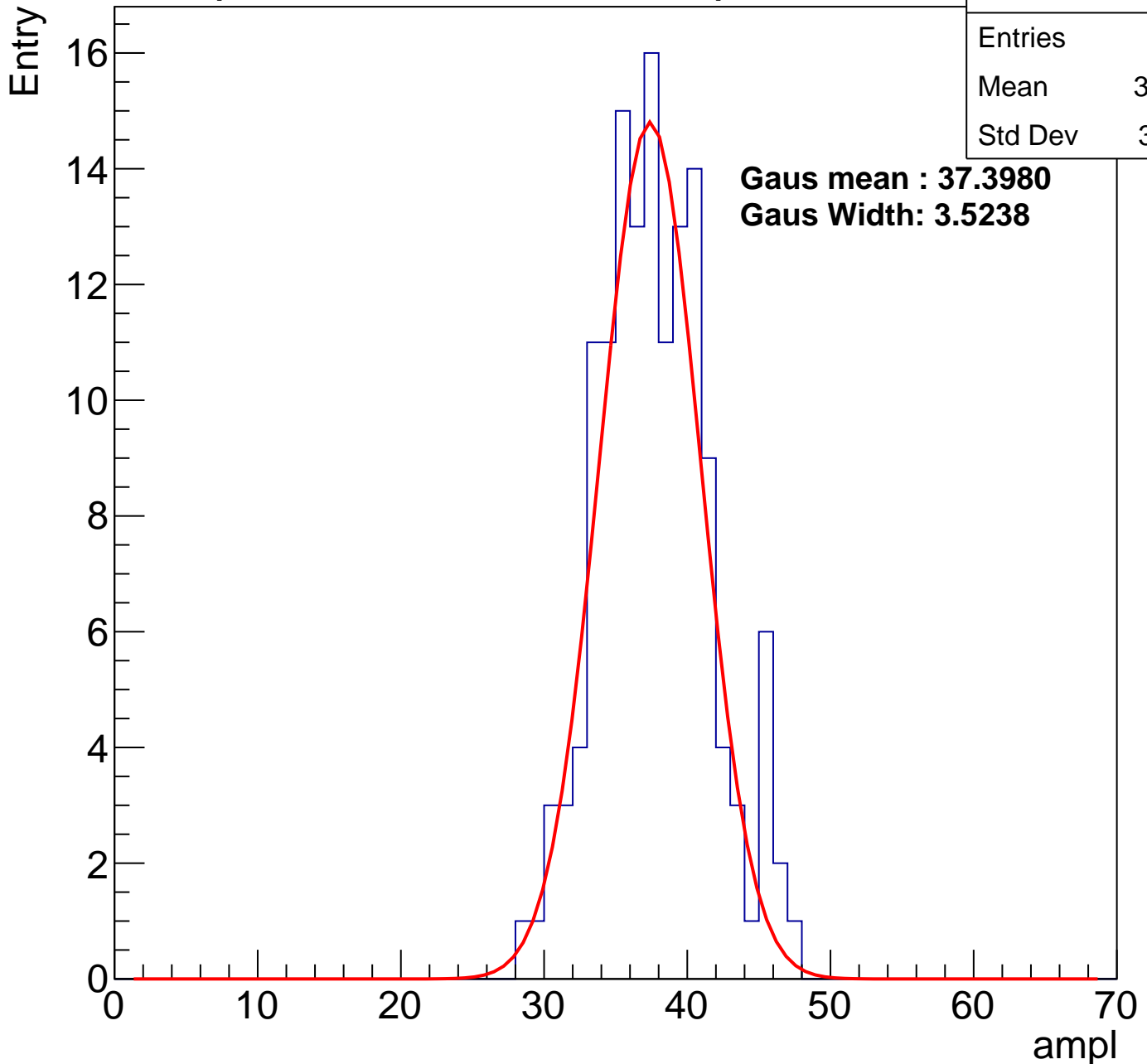
B0L101S, U20-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	37.28
Std Dev	3.821

Gaus mean : 37.3980

Gaus Width: 3.5238



B0L101S, U20-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

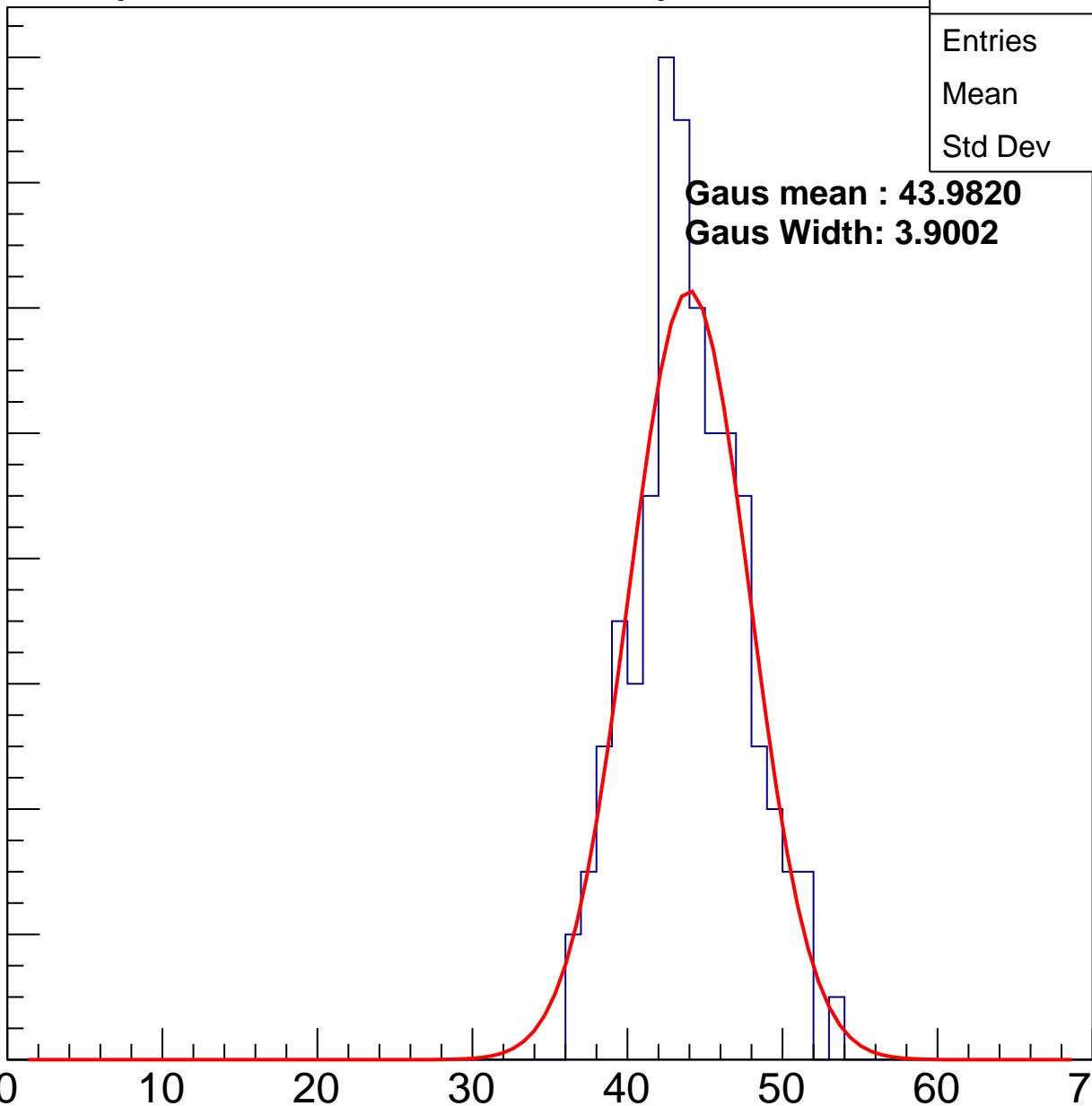
16
14
12
10
8
6
4
2
0

Entries	120
Mean	43.54
Std Dev	3.545

Gaus mean : 43.9820

Gaus Width: 3.9002

0 10 20 30 40 50 60 70
ampl

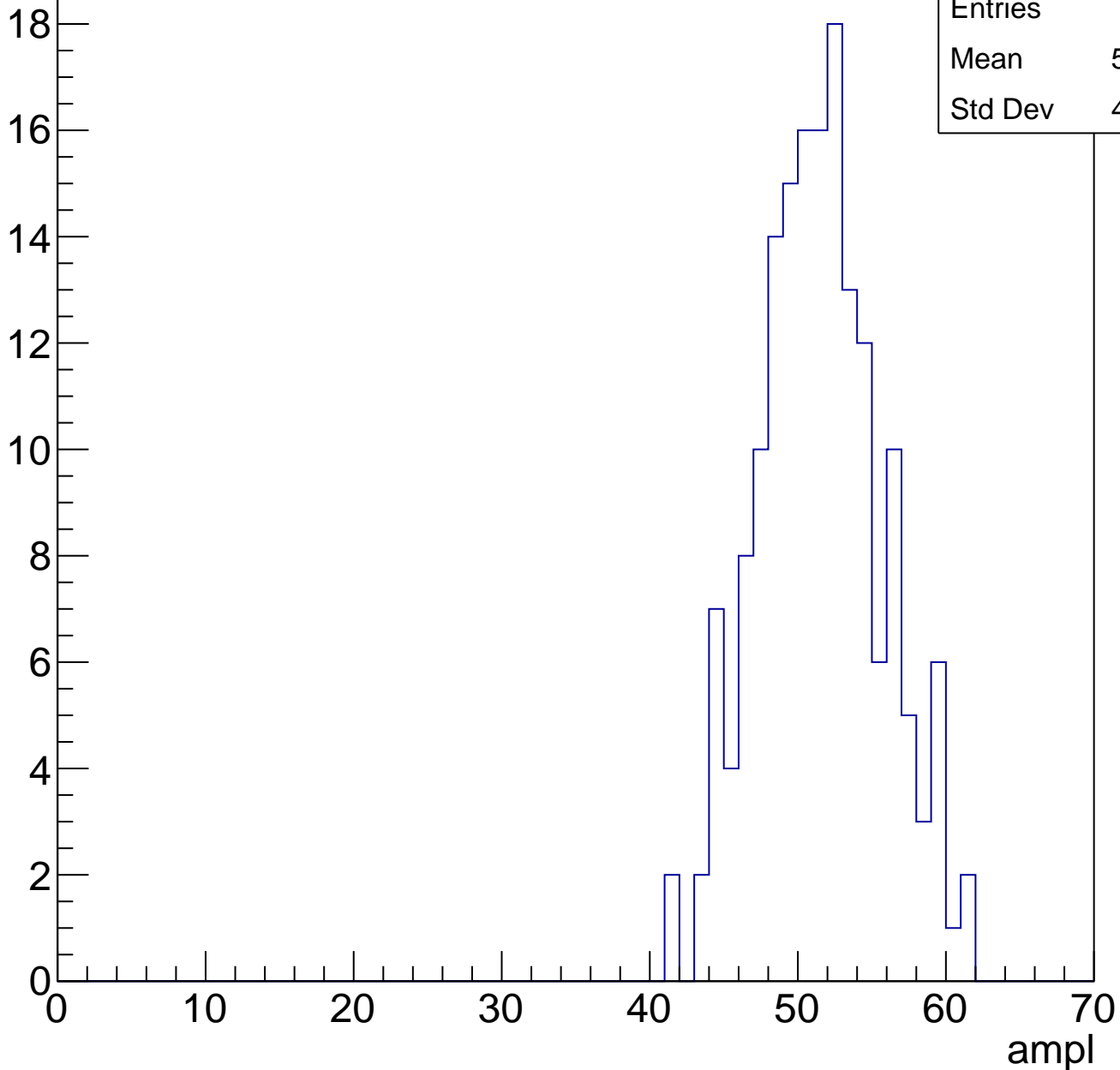


B0L101S, U20-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	51.03
Std Dev	4.125

Entry



B0L101S, U20-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

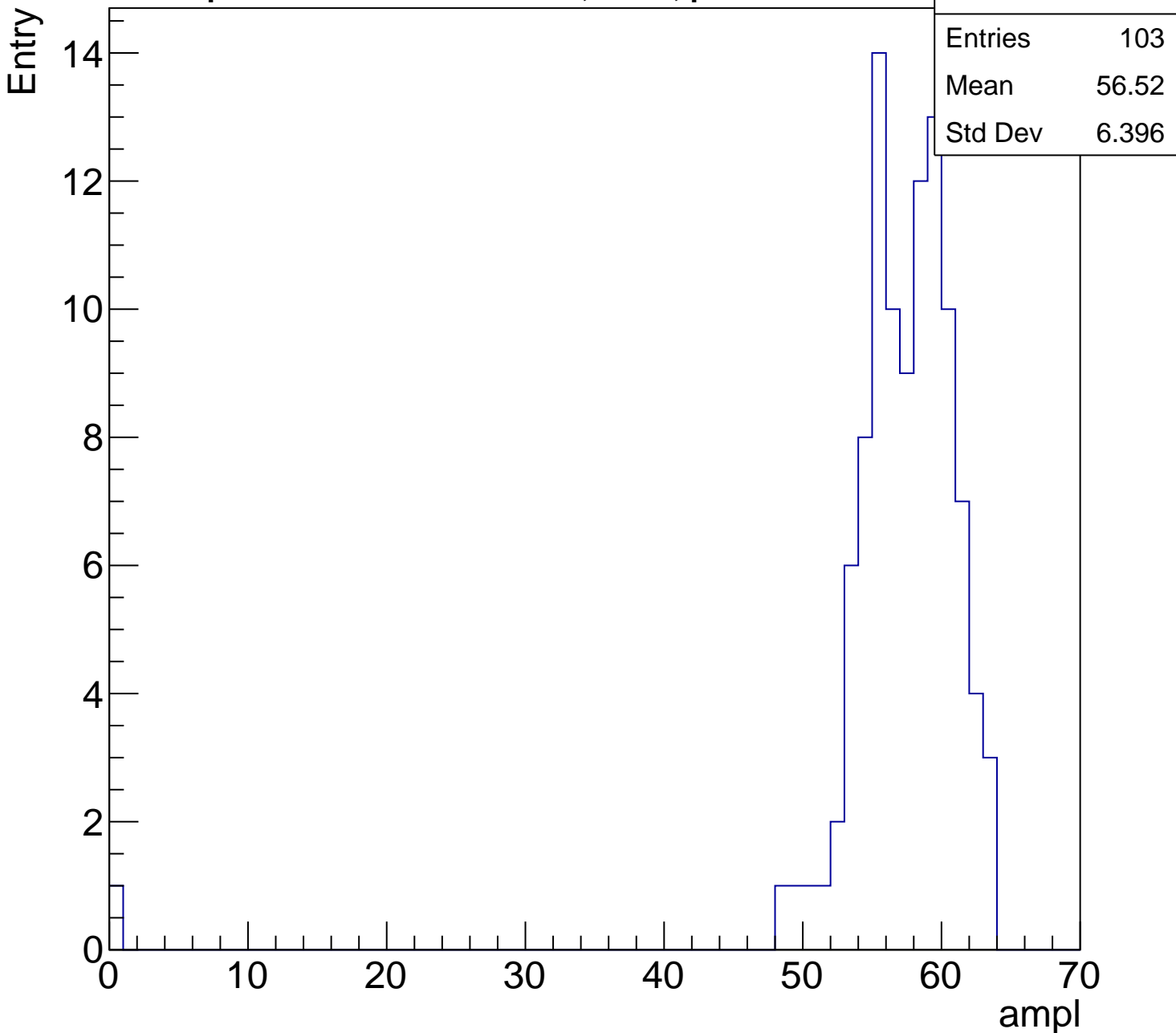
Entries	103
Mean	56.52
Std Dev	6.396

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch93, adc5

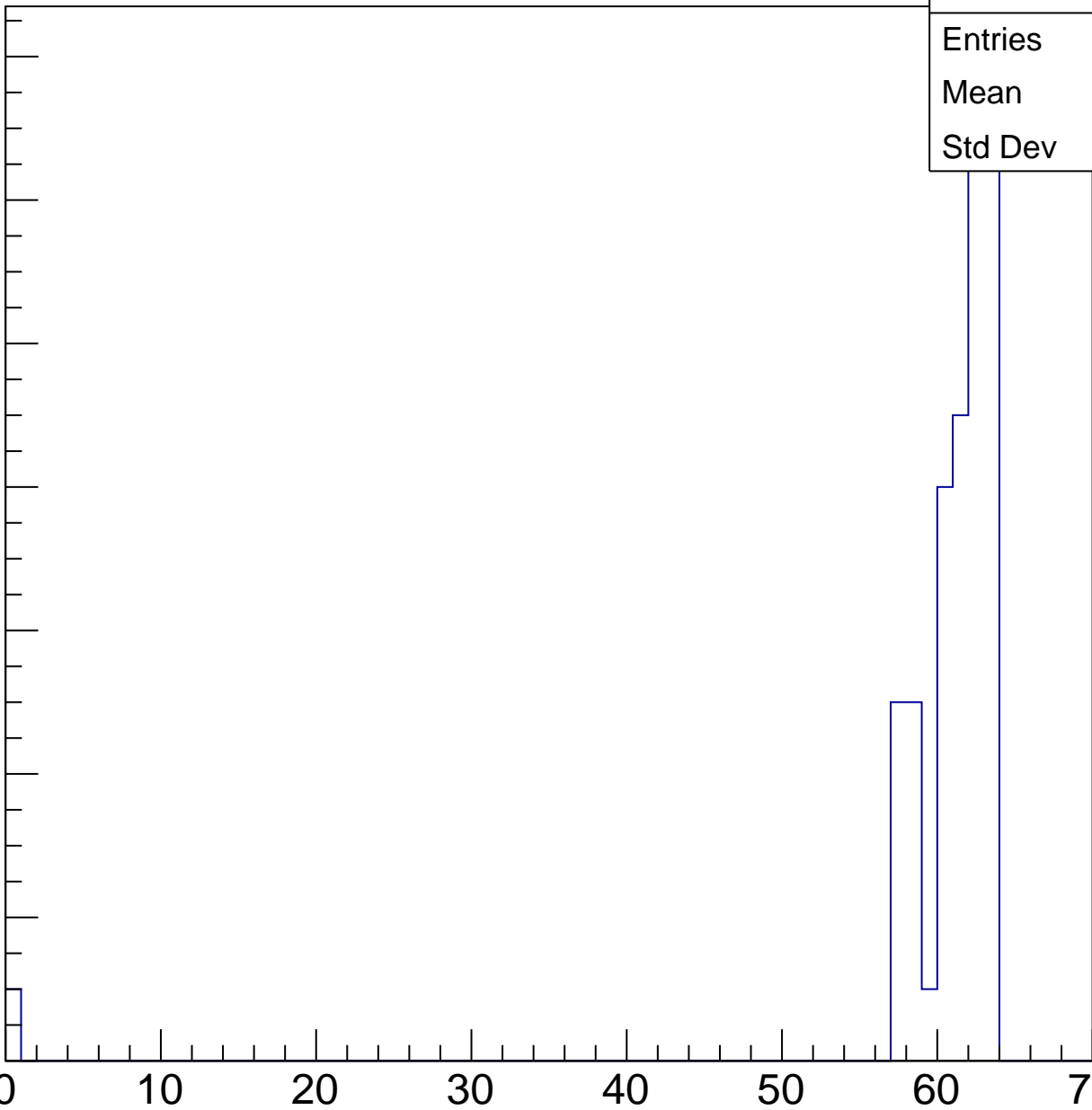
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	56
Mean	59.82
Std Dev	8.287

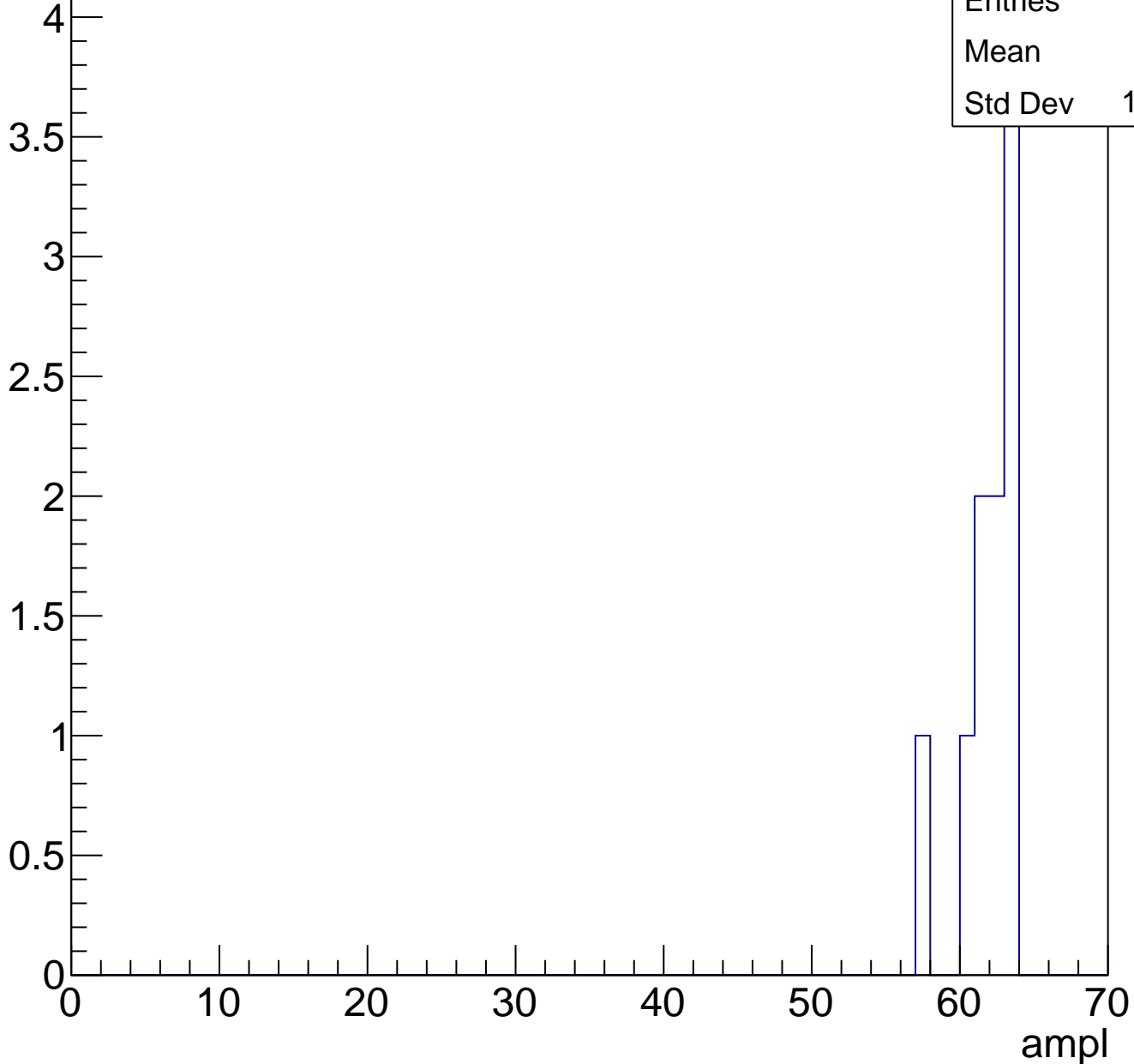
ampl



B0L101S, U20-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch94, adc0

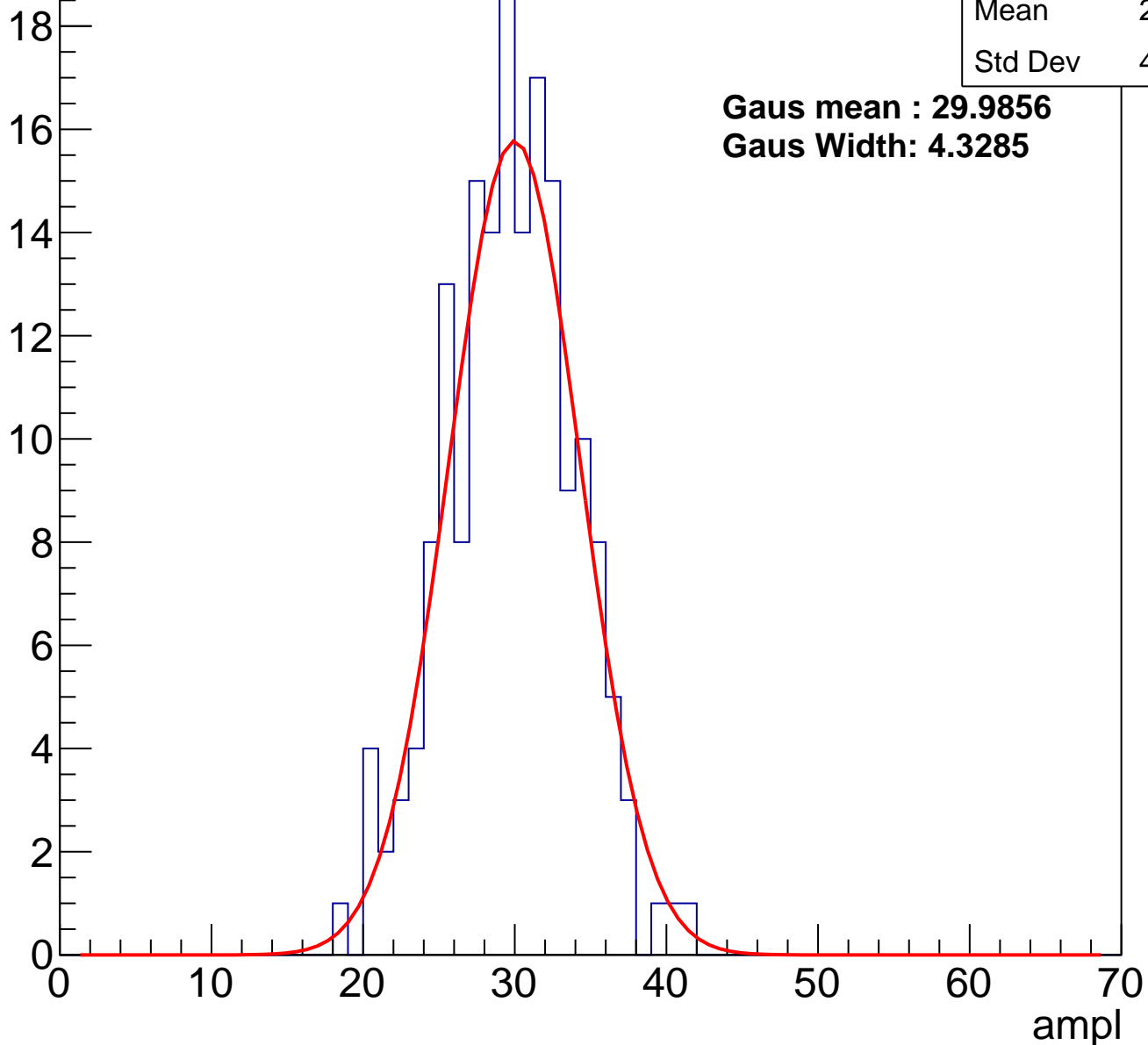
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	29.29
Std Dev	4.212

Gaus mean : 29.9856

Gaus Width: 4.3285

Entry



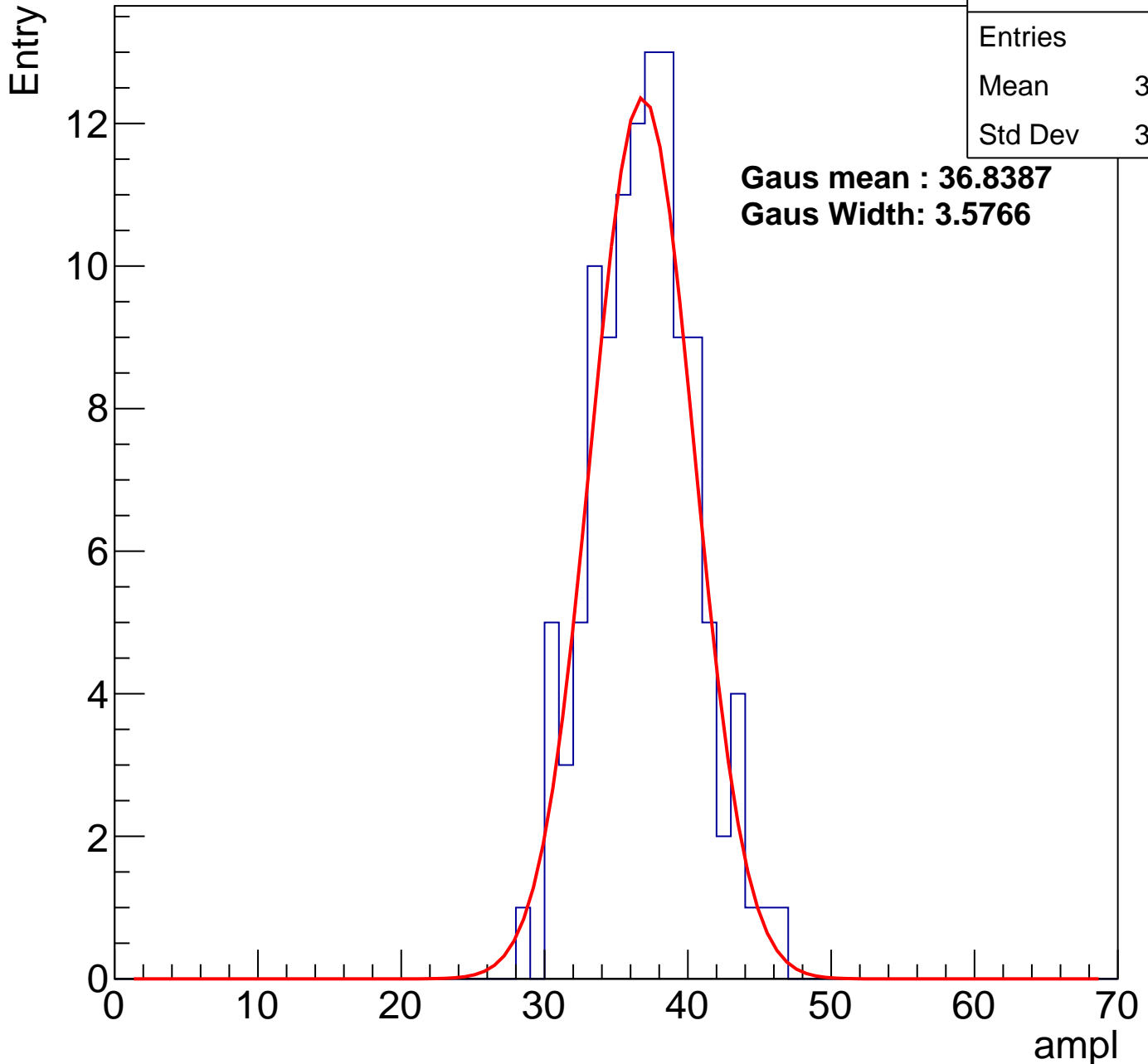
B0L101S, U20-ch94, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	36.54
Std Dev	3.542

Gaus mean : 36.8387

Gaus Width: 3.5766



B0L101S, U20-ch94, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

152

Mean

43.16

Std Dev

3.774

Gaus mean : 43.5171

Gaus Width: 3.6661

0

10

20

30

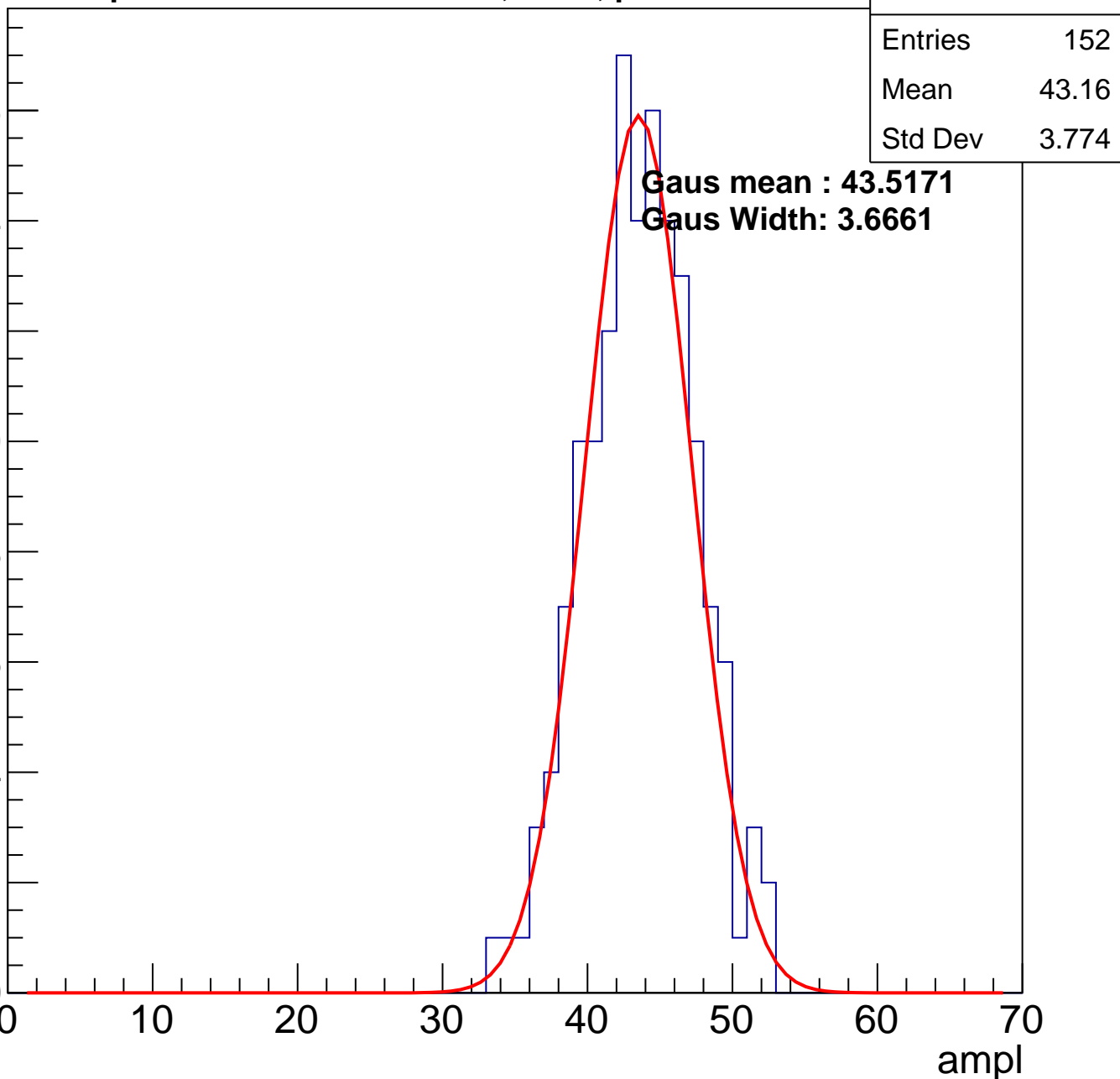
40

50

60

70

ampl



B0L101S, U20-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

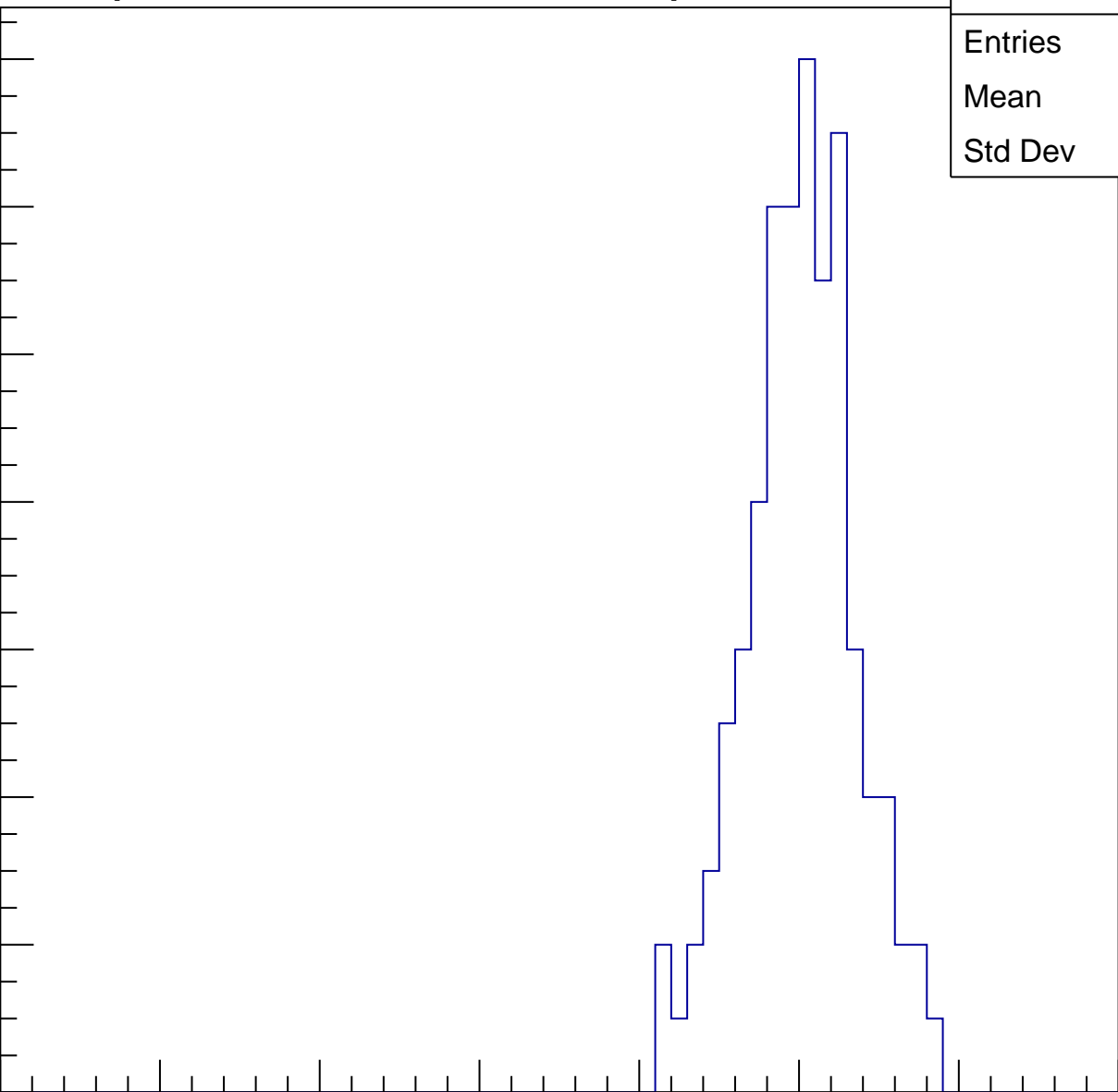
Entries	108
Mean	49.61
Std Dev	3.448

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch94, adc4

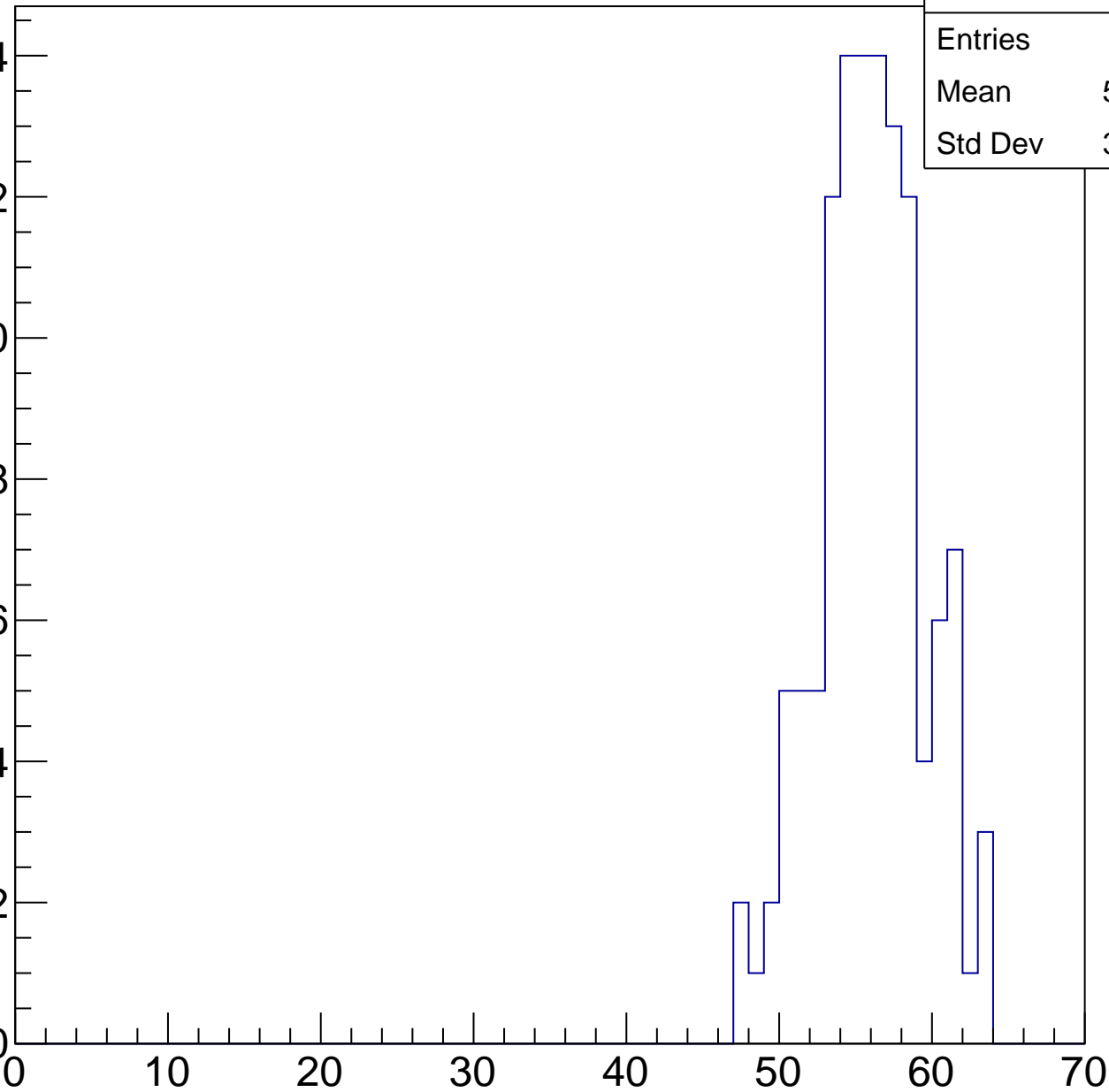
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.52
Std Dev	3.428

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch94, adc5

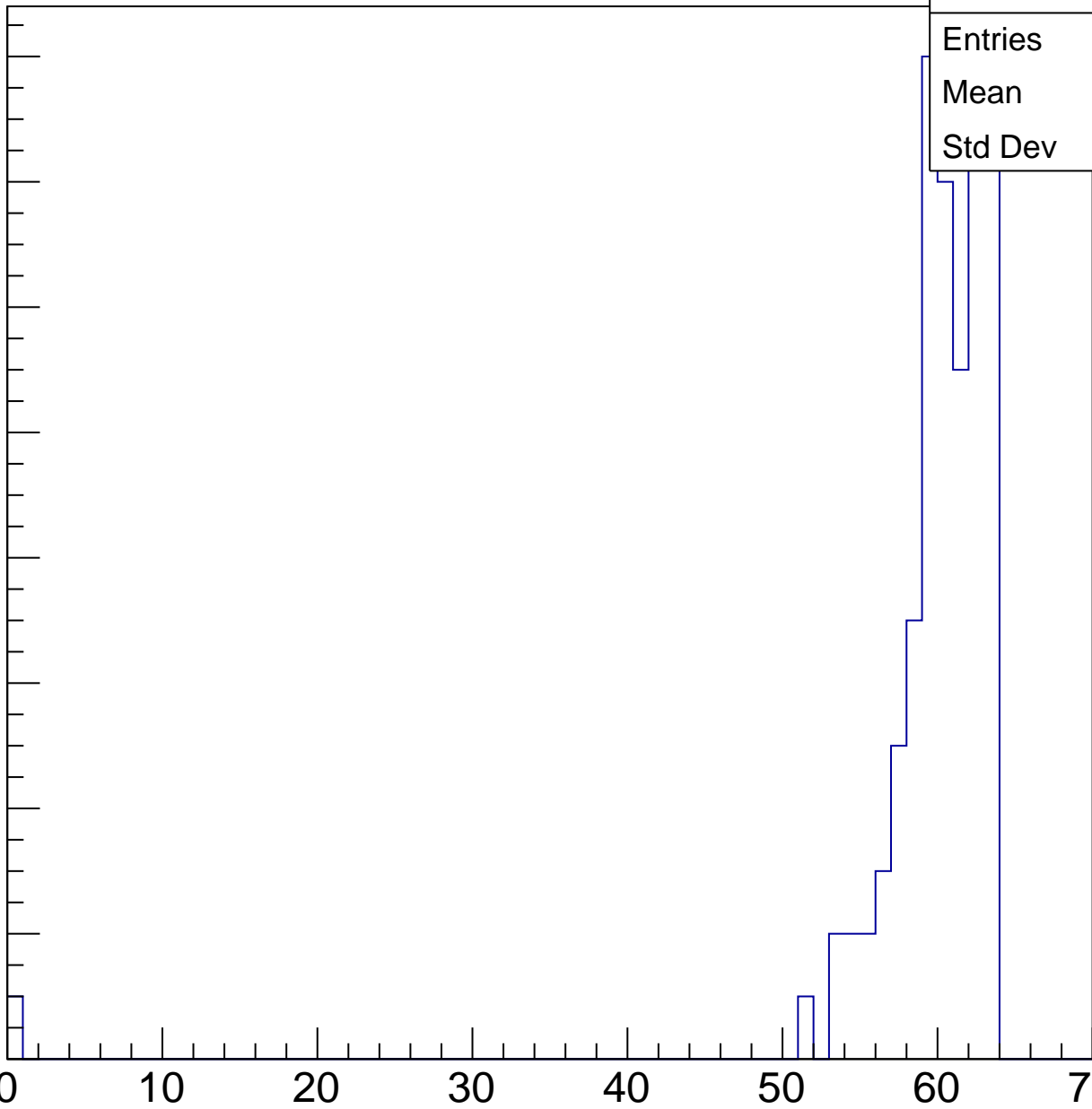
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.22
Std Dev	6.641

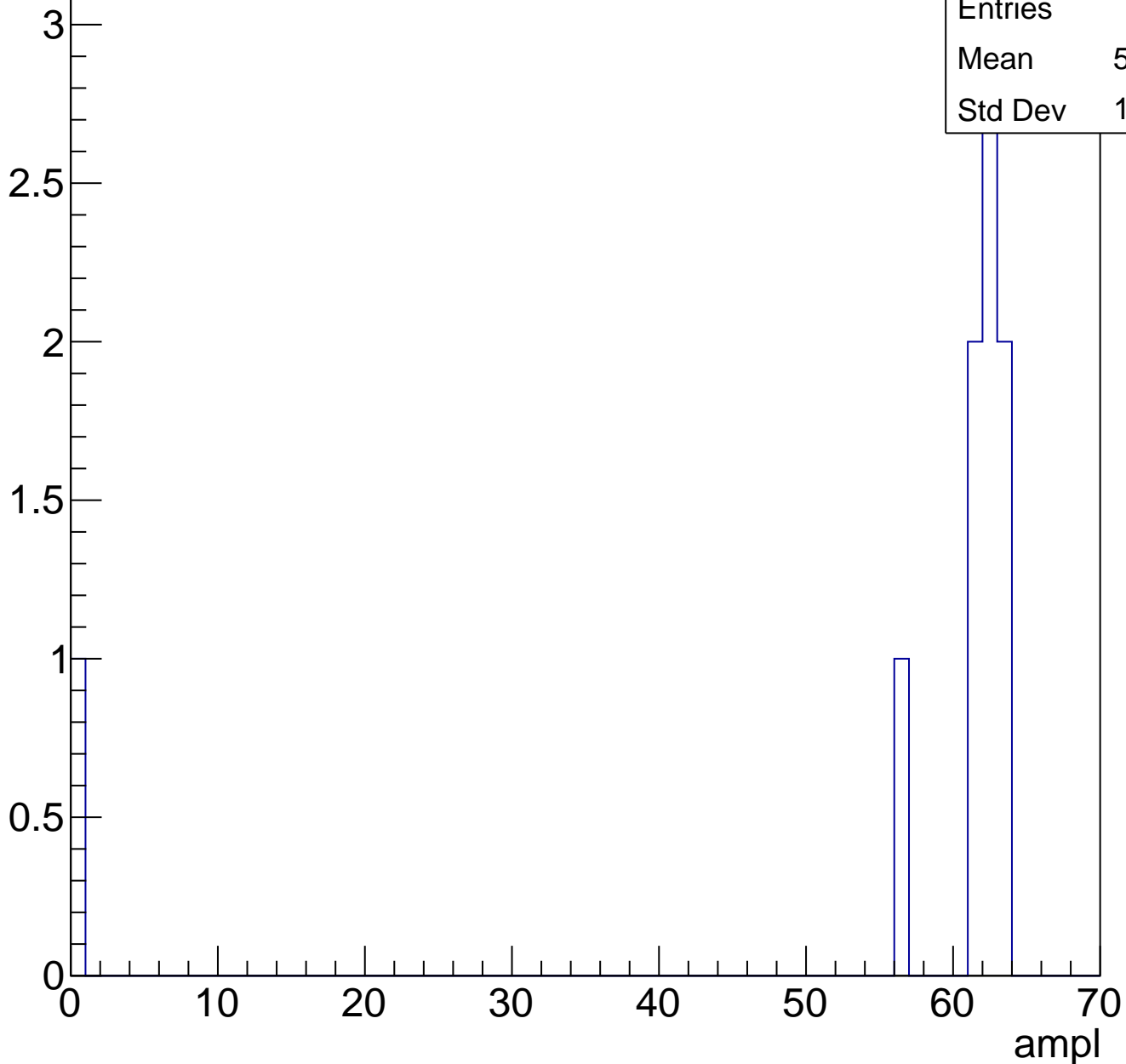
ampl



B0L101S, U20-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

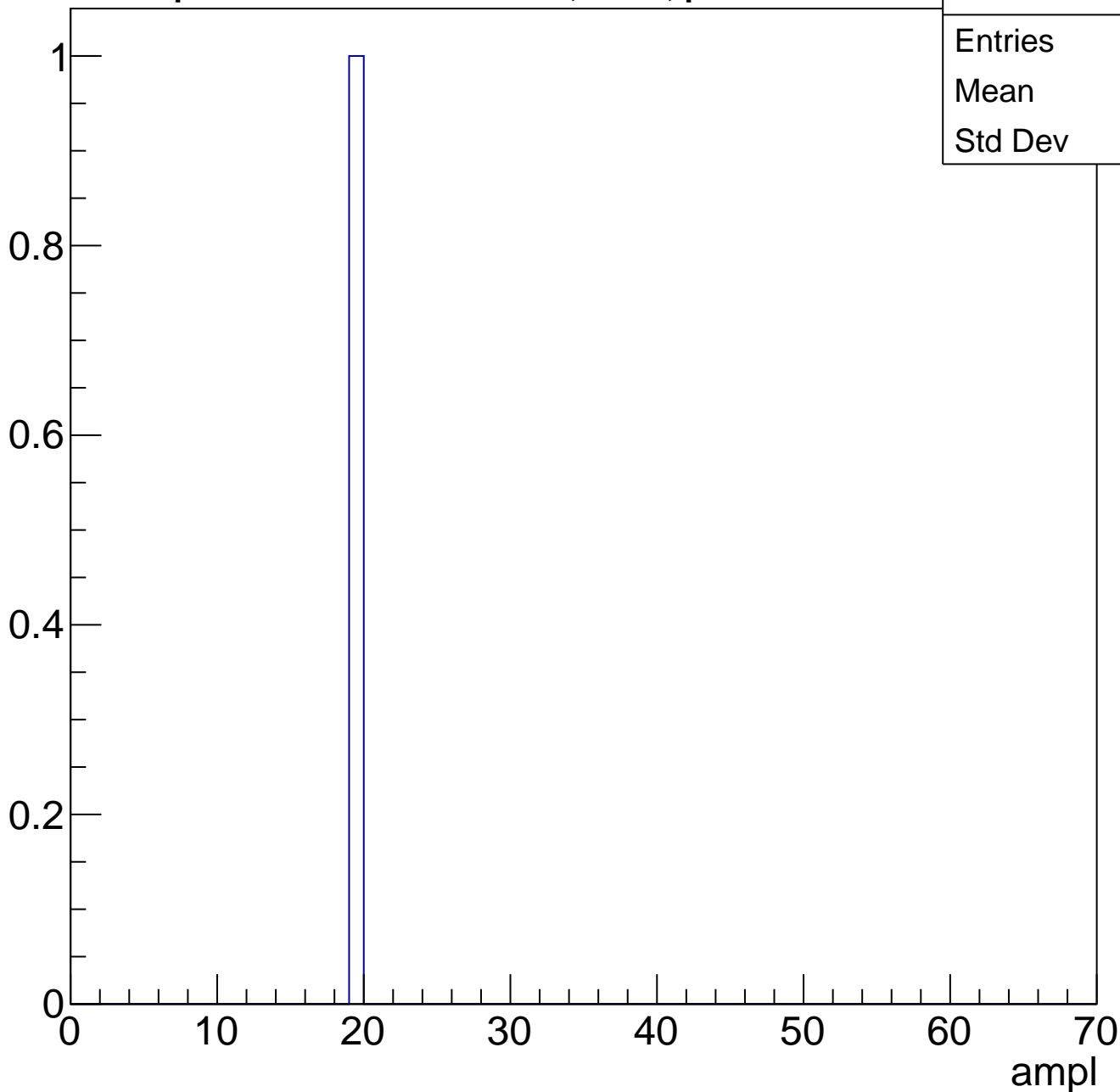
Entry



B0L101S, U20-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch95, adc0

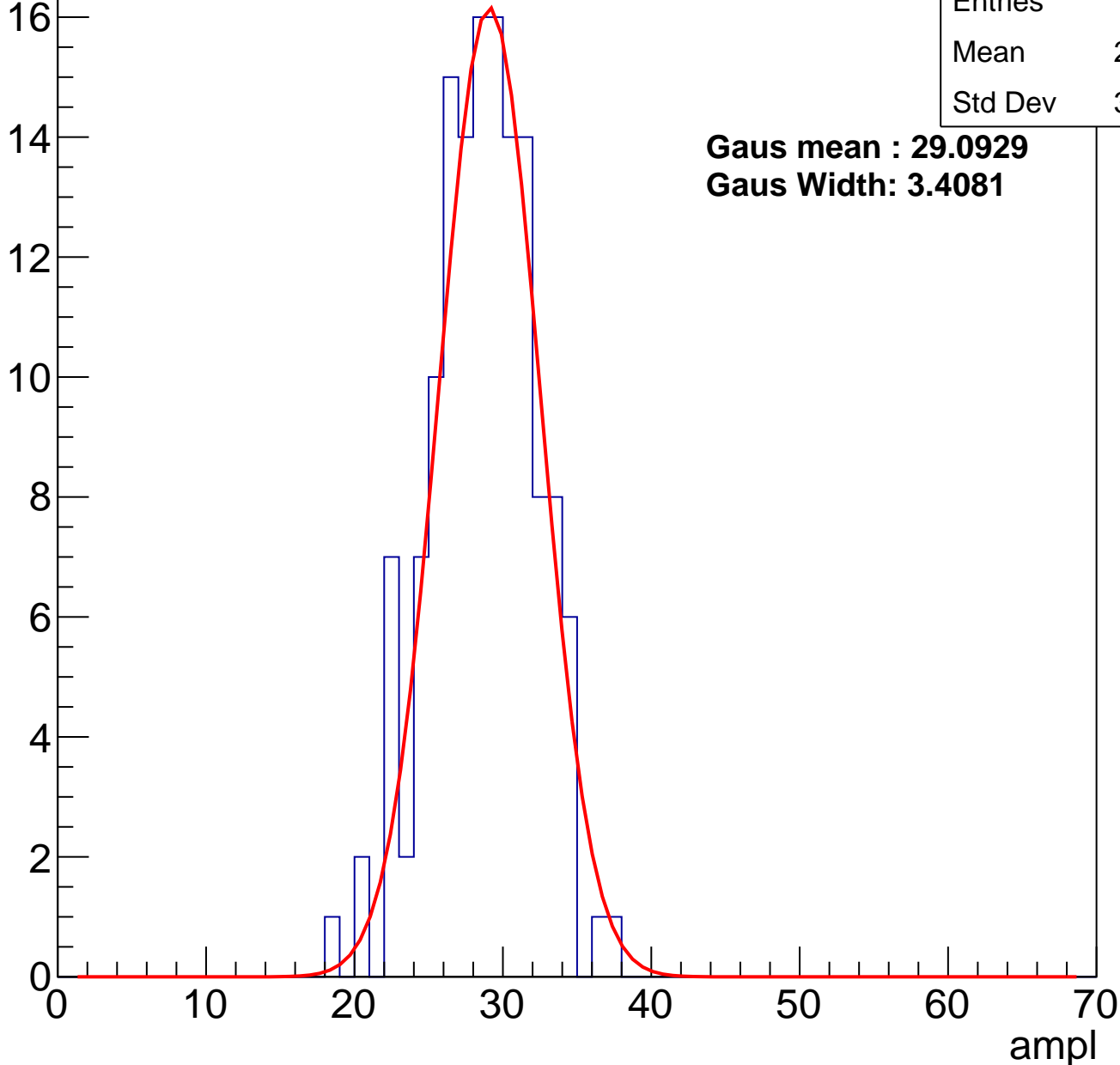
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.22
Std Dev	3.446

Gaus mean : 29.0929

Gaus Width: 3.4081

Entry



B0L101S, U20-ch95, adc1

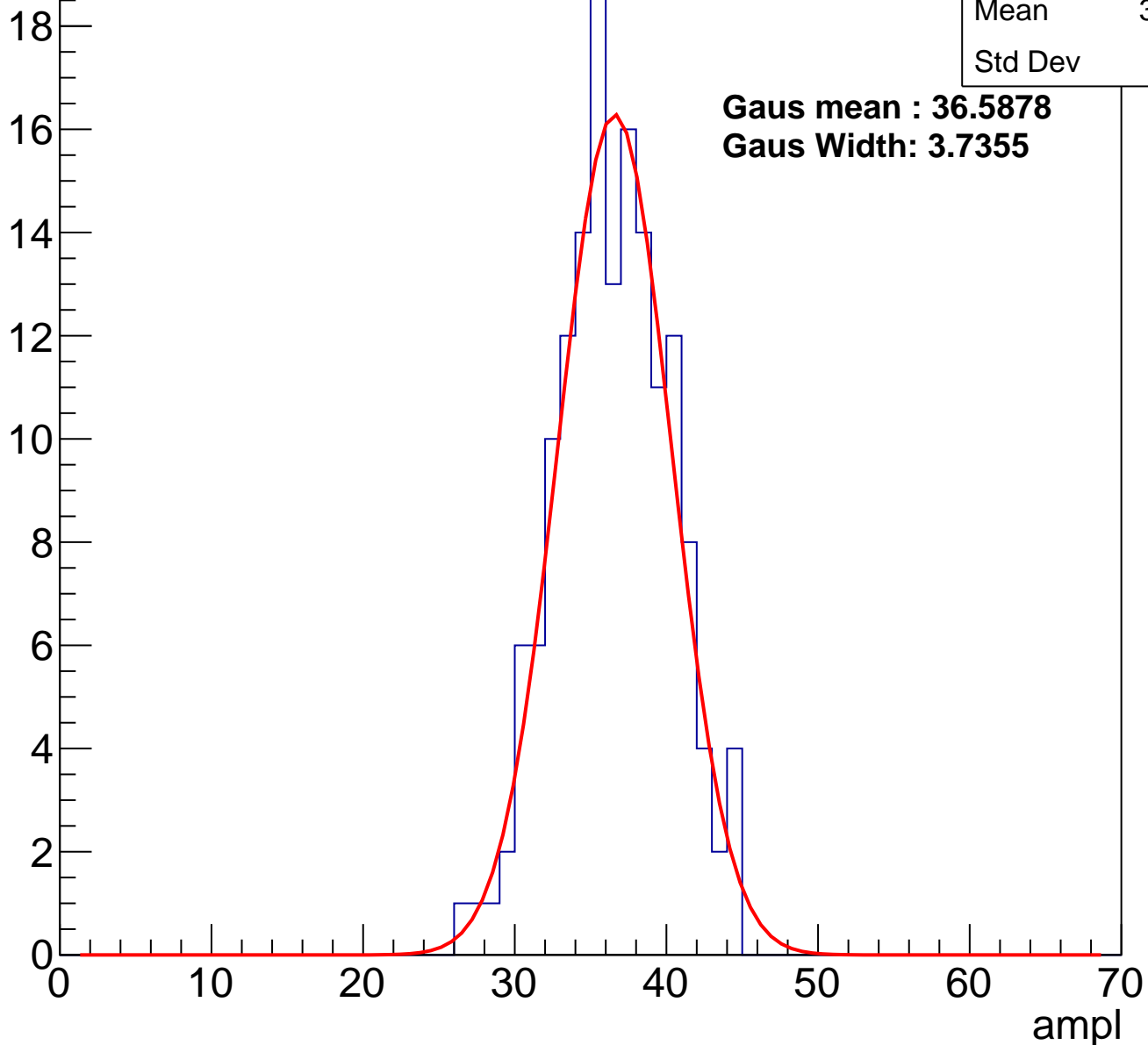
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	36.03
Std Dev	3.68

Gaus mean : 36.5878

Gaus Width: 3.7355

Entry



B0L101S, U20-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	43.14
Std Dev	3.982

Gaus mean : 43.6495

Gaus Width: 3.8396

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

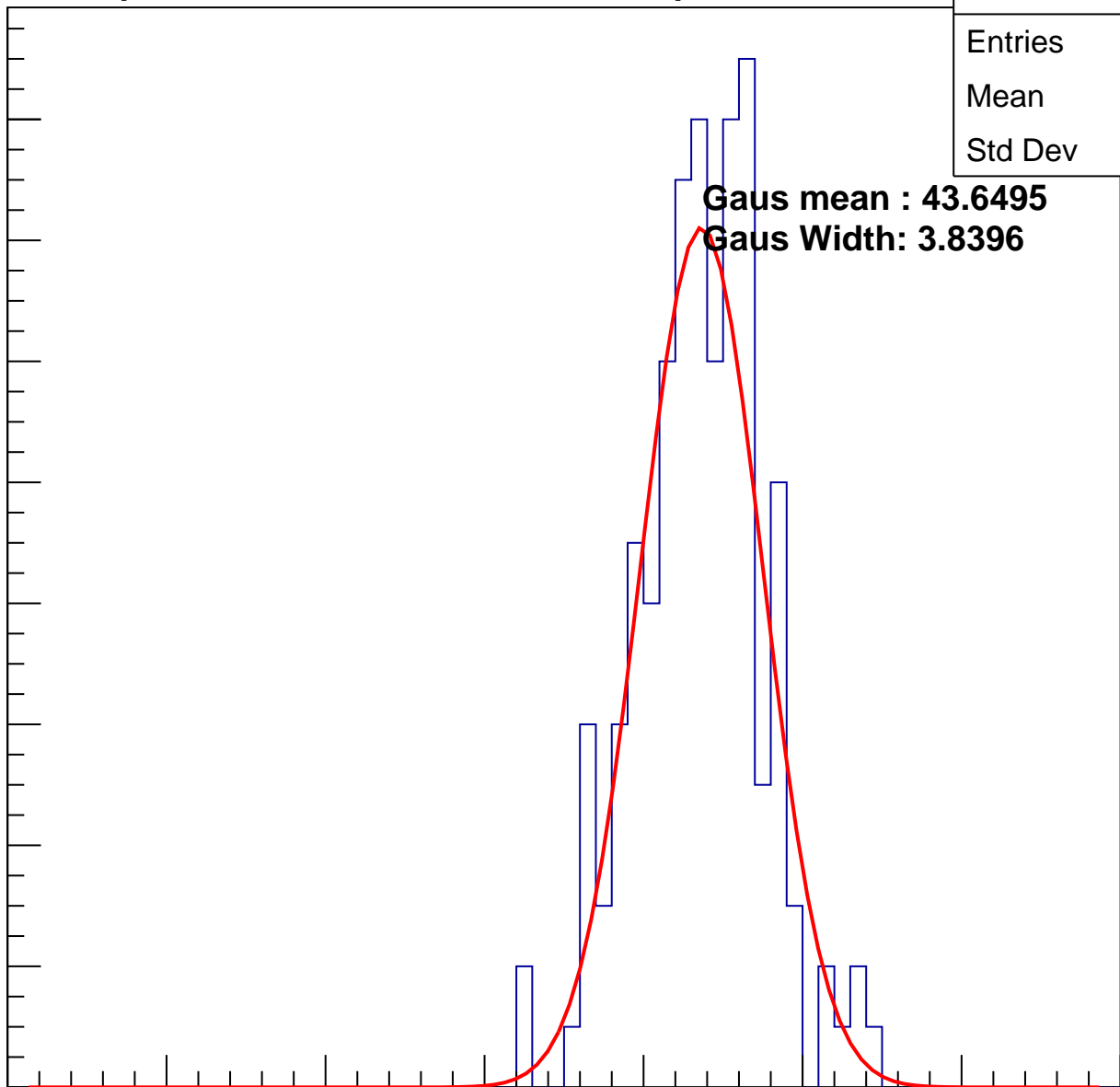
40

50

60

70

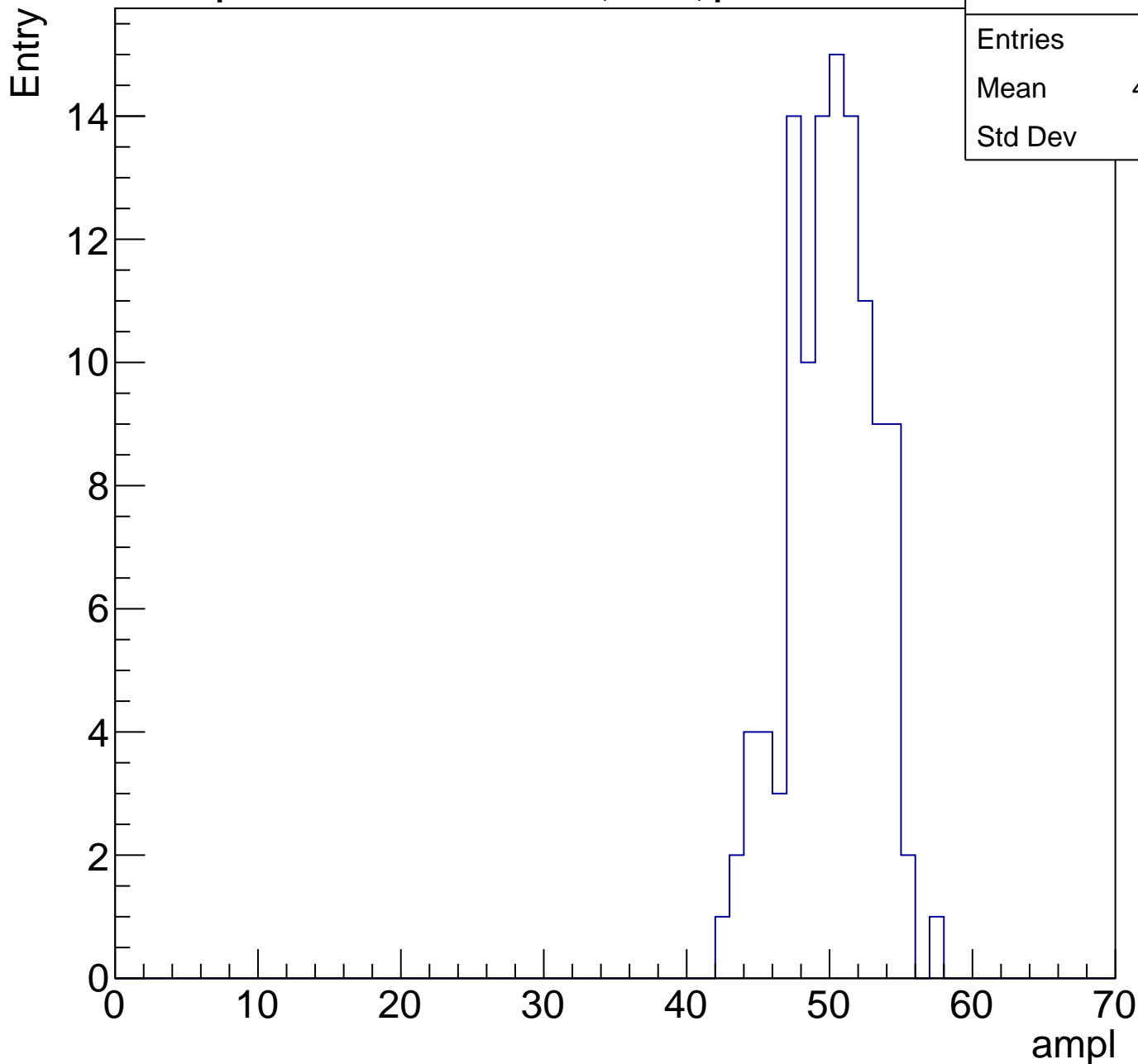
ampl



B0L101S, U20-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

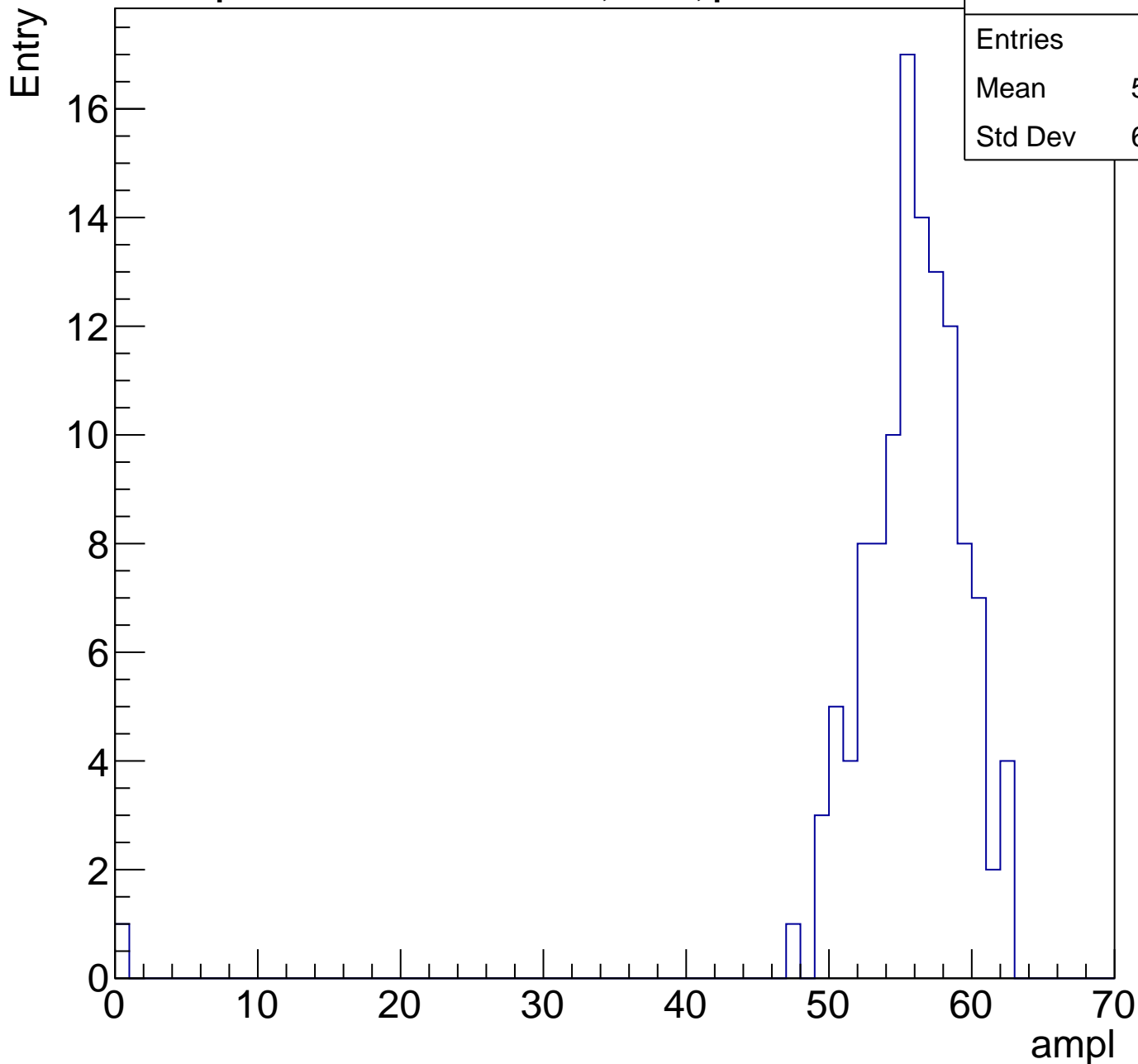
Entries	113
Mean	49.66
Std Dev	2.98



B0L101S, U20-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	55.09
Std Dev	6.023



B0L101S, U20-ch95, adc5

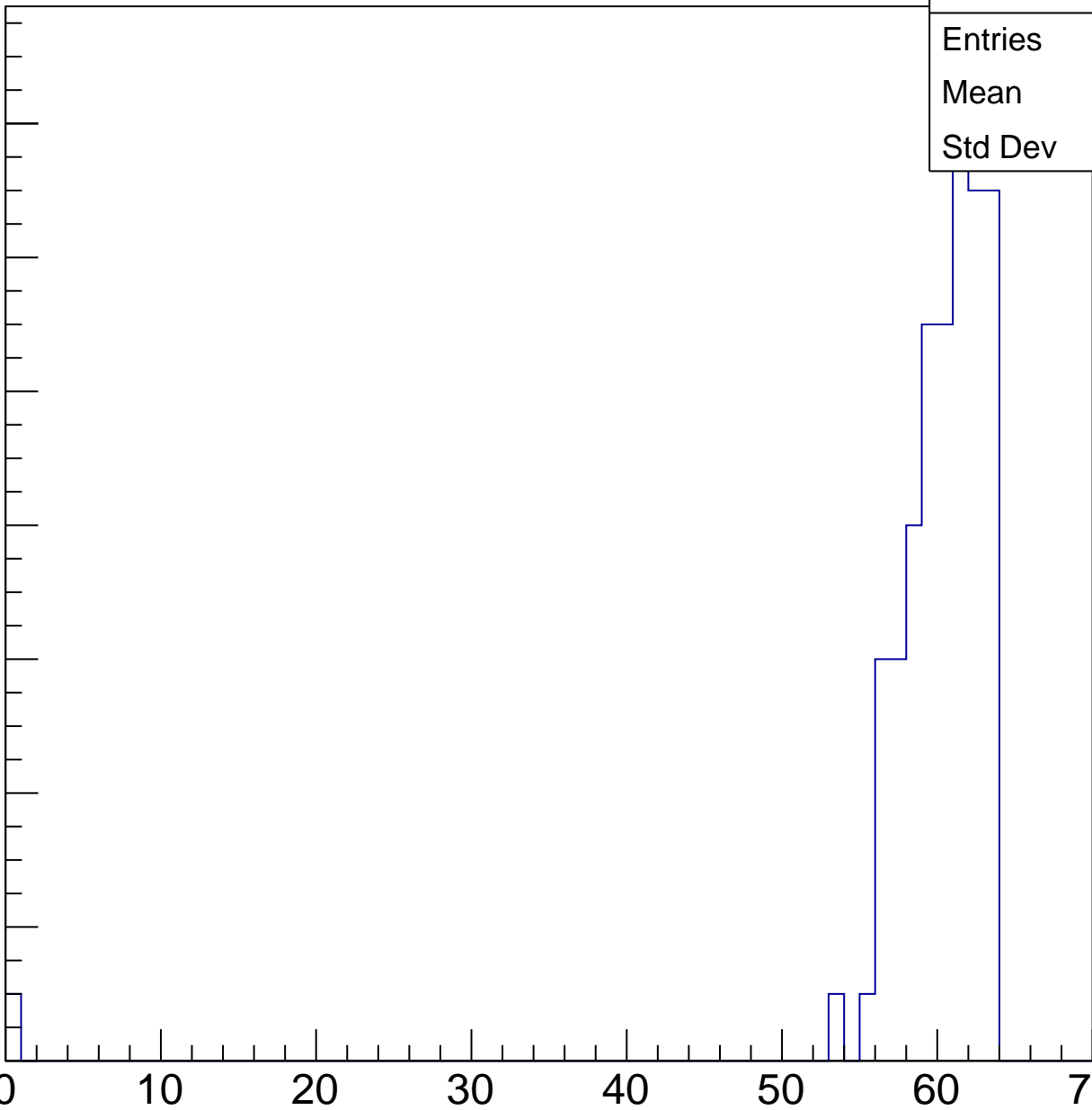
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	59.29
Std Dev	6.827

ampl



B0L101S, U20-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

7

6

5

4

3

2

1

0

Entries

16

Mean

61.81

Std Dev

1.285

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

60

70

ampl

B0L101S, U20-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch96, adc0

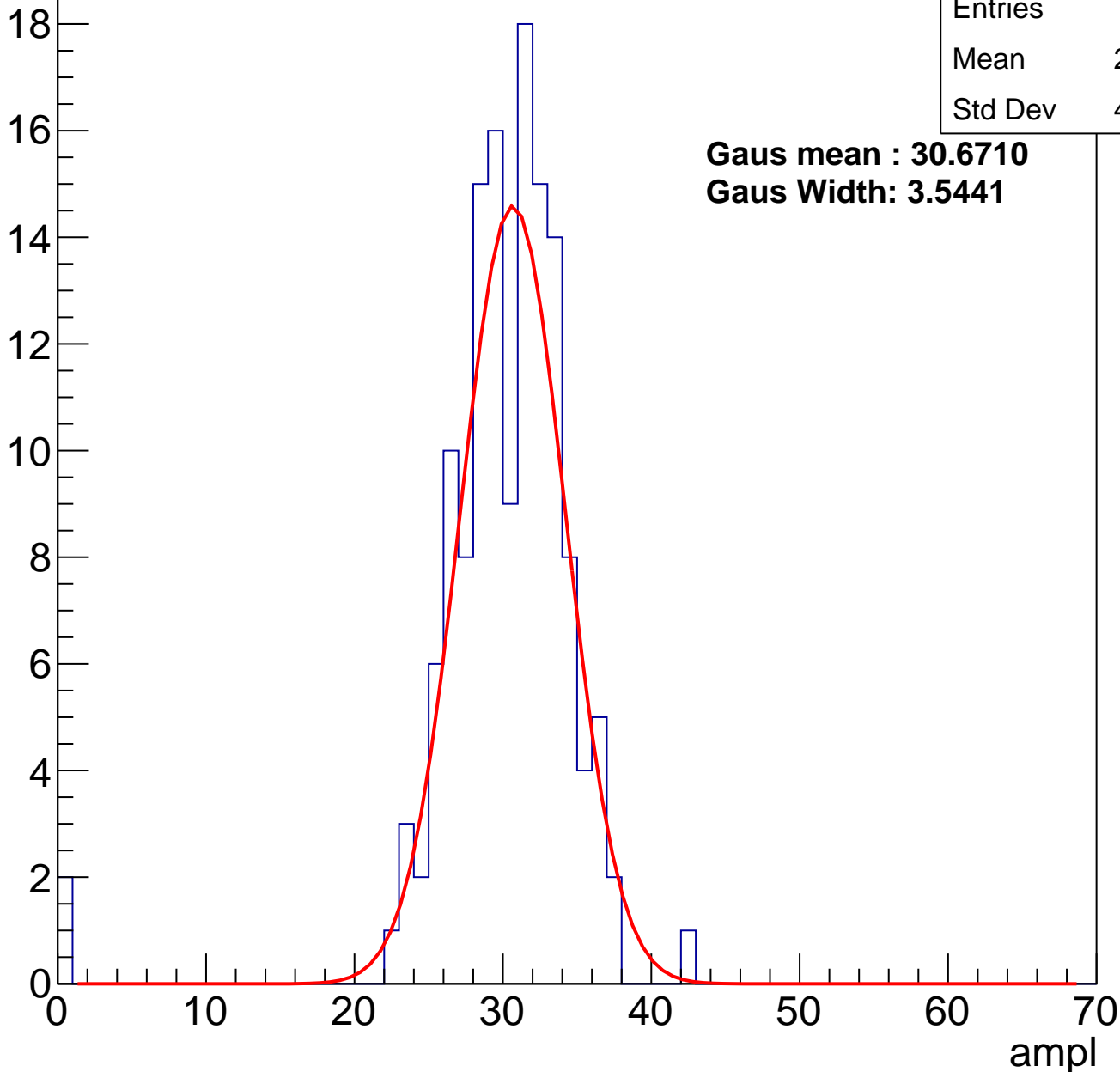
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.69
Std Dev	4.932

Gaus mean : 30.6710

Gaus Width: 3.5441

Entry



B0L101S, U20-ch96, adc1

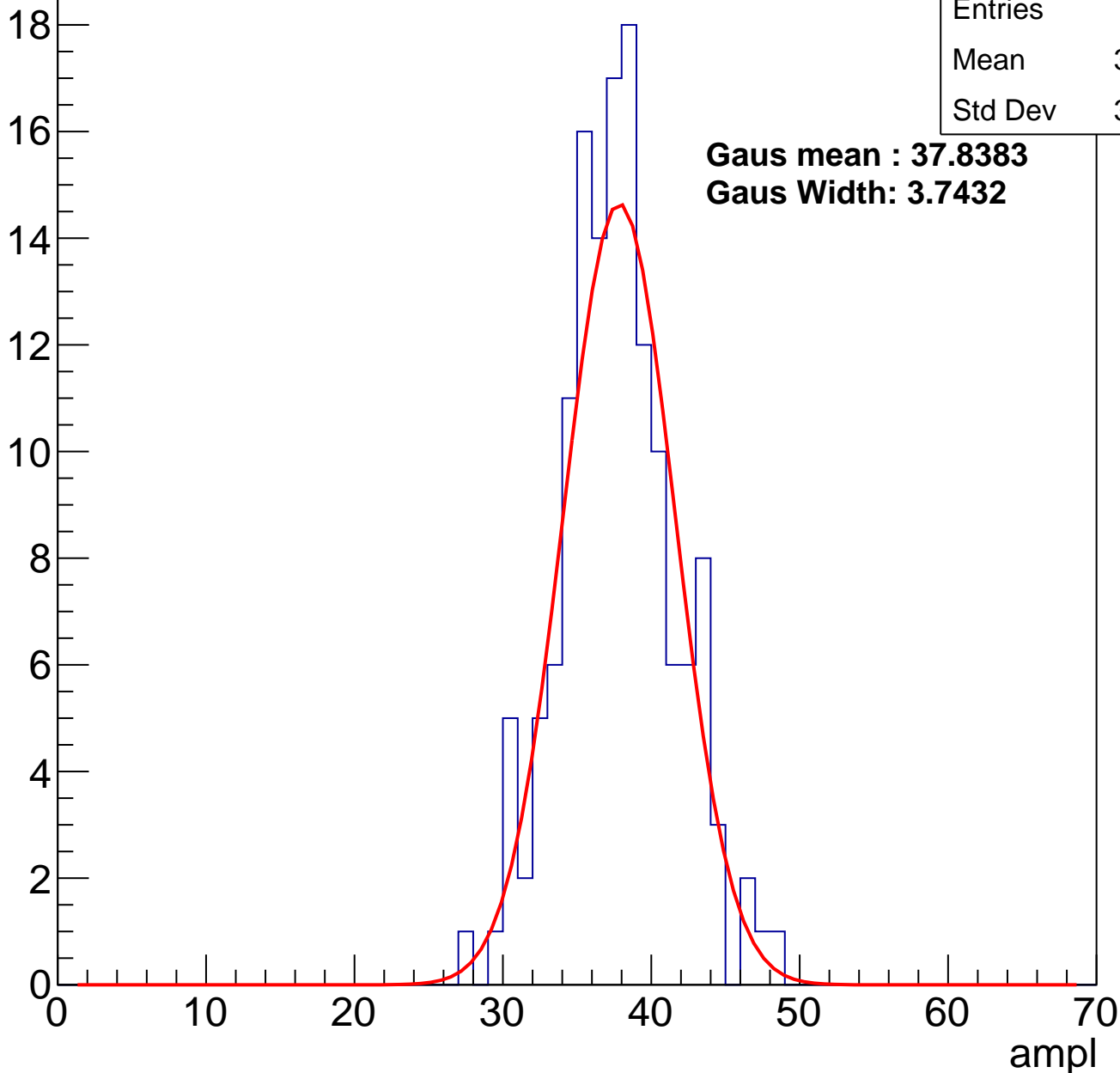
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	37.28
Std Dev	3.794

Gaus mean : 37.8383

Gaus Width: 3.7432

Entry



B0L101S, U20-ch96, adc2

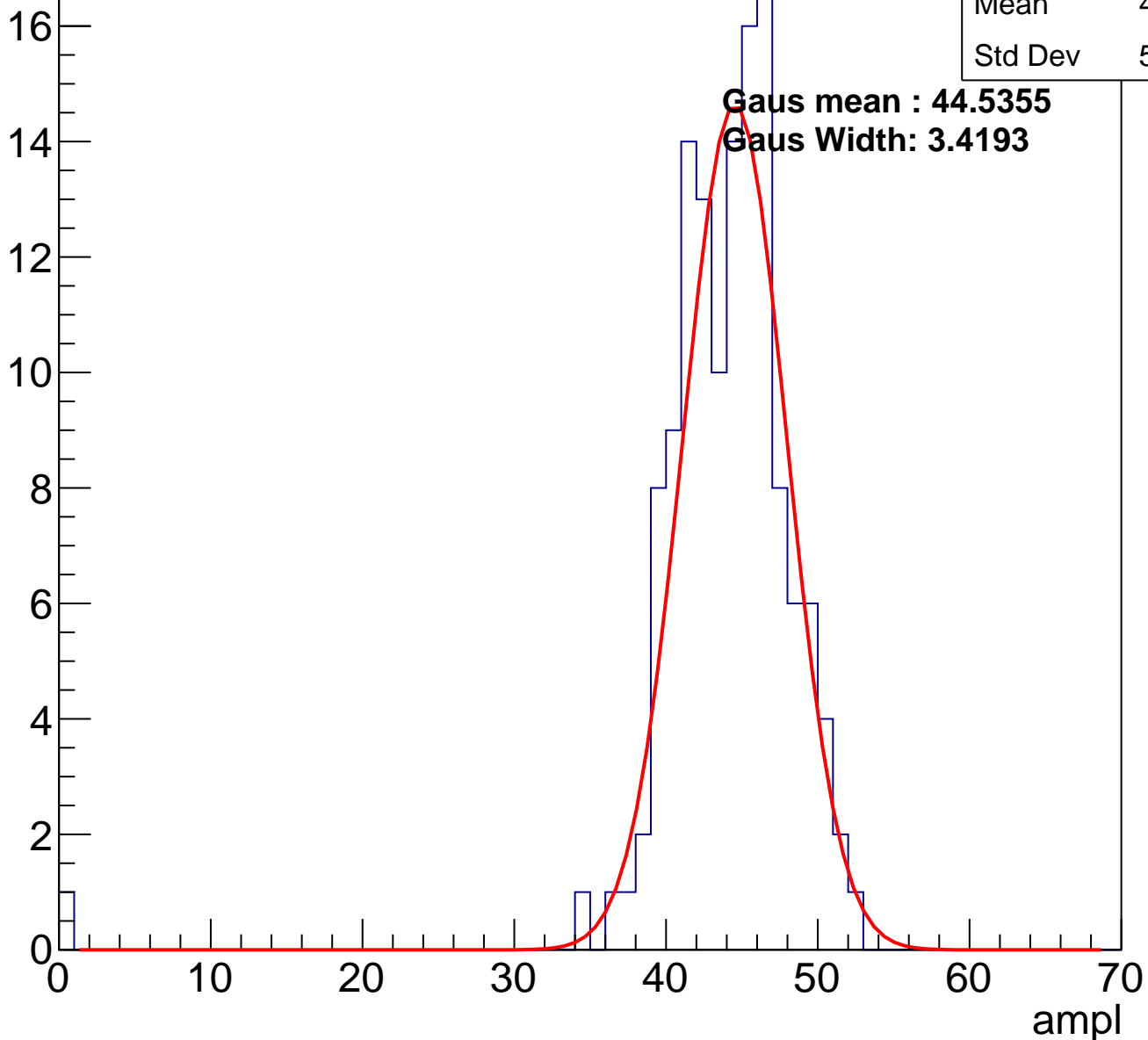
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	134
Mean	43.54
Std Dev	5.062

Gaus mean : 44.5355

Gaus Width: 3.4193

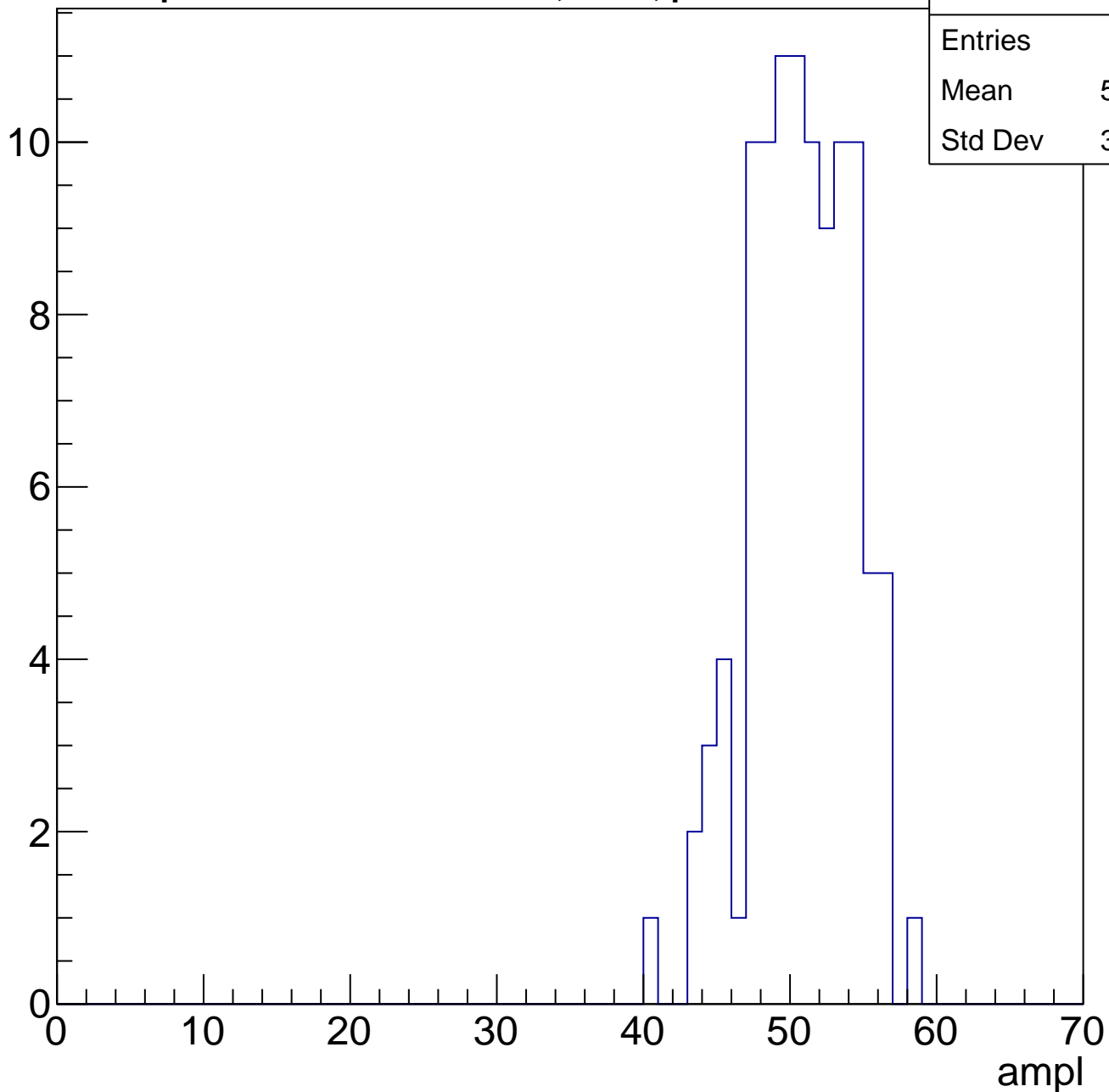


B0L101S, U20-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	50.33
Std Dev	3.437

Entry



B0L101S, U20-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	55.59
Std Dev	3.639

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

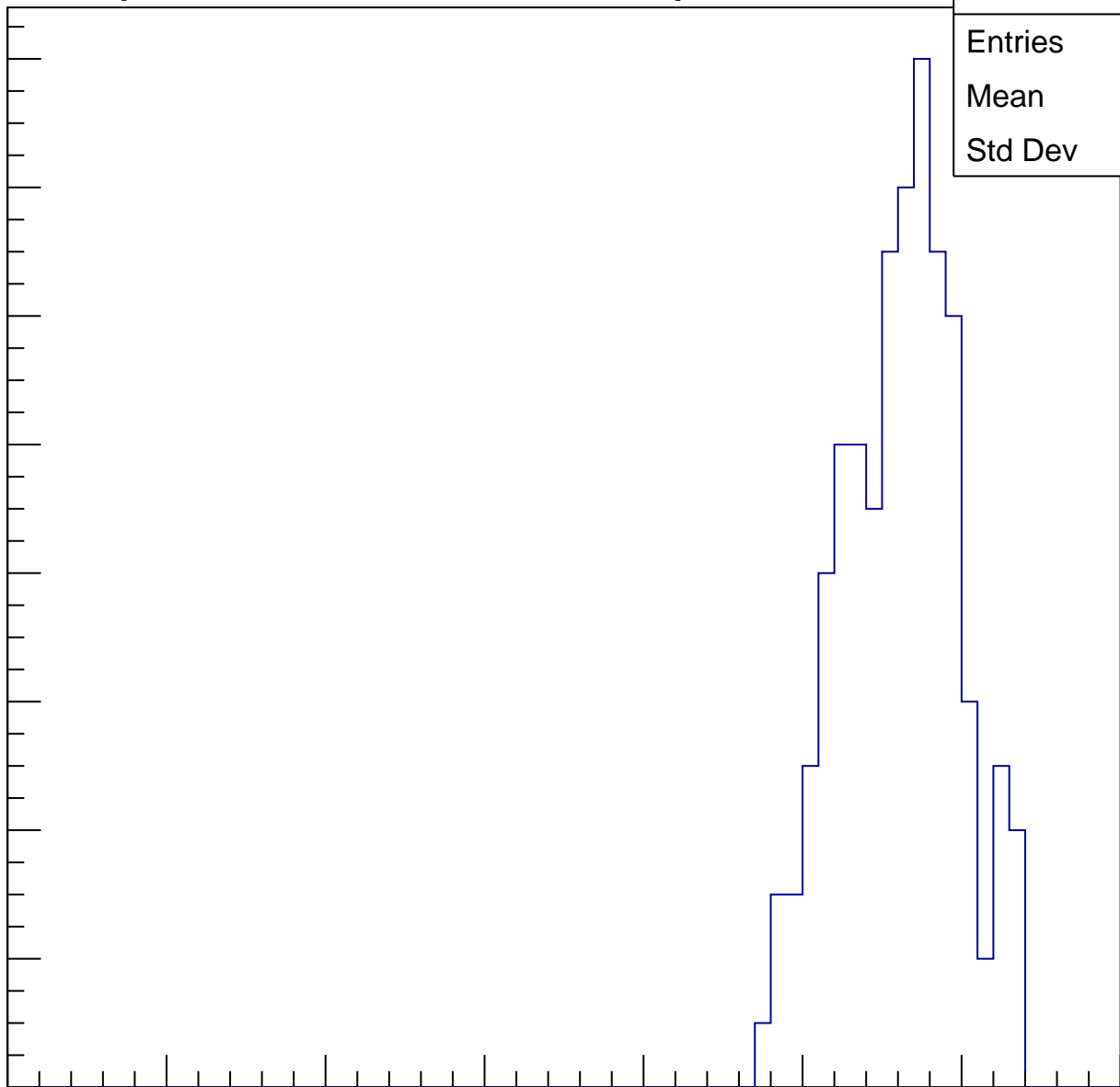
40

50

60

70

ampl



B0L101S, U20-ch96, adc5

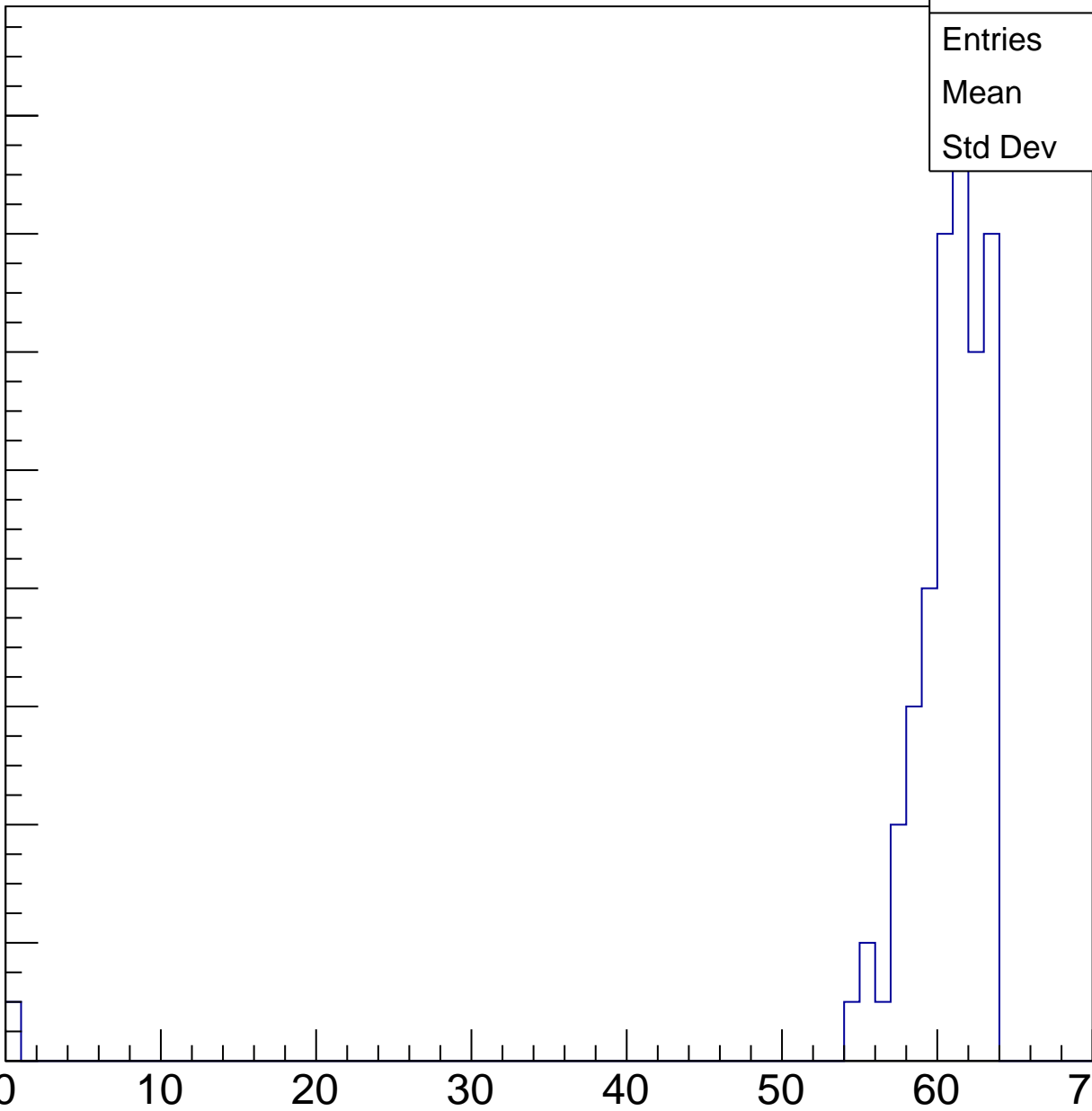
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.64
Std Dev	7.027

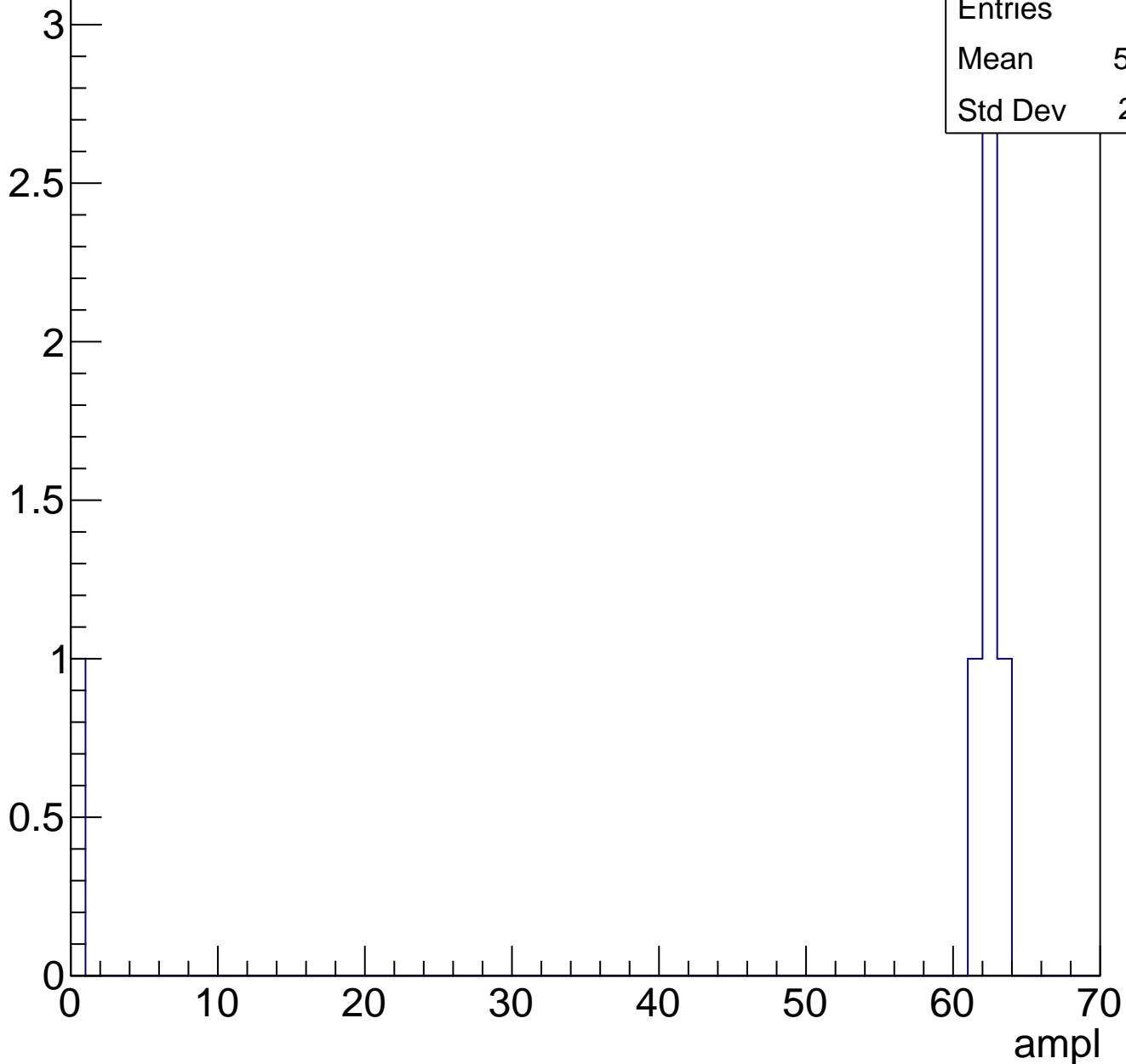
ampl



B0L101S, U20-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch97, adc0

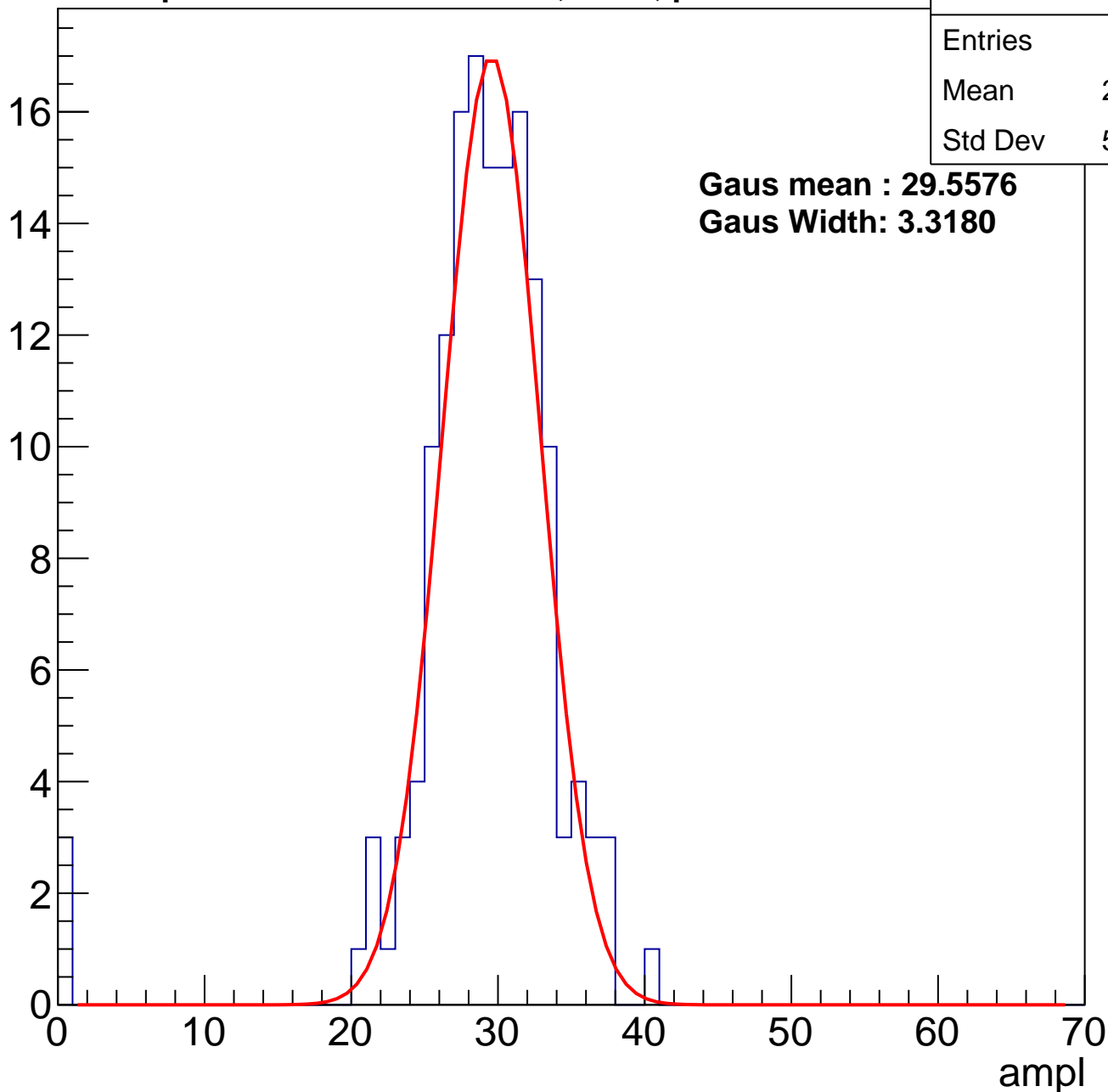
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.55
Std Dev	5.366

Gaus mean : 29.5576

Gaus Width: 3.3180

Entry



B0L101S, U20-ch97, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	36.35
Std Dev	3.7

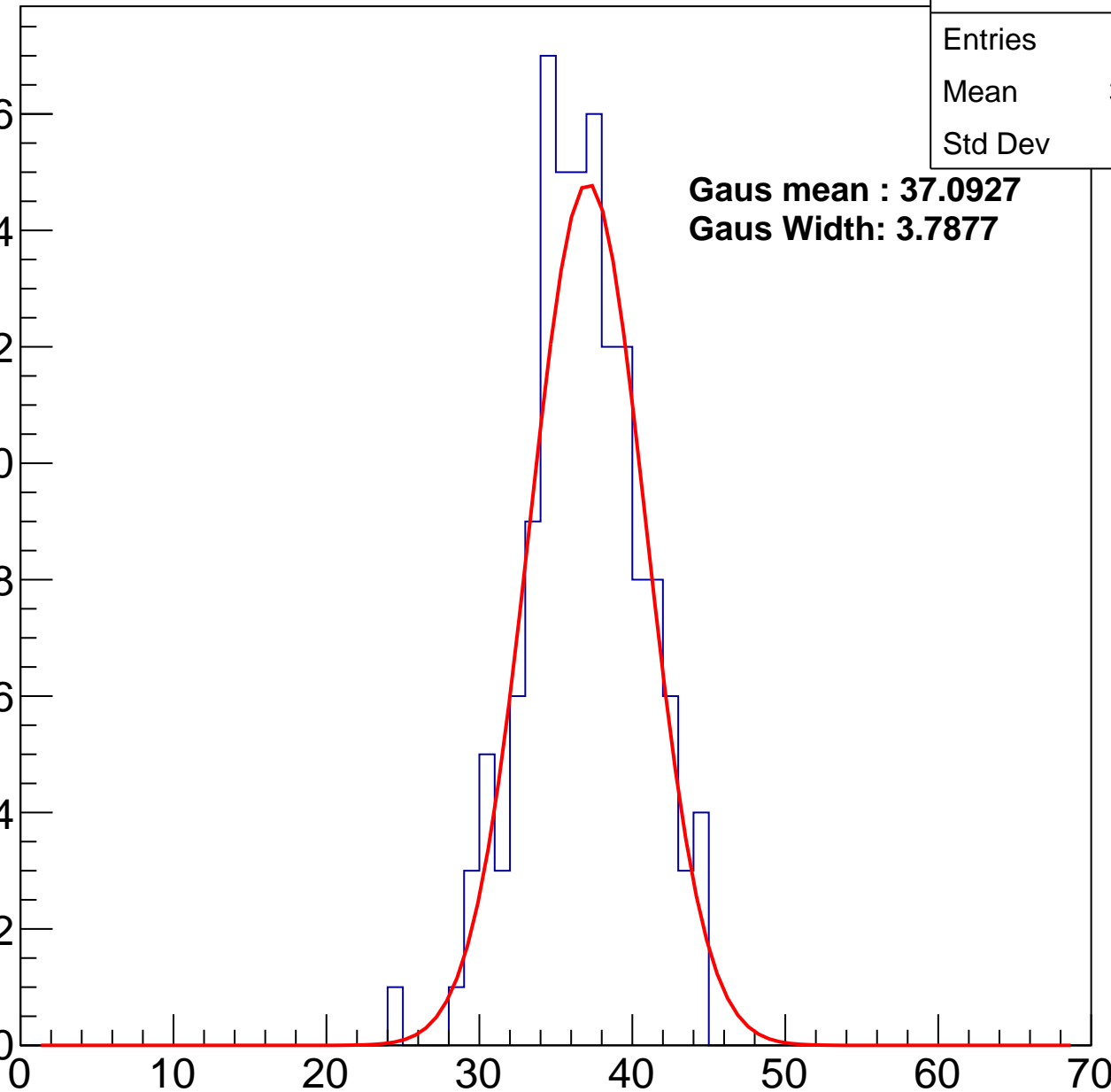
Gaus mean : 37.0927

Gaus Width: 3.7877

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch97, adc2

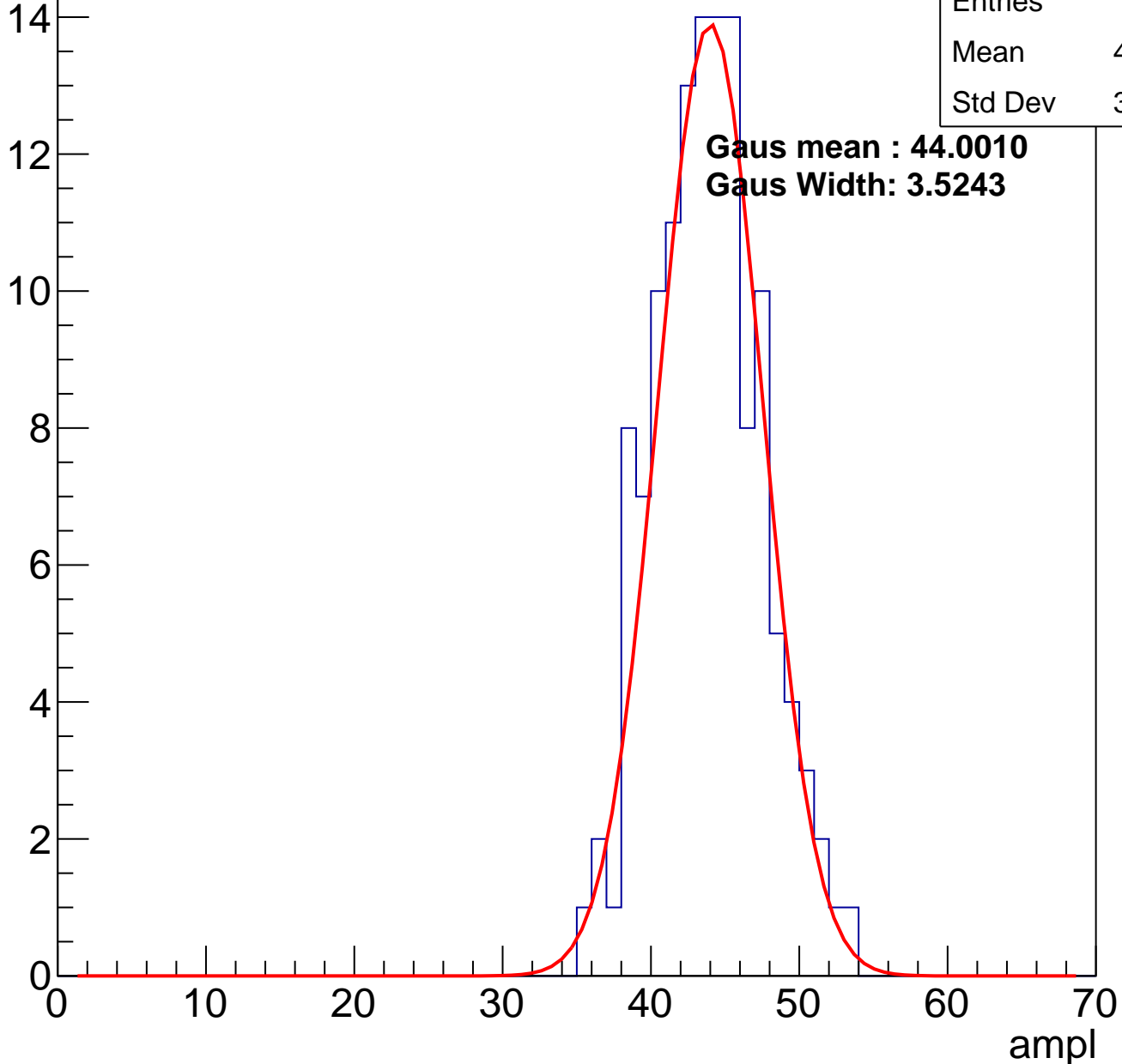
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	43.39
Std Dev	3.592

Gaus mean : 44.0010

Gaus Width: 3.5243

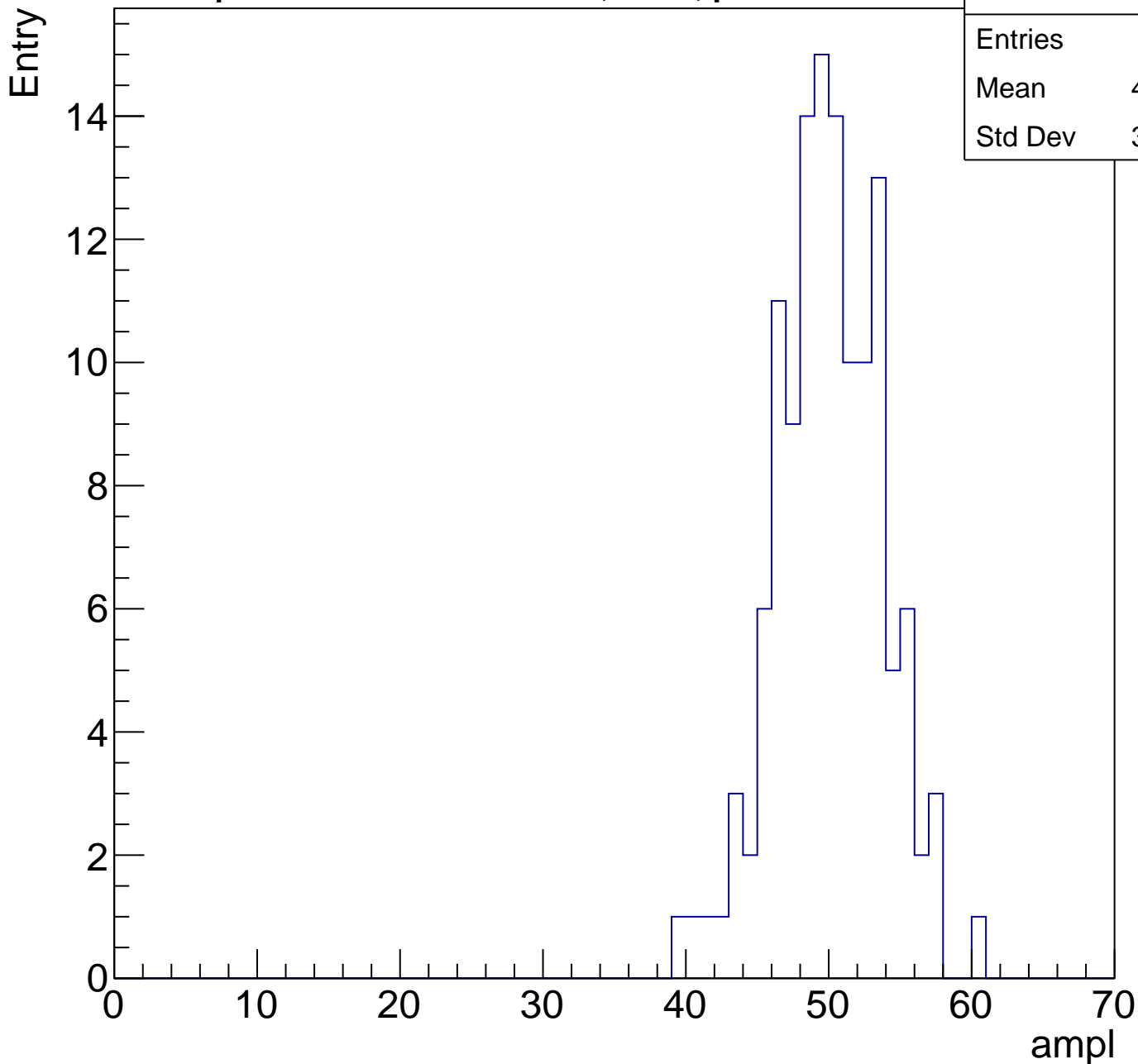
Entry



B0L101S, U20-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	49.59
Std Dev	3.705



B0L101S, U20-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

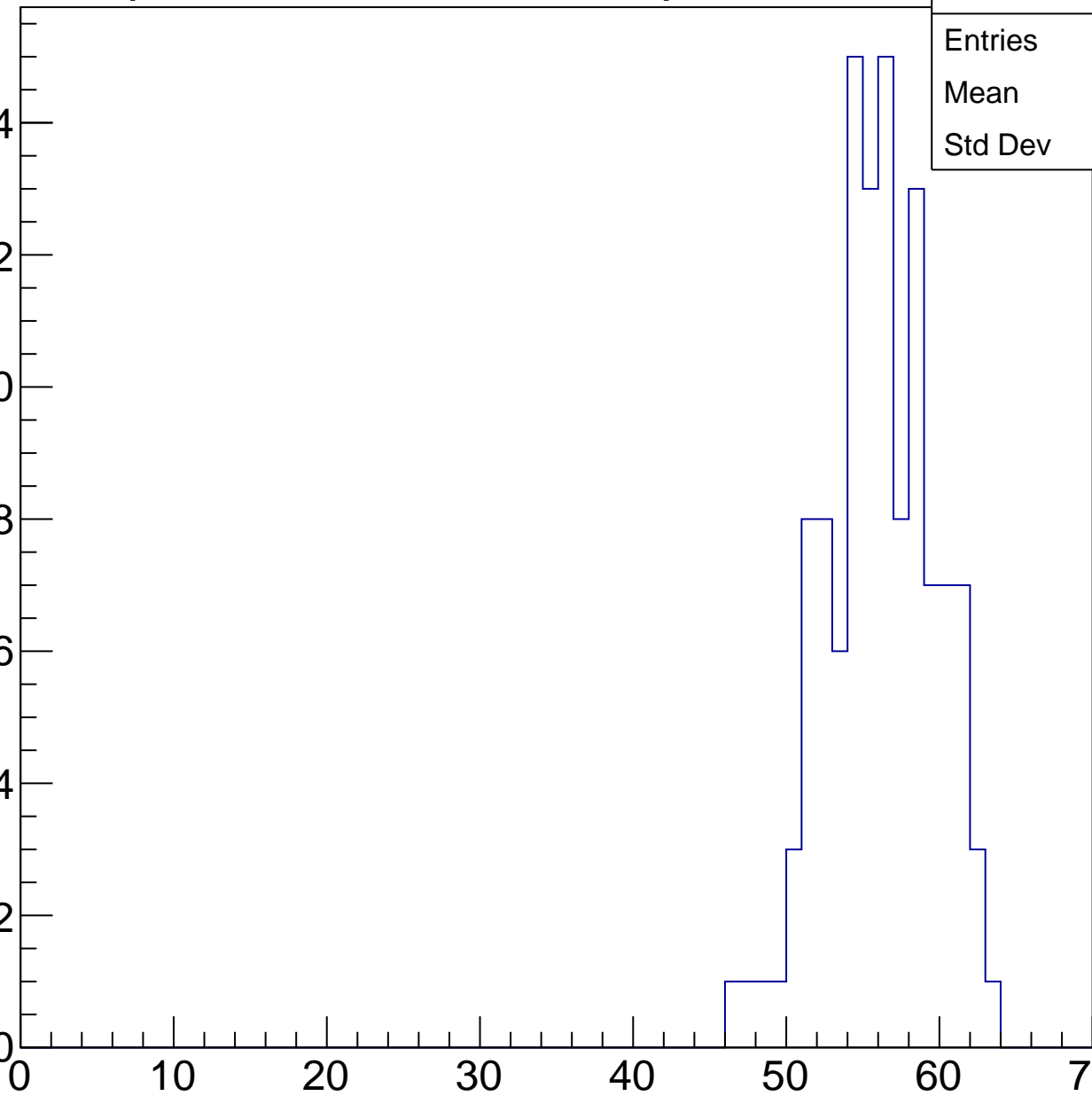
Mean

55.64

Std Dev

3.456

ampl



B0L101S, U20-ch97, adc5

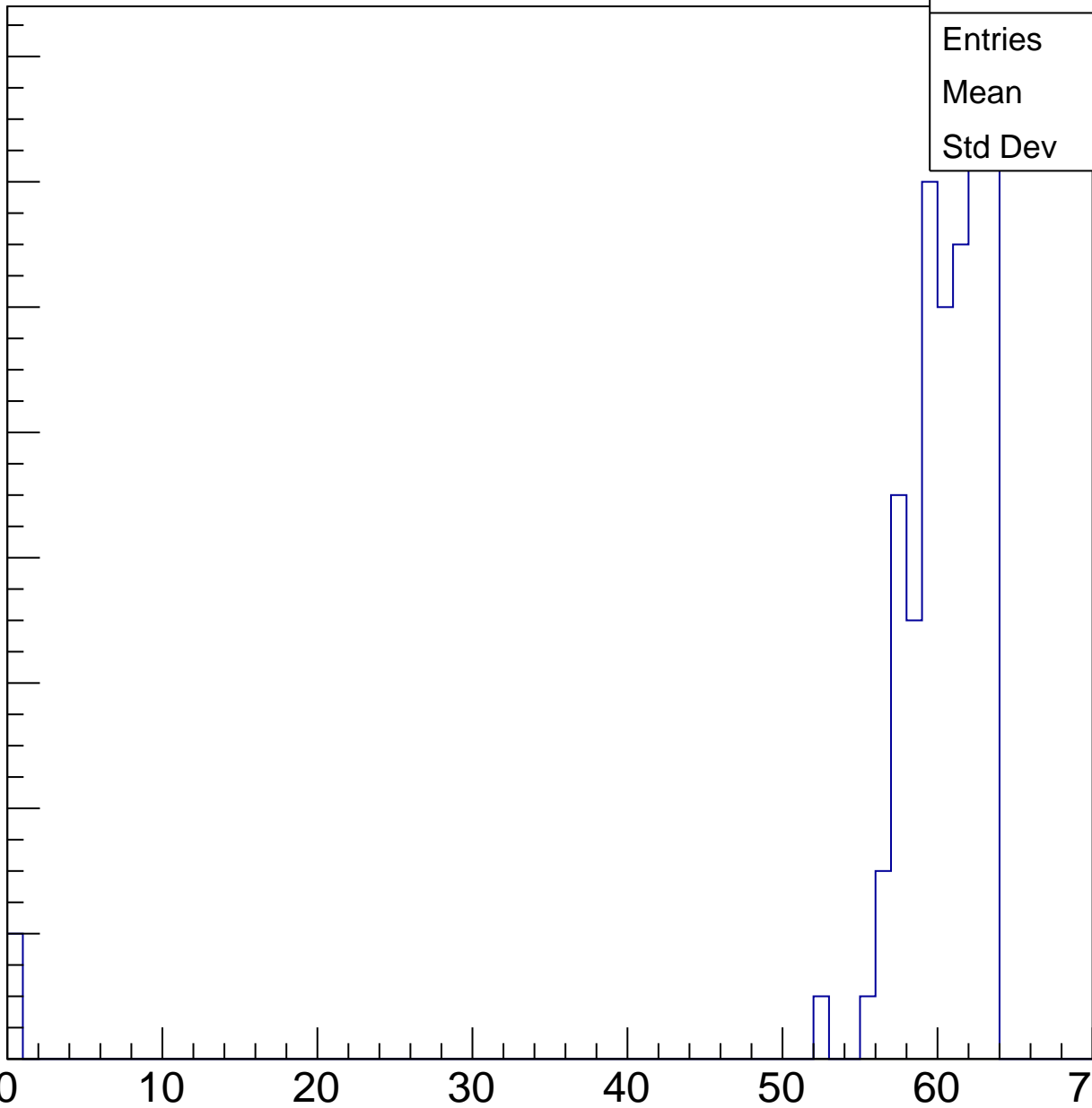
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.82
Std Dev	9.005

ampl



B0L101S, U20-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

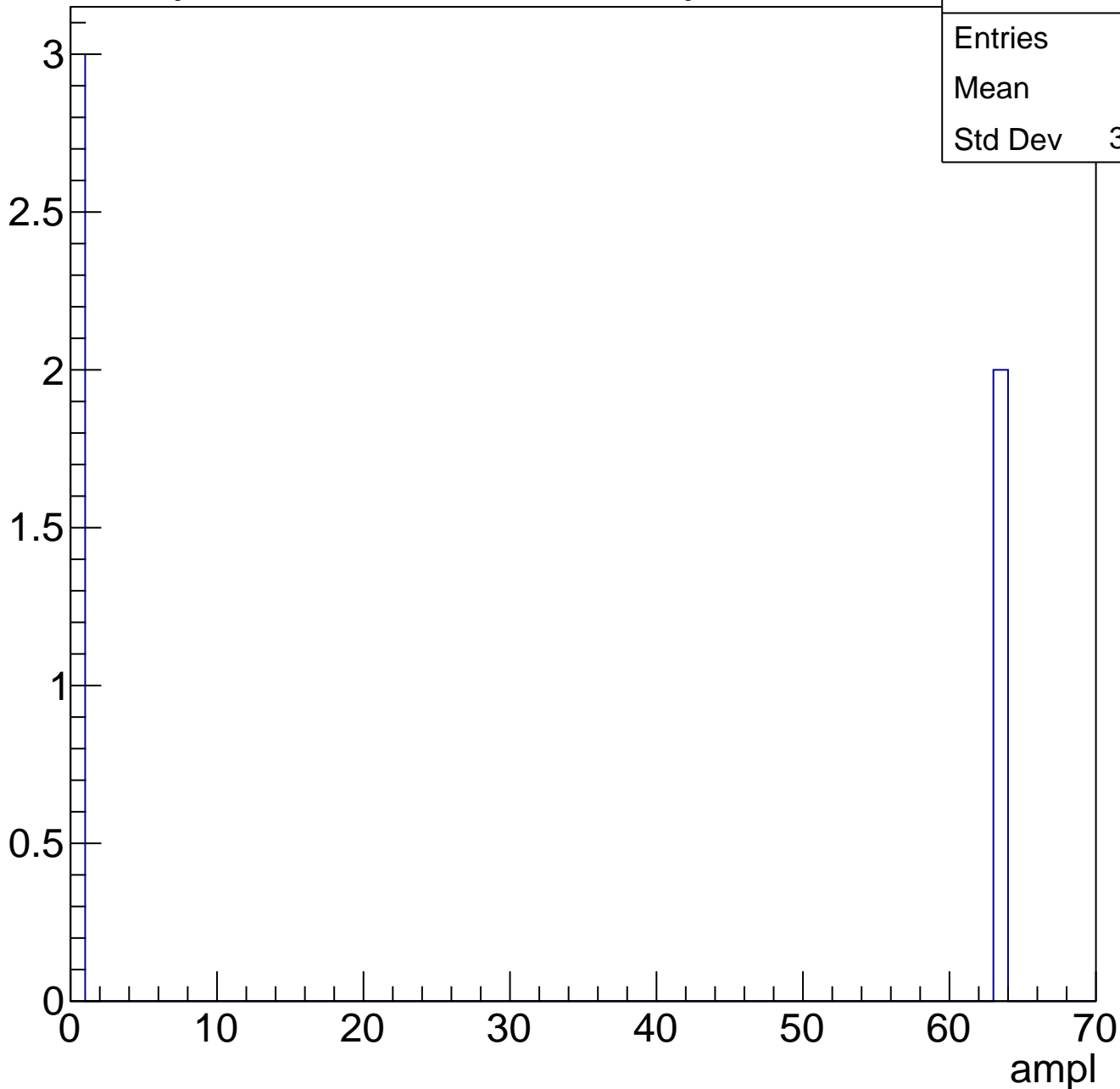
Entry



B0L101S, U20-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	25.2
Std Dev	30.86

B0L101S, U20-ch98, adc0

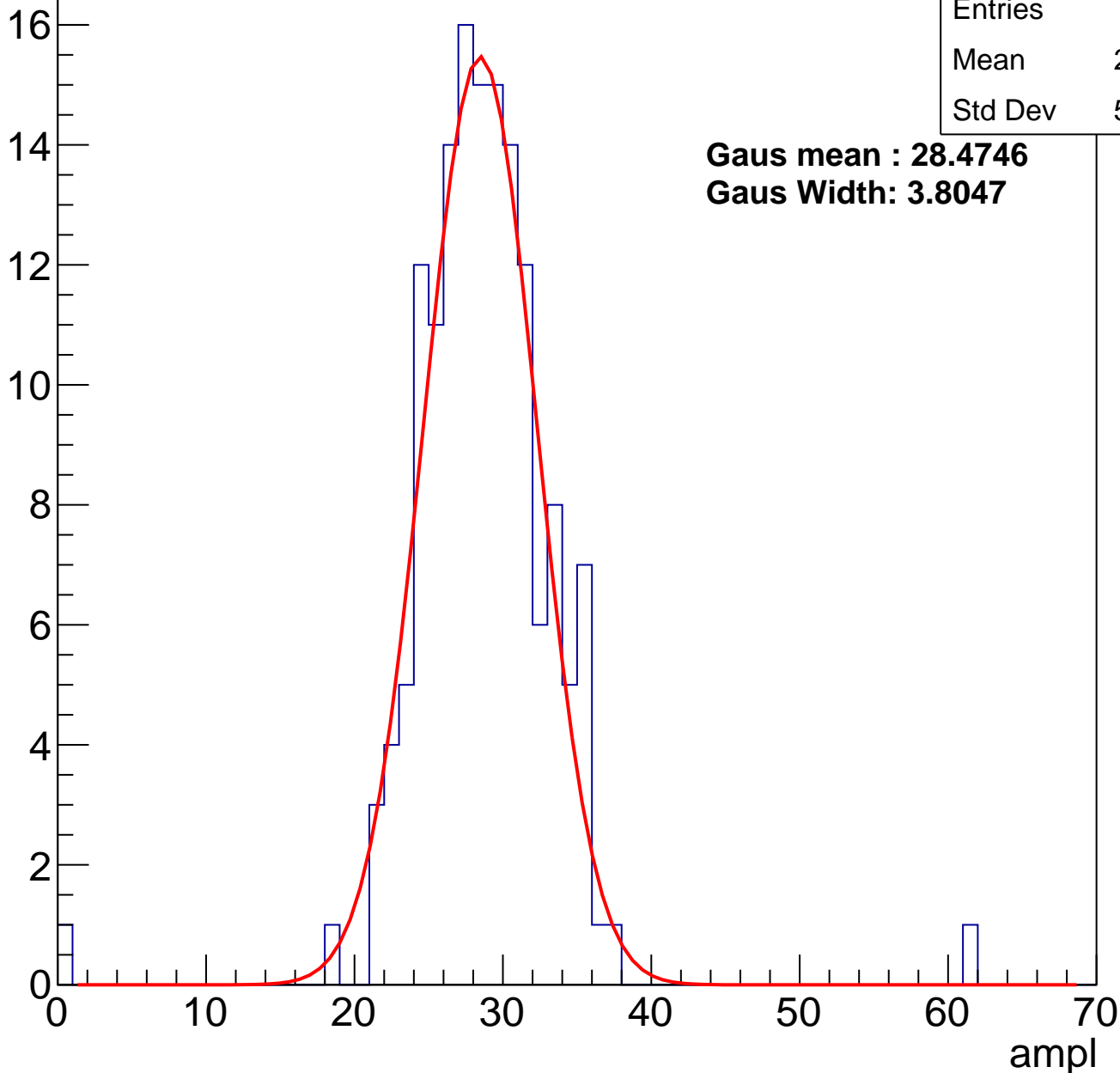
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.26
Std Dev	5.056

Gaus mean : 28.4746

Gaus Width: 3.8047

Entry



B0L101S, U20-ch98, adc1

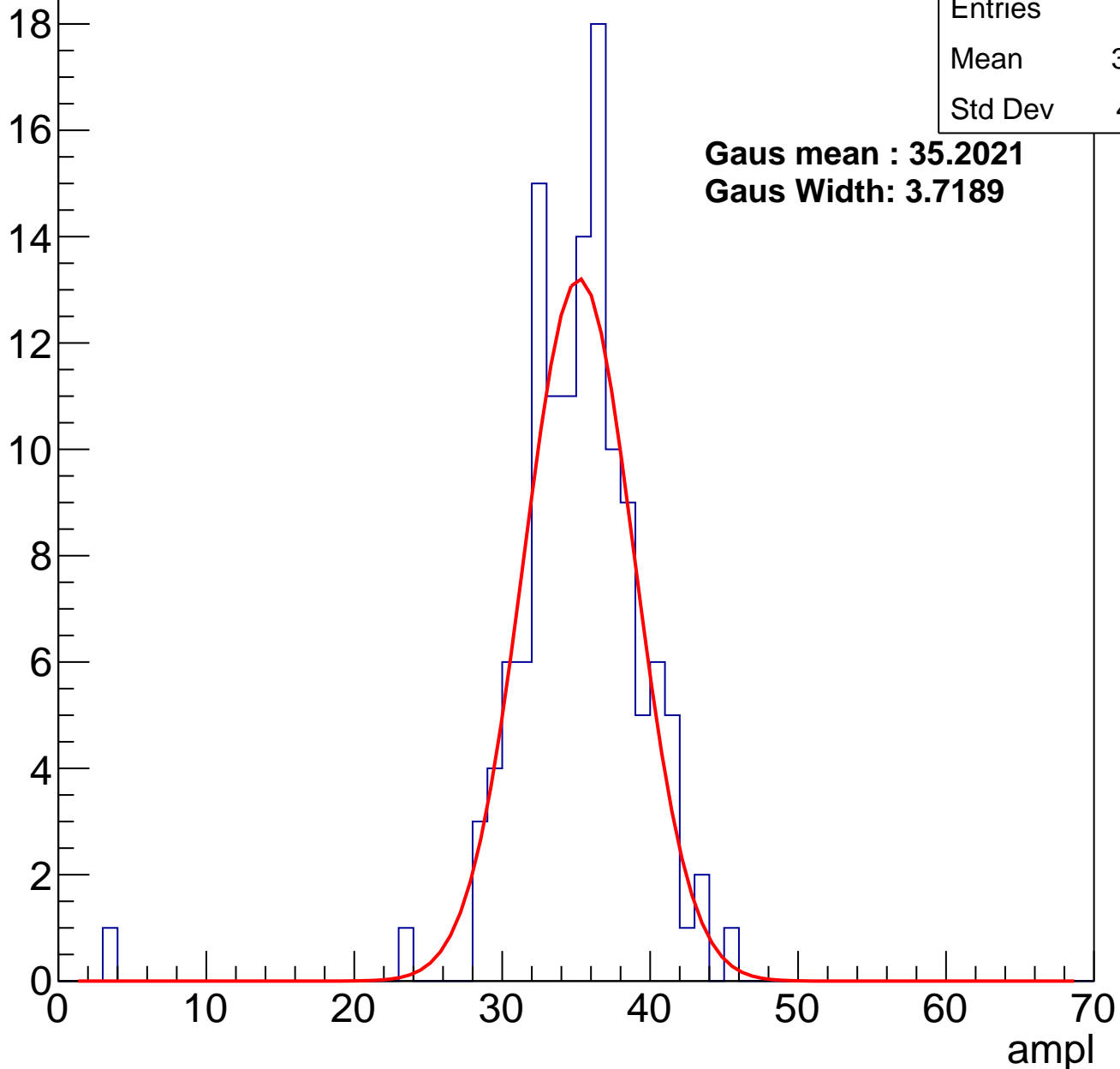
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	34.67
Std Dev	4.581

Gaus mean : 35.2021

Gaus Width: 3.7189

Entry



B0L101S, U20-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

41.79

Std Dev

3.561

Gaus mean : 42.6331

Gaus Width: 3.2479

0

10

20

30

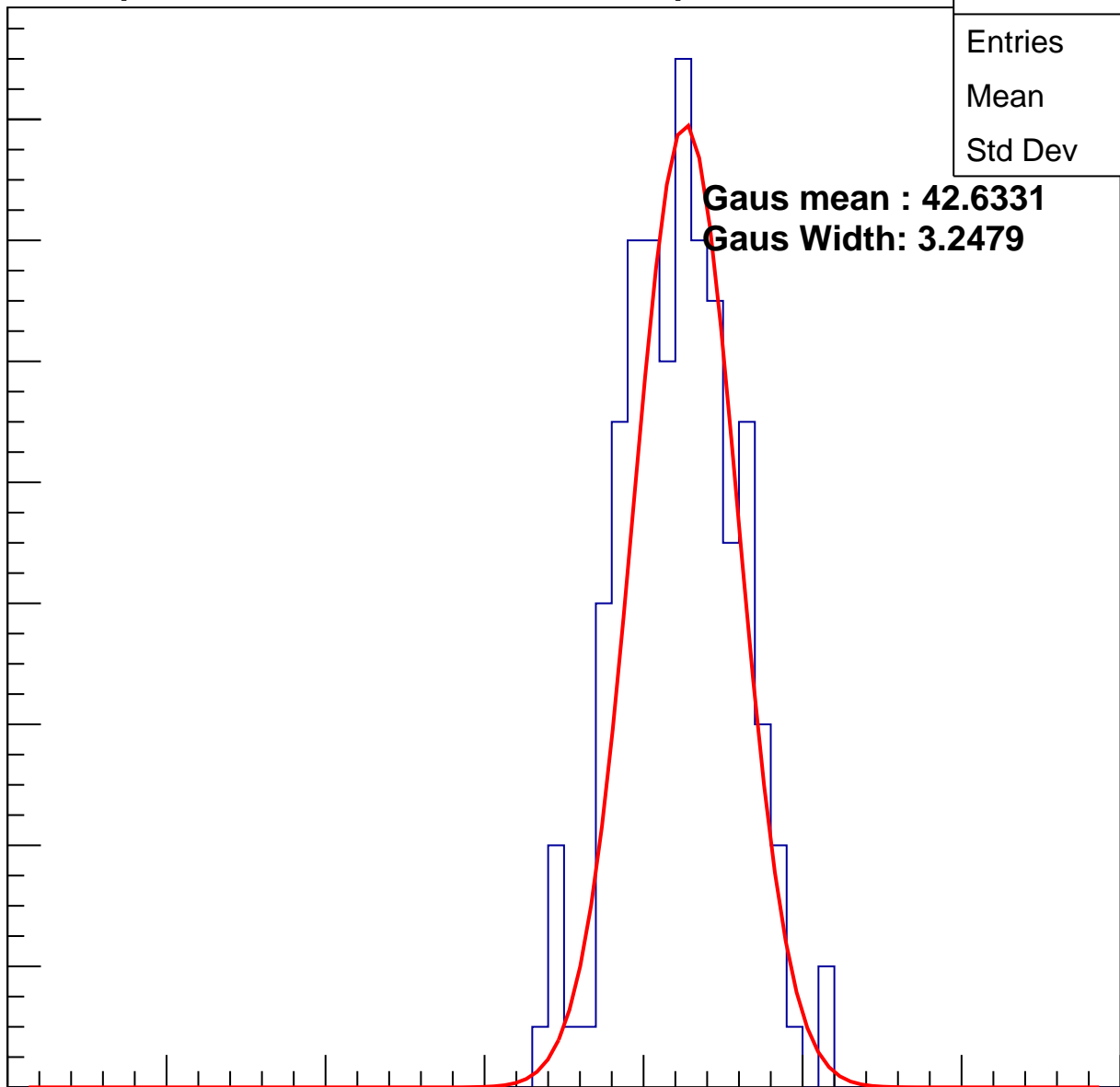
40

50

60

70

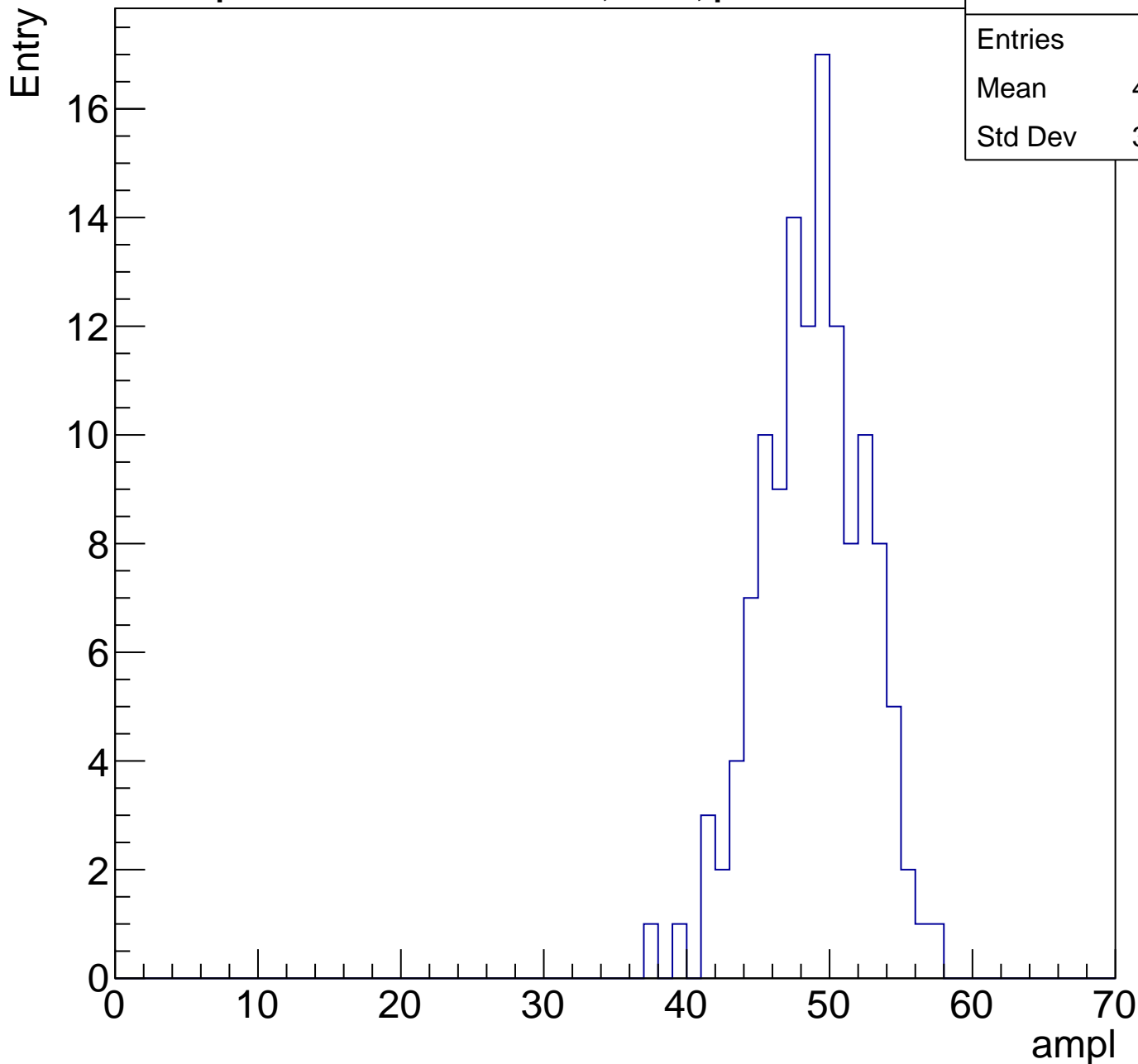
ampl



B0L101S, U20-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

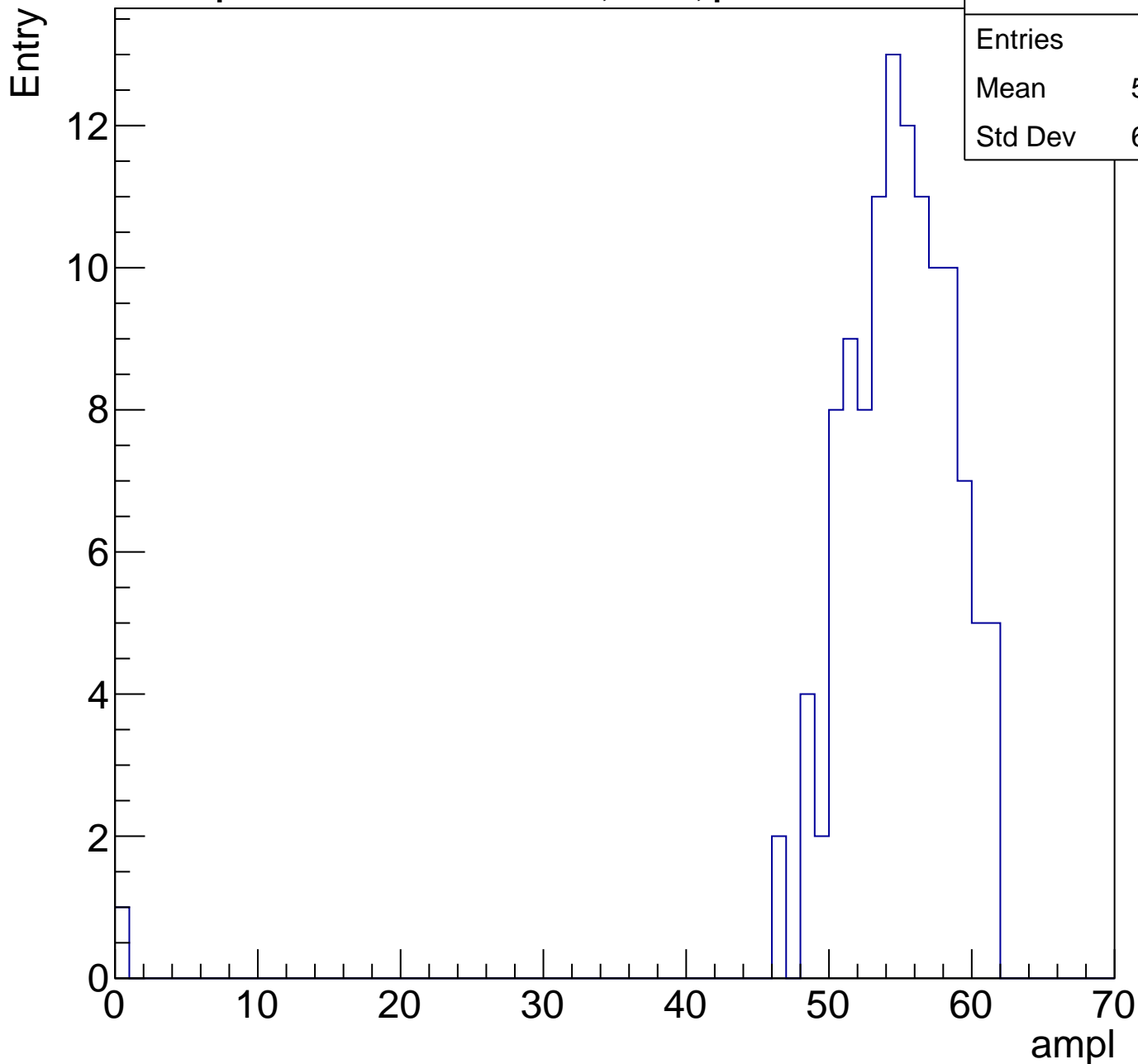
Entries	127
Mean	48.34
Std Dev	3.635



B0L101S, U20-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	54.12
Std Dev	6.102



B0L101S, U20-ch98, adc5

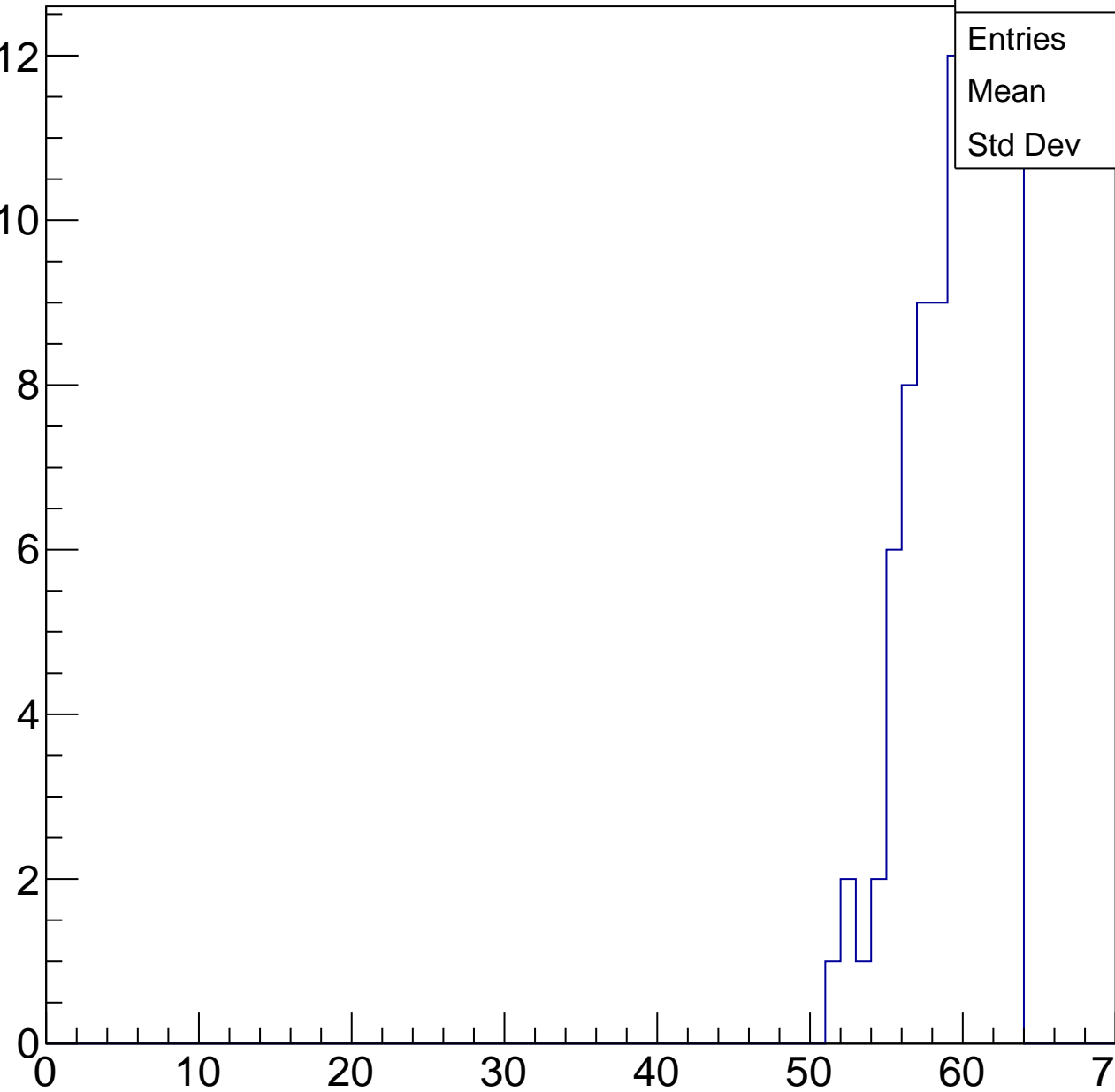
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	96
Mean	59.01
Std Dev	2.889

ampl

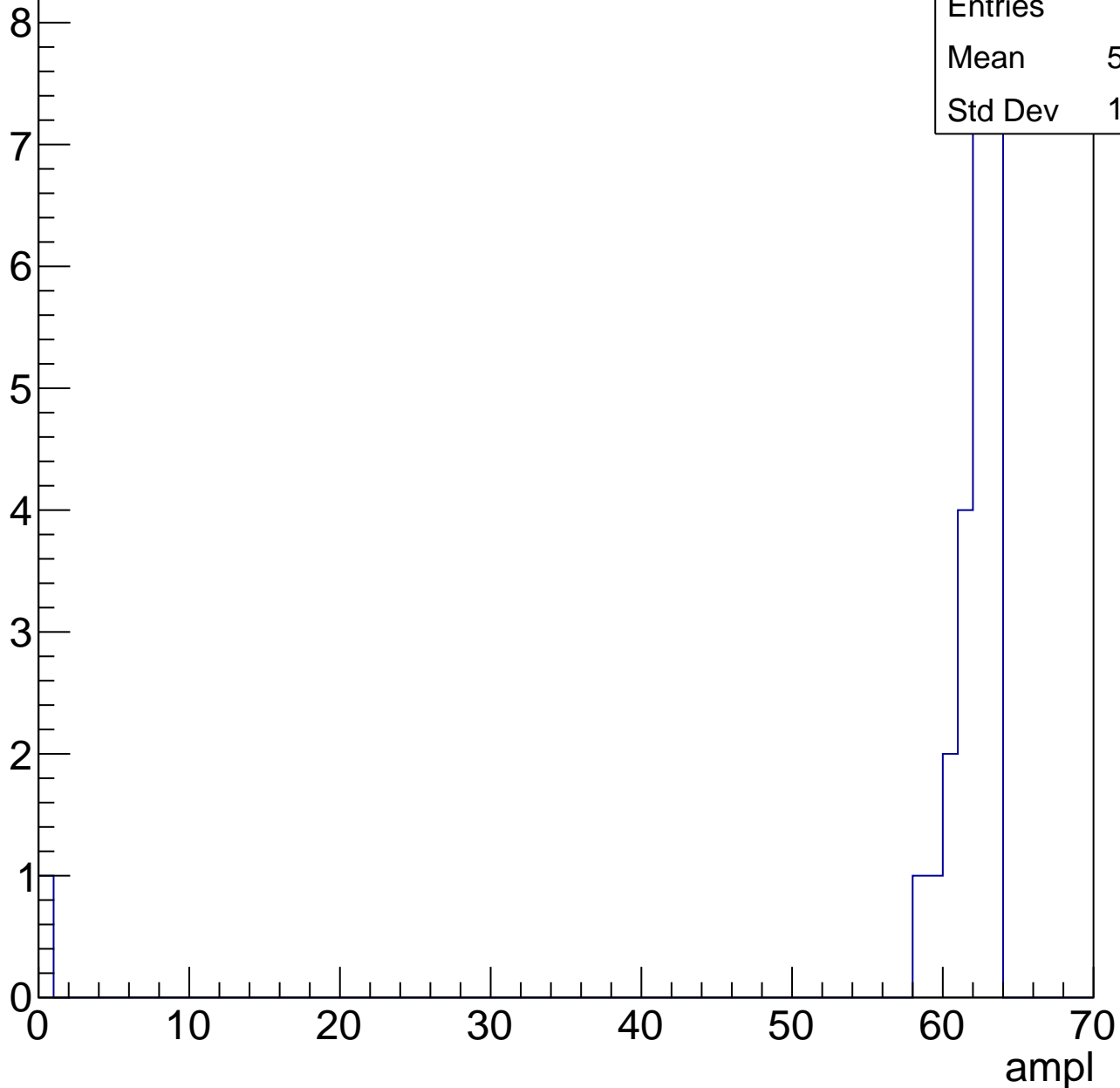


B0L101S, U20-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

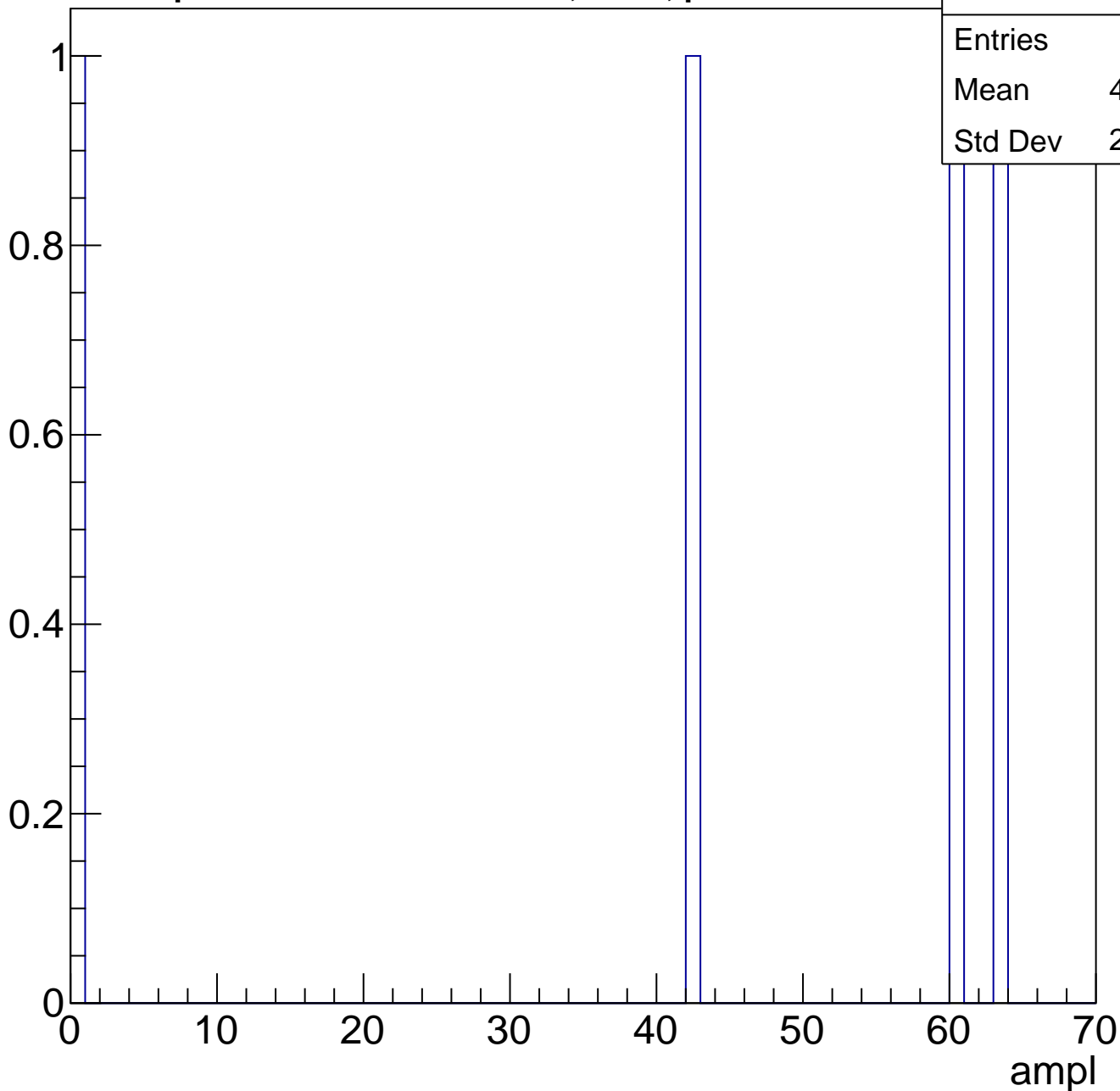
Entries	25
Mean	59.24
Std Dev	12.16



B0L101S, U20-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch99, adc0

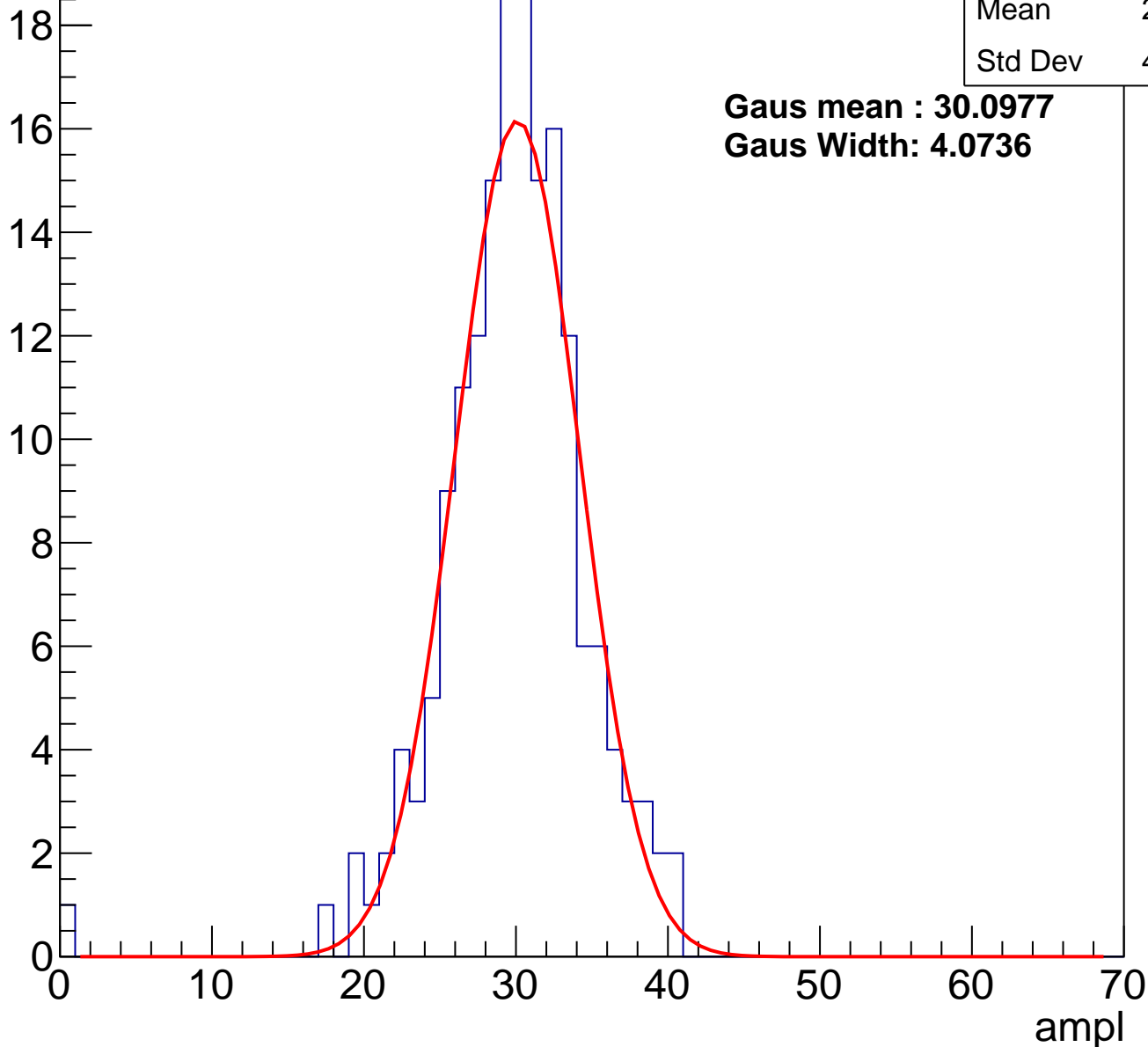
calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	29.39
Std Dev	4.783

Gaus mean : 30.0977

Gaus Width: 4.0736

Entry



B0L101S, U20-ch99, adc1

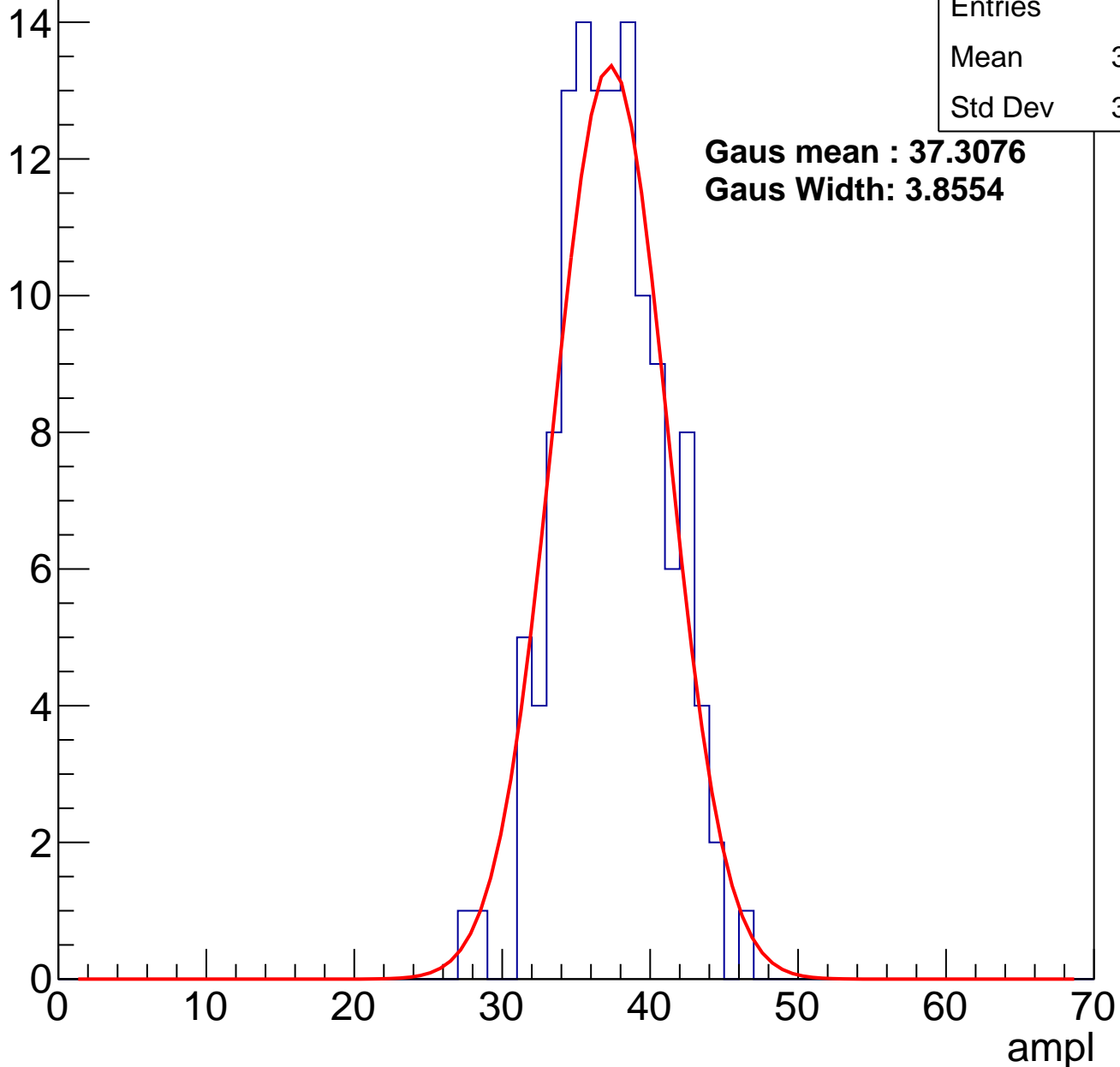
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	36.93
Std Dev	3.483

Gaus mean : 37.3076

Gaus Width: 3.8554

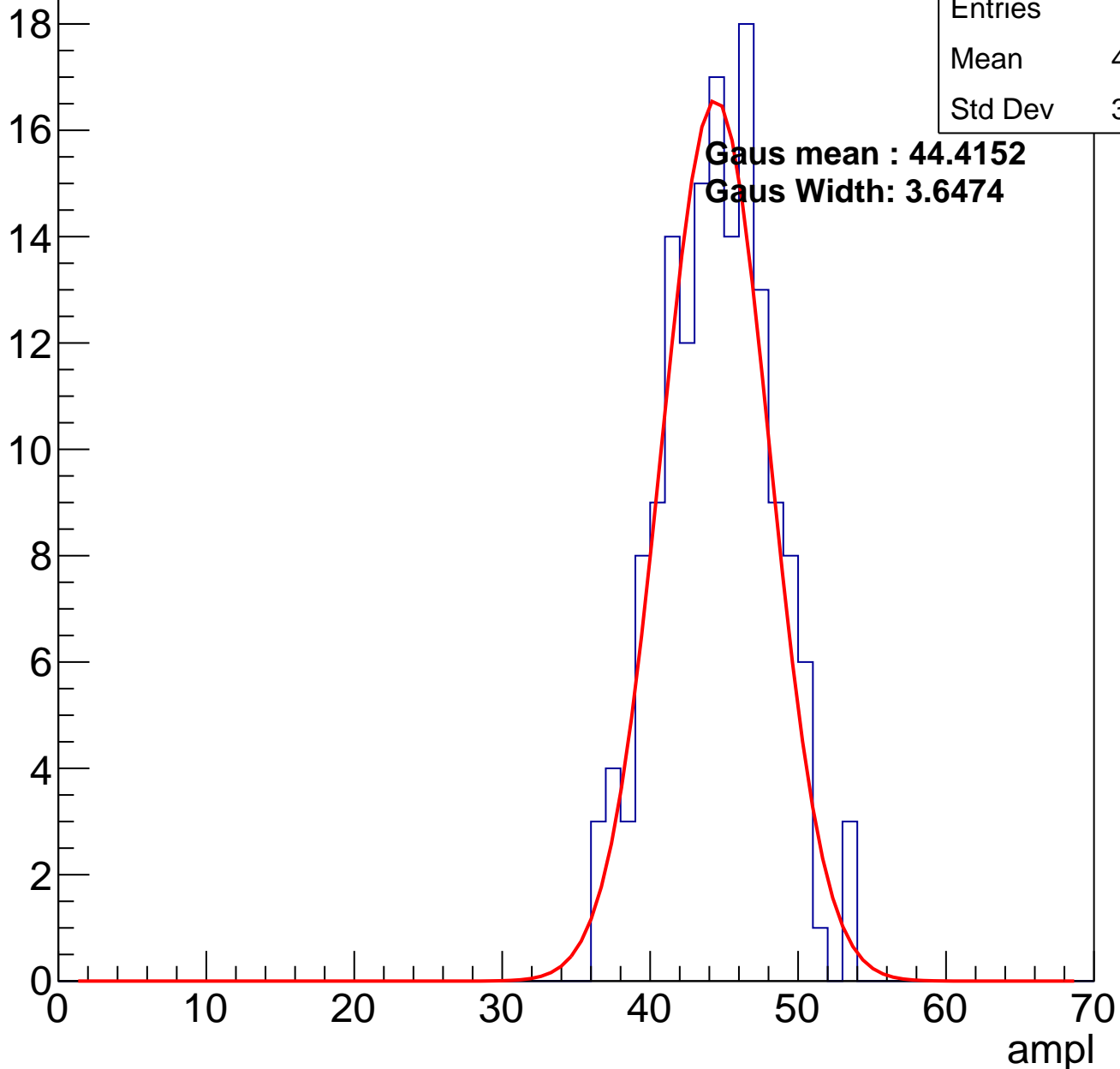
Entry



B0L101S, U20-ch99, adc2

calib_packv5_042523_0143.root, FC#1, port C1

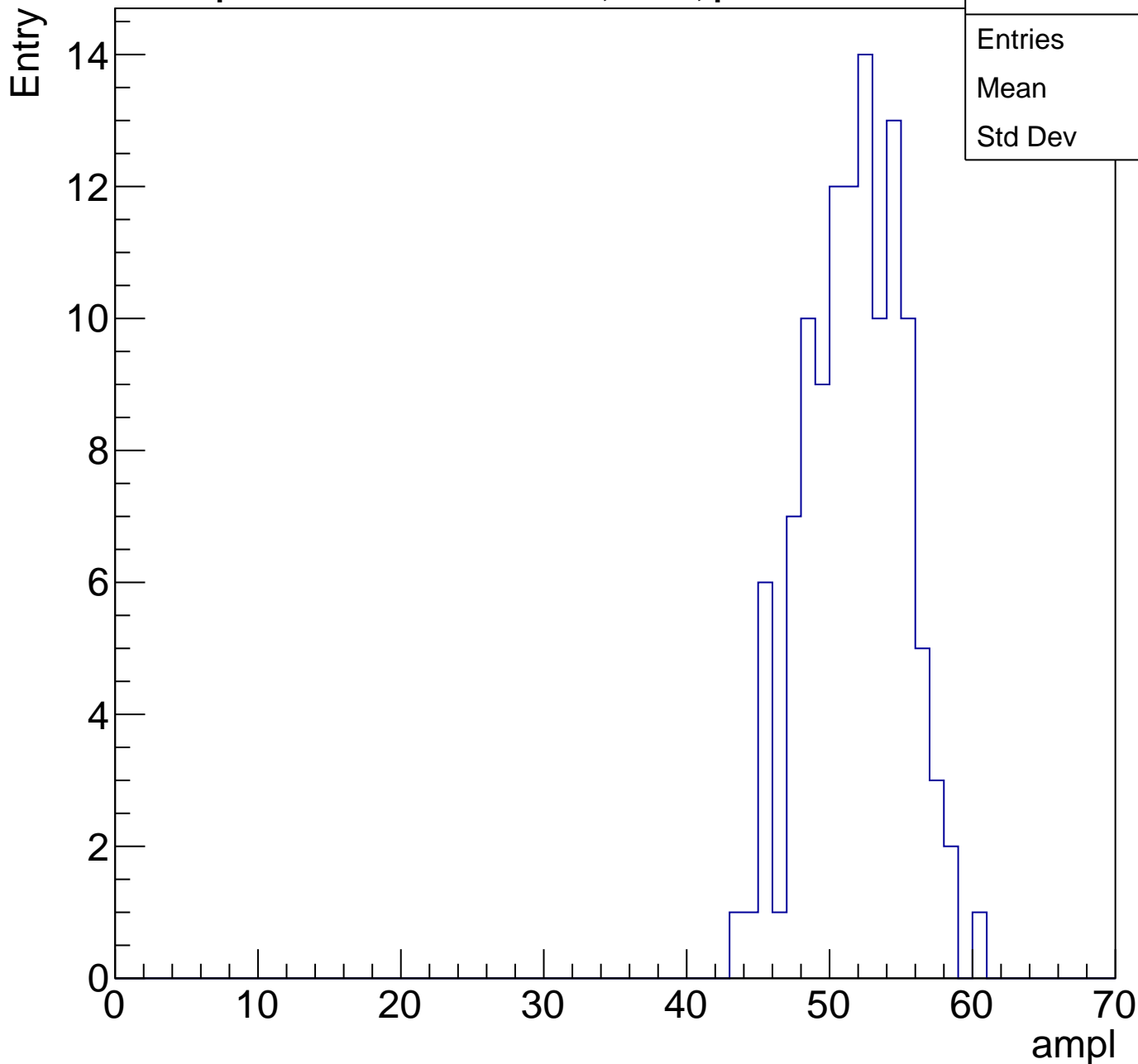
Entry



B0L101S, U20-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

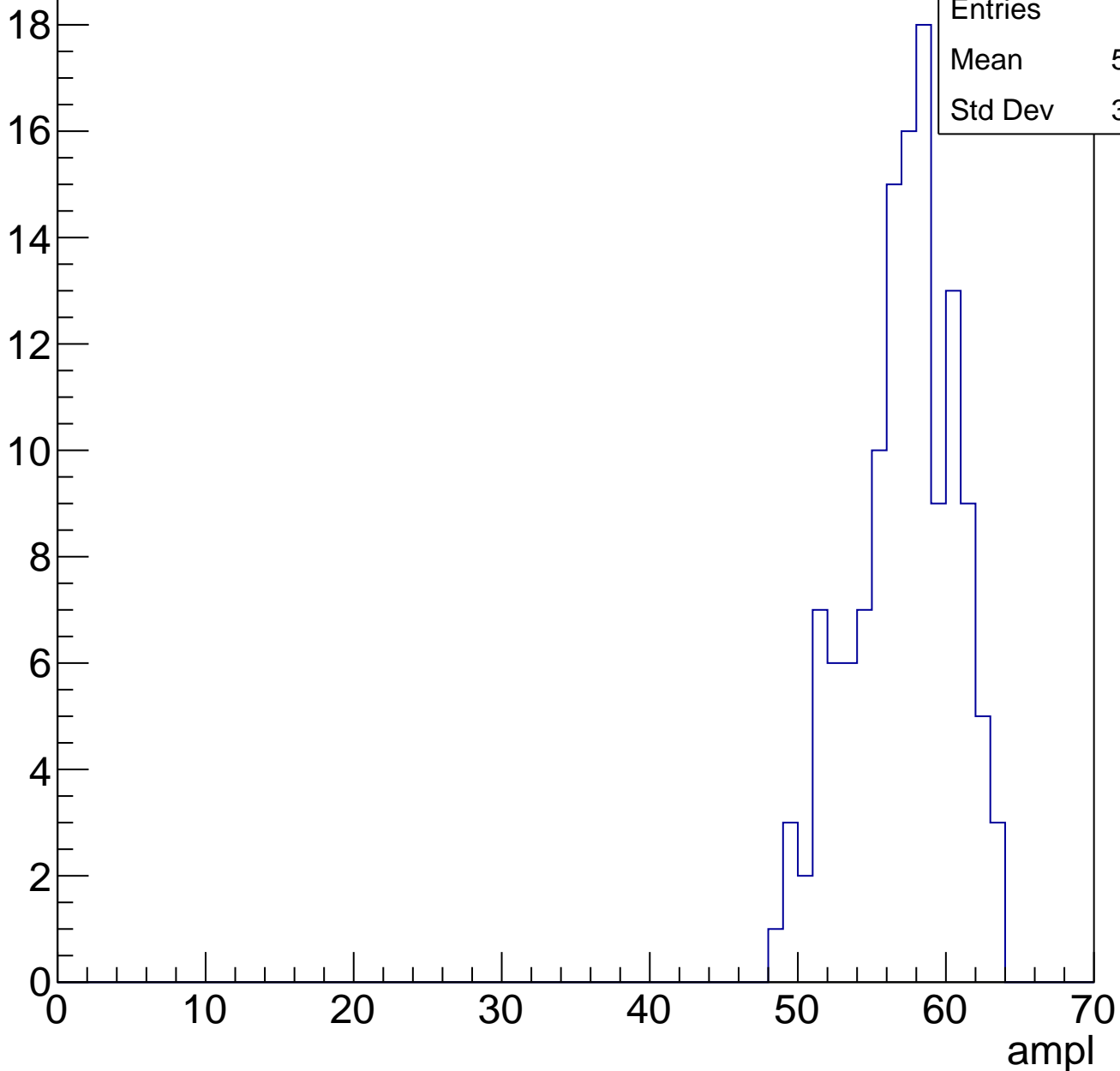
Entries	117
Mean	51.3
Std Dev	3.38



B0L101S, U20-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch99, adc5

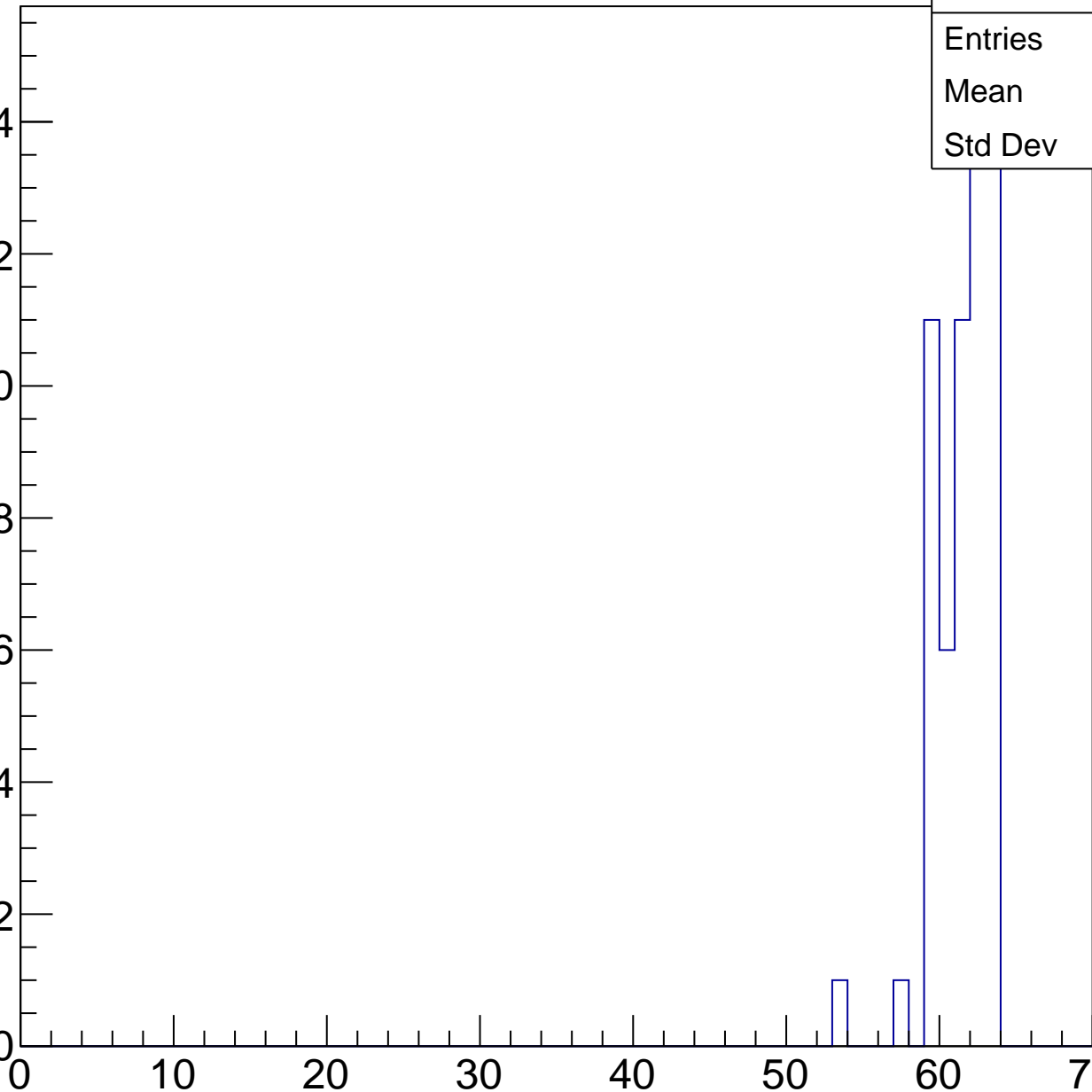
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	61.07
Std Dev	1.858

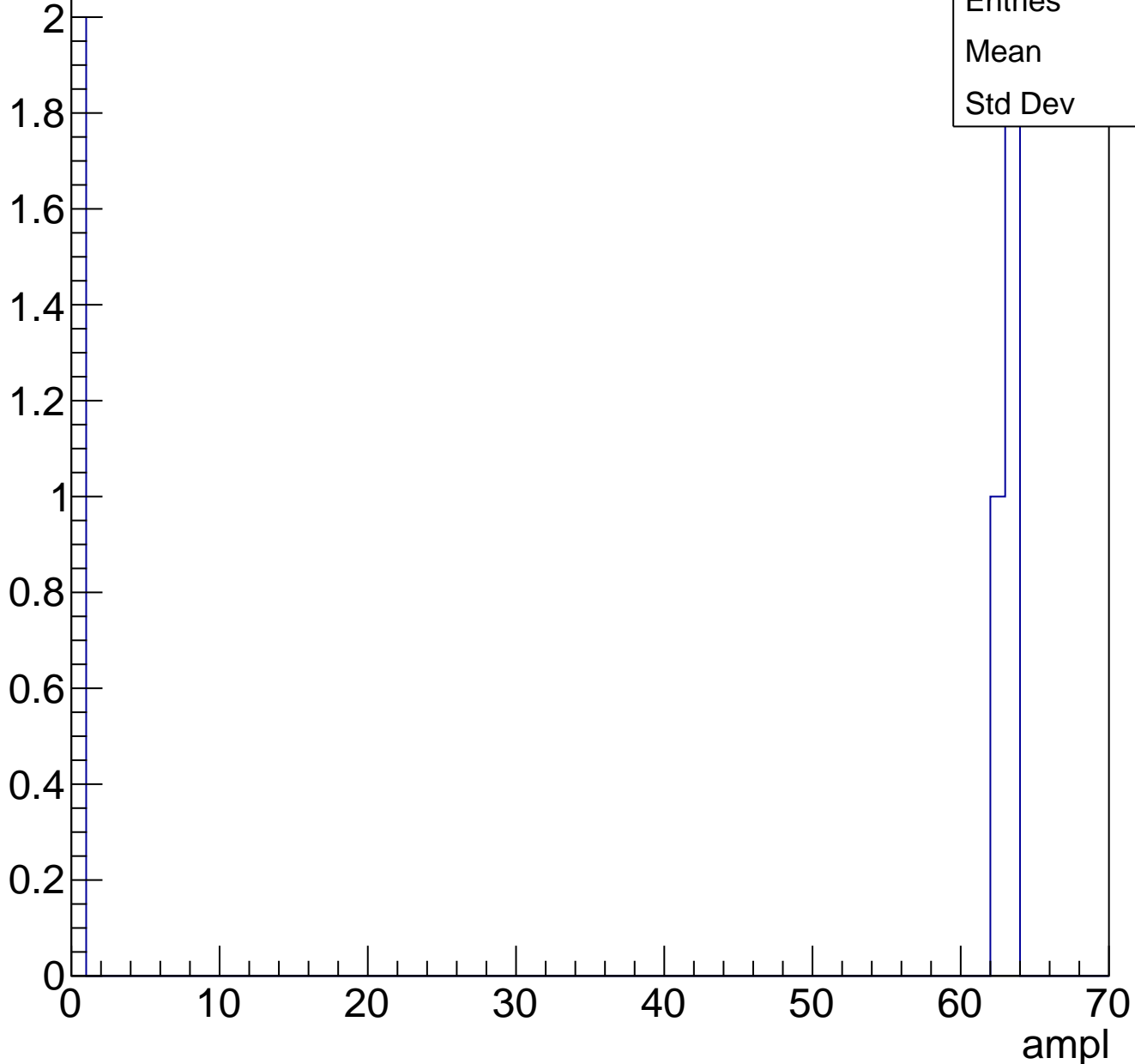
ampl



B0L101S, U20-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch100, adc0

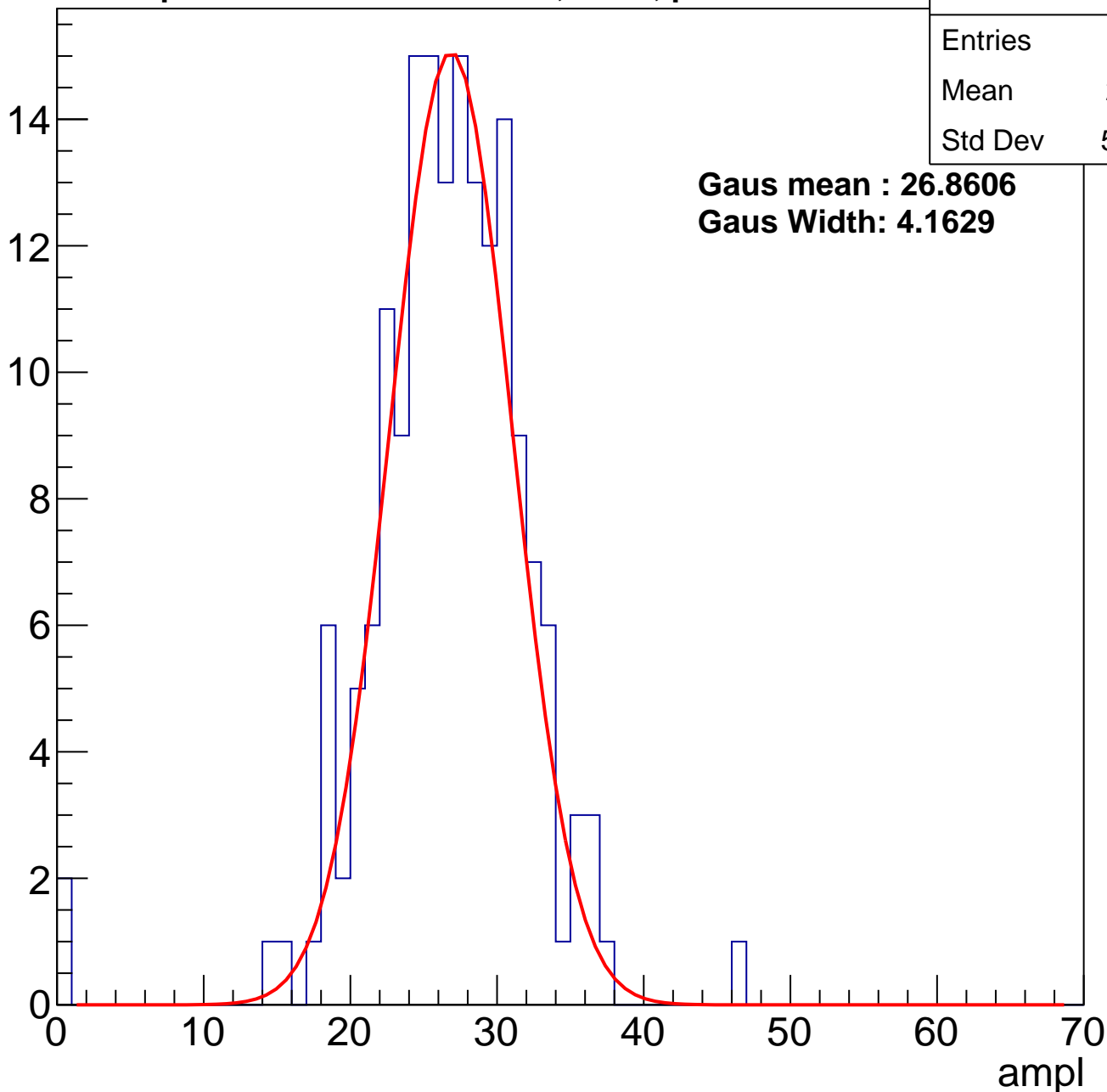
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	26.21
Std Dev	5.463

Gaus mean : 26.8606

Gaus Width: 4.1629

Entry



B0L101S, U20-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

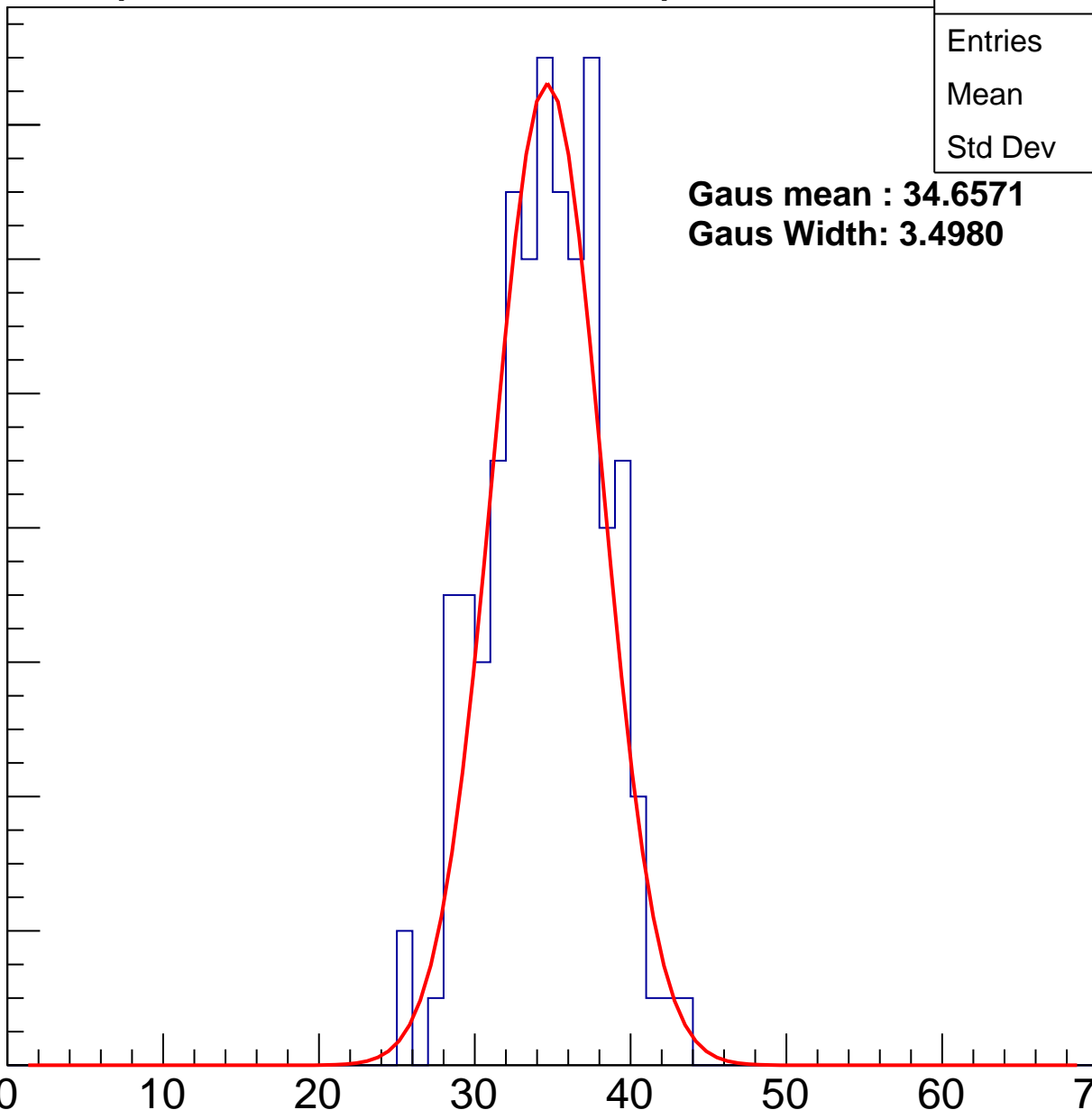
14
12
10
8
6
4
2
0

Entries	136
Mean	34.12
Std Dev	3.602

Gaus mean : 34.6571

Gaus Width: 3.4980

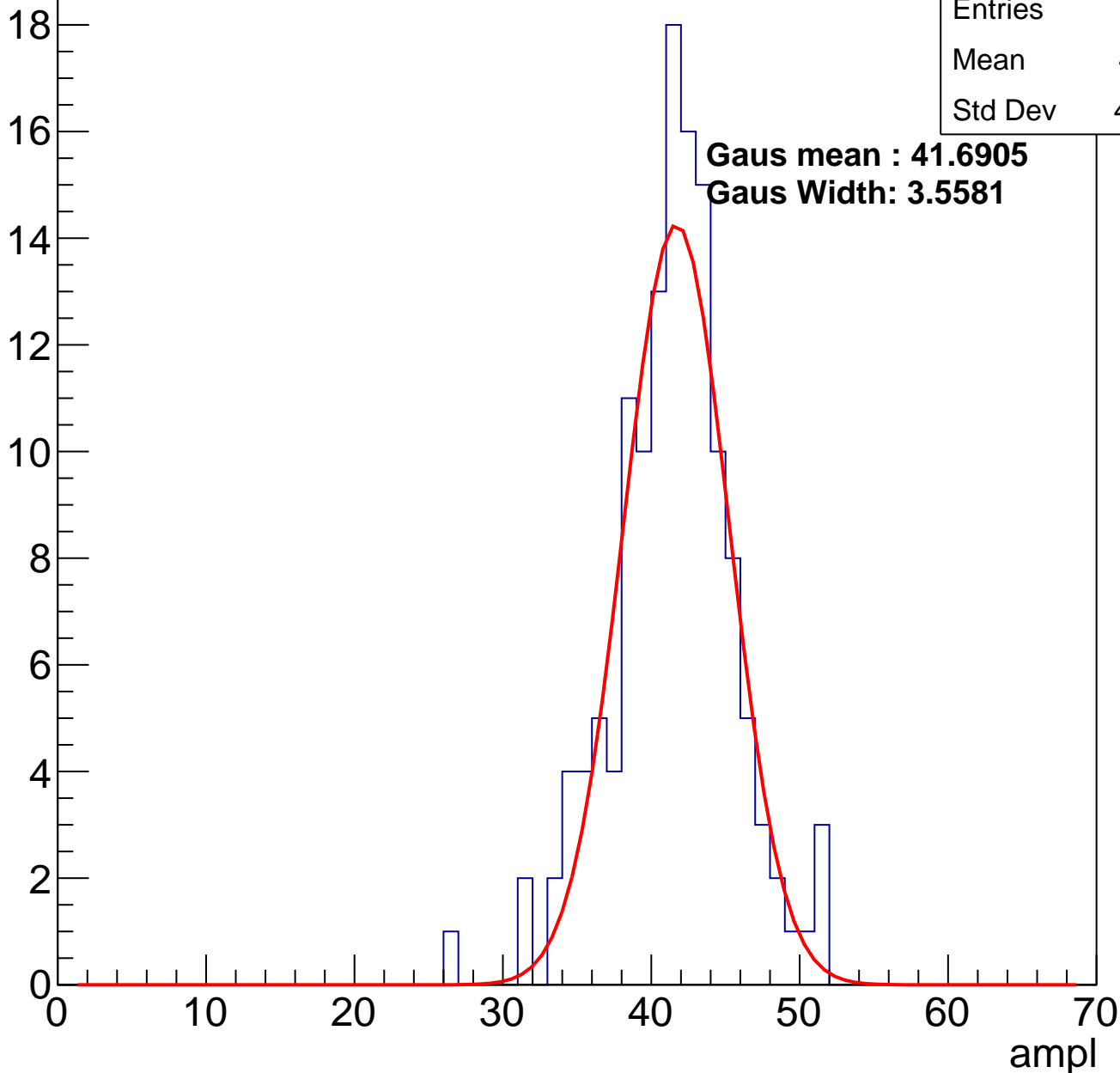
ampl



B0L101S, U20-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

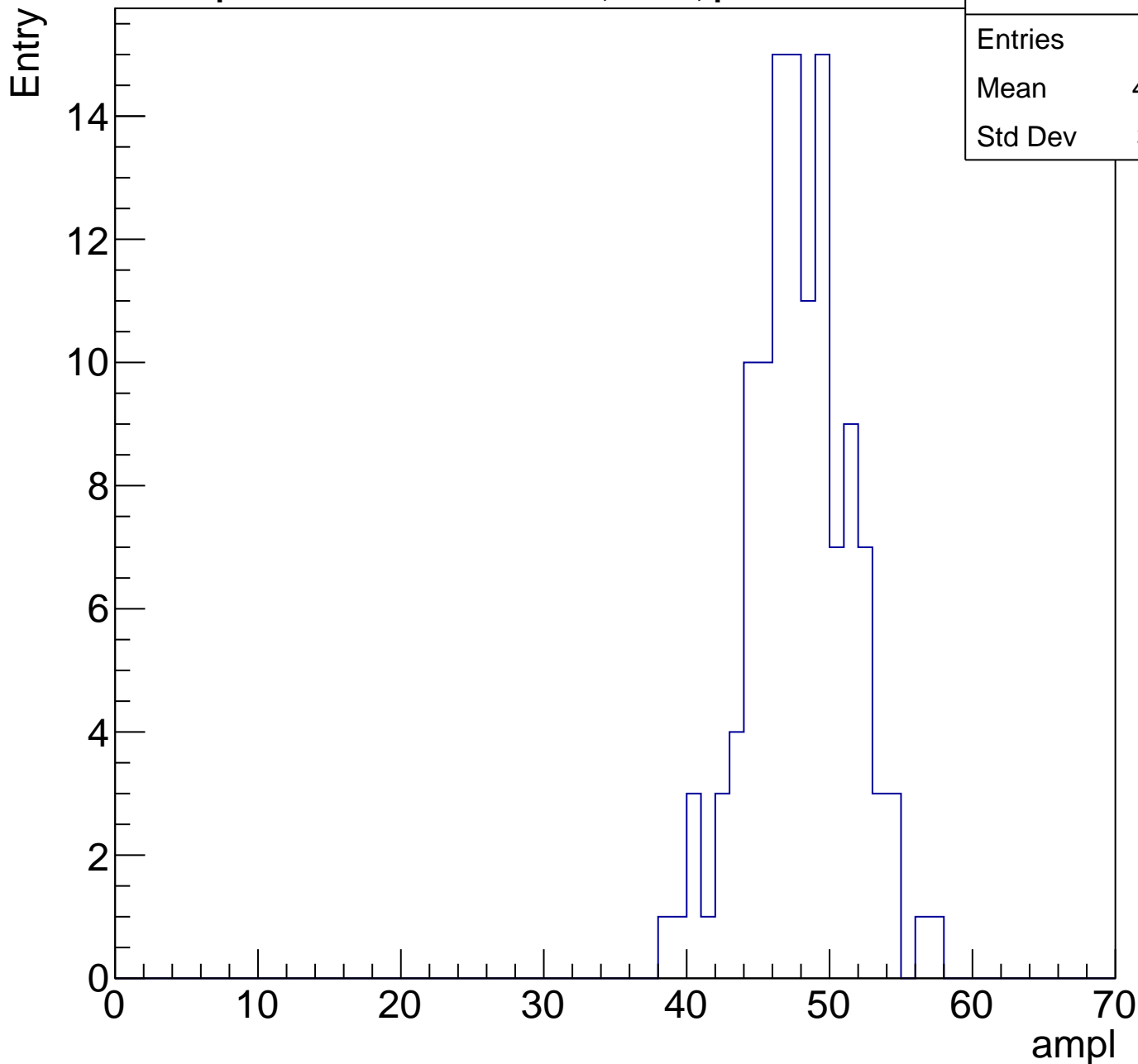
Entry



B0L101S, U20-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

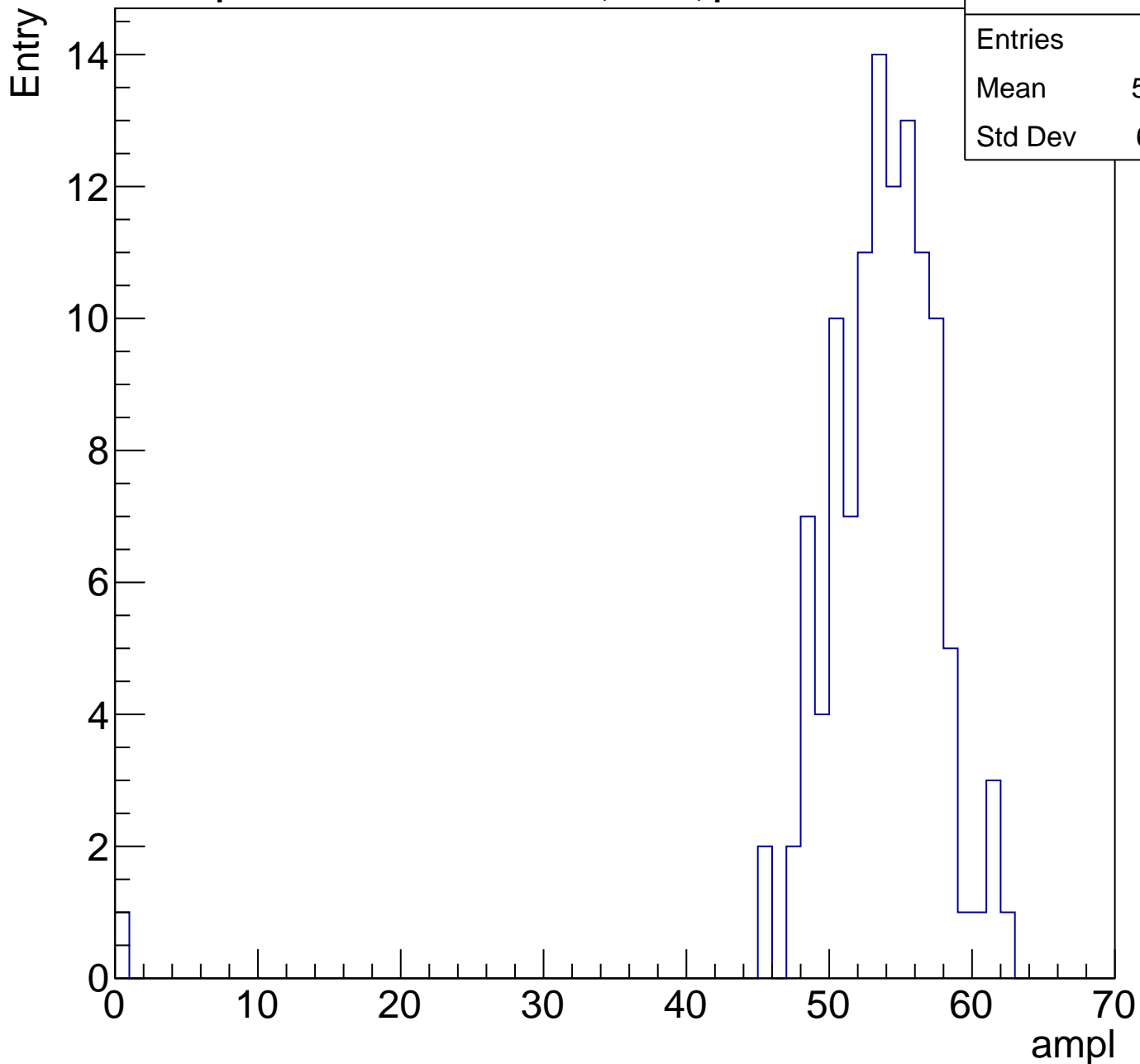
Entries	120
Mean	47.42
Std Dev	3.511



B0L101S, U20-ch100, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	52.96
Std Dev	6.031



B0L101S, U20-ch100, adc5

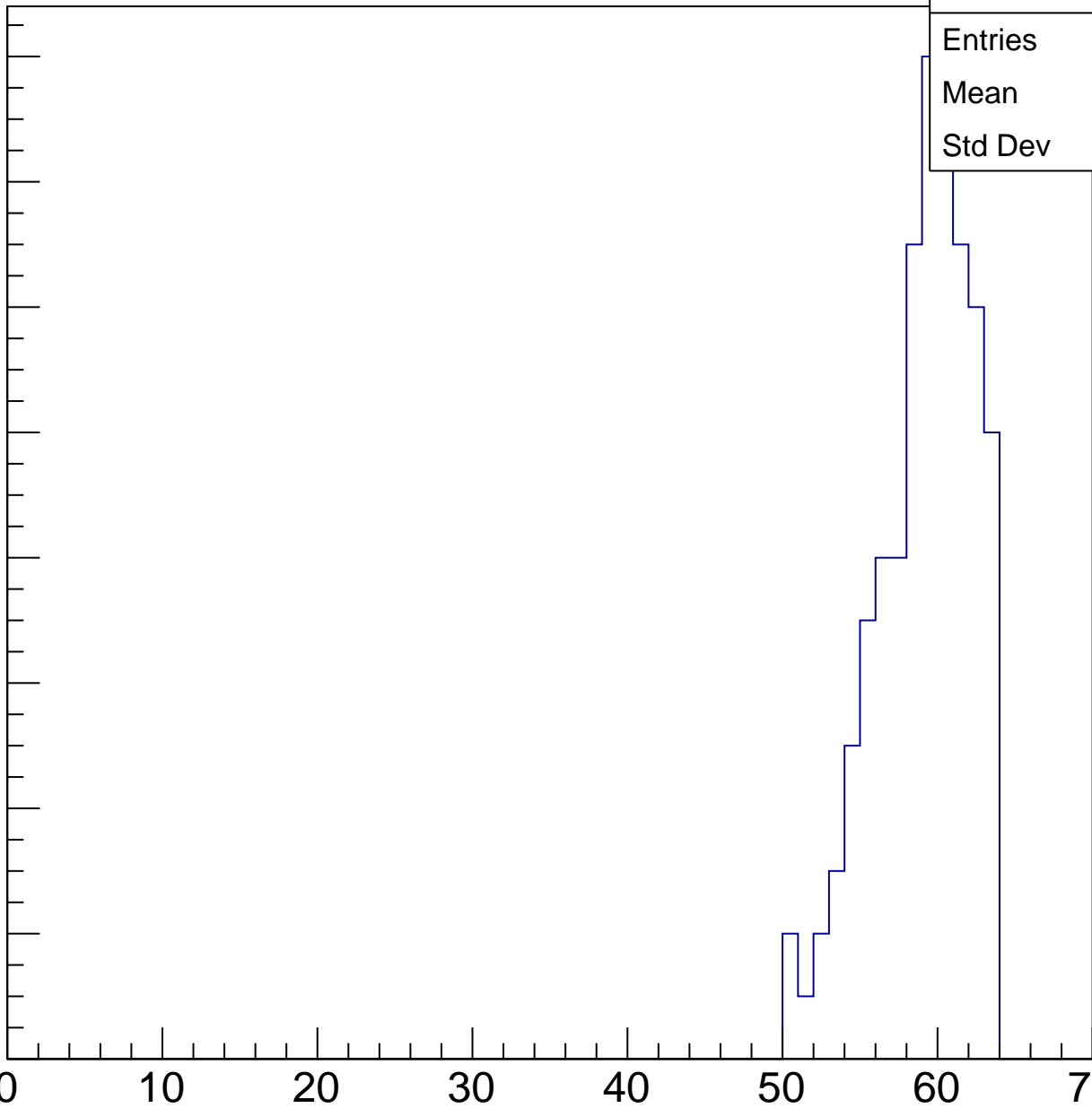
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	58.59
Std Dev	3.095

ampl

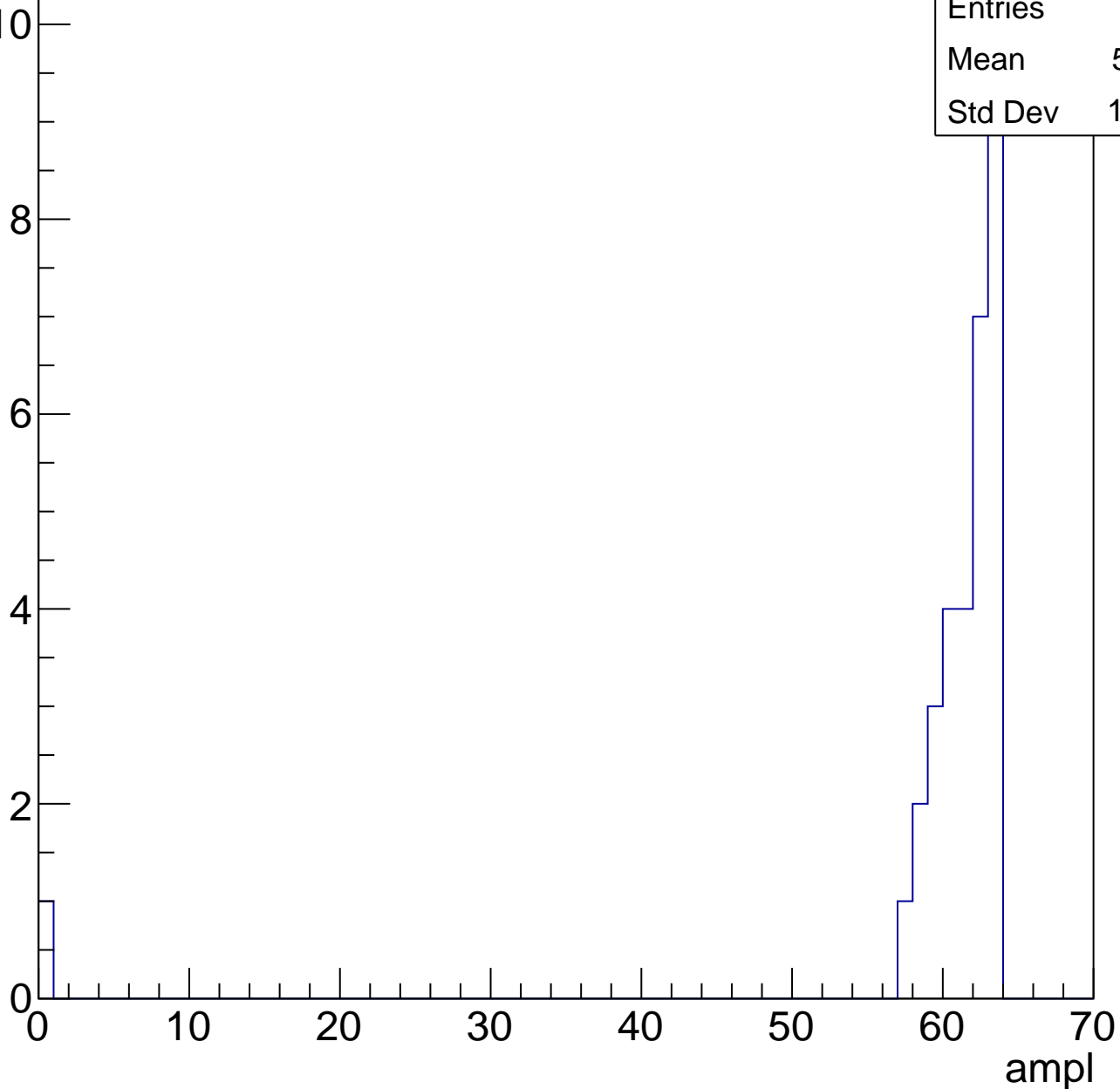


B0L101S, U20-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

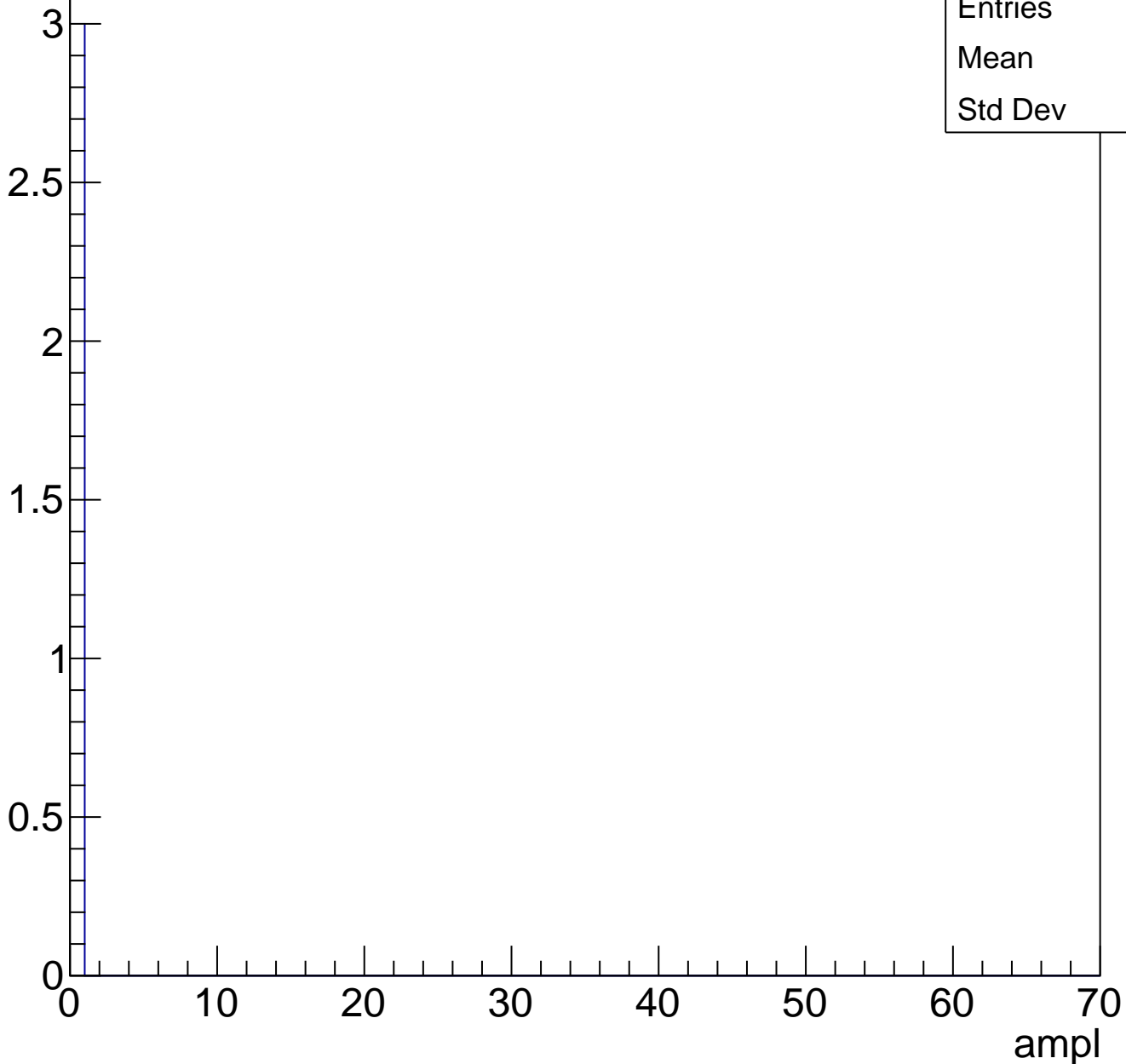
Entries	32
Mean	59.31
Std Dev	10.79



B0L101S, U20-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U20-ch101, adc0

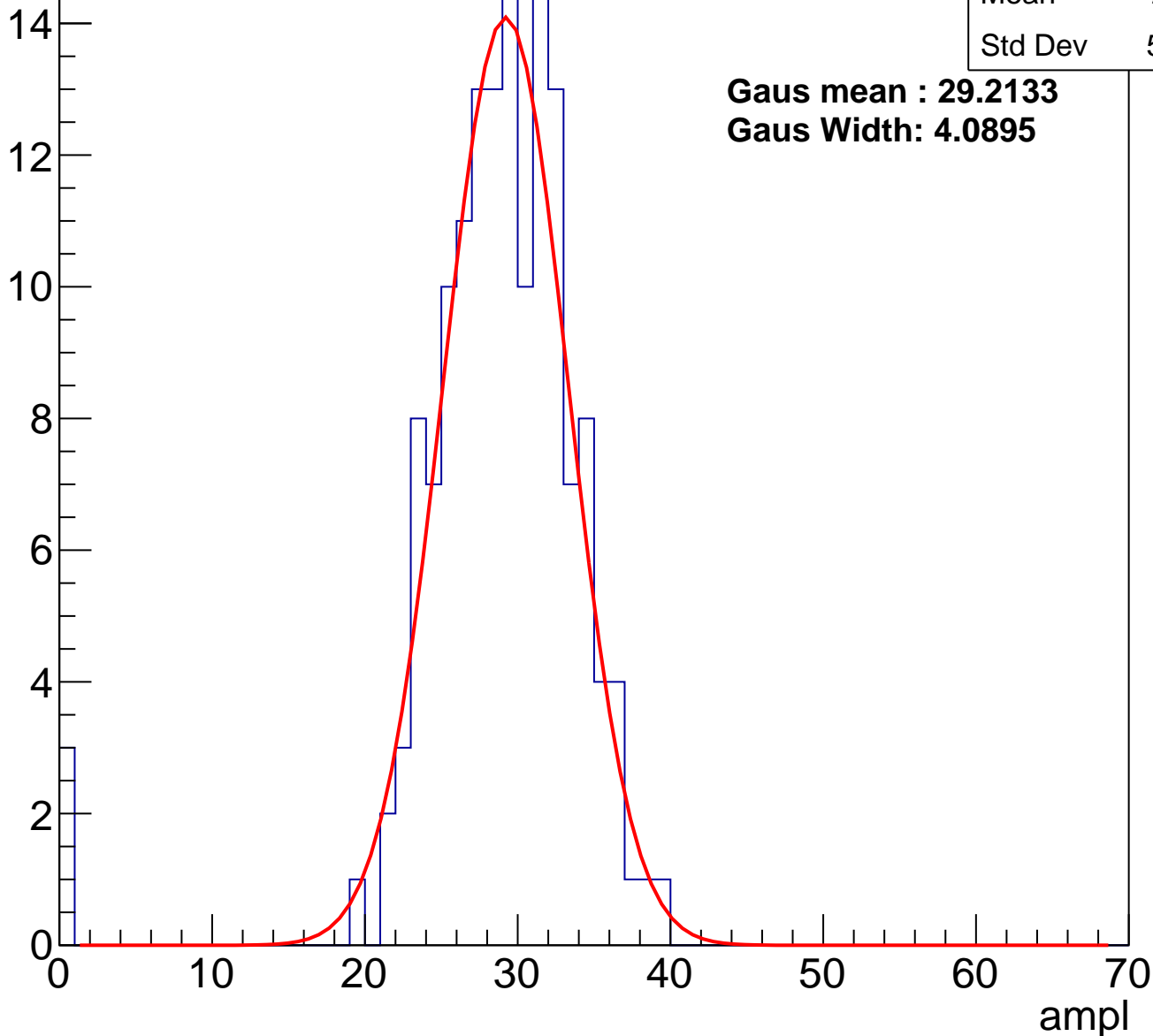
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	28.31
Std Dev	5.597

Gaus mean : 29.2133

Gaus Width: 4.0895

Entry



B0L101S, U20-ch101, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	36.2
Std Dev	4.777

Gaus mean : 37.0582

Gaus Width: 3.9659

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

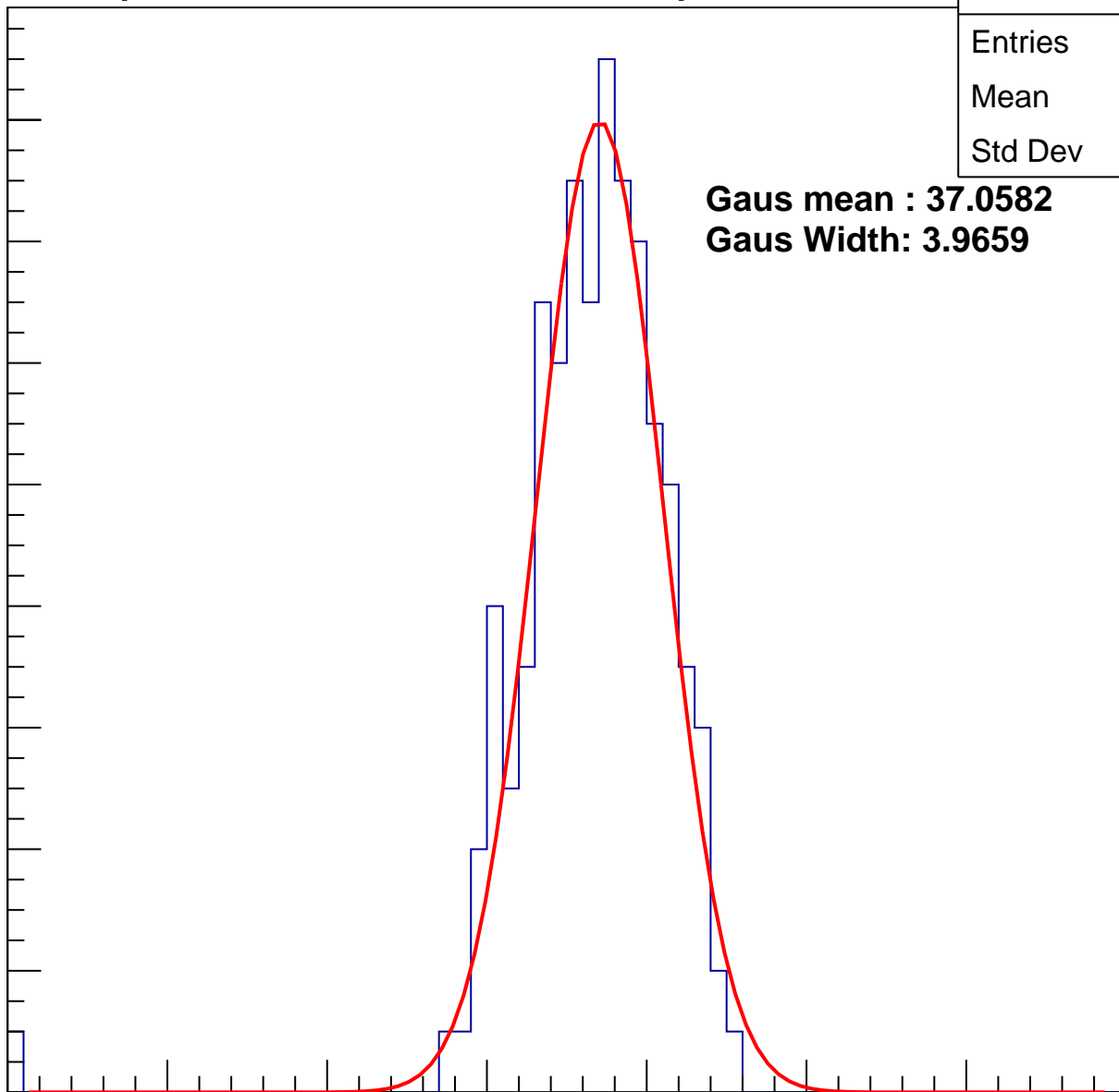
40

50

60

70

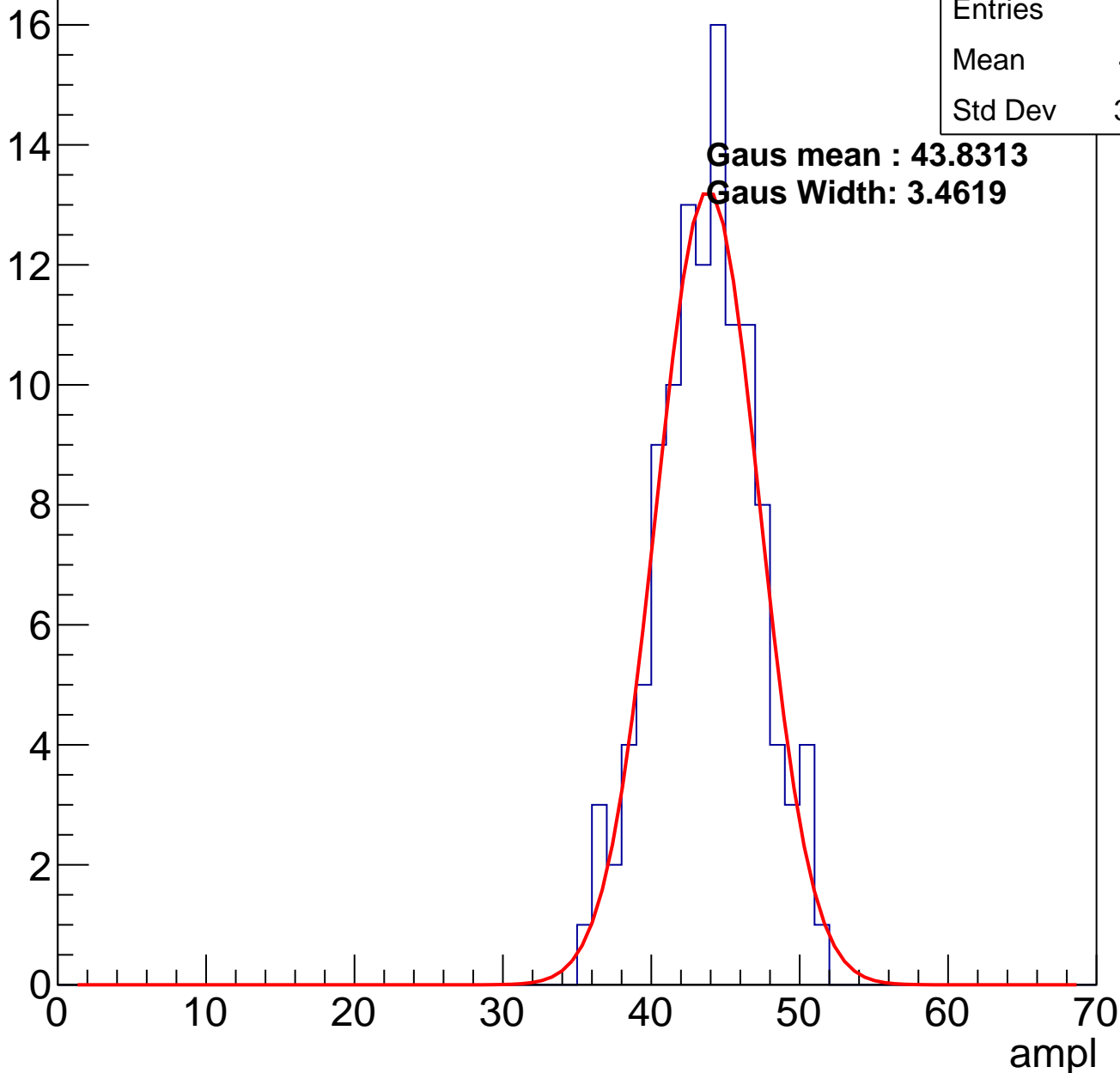
ampl



B0L101S, U20-ch101, adc2

calib_packv5_042523_0143.root, FC#1, port C1

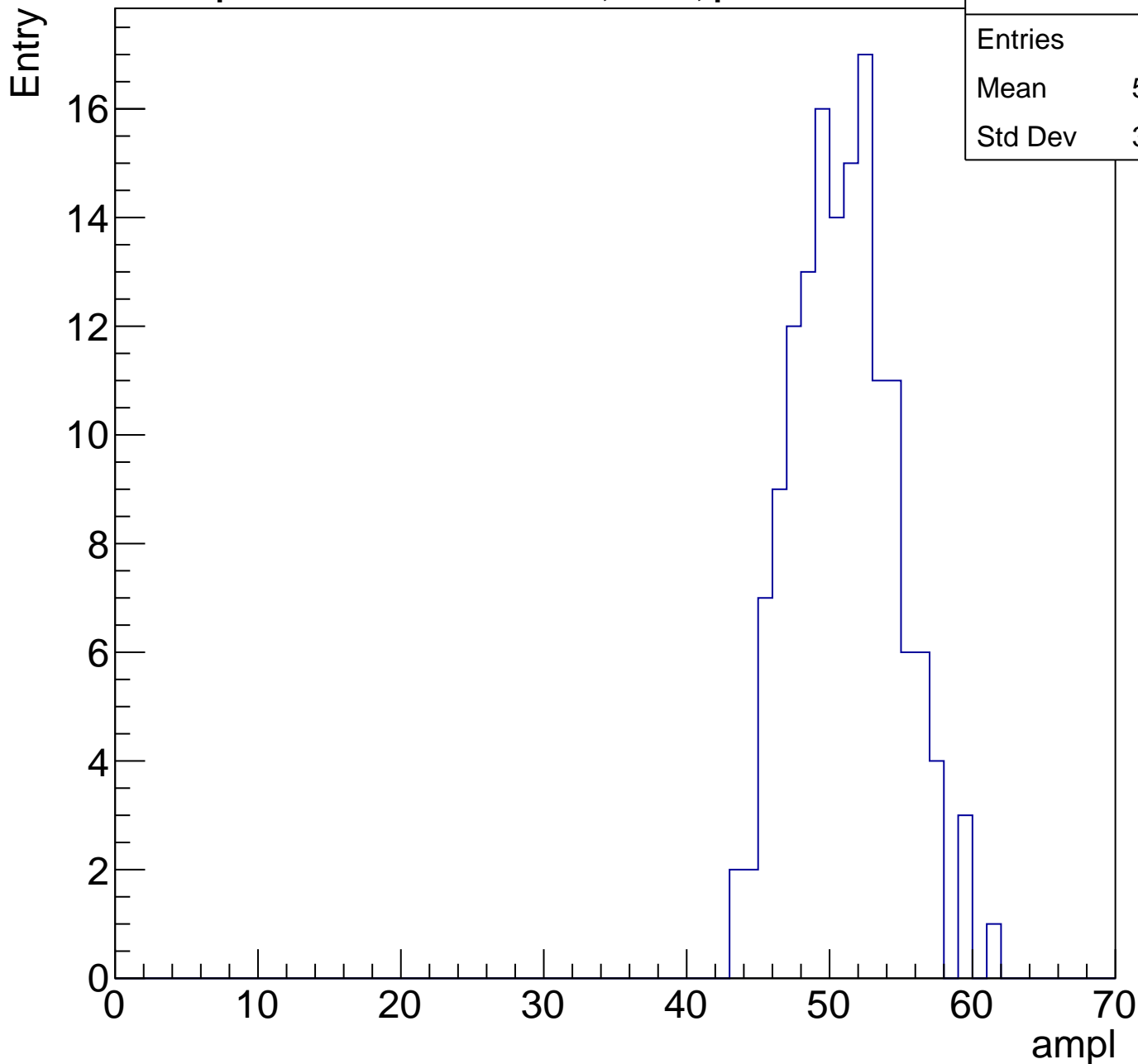
Entry



B0L101S, U20-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

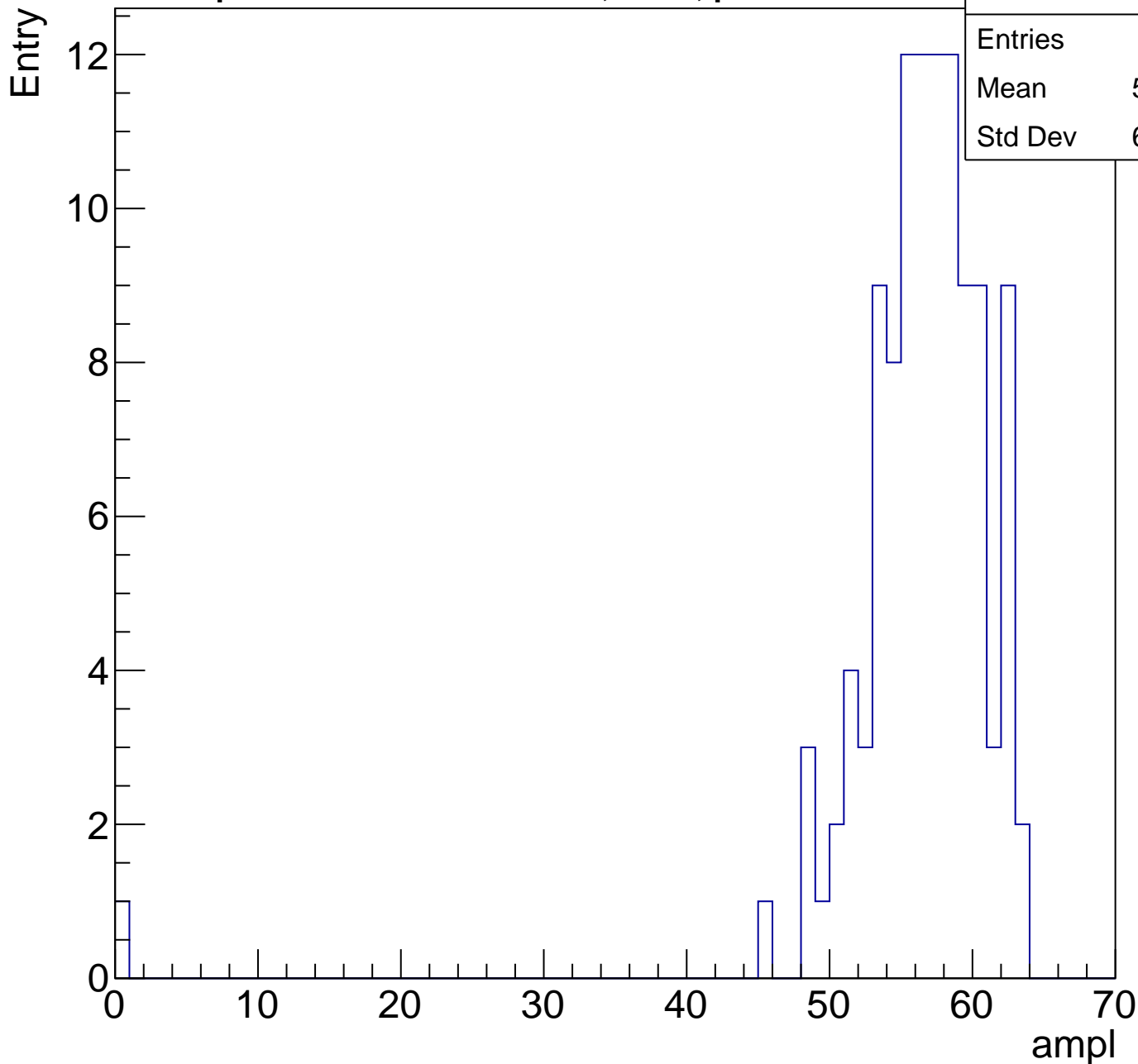
Entries	149
Mean	50.56
Std Dev	3.574



B0L101S, U20-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	55.87
Std Dev	6.425



B0L101S, U20-ch101, adc5

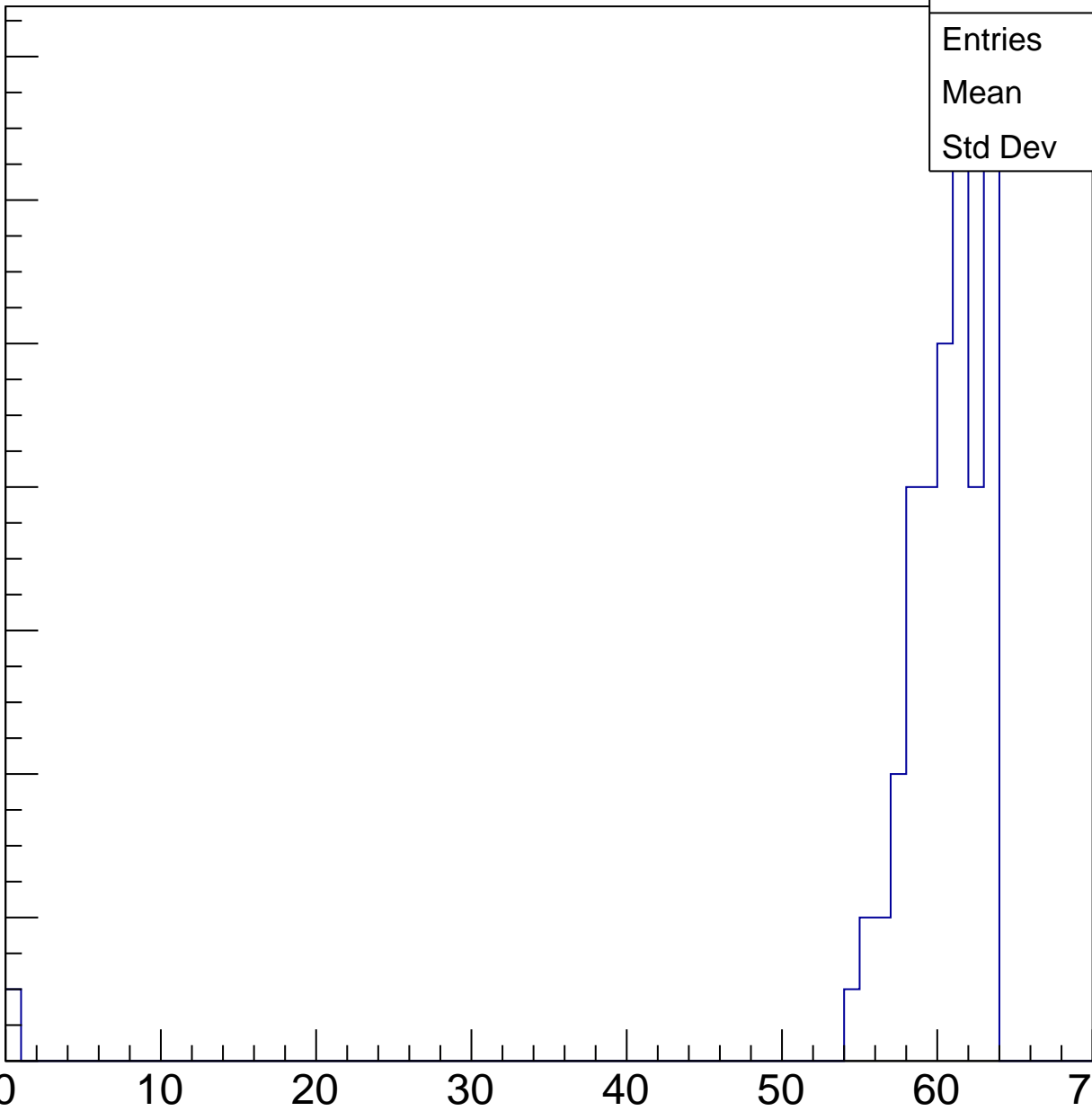
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.33
Std Dev	7.392

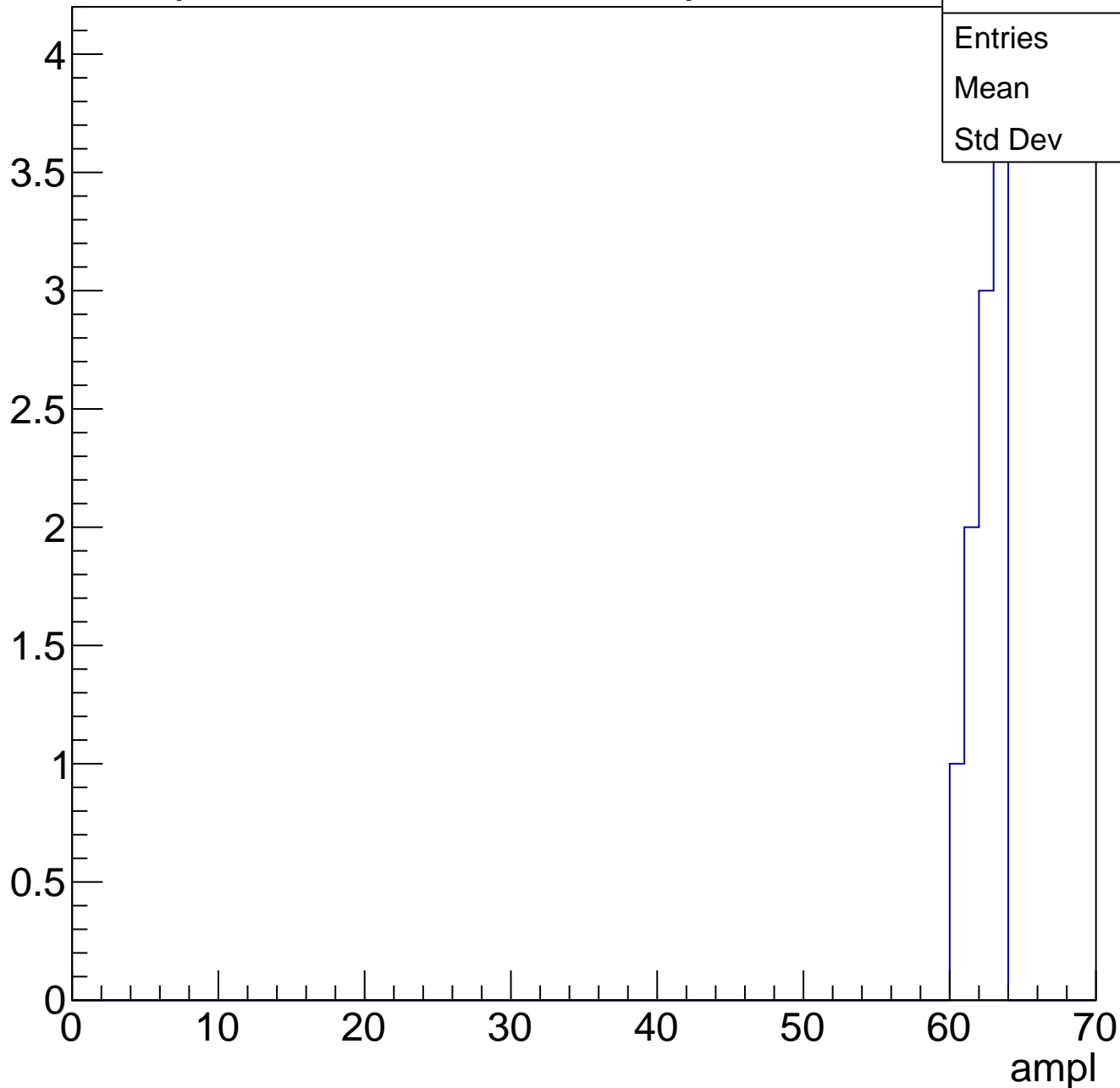
ampl



B0L101S, U20-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U20-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	28.57
Std Dev	3.799

Gaus mean : 28.5096

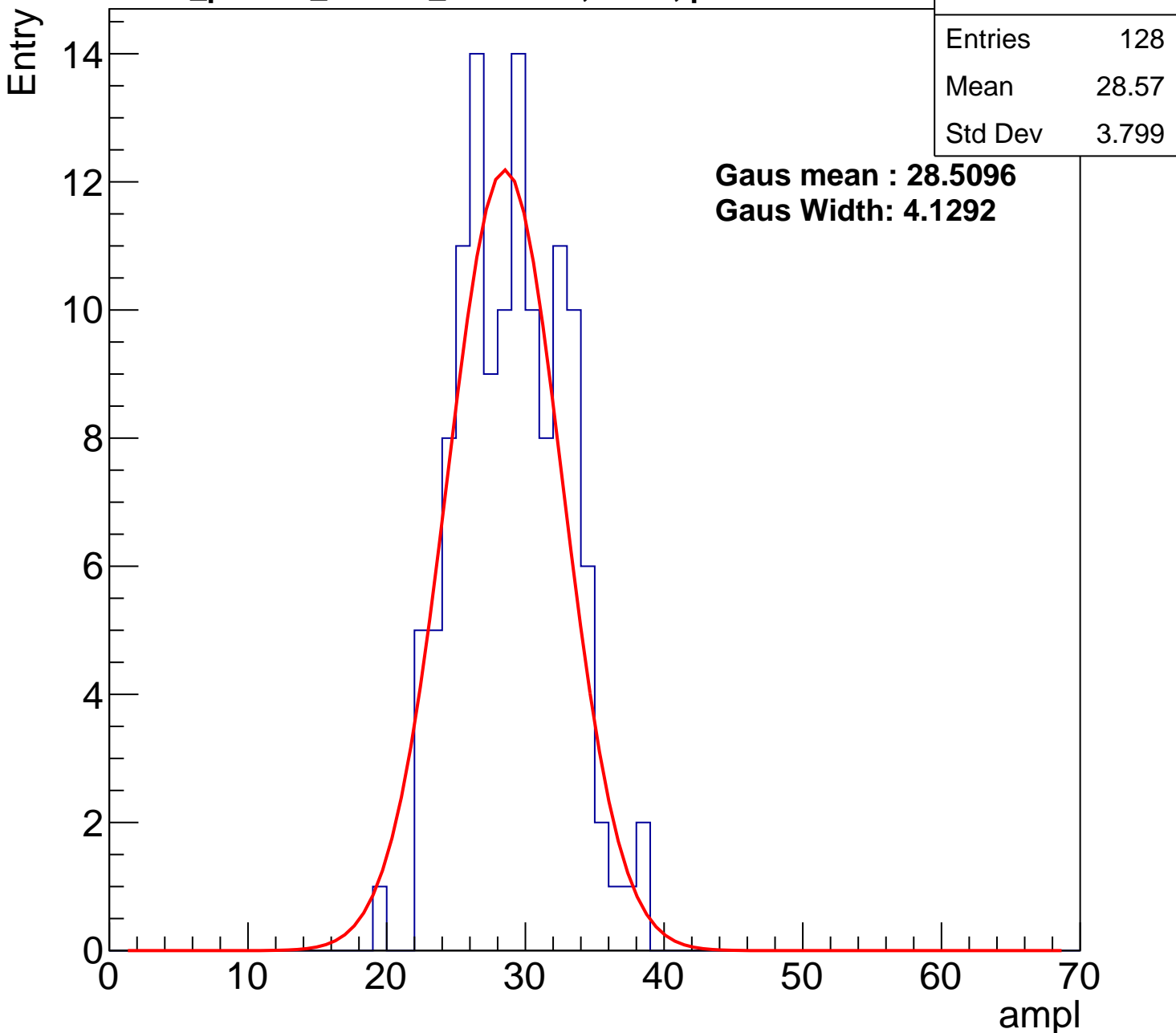
Gaus Width: 4.1292

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch102, adc1

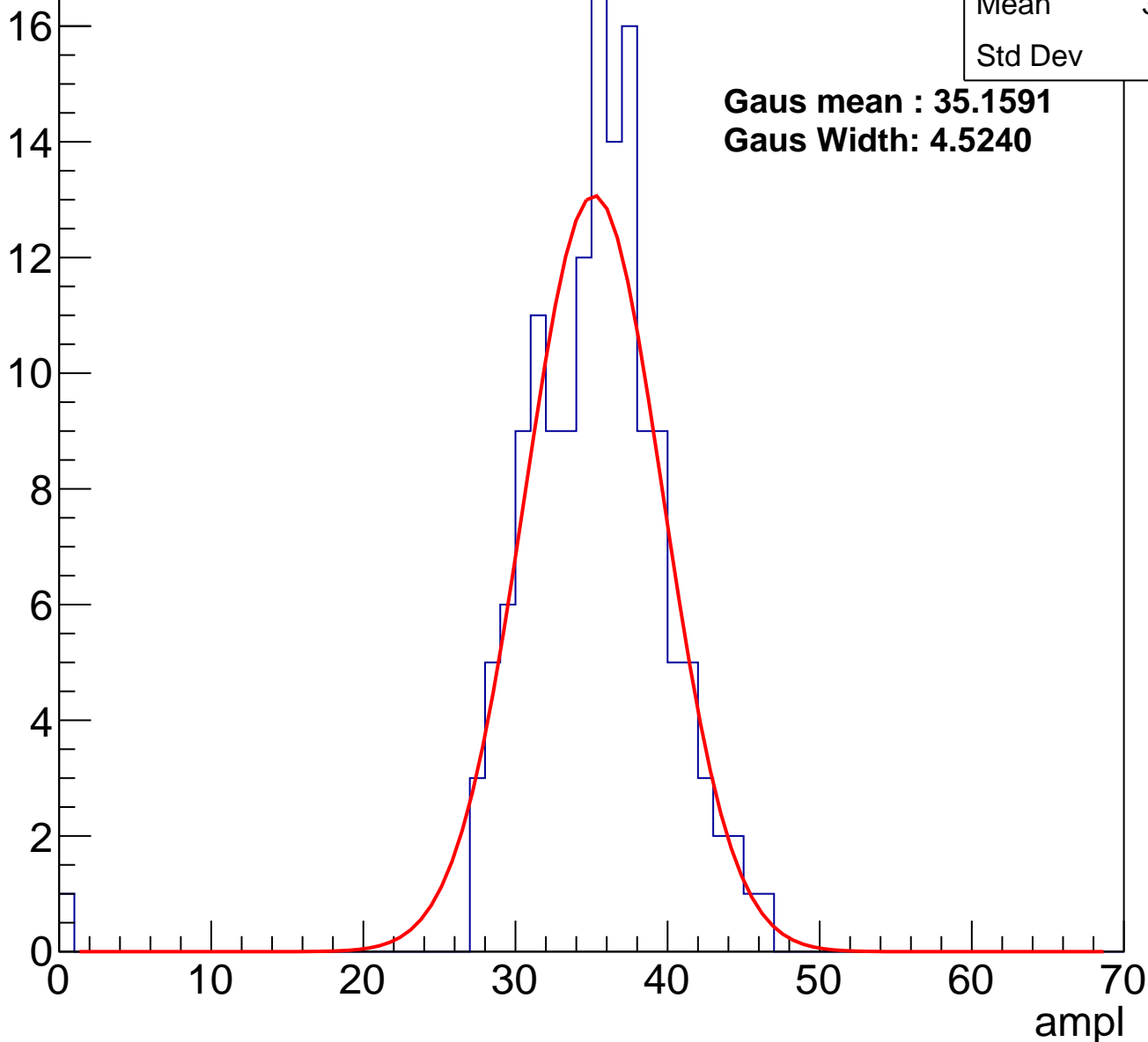
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	149
Mean	34.76
Std Dev	4.96

Gaus mean : 35.1591

Gaus Width: 4.5240



B0L101S, U20-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

149

Mean

42.74

Std Dev

3.97

Gaus mean : 43.2179

Gaus Width: 3.8459

0

10

20

30

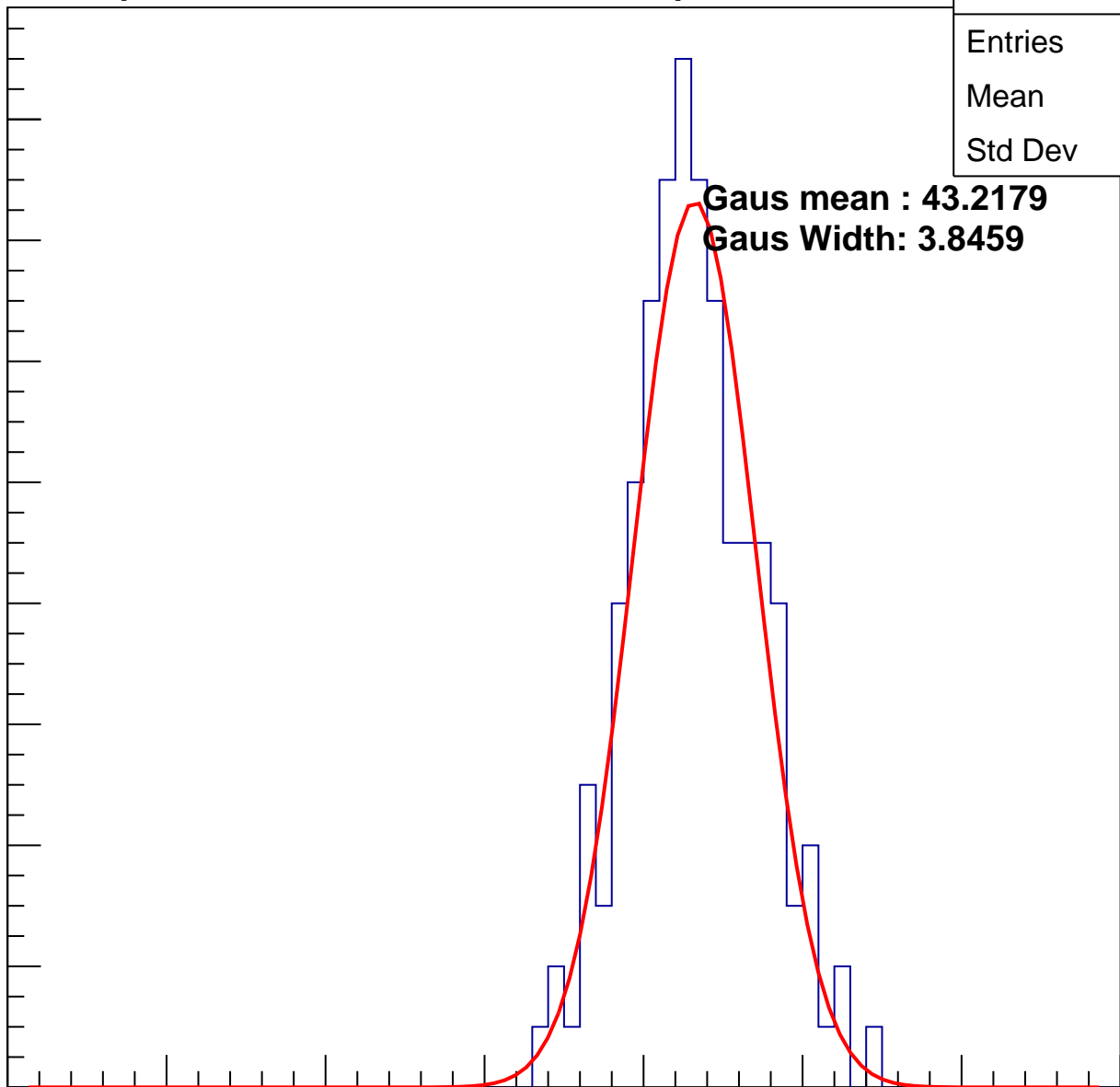
40

50

60

70

ampl



B0L101S, U20-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	49.16
Std Dev	4.105

Entry

12

10

8

6

4

2

0

0

10

20

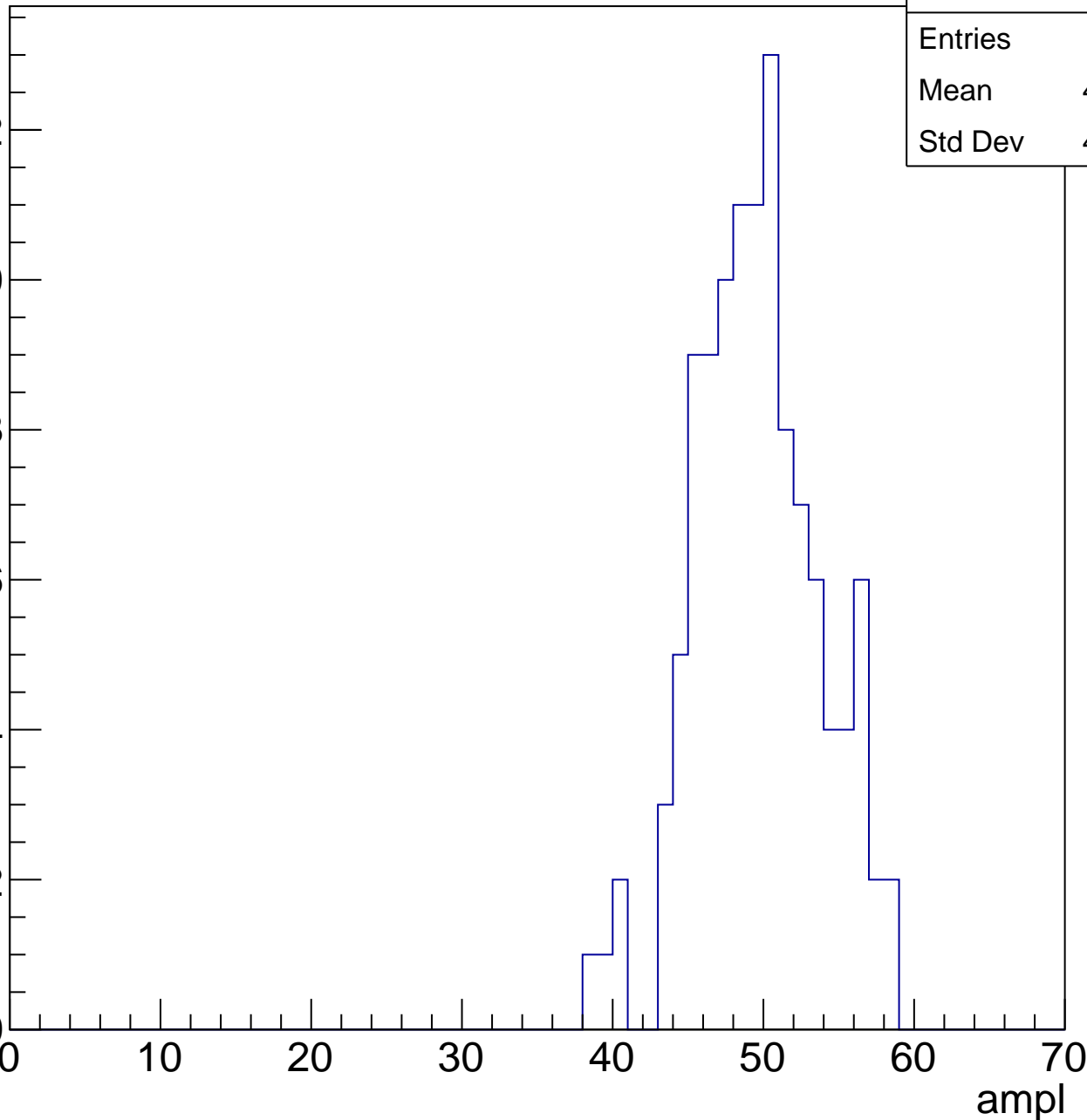
30

40

50

60

ampl



B0L101S, U20-ch102, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.23
Std Dev	3.733

Entry

12

10

8

6

4

2

0

0

10

20

30

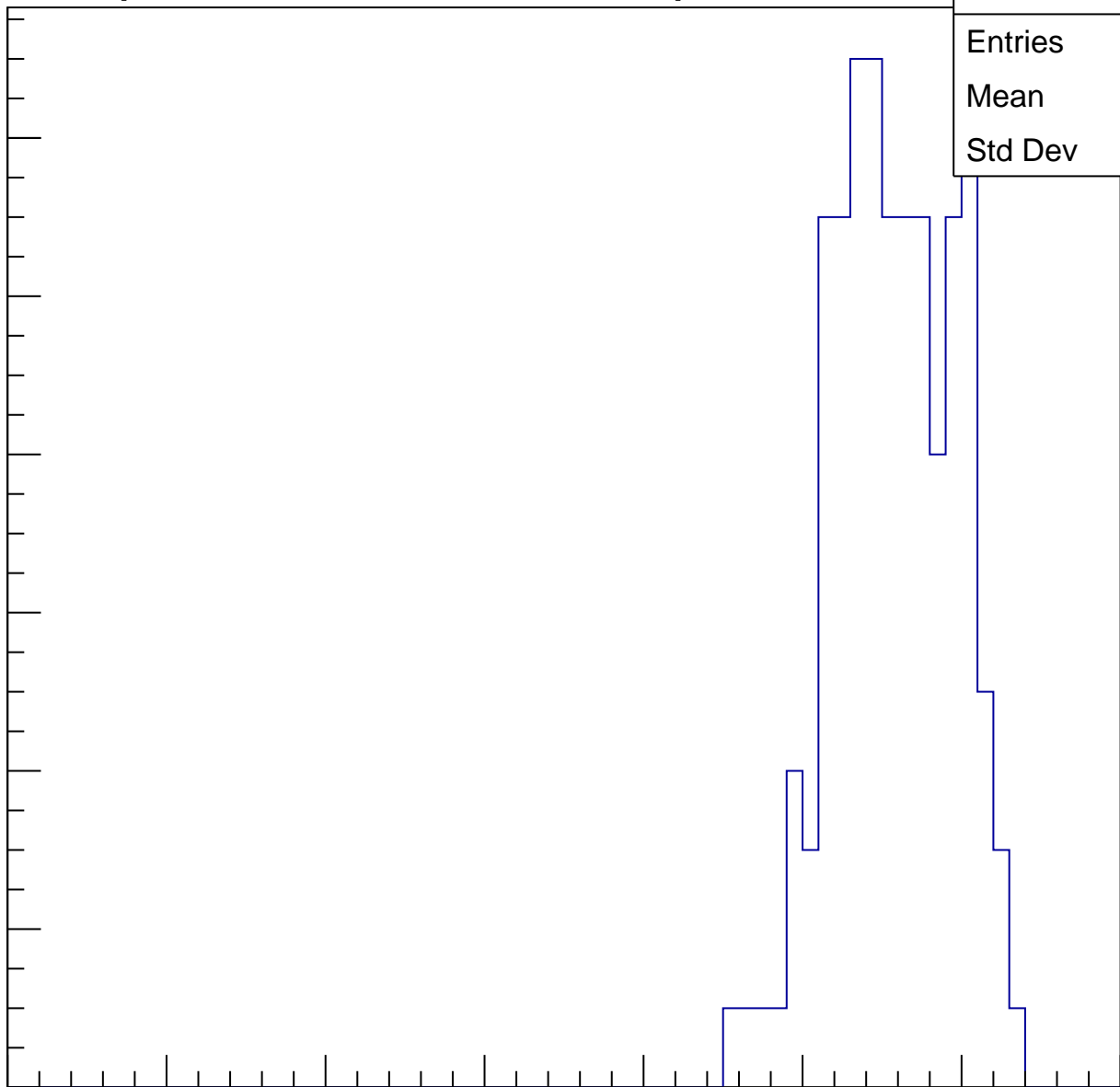
40

50

60

70

ampl



B0L101S, U20-ch102, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	82
Mean	57.88
Std Dev	9.635

Entry

12

10

8

6

4

2

0

0

10

20

30

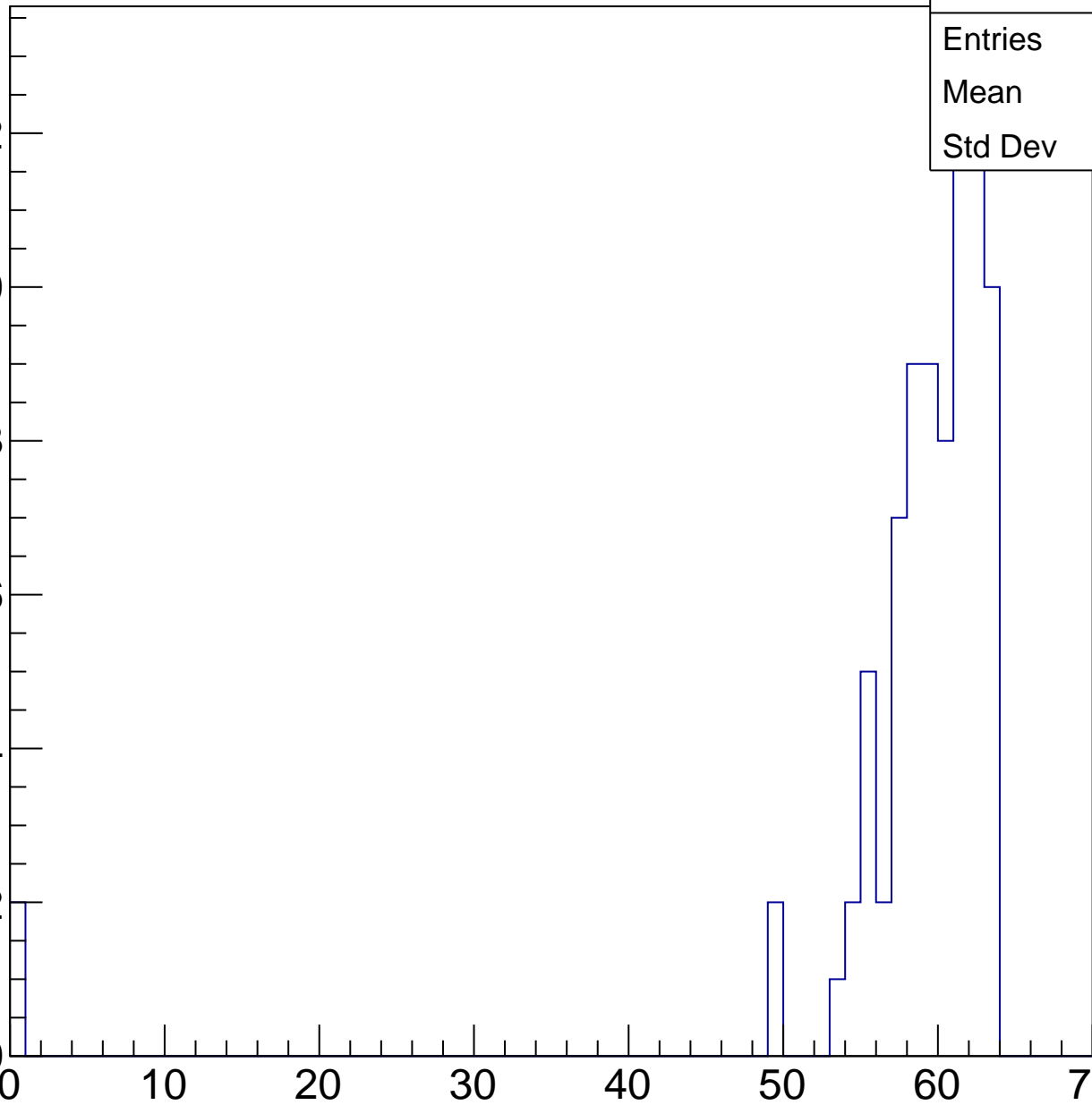
40

50

60

70

ampl

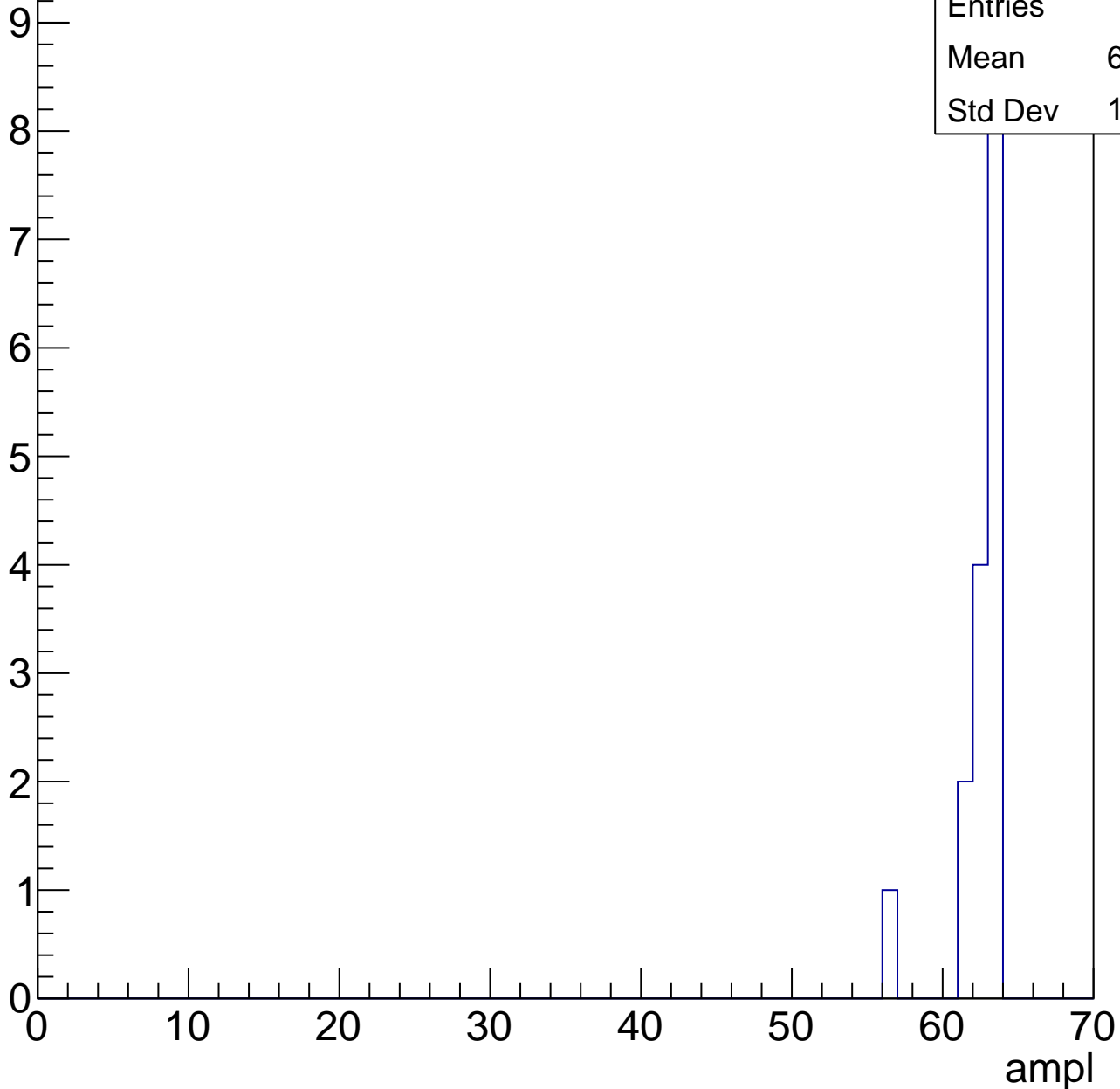


B0L101S, U20-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

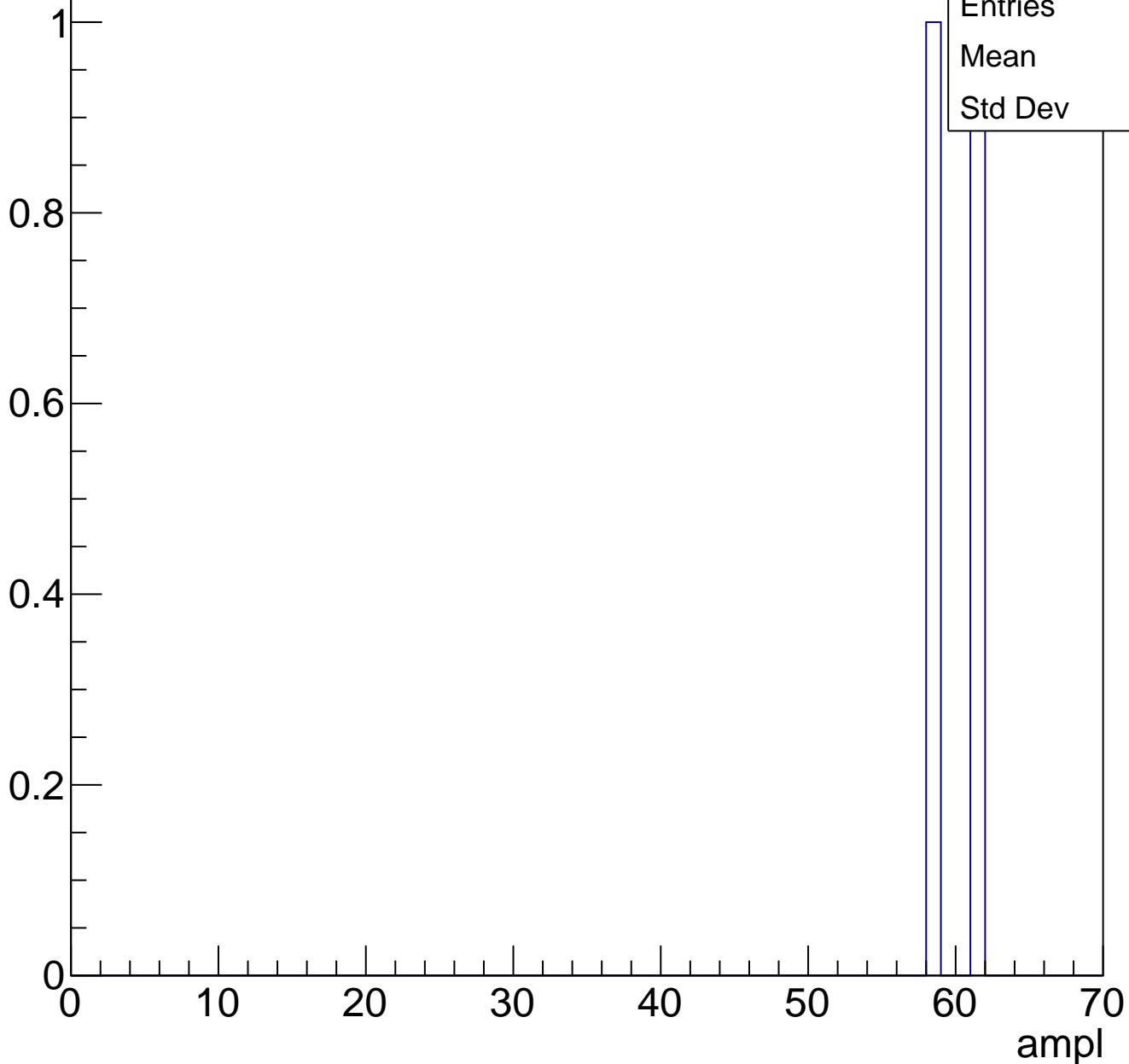
Entries	16
Mean	62.06
Std Dev	1.713



B0L101S, U20-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch103, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	27.23
Std Dev	4.19

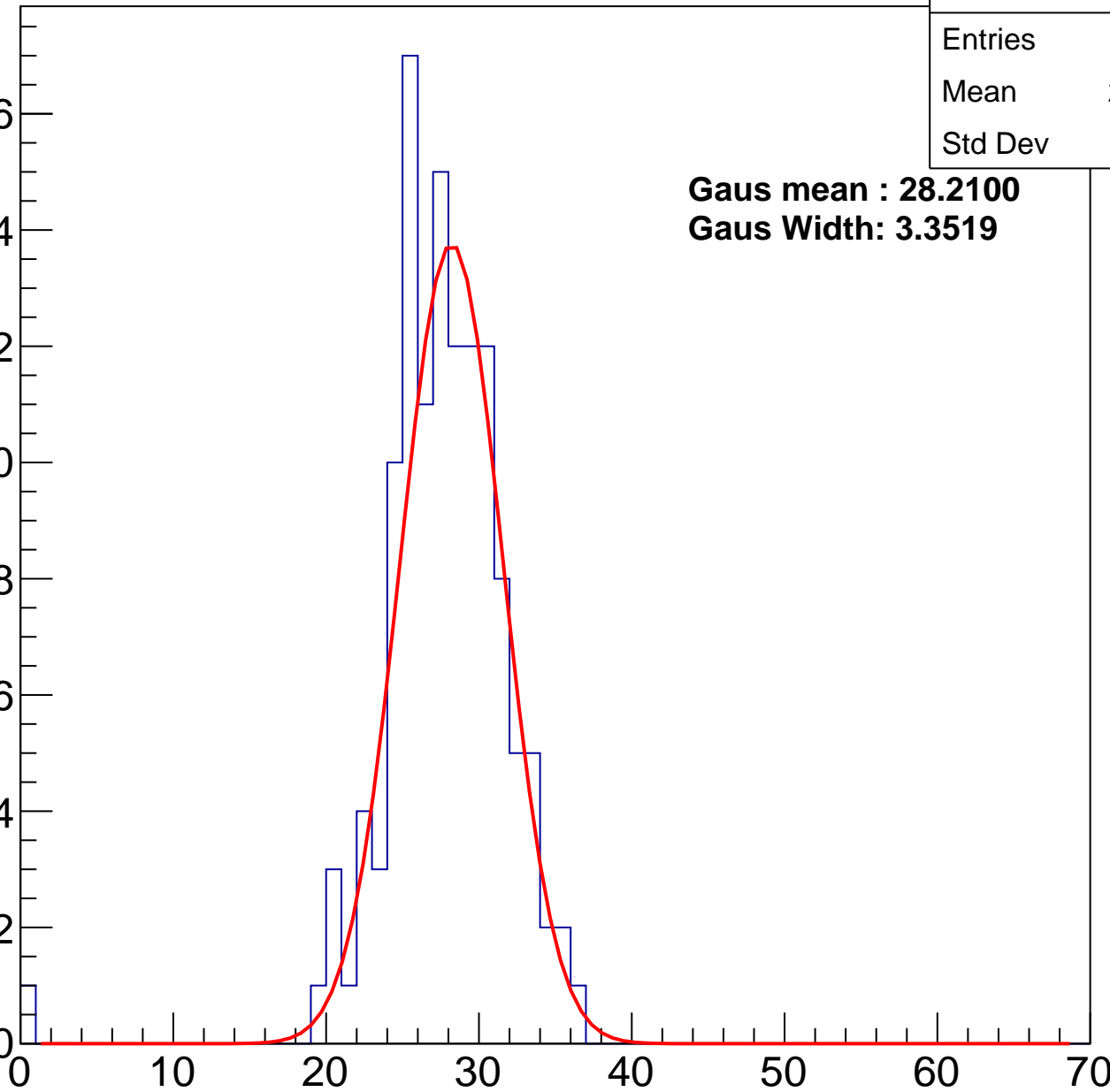
Gaus mean : 28.2100

Gaus Width: 3.3519

Entry

16
14
12
10
8
6
4
2
0

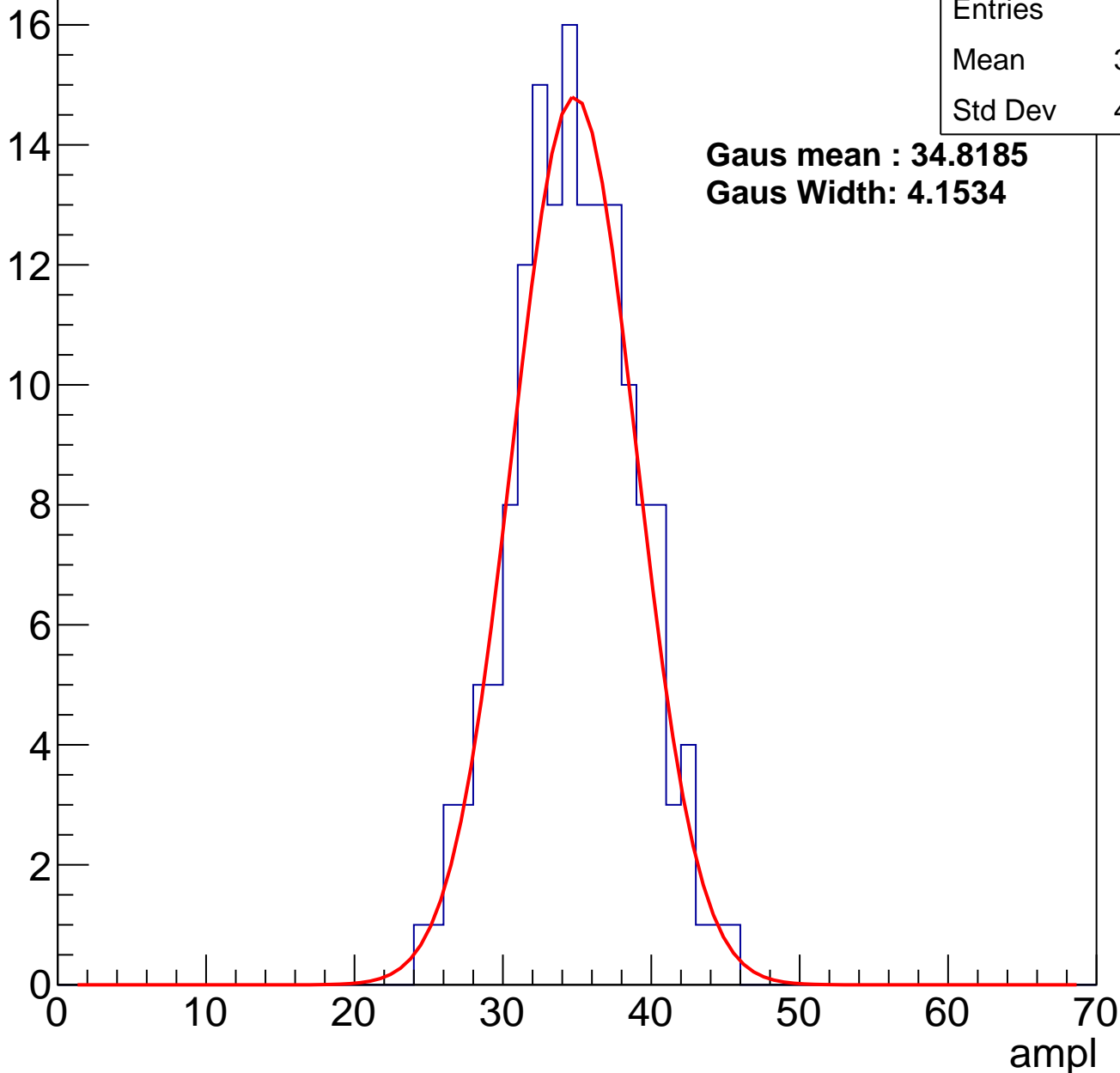
ampl



B0L101S, U20-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch103, adc2

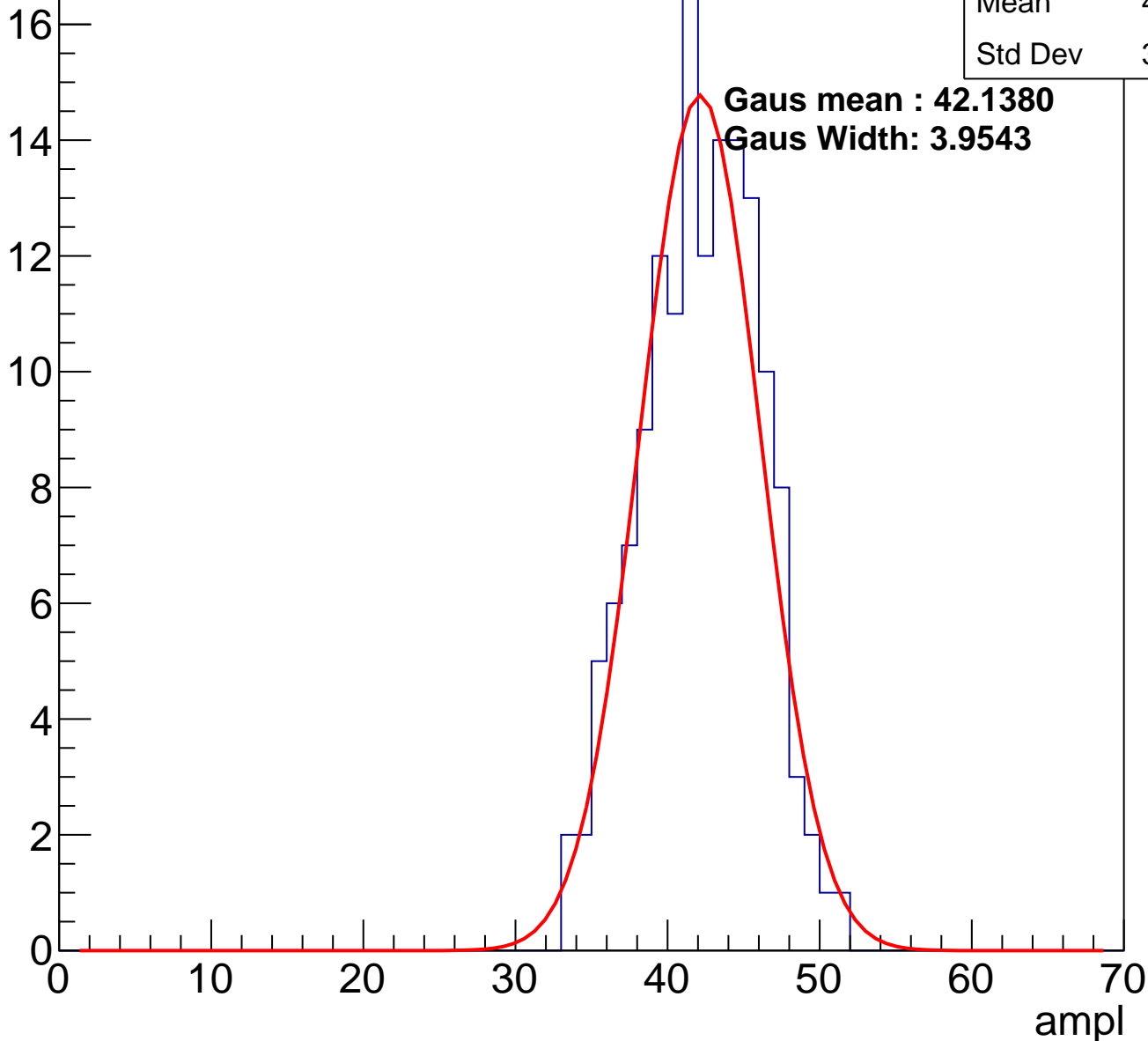
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	149
Mean	41.72
Std Dev	3.777

Gaus mean : 42.1380

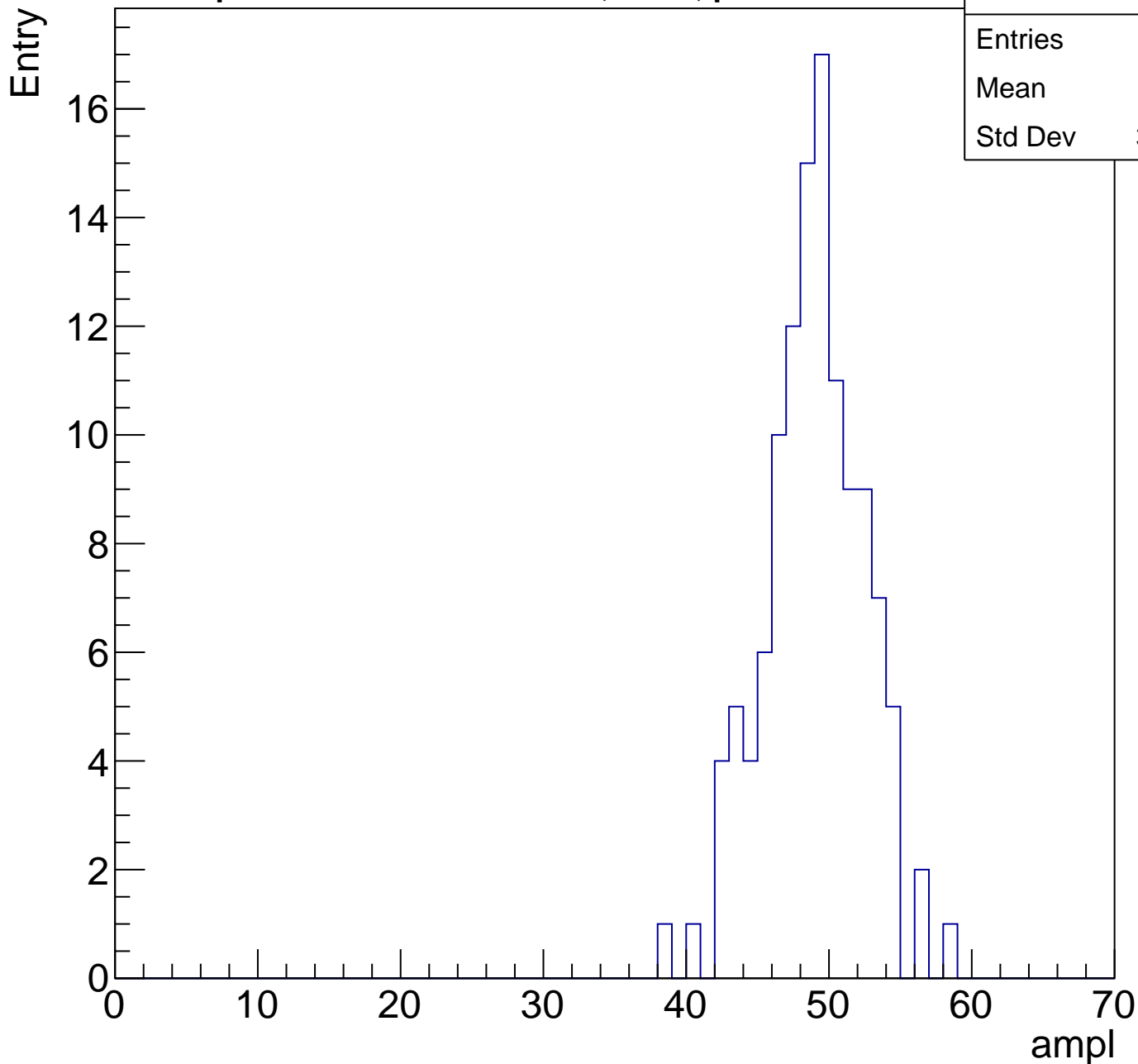
Gaus Width: 3.9543



B0L101S, U20-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

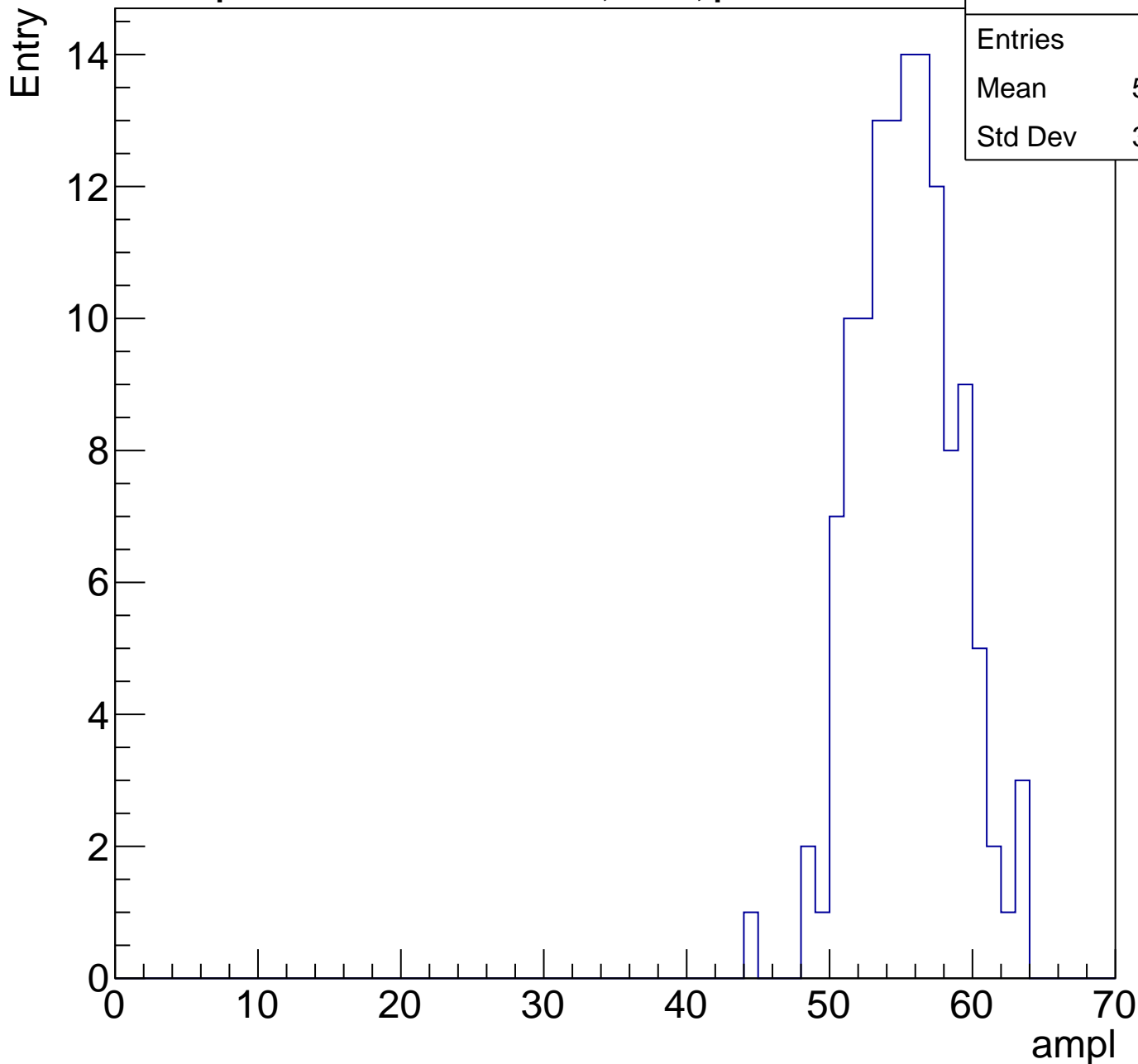
Entries	119
Mean	48.5
Std Dev	3.481



B0L101S, U20-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	54.93
Std Dev	3.418



B0L101S, U20-ch103, adc5

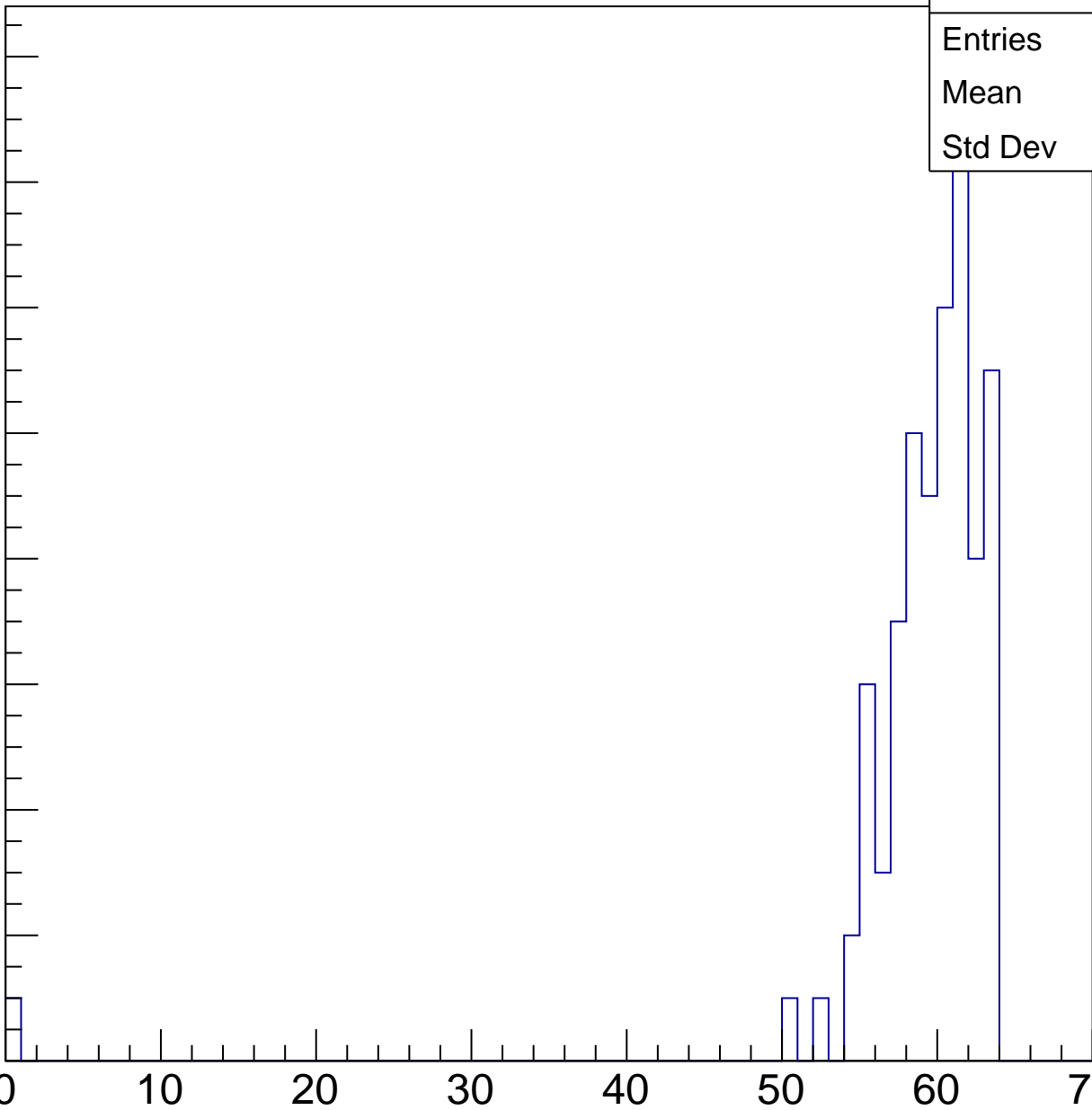
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.66
Std Dev	6.895

ampl



B0L101S, U20-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

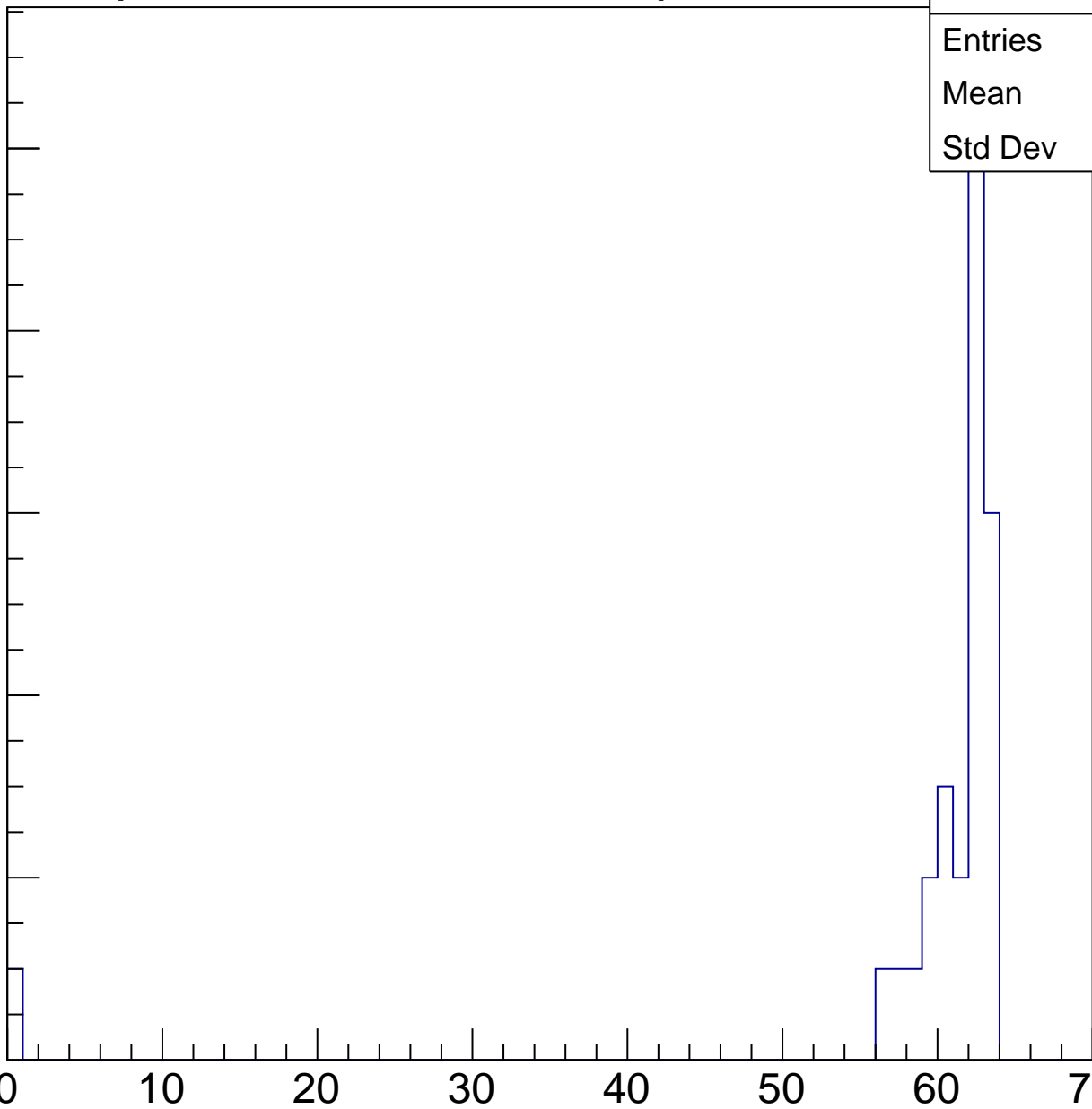
4

2

0

Entries	28
Mean	58.96
Std Dev	11.5

ampl



B0L101S, U20-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch104, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	27.17
Std Dev	5.519

Gaus mean : 28.1927

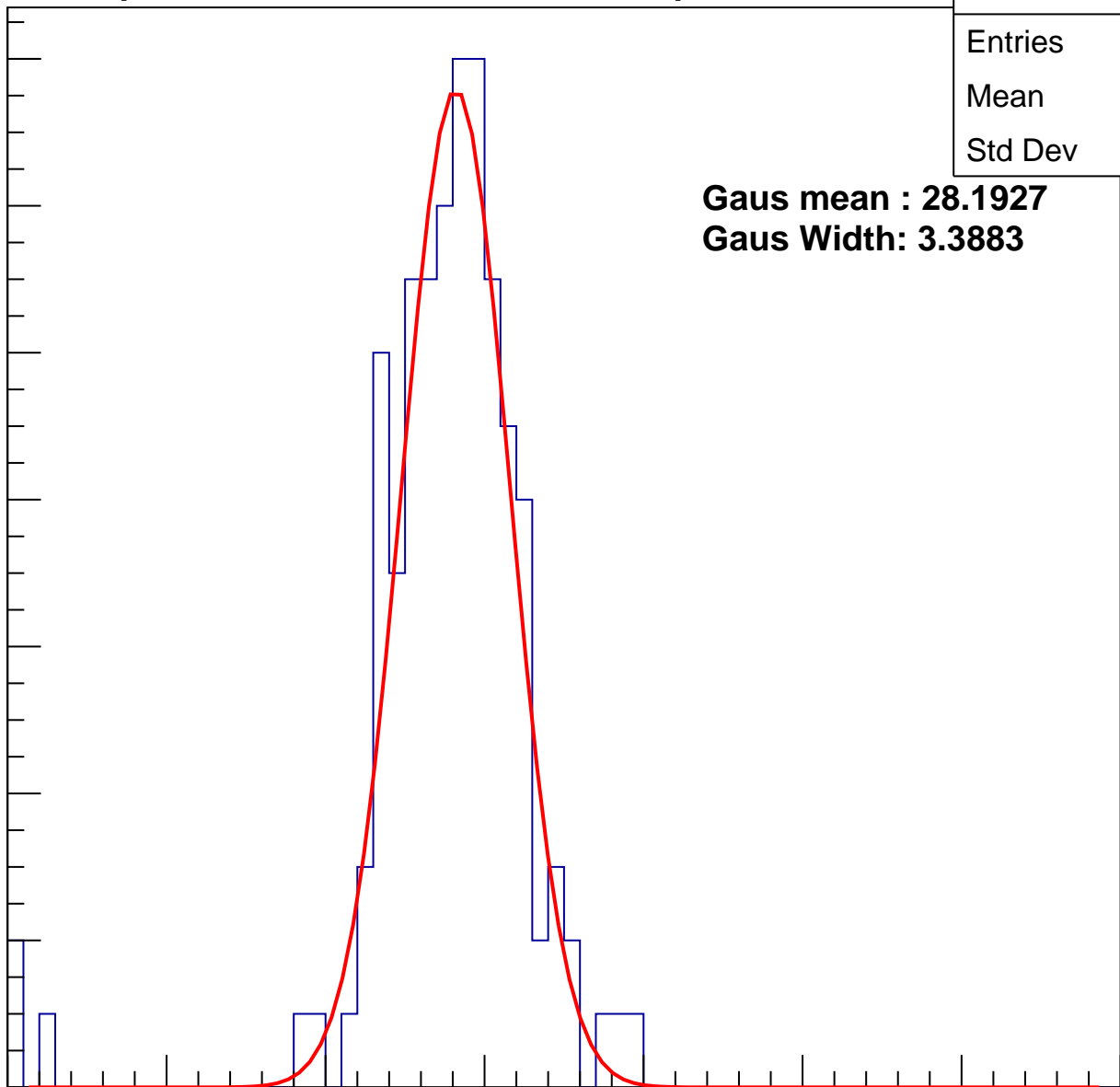
Gaus Width: 3.3883

Entry

14
12
10
8
6
4
2
0

ampl

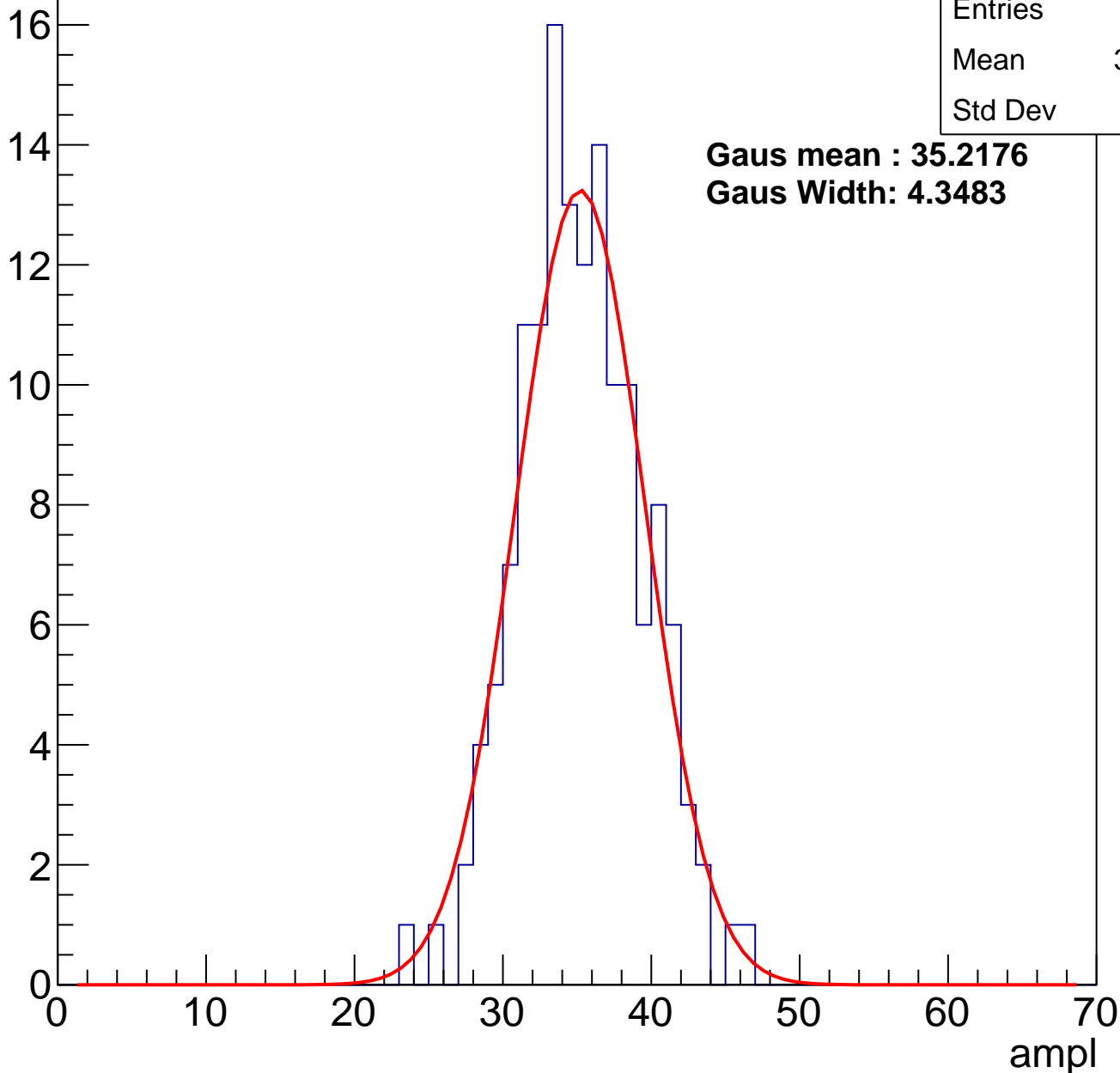
0 10 20 30 40 50 60 70



B0L101S, U20-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch104, adc2

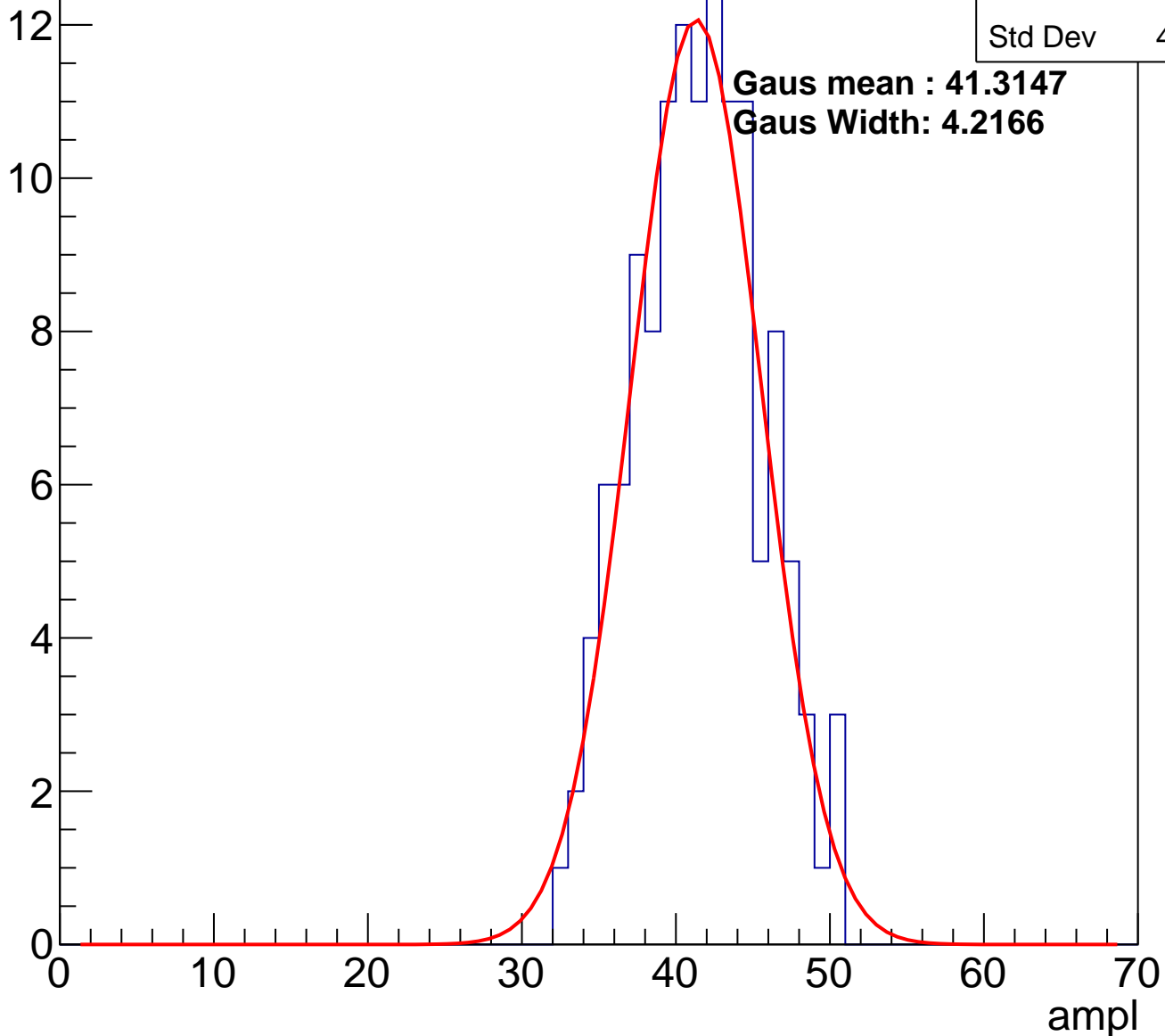
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	41.01
Std Dev	4.032

Gaus mean : 41.3147

Gaus Width: 4.2166

Entry



B0L101S, U20-ch104, adc3

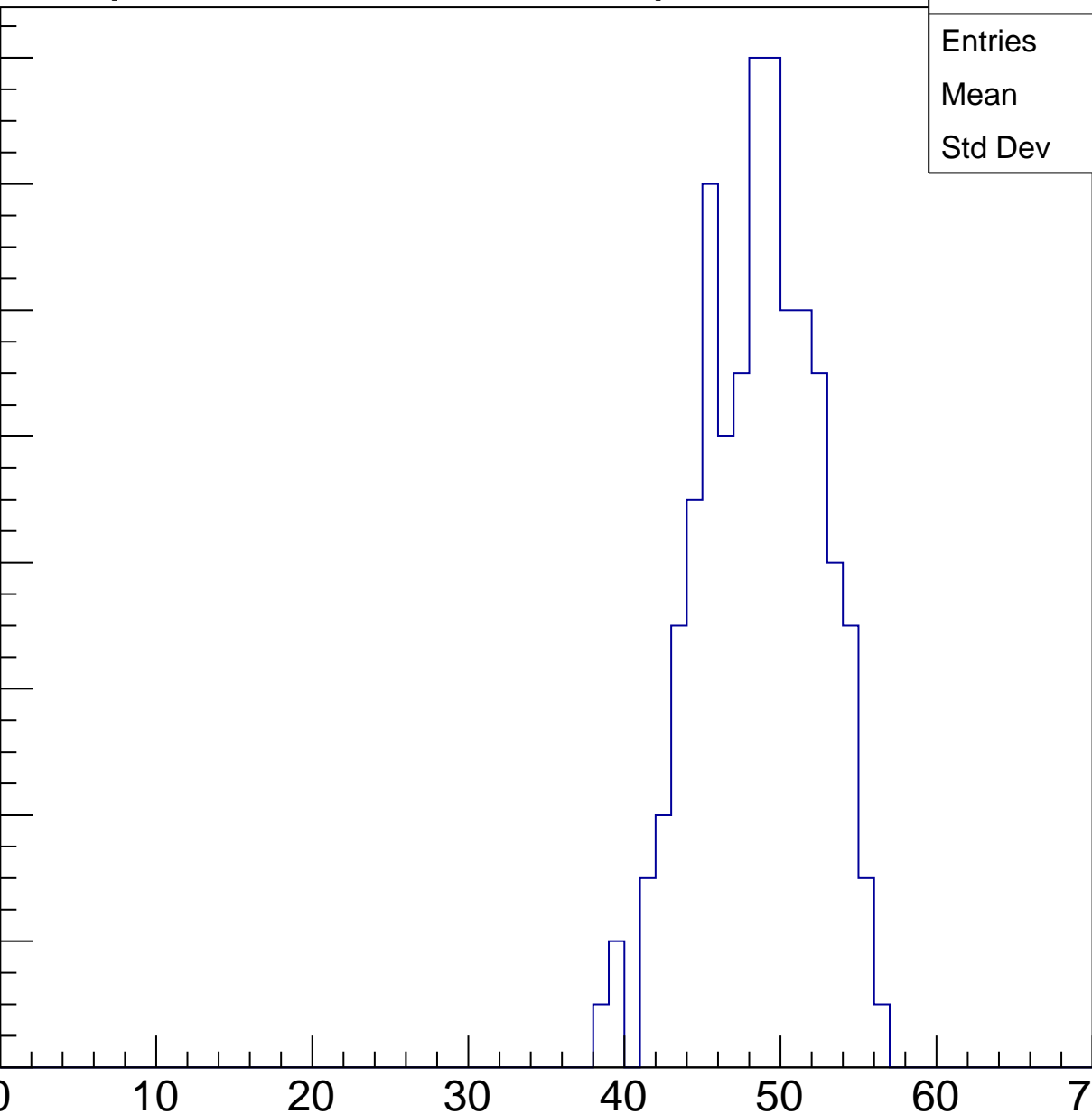
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	147
Mean	48.1
Std Dev	3.742

ampl



B0L101S, U20-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

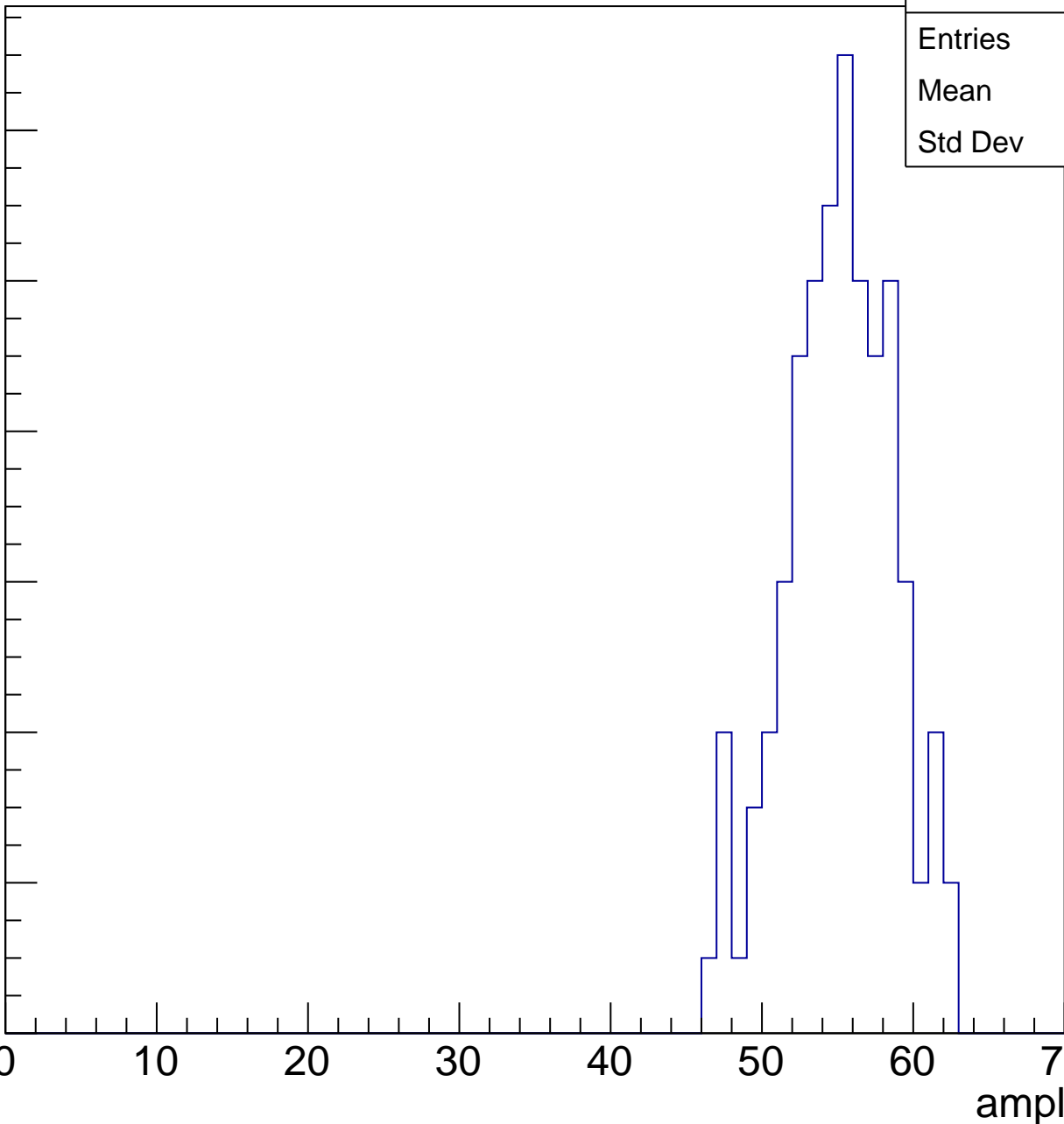
50

60

70

ampl

Entries	105
Mean	54.64
Std Dev	3.562



B0L101S, U20-ch104, adc5

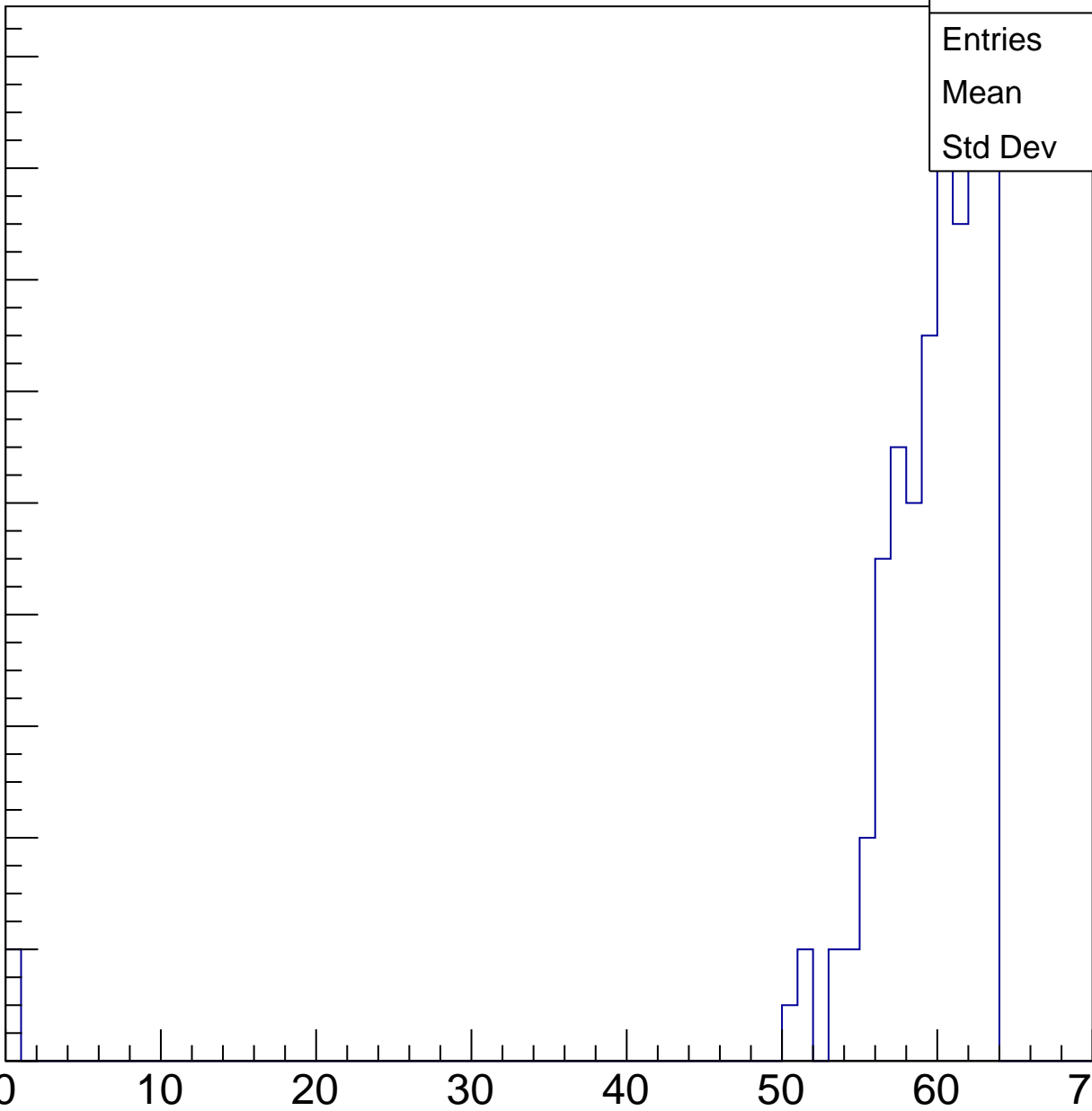
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	121
Mean	58.39
Std Dev	8.108

ampl



B0L101S, U20-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.25
Std Dev	1.392

ampl

B0L101S, U20-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch105, adc0

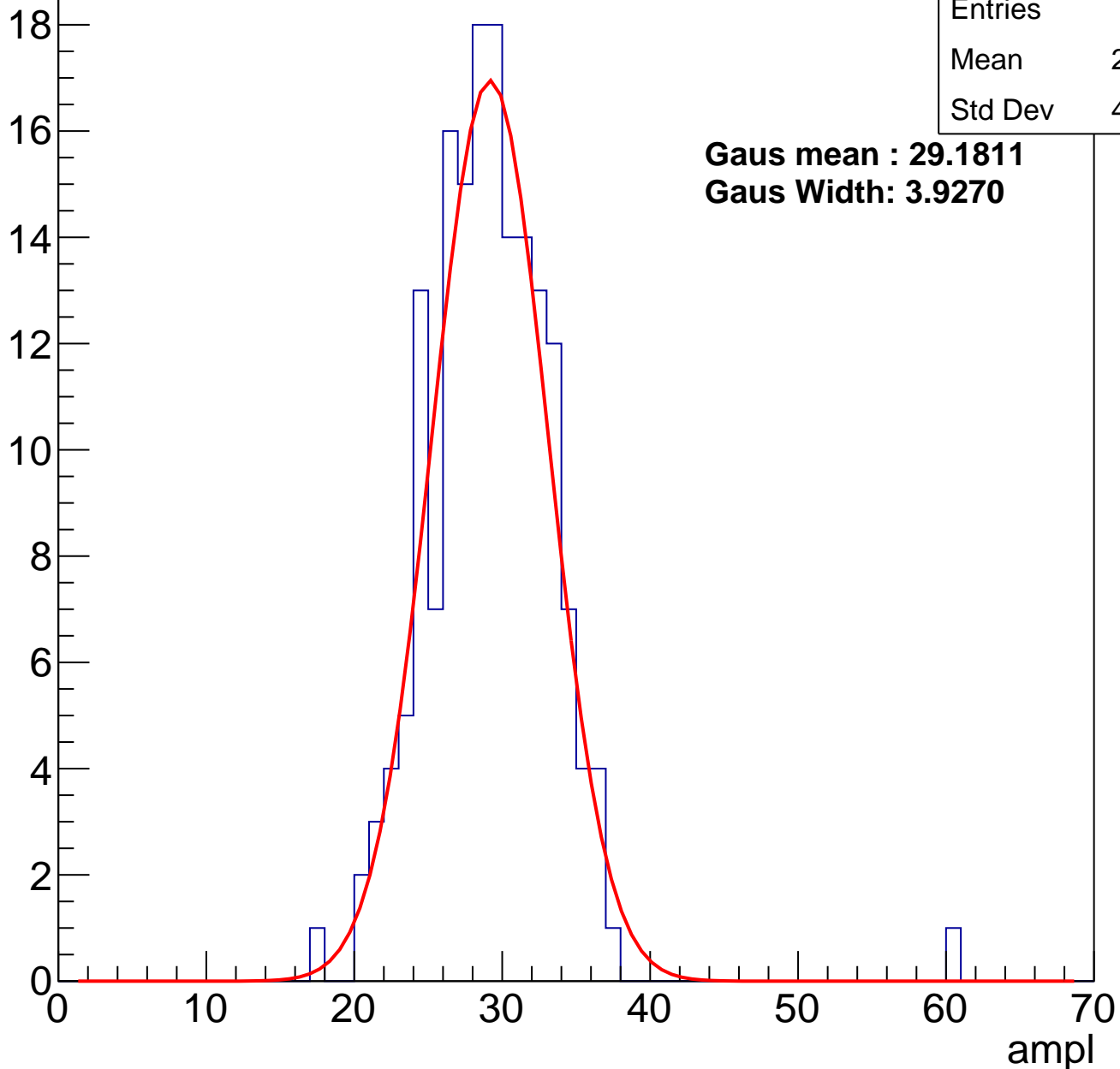
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	28.73
Std Dev	4.472

Gaus mean : 29.1811

Gaus Width: 3.9270

Entry



B0L101S, U20-ch105, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

126

Mean

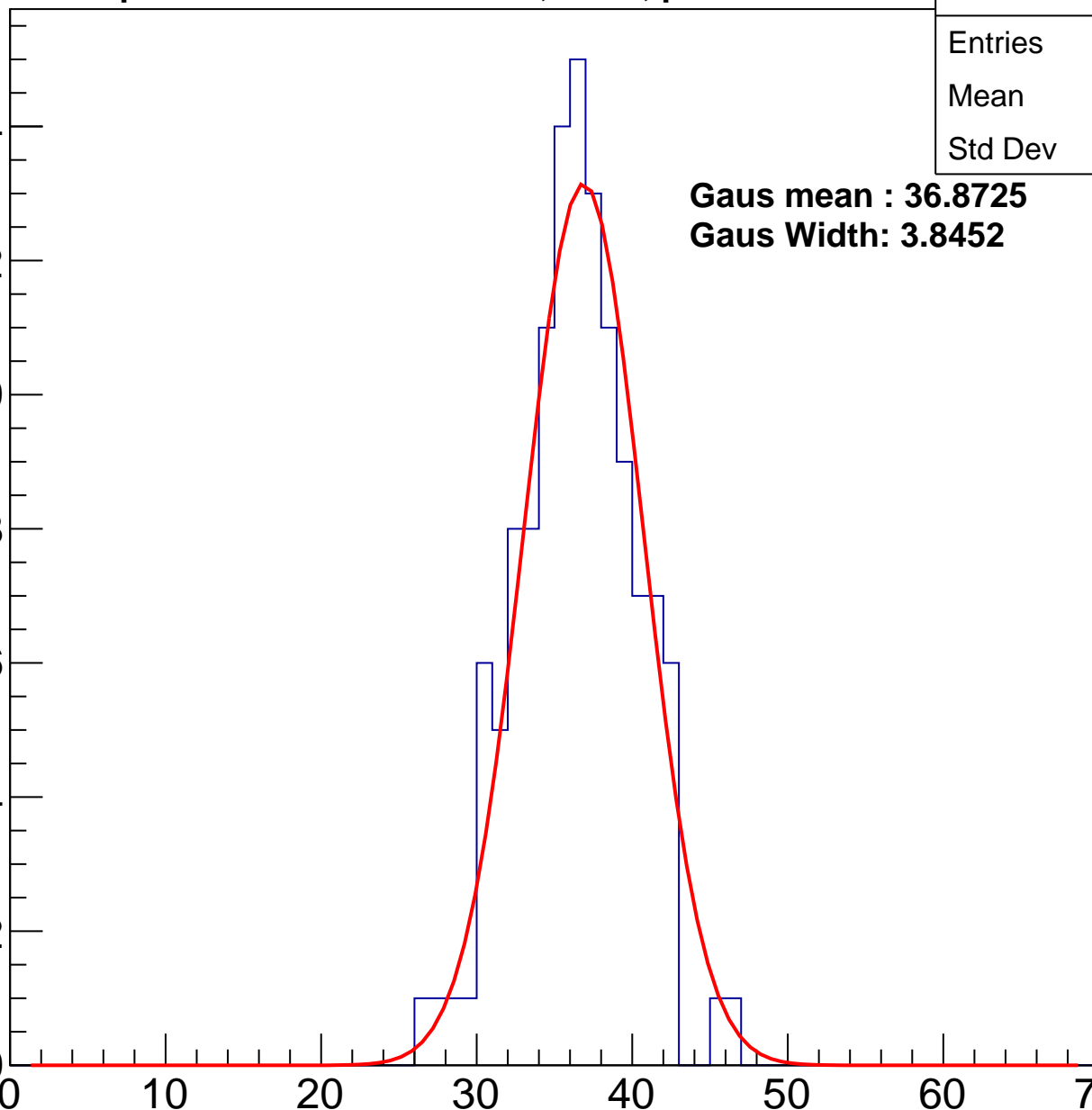
35.94

Std Dev

3.689

Gaus mean : 36.8725

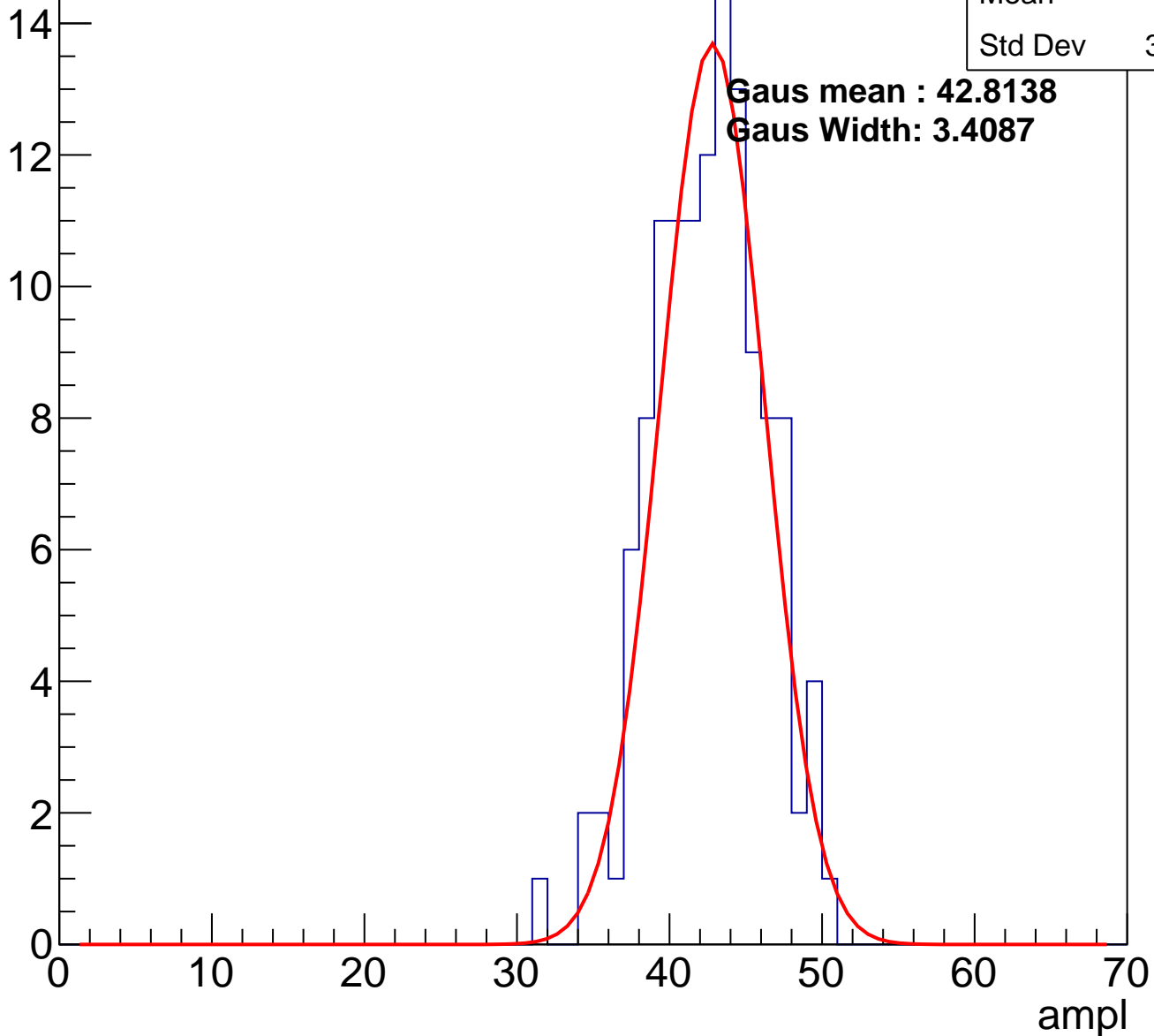
Gaus Width: 3.8452



B0L101S, U20-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

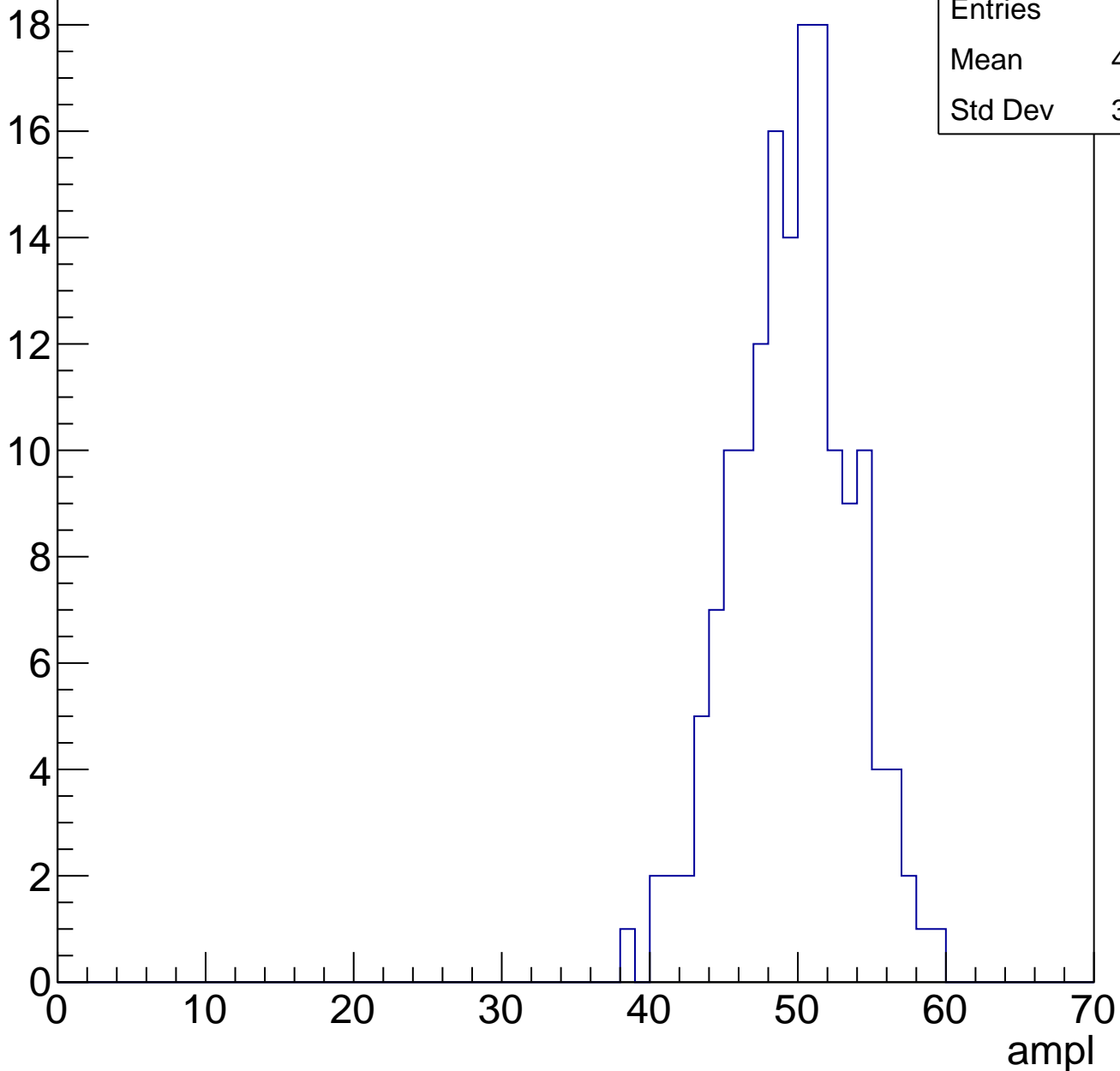
Entry



B0L101S, U20-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	55.79
Std Dev	3.386

Entry

12

10

8

6

4

2

0

0

10

20

30

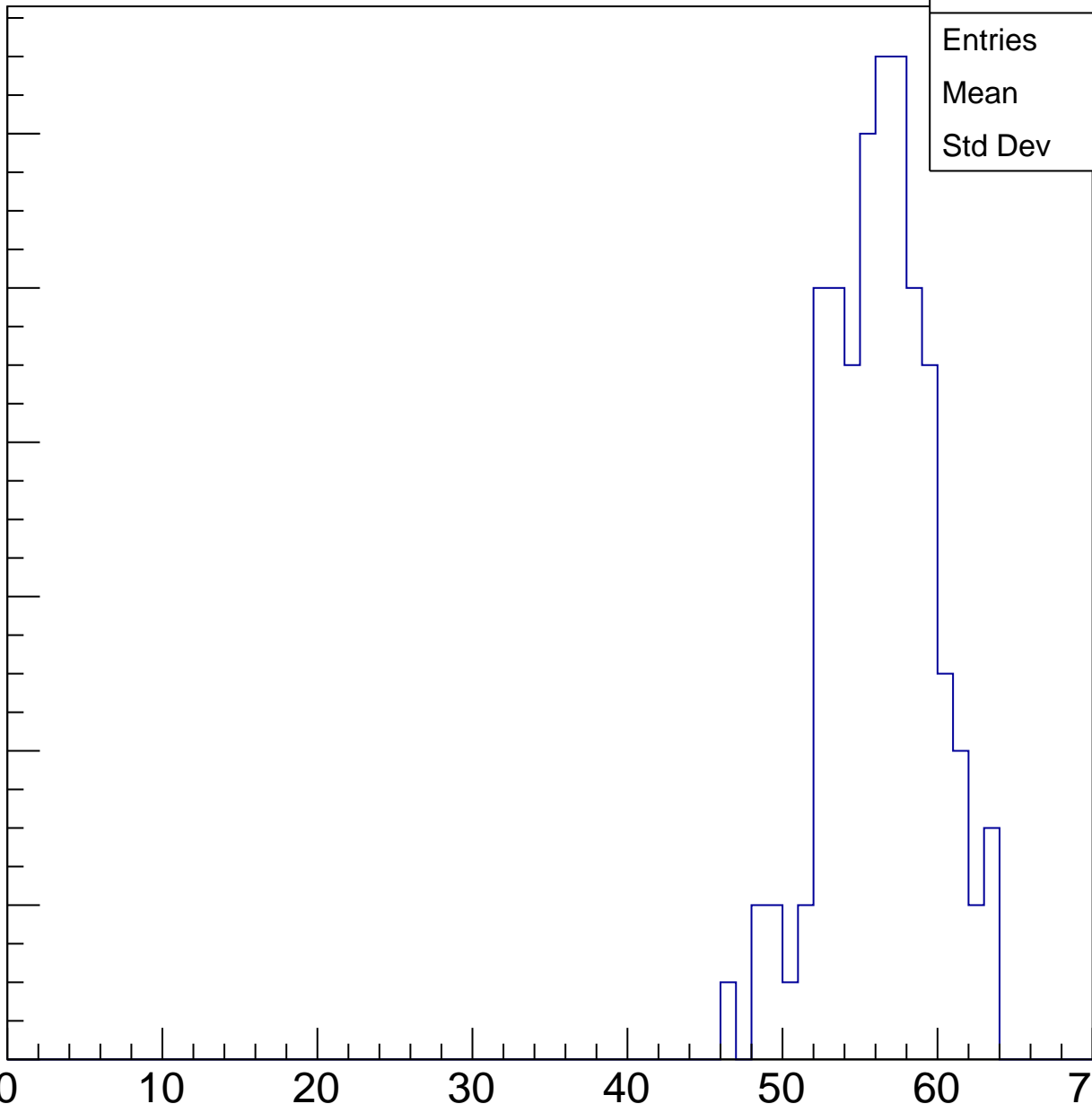
40

50

60

70

ampl



B0L101S, U20-ch105, adc5

calib_packv5_042523_0143.root, FC#1, port C1

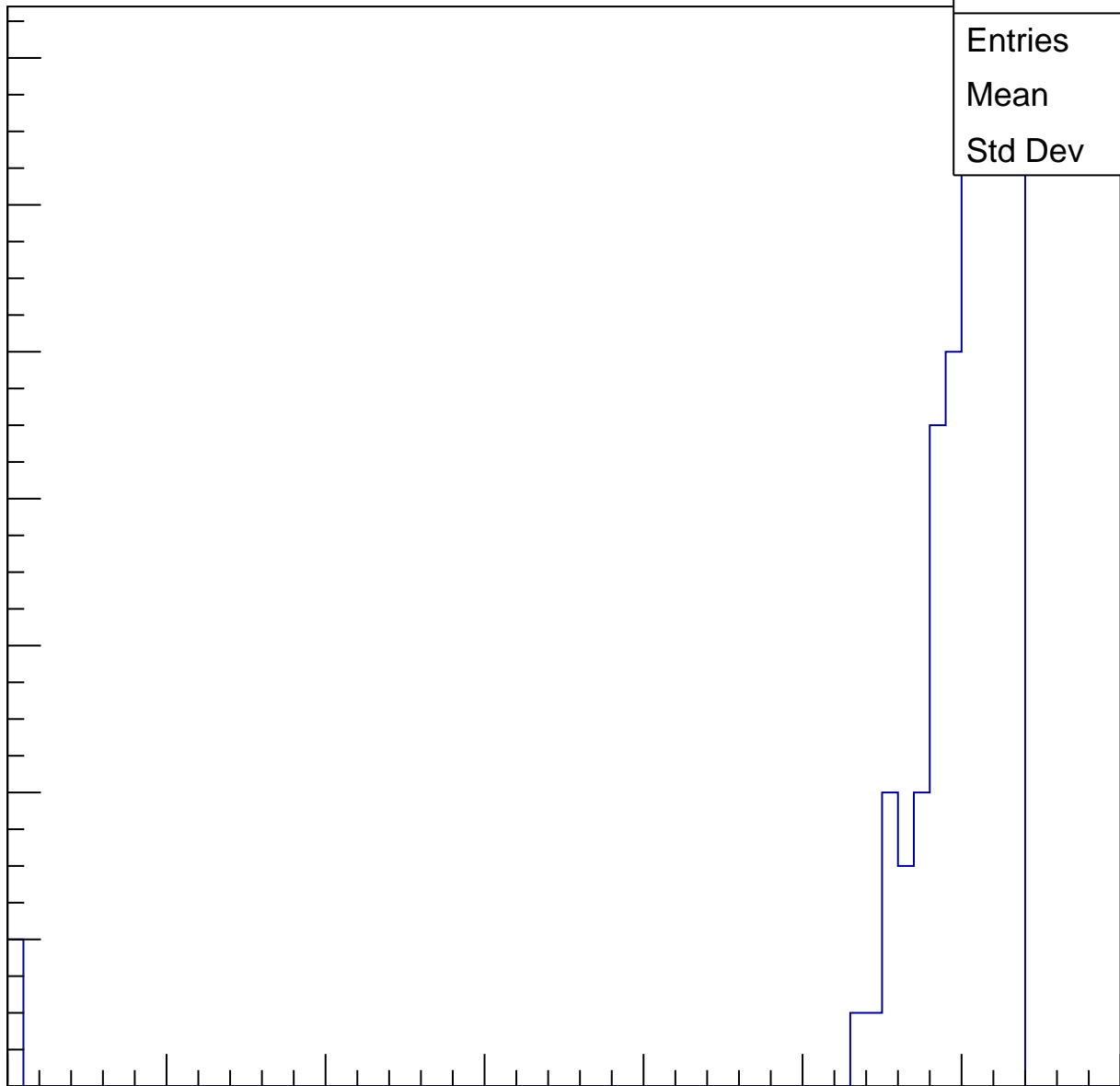
Entries	88
Mean	58.57
Std Dev	9.243

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

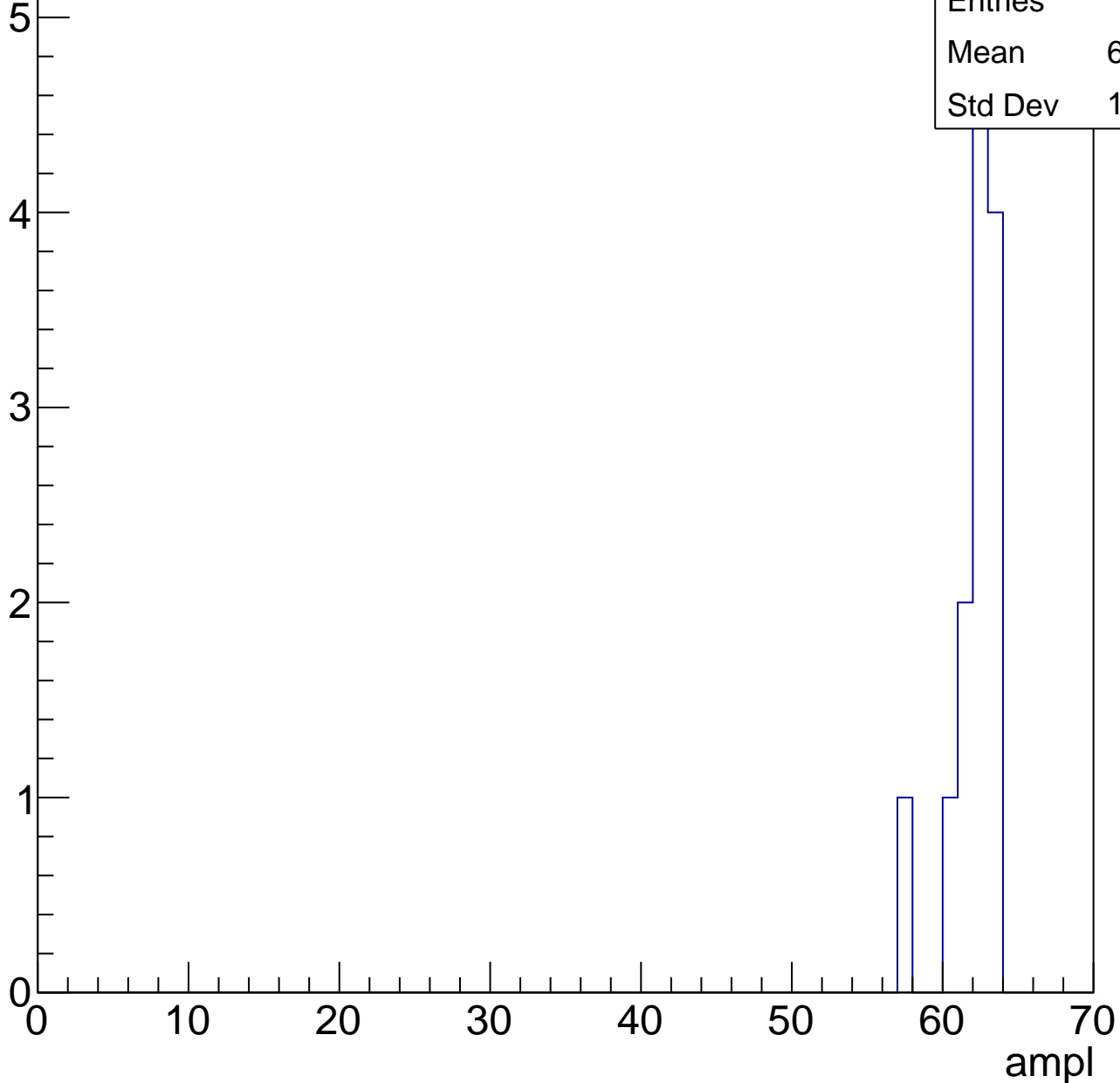


B0L101S, U20-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.62
Std Dev	1.595



B0L101S, U20-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	26.92
Std Dev	5.799

Gaus mean : 27.6302

Gaus Width: 3.9593

Entry

16

14

12

10

8

6

4

2

0

0

10

20

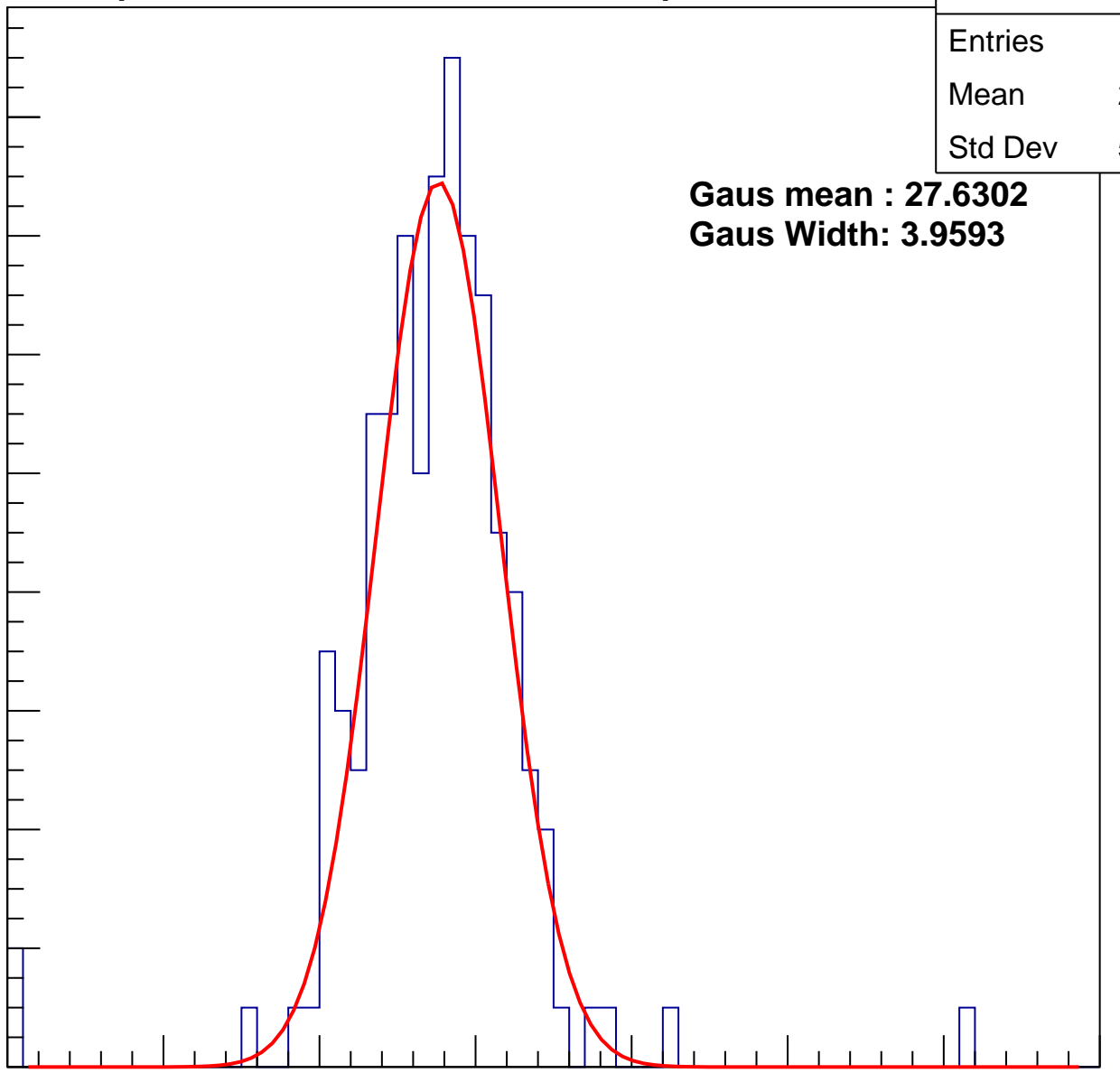
30

40

50

60

ampl



B0L101S, U20-ch106, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

127

Mean

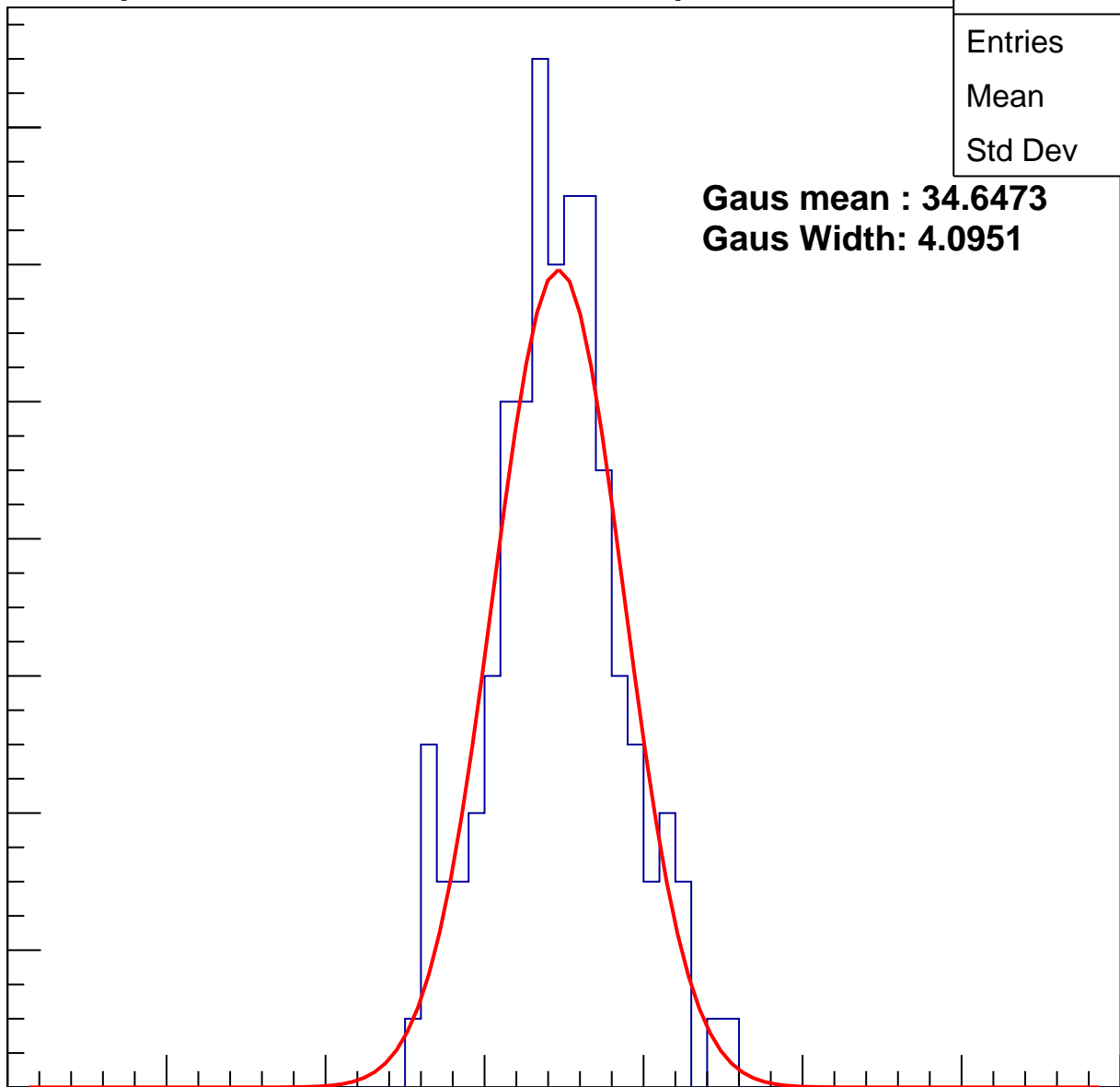
34.07

Std Dev

4.047

Gaus mean : 34.6473

Gaus Width: 4.0951



B0L101S, U20-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

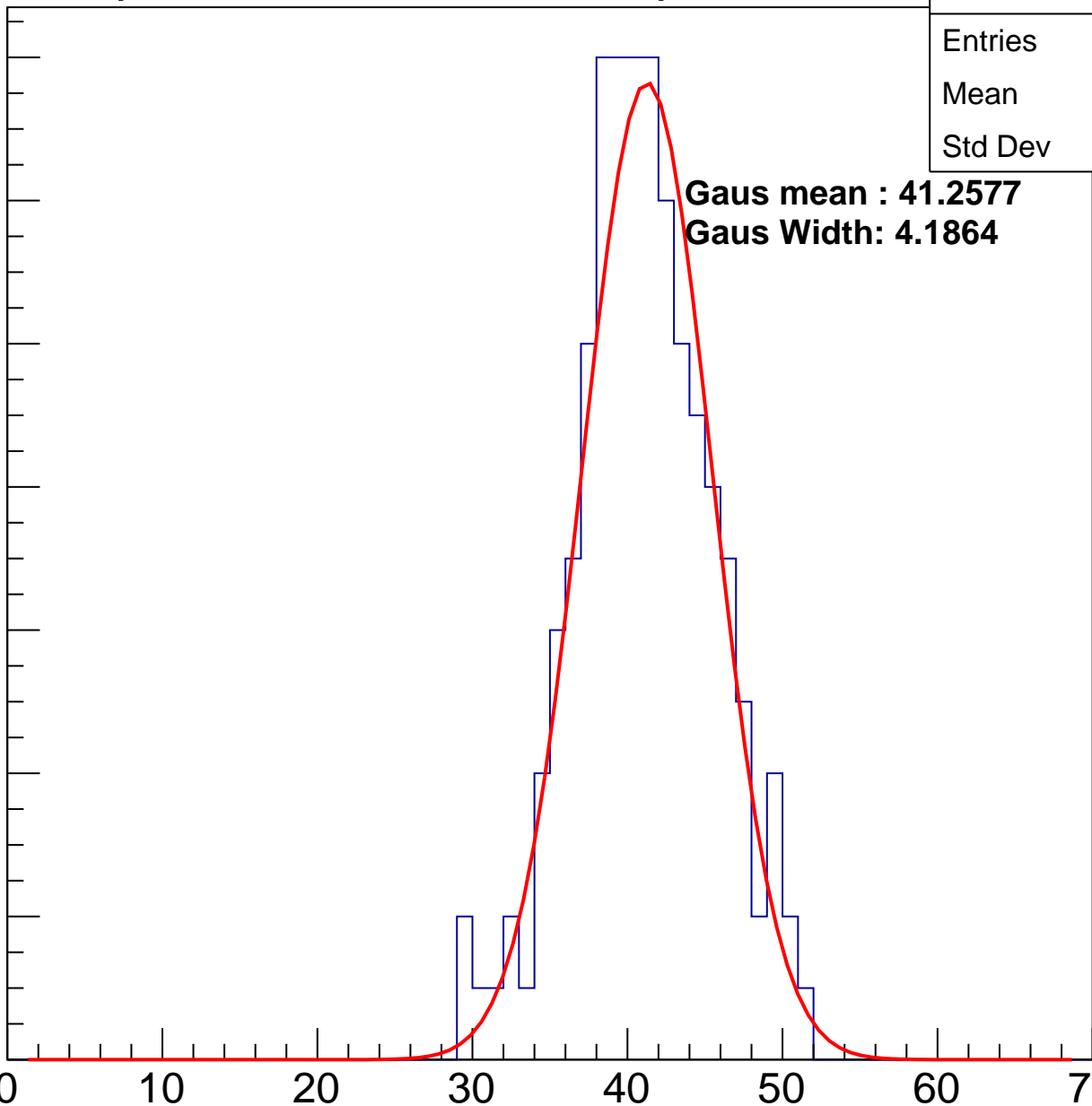
14
12
10
8
6
4
2
0

Entries	150
Mean	40.57
Std Dev	4.384

Gaus mean : 41.2577

Gaus Width: 4.1864

ampl



B0L101S, U20-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

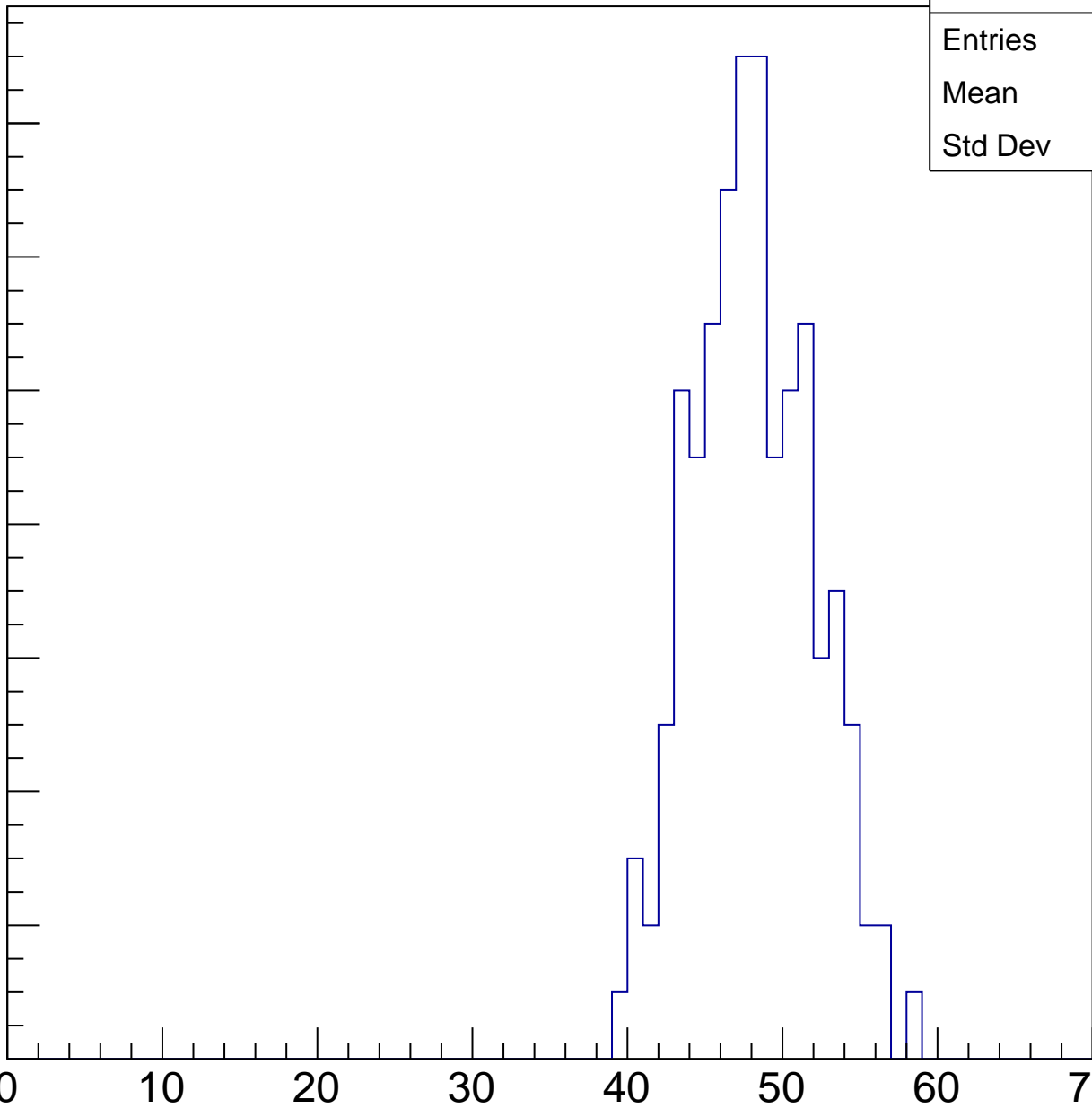
Entries	137
Mean	47.66
Std Dev	3.84

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

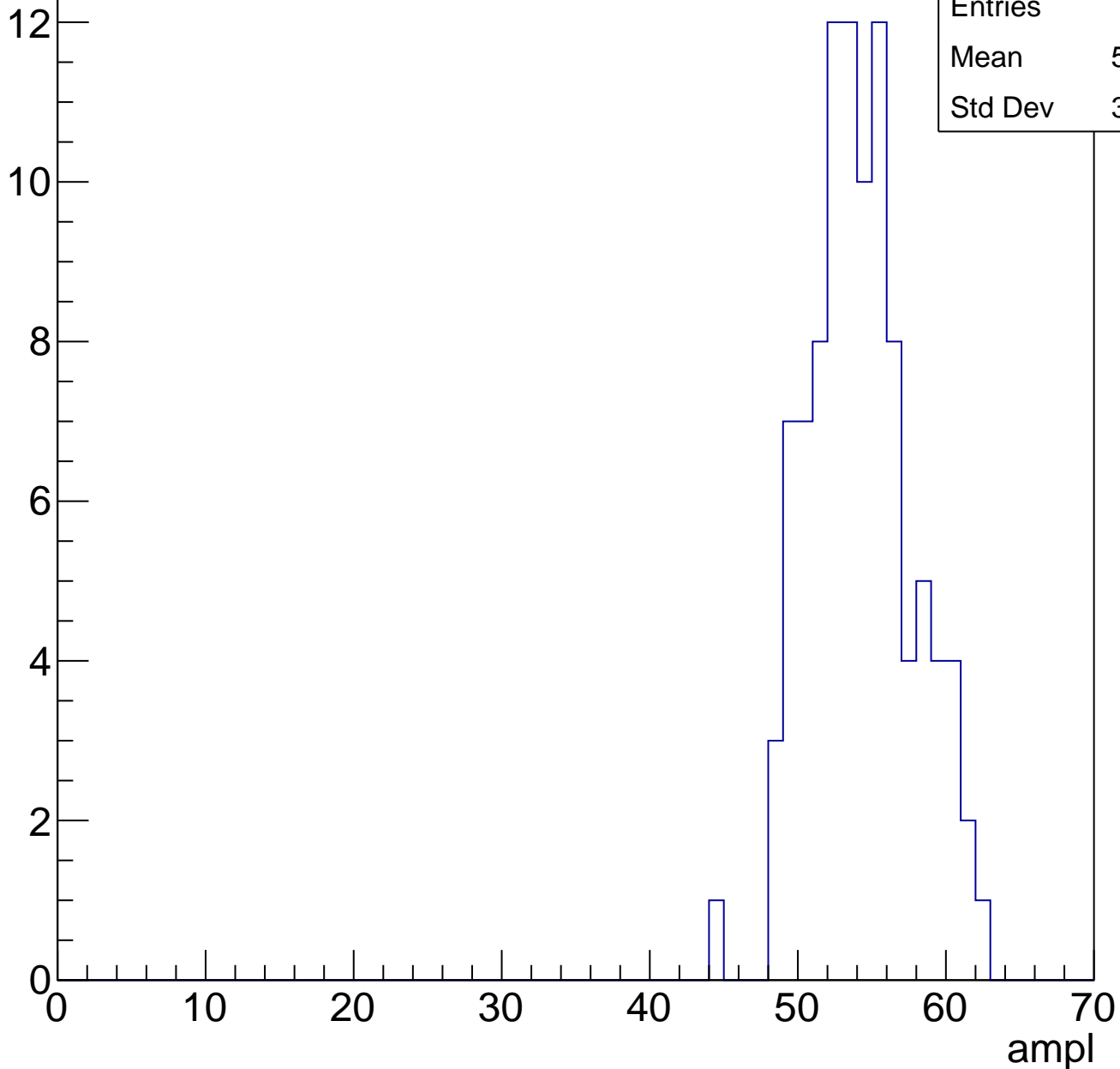
ampl



B0L101S, U20-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch106, adc5

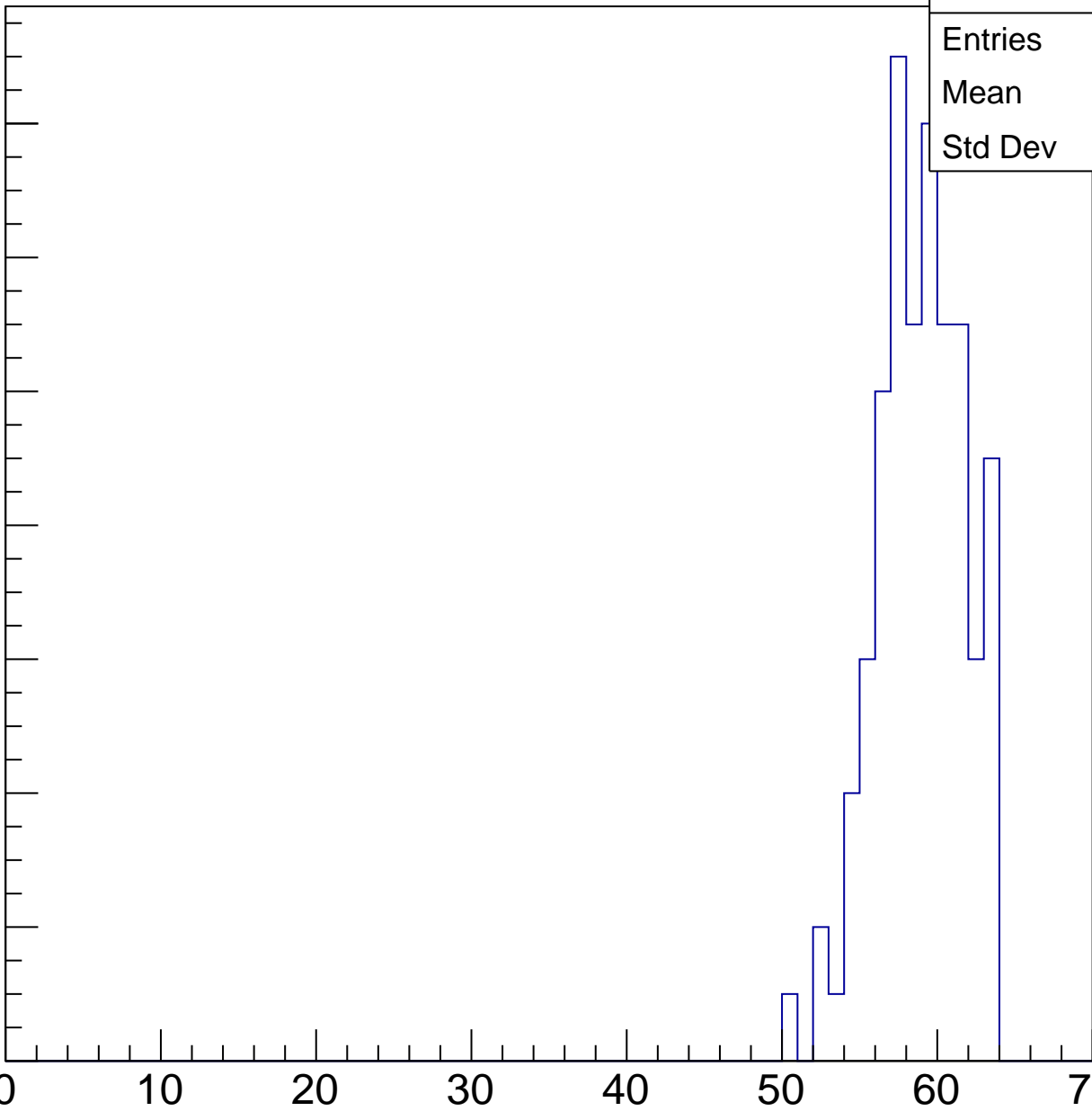
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	58.44
Std Dev	2.802

ampl



B0L101S, U20-ch106, adc6

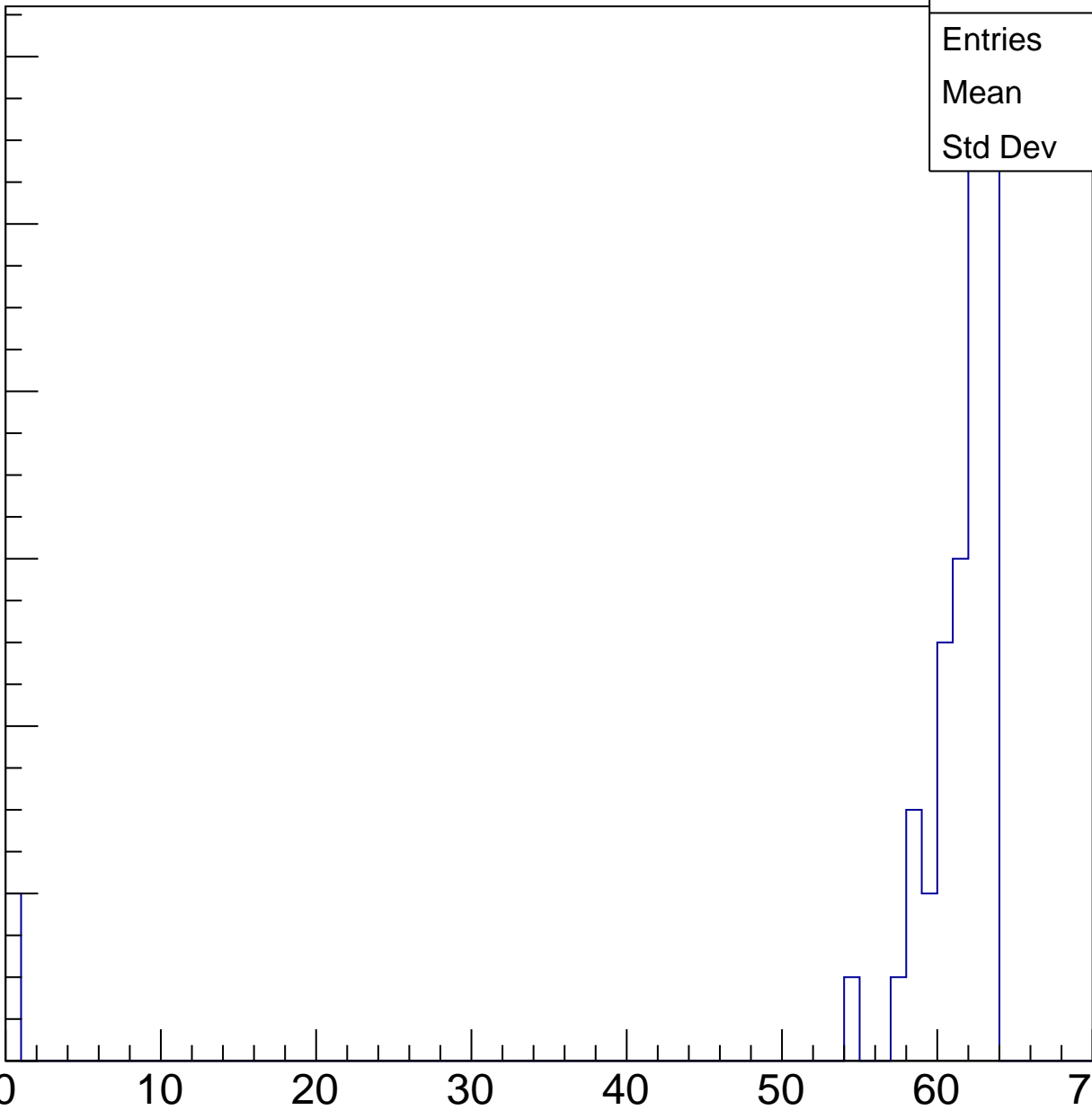
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	43
Mean	58.28
Std Dev	13.02

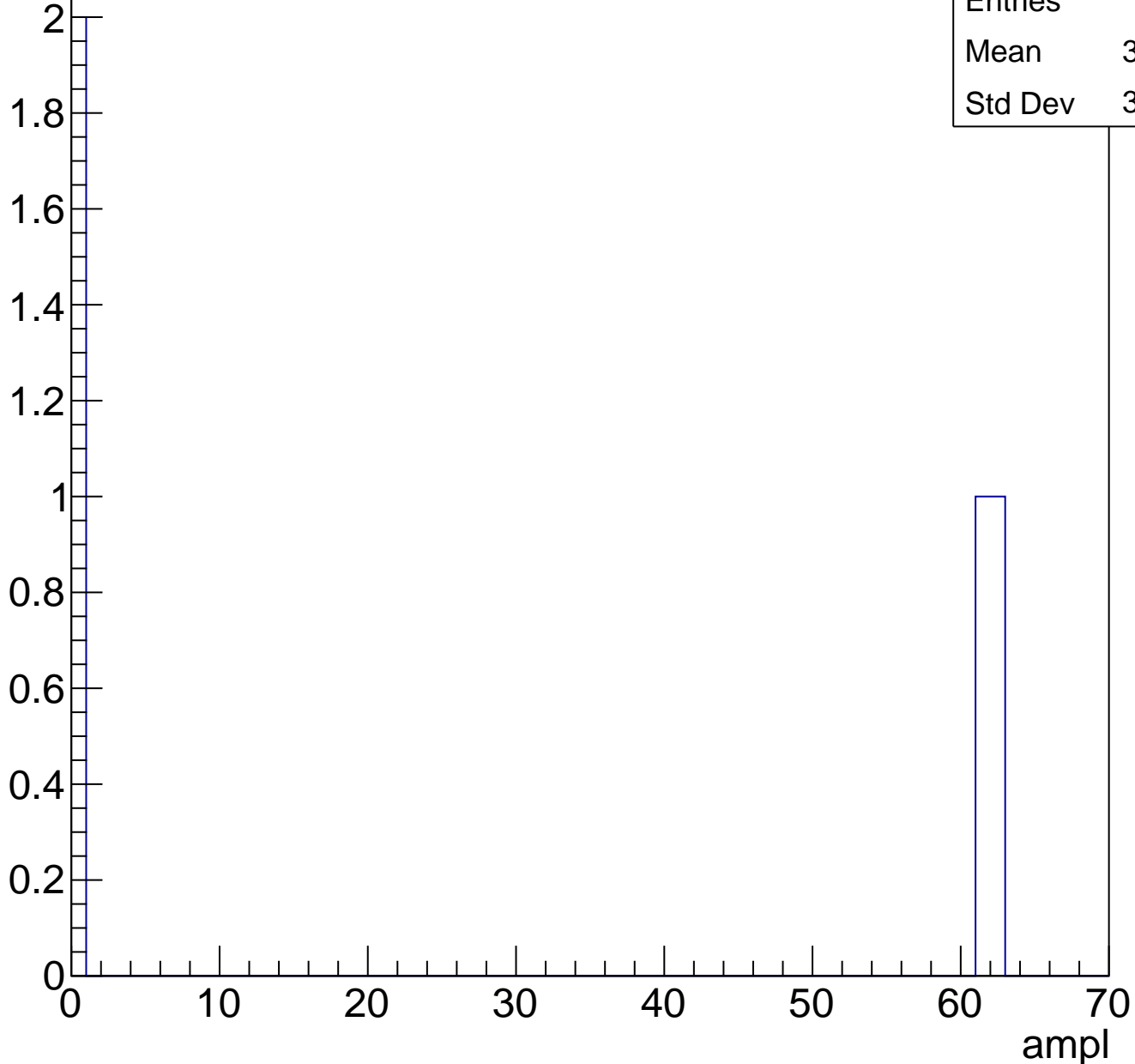
ampl



B0L101S, U20-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	30.75
Std Dev	30.75

B0L101S, U20-ch107, adc0

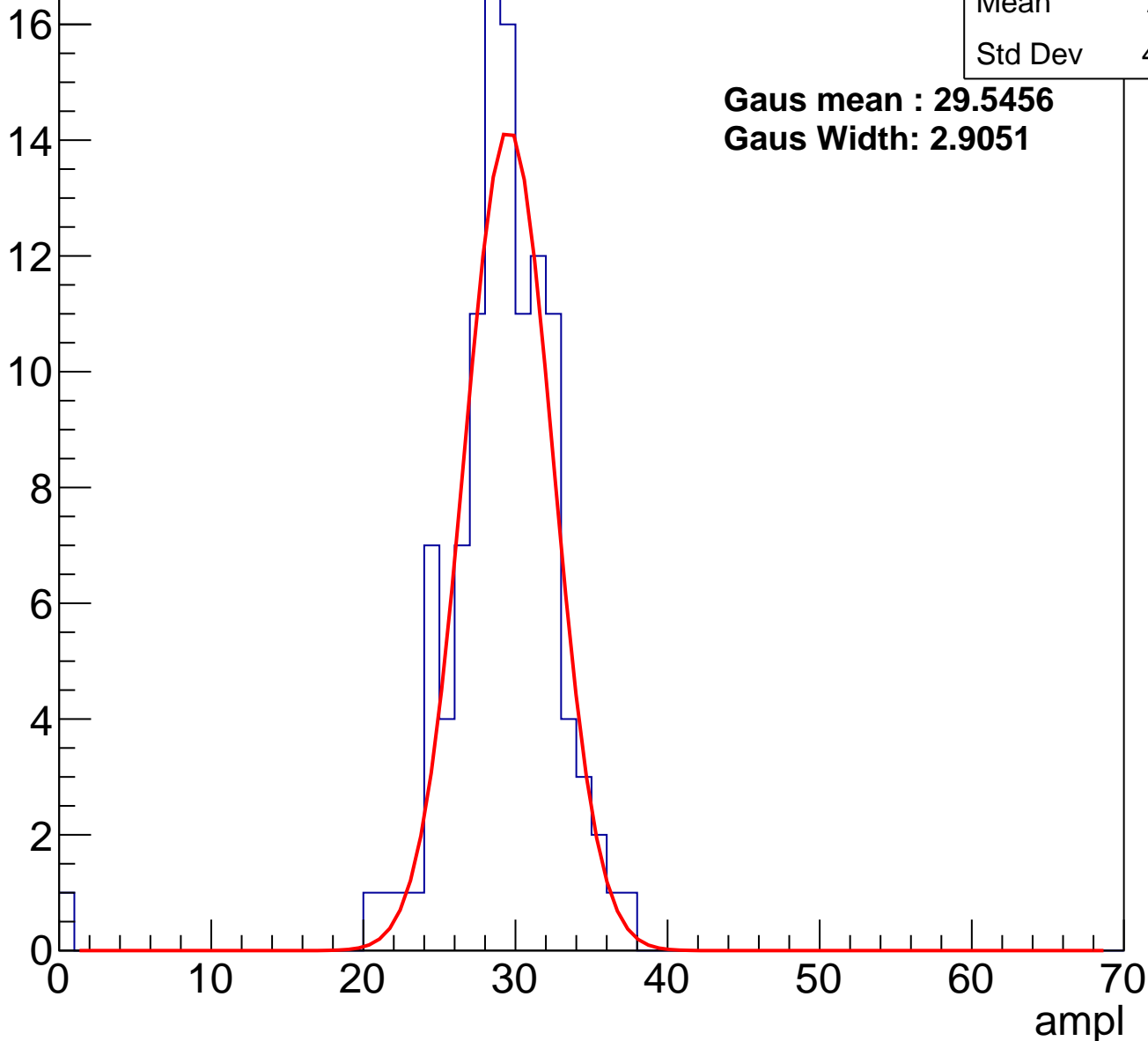
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	112
Mean	28.61
Std Dev	4.117

Gaus mean : 29.5456

Gaus Width: 2.9051



B0L101S, U20-ch107, adc1

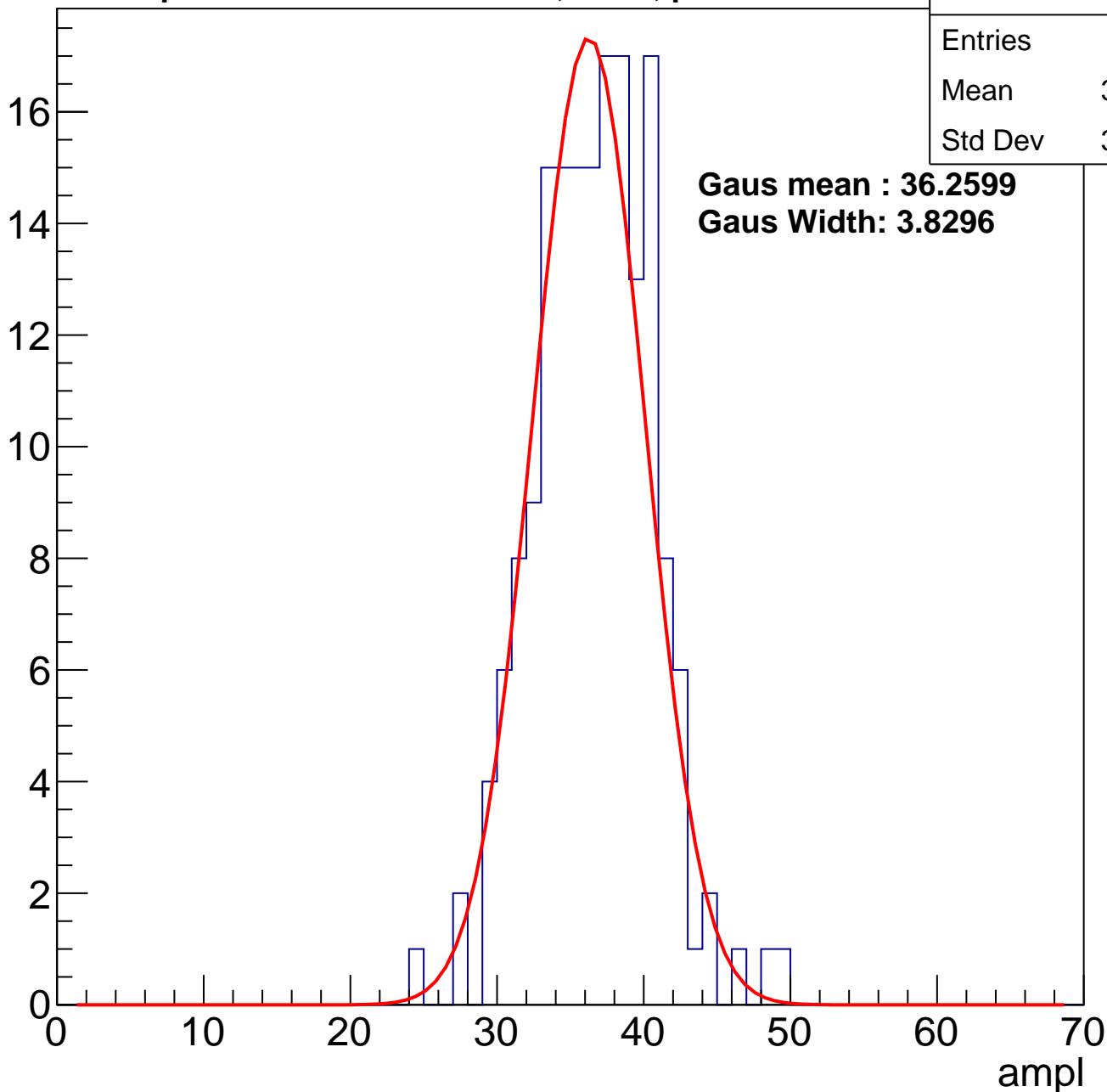
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	36.18
Std Dev	3.973

Gaus mean : 36.2599

Gaus Width: 3.8296

Entry



B0L101S, U20-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

114

Mean

43.12

Std Dev

3.756

Gaus mean : 44.0646

Gaus Width: 3.4320

0

10

20

30

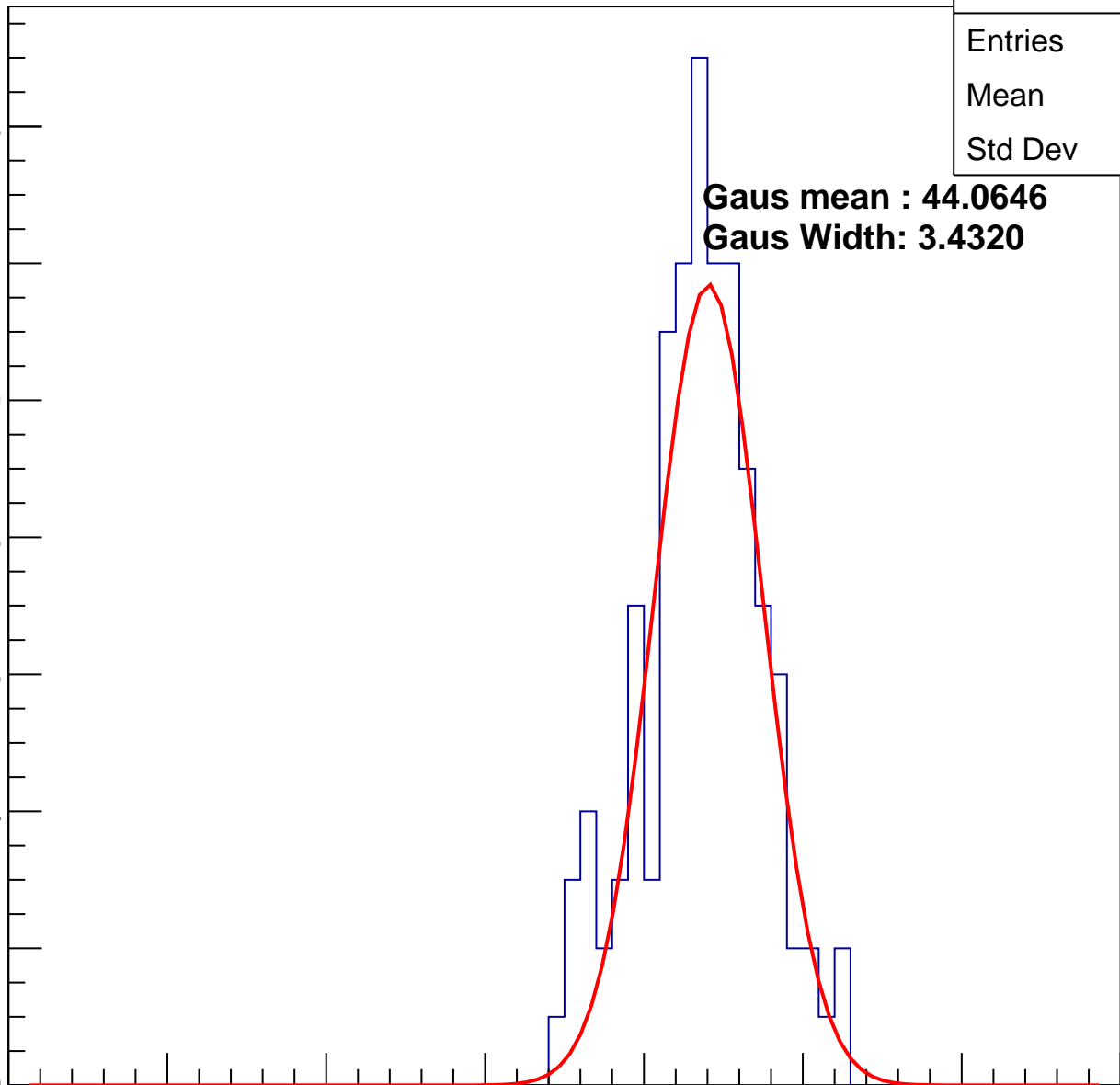
40

50

60

70

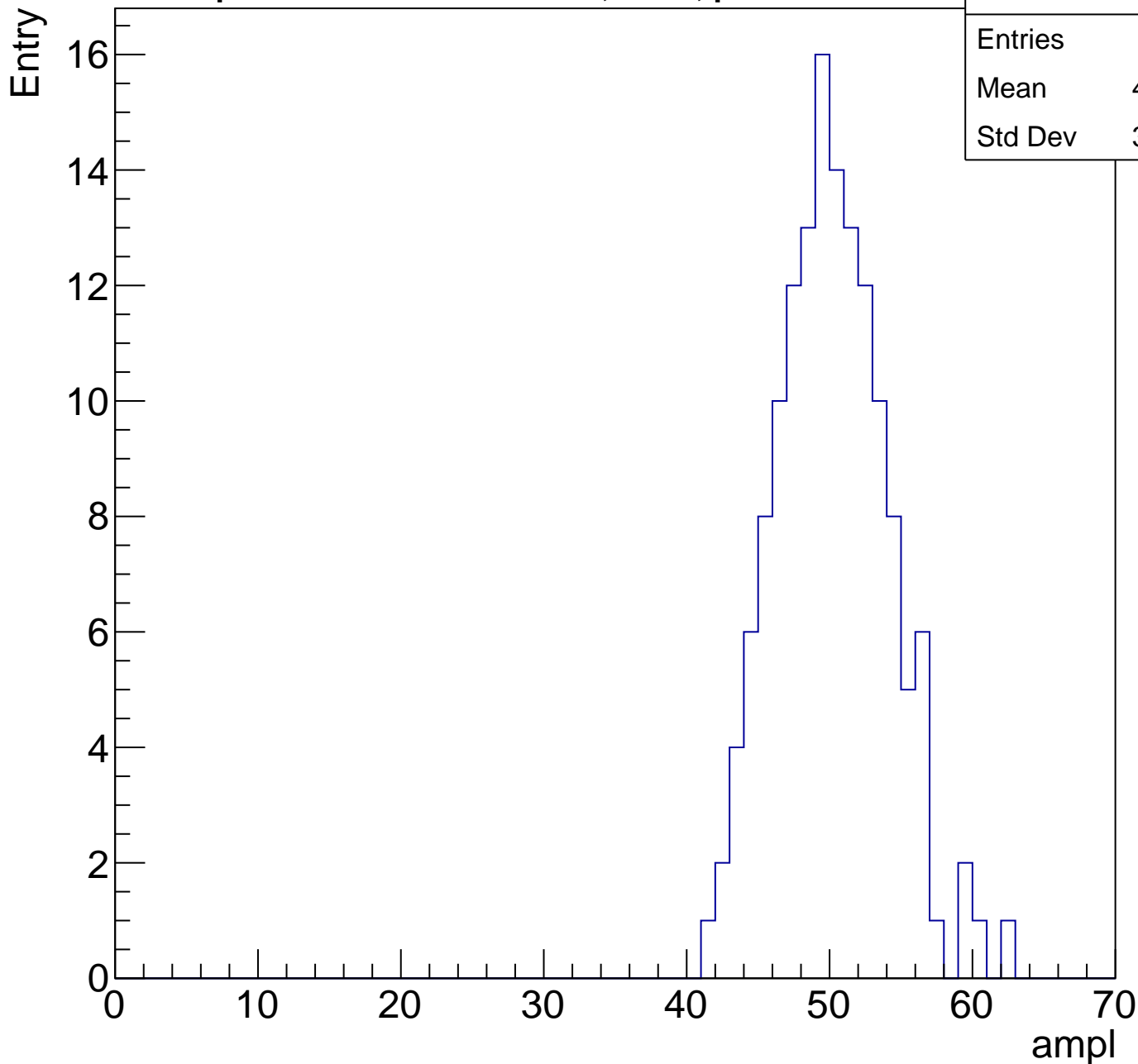
ampl



B0L101S, U20-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

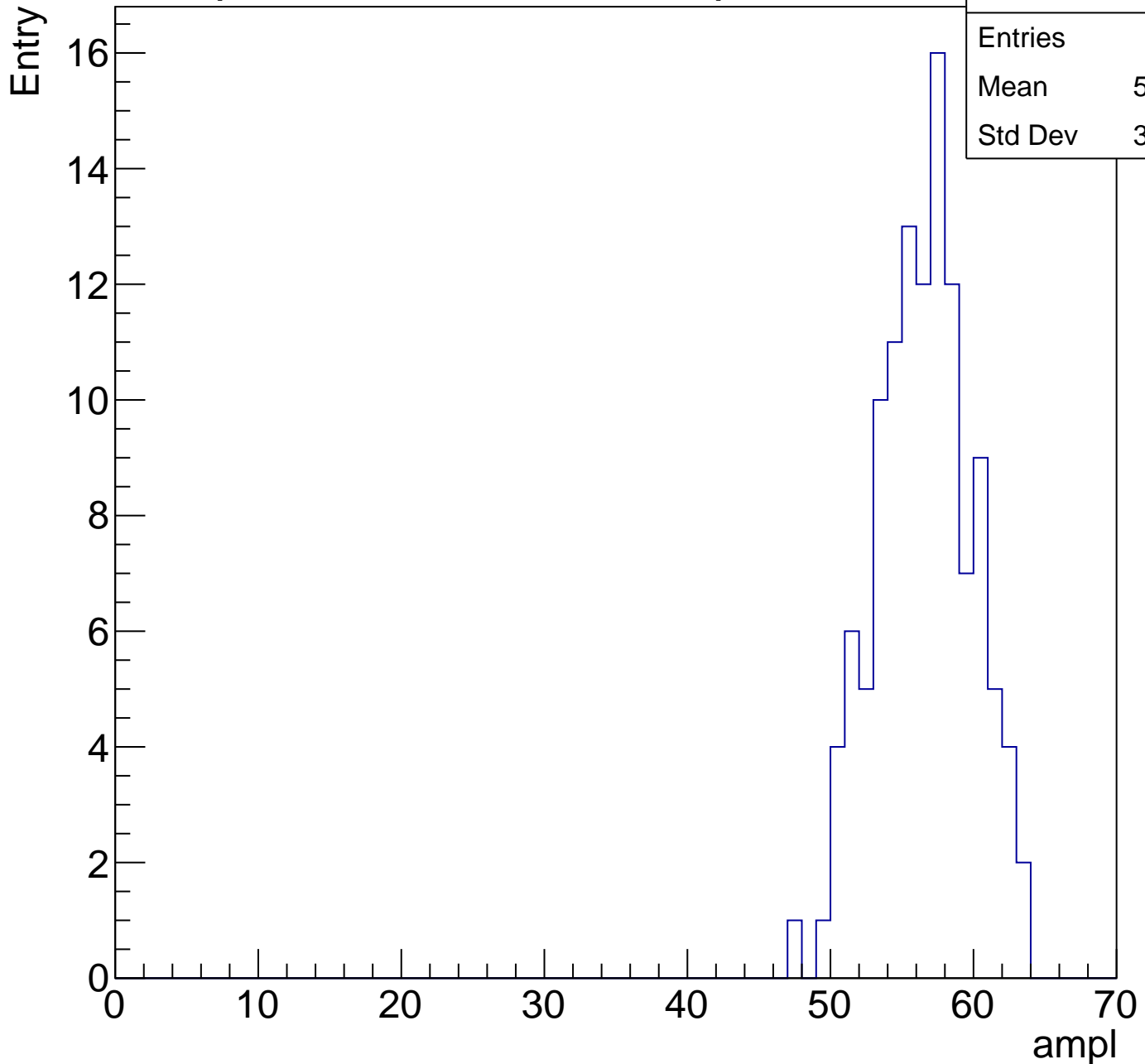
Entries	145
Mean	49.72
Std Dev	3.914



B0L101S, U20-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	56.04
Std Dev	3.305



B0L101S, U20-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

75

Mean

59.2

Std Dev

7.263

ampl

0

10

20

30

40

50

60

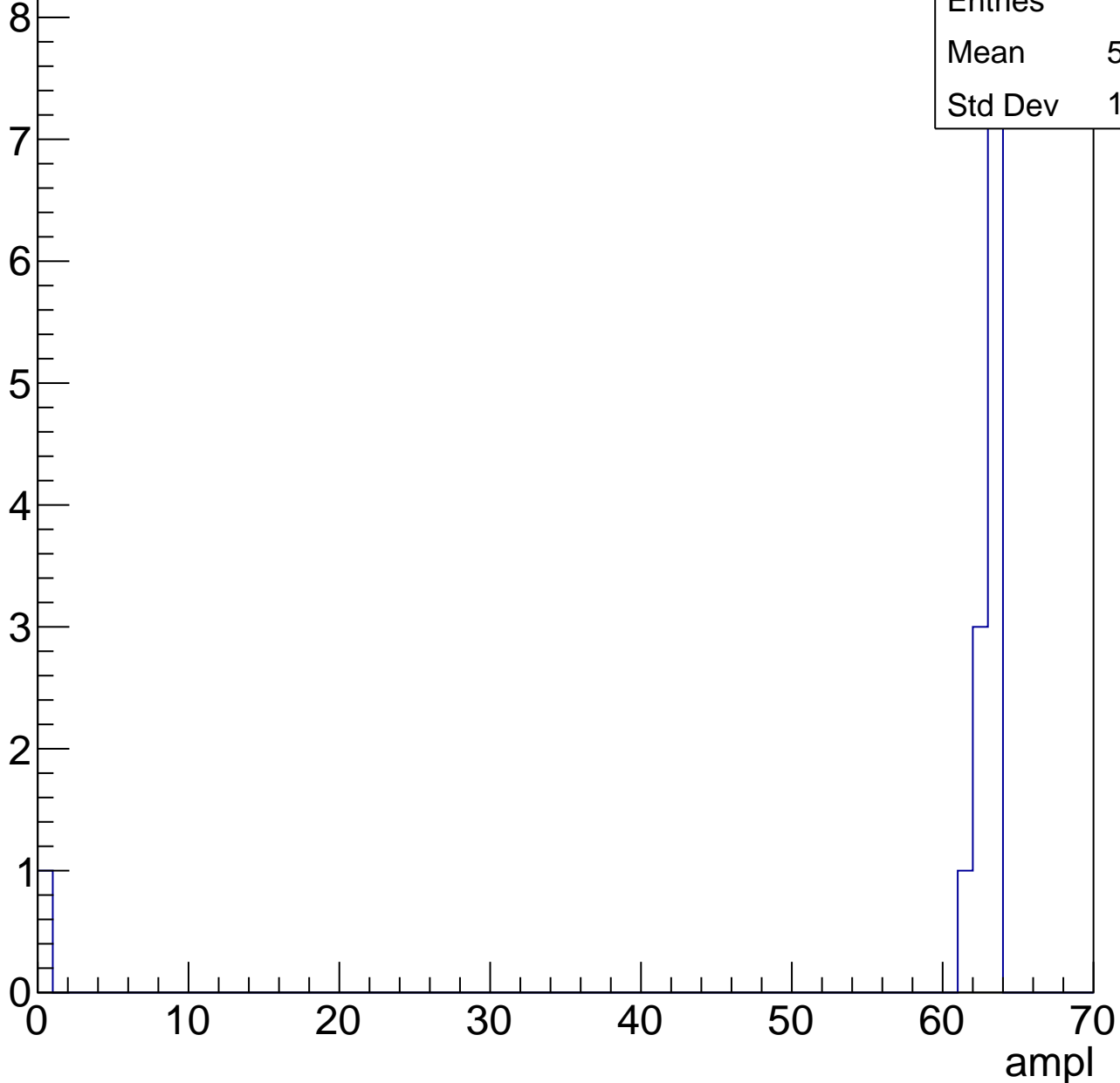
70

B0L101S, U20-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.77
Std Dev	16.69



B0L101S, U20-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch108, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	26.79
Std Dev	5.108

Gaus mean : 27.3572

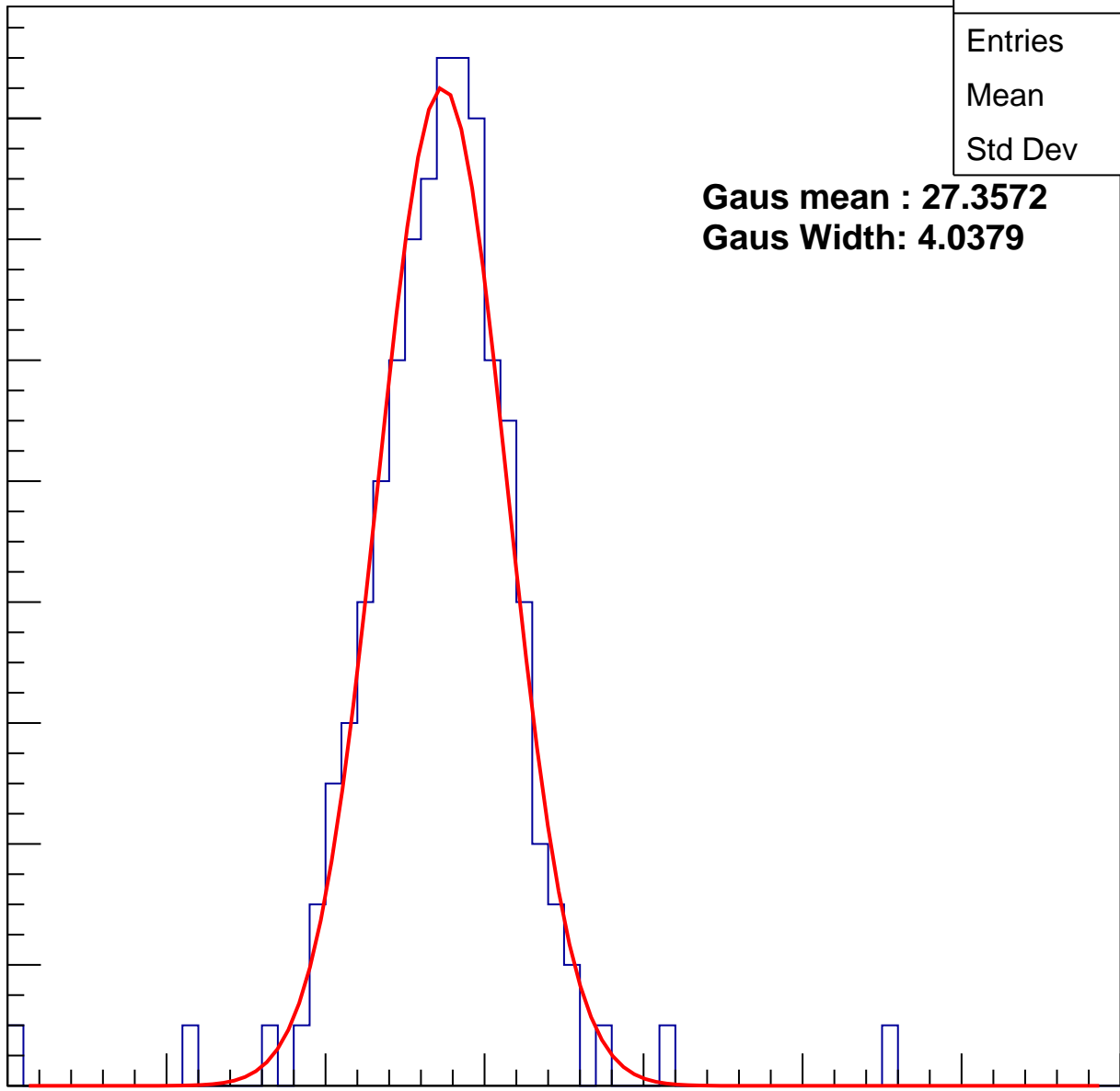
Gaus Width: 4.0379

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

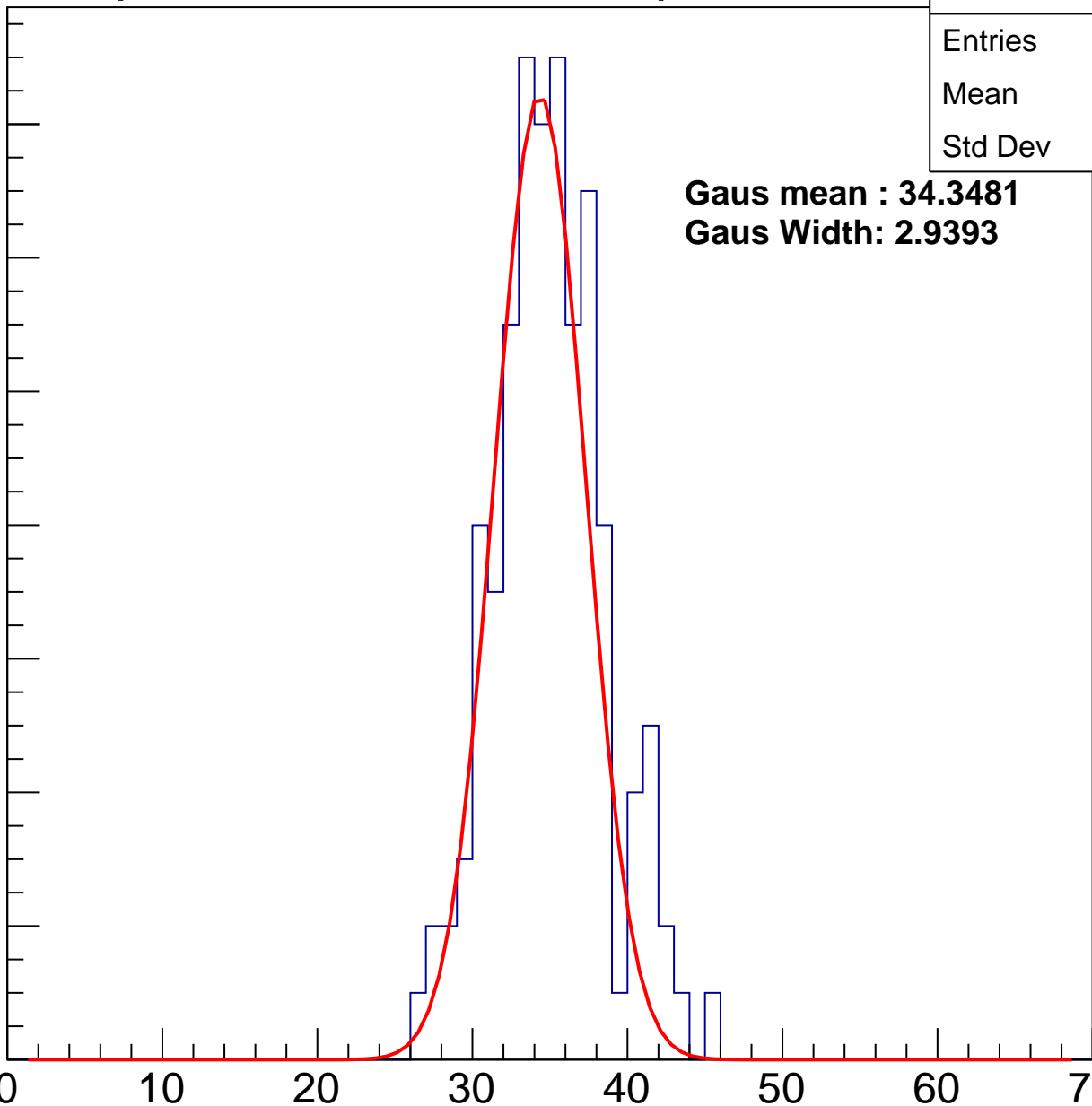
14
12
10
8
6
4
2
0

Entries	124
Mean	34.56
Std Dev	3.557

Gaus mean : 34.3481

Gaus Width: 2.9393

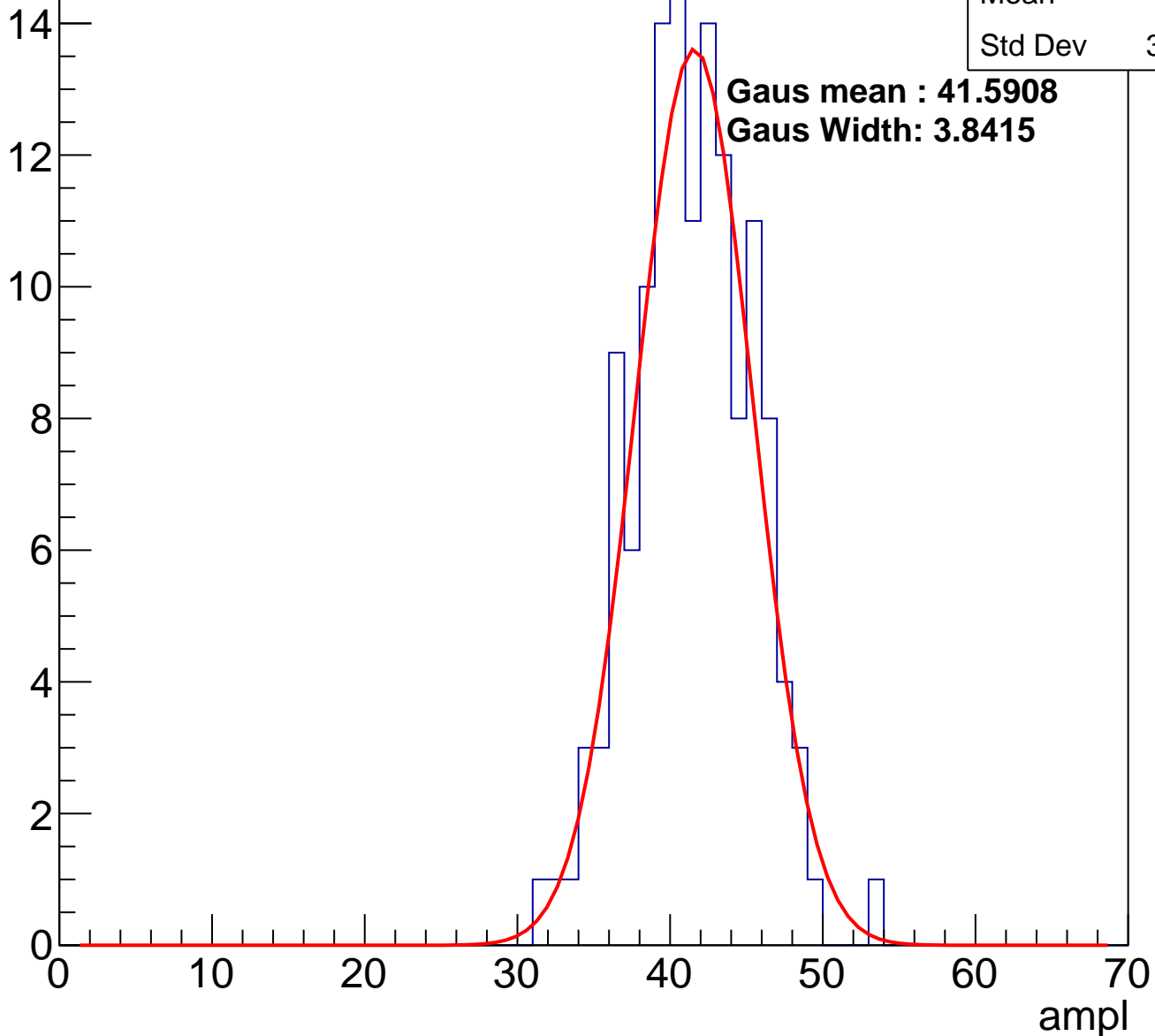
ampl



B0L101S, U20-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

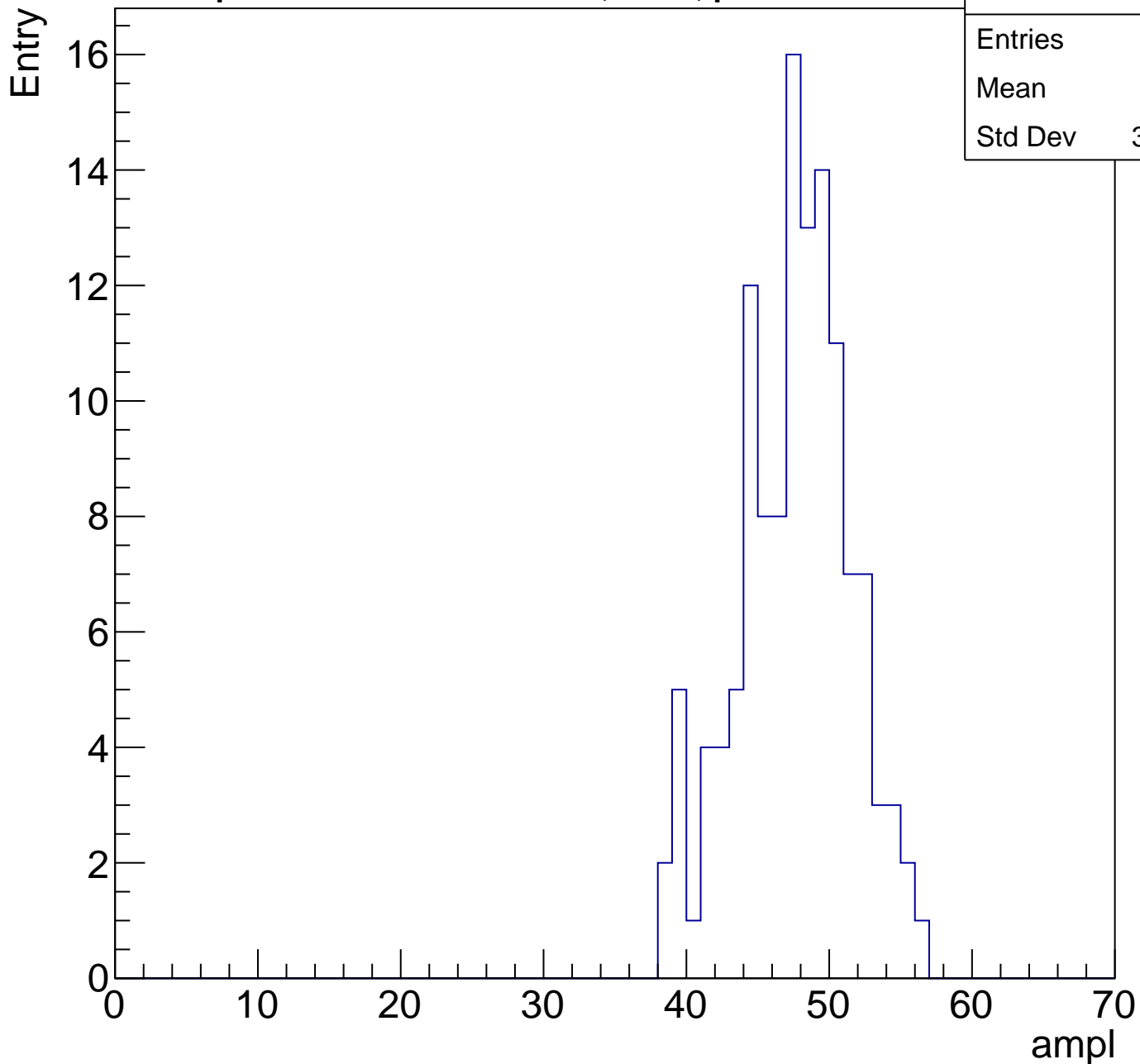
Entry



B0L101S, U20-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	47.1
Std Dev	3.904



B0L101S, U20-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

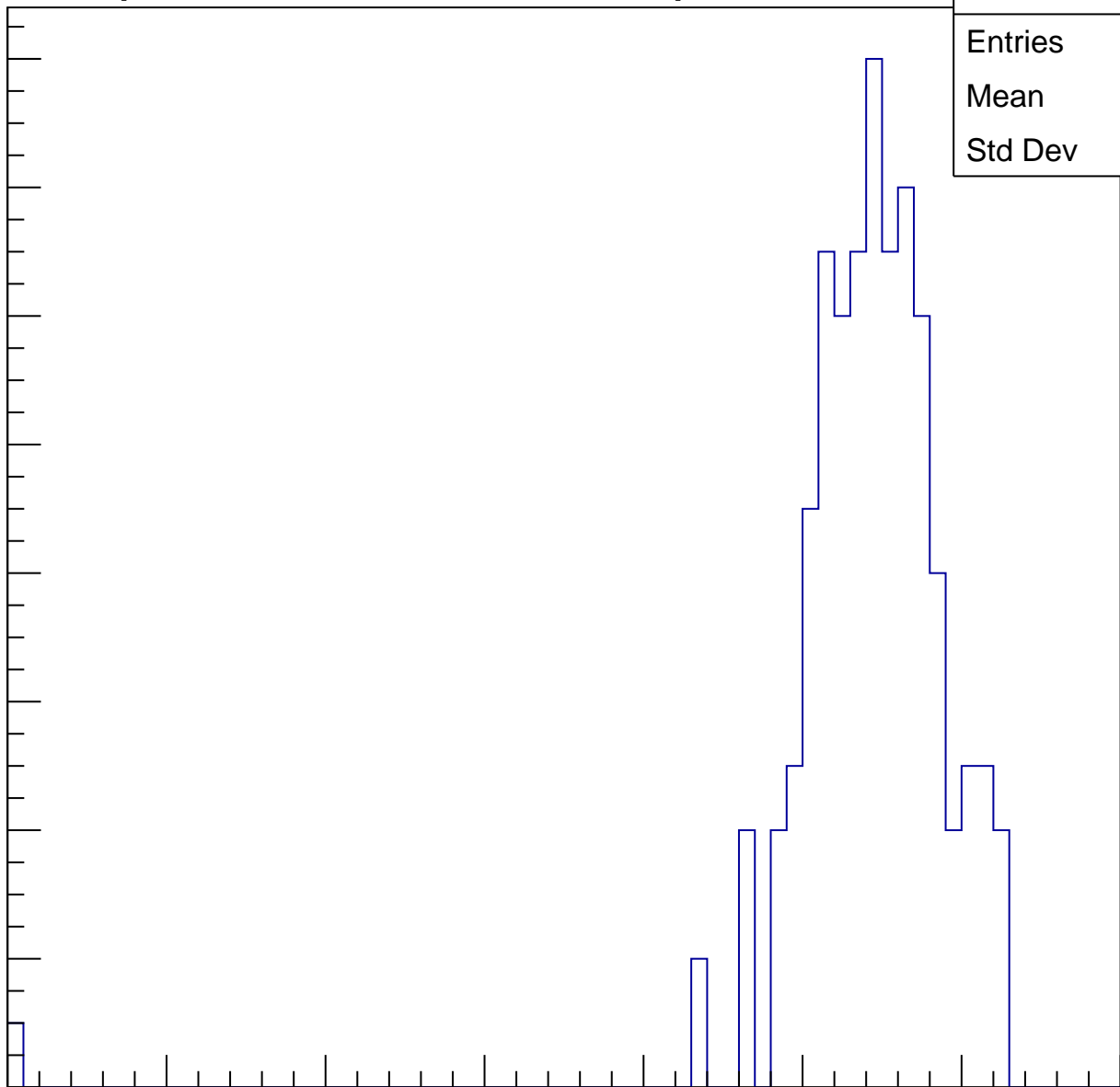
Entries	144
Mean	53.7
Std Dev	5.938

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch108, adc5

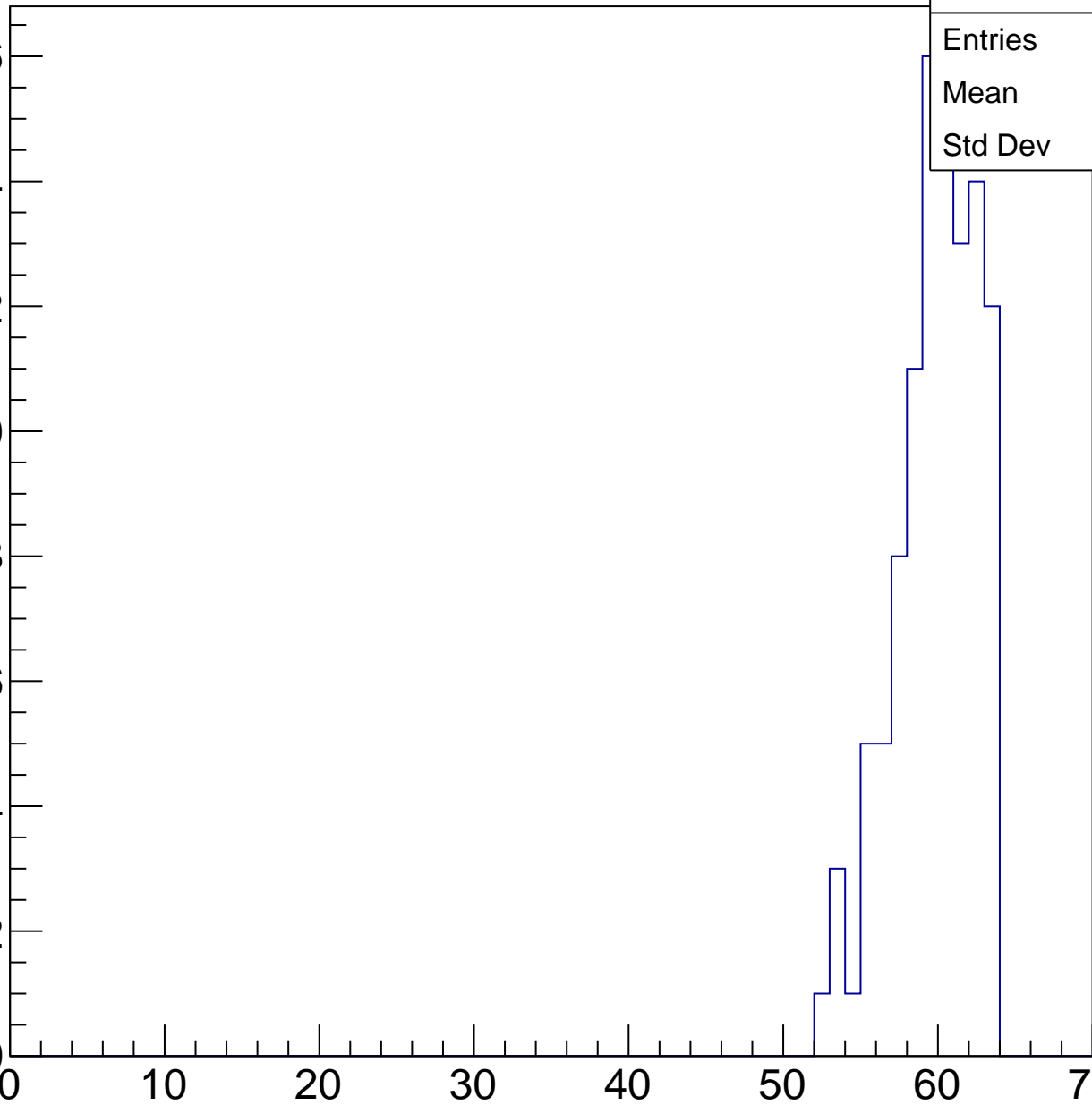
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	59.38
Std Dev	2.632

ampl

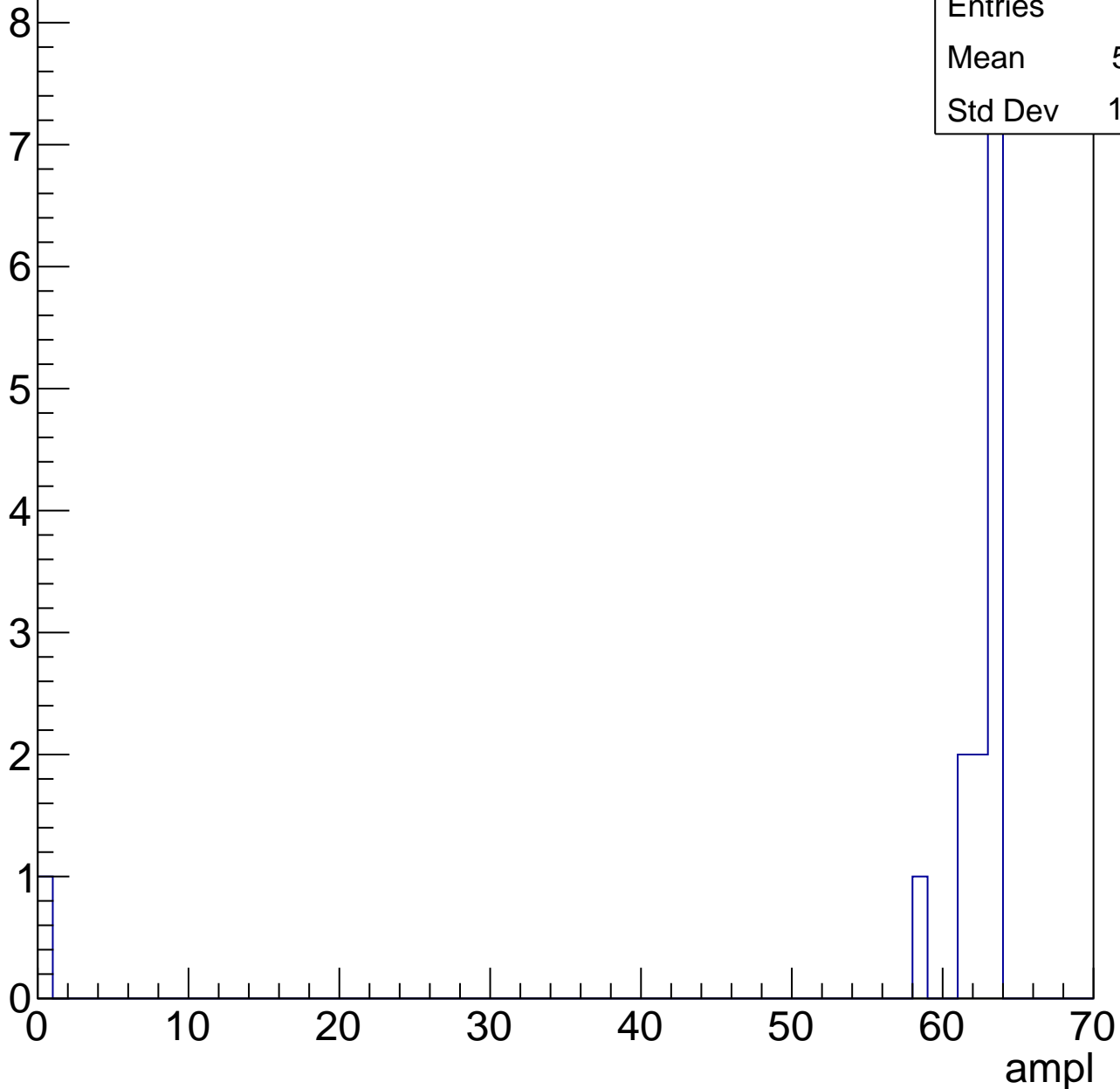


B0L101S, U20-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

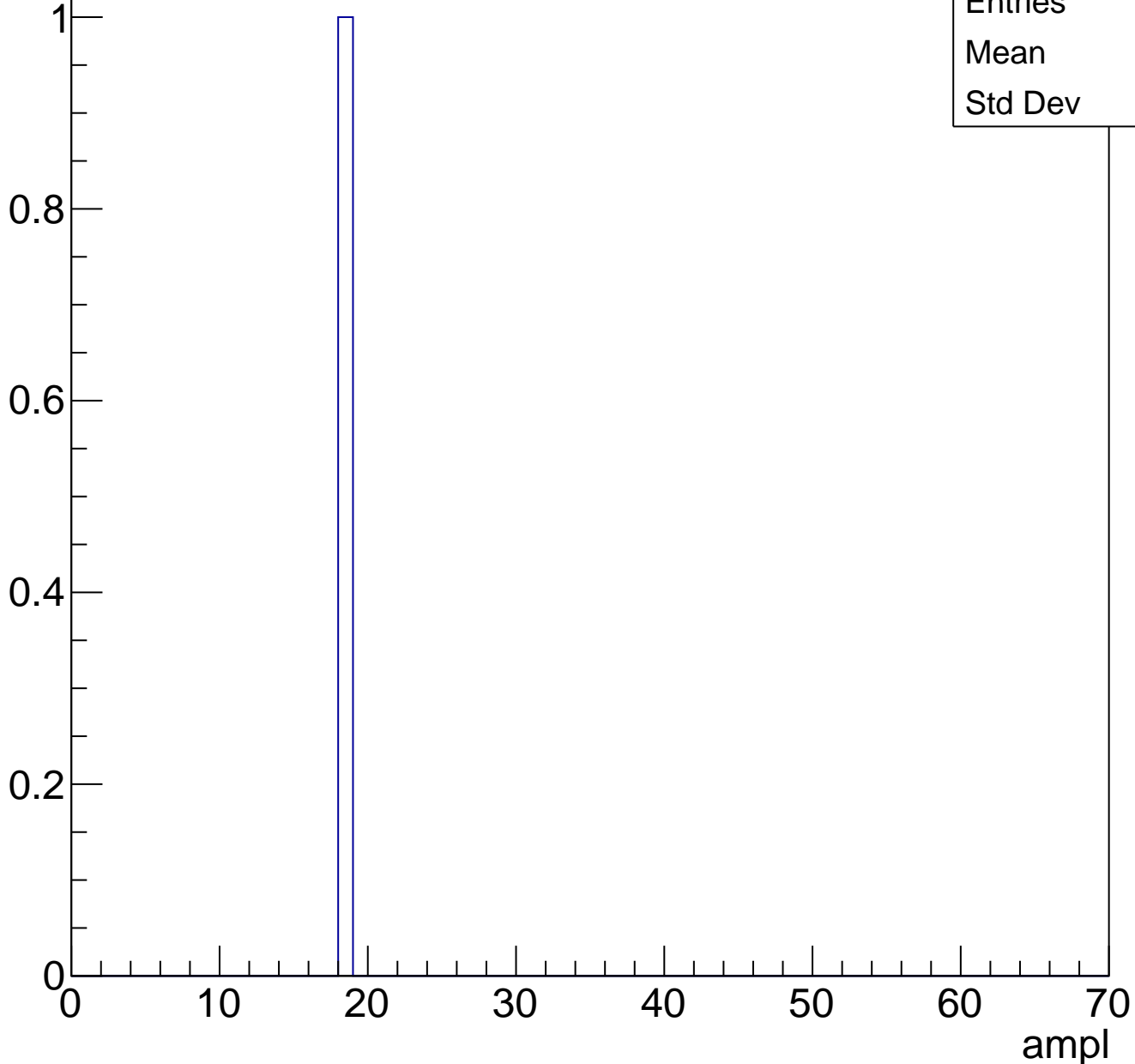
Entries	14
Mean	57.71
Std Dev	16.06



B0L101S, U20-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	18
Std Dev	0

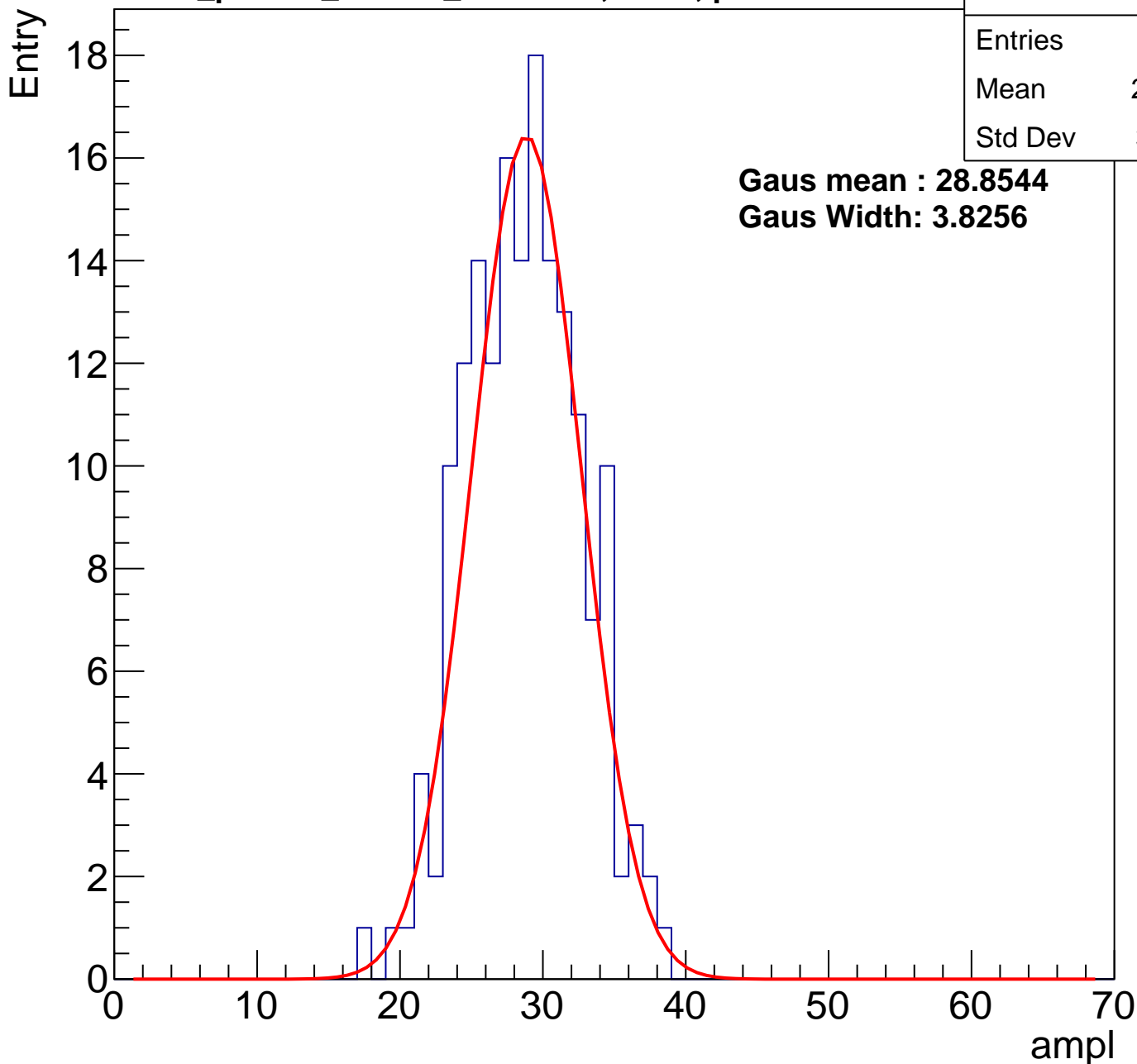
B0L101S, U20-ch109, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.25
Std Dev	3.941

Gaus mean : 28.8544

Gaus Width: 3.8256



B0L101S, U20-ch109, adc1

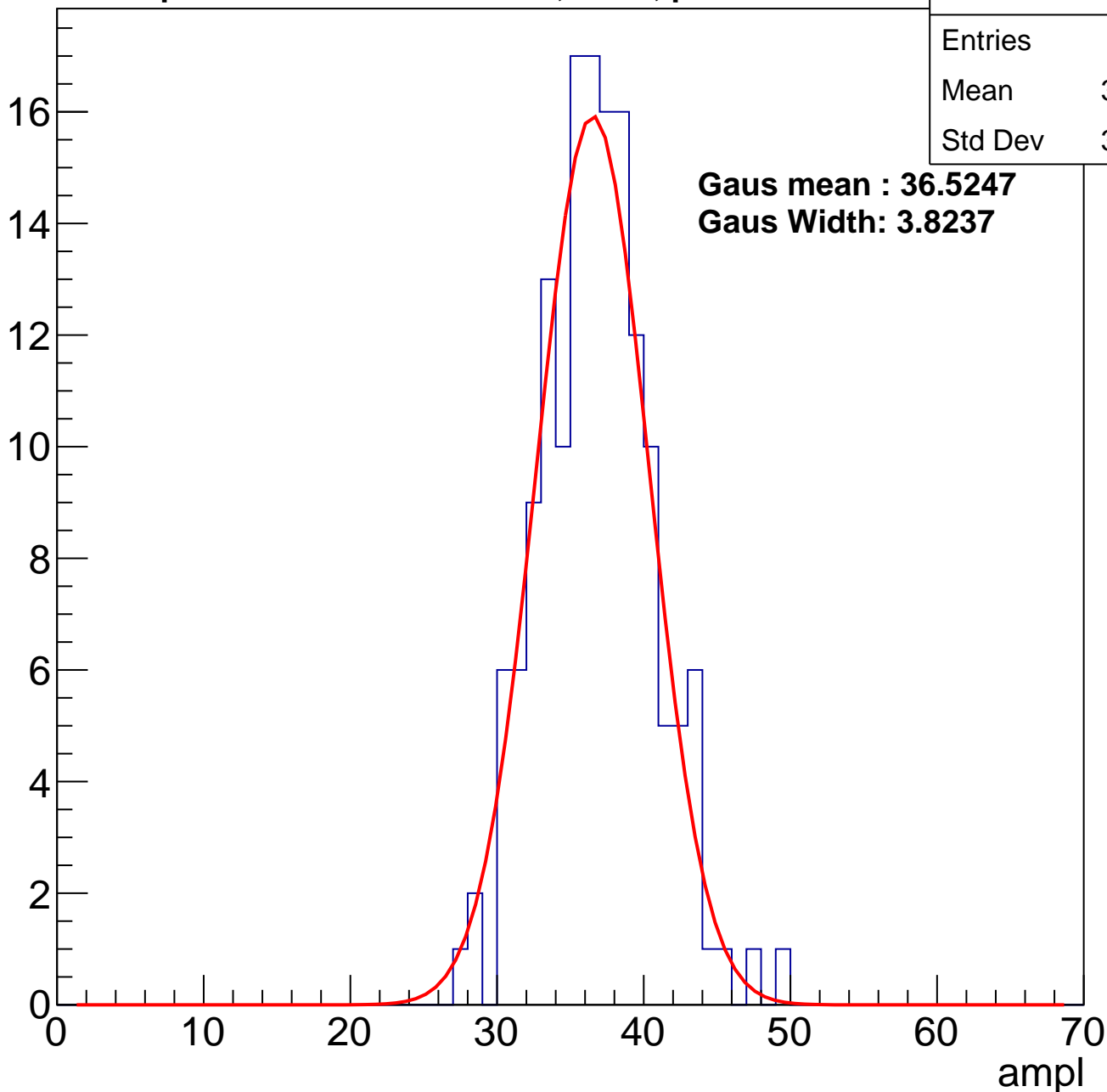
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	36.38
Std Dev	3.812

Gaus mean : 36.5247

Gaus Width: 3.8237

Entry



B0L101S, U20-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

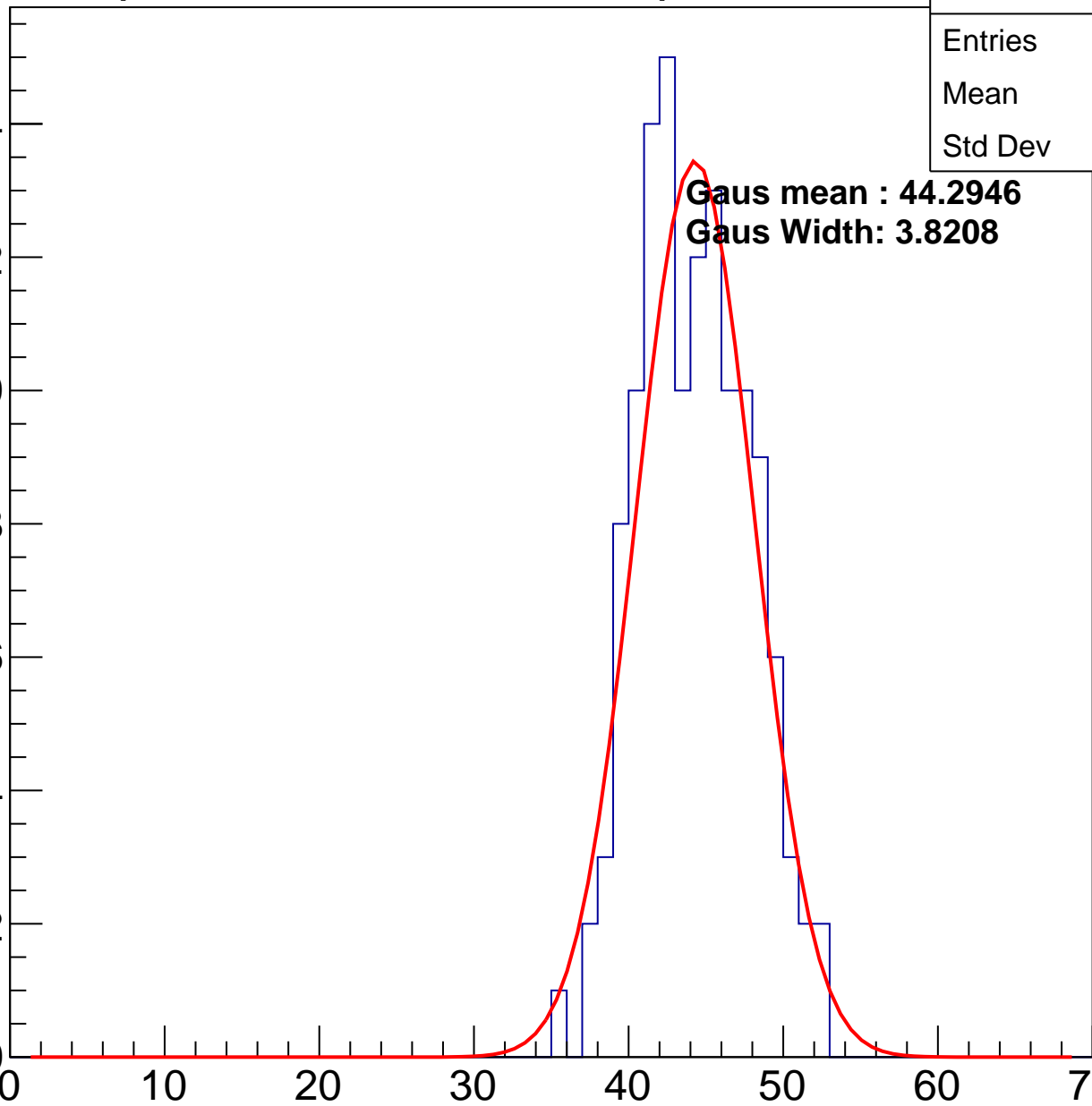
60

ampl

Entries	130
Mean	43.8
Std Dev	3.518

Gaus mean : 44.2946

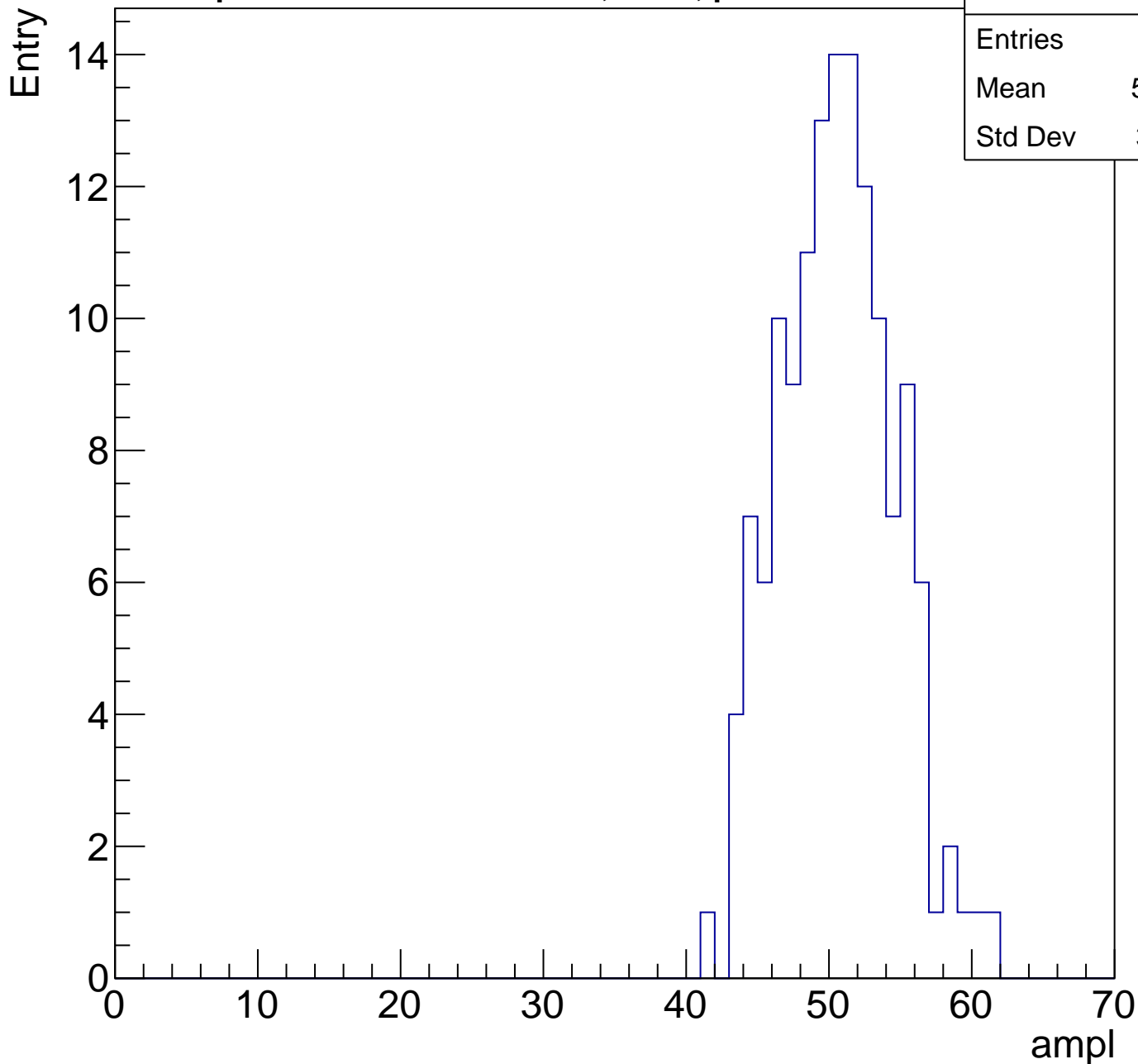
Gaus Width: 3.8208



B0L101S, U20-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.14
Std Dev	3.941



B0L101S, U20-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

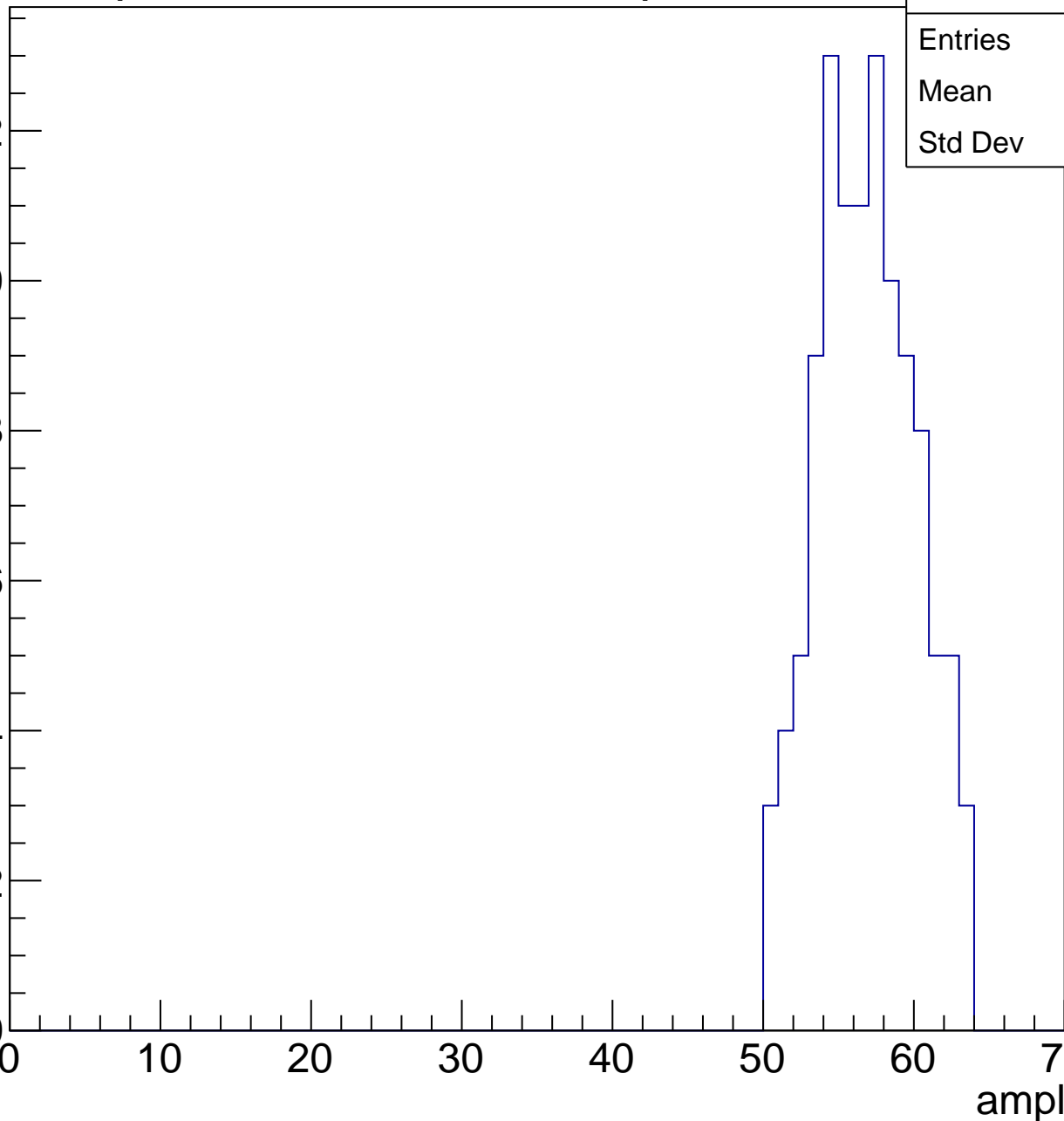
50

60

70

ampl

Entries	109
Mean	56.42
Std Dev	3.215



B0L101S, U20-ch109, adc5

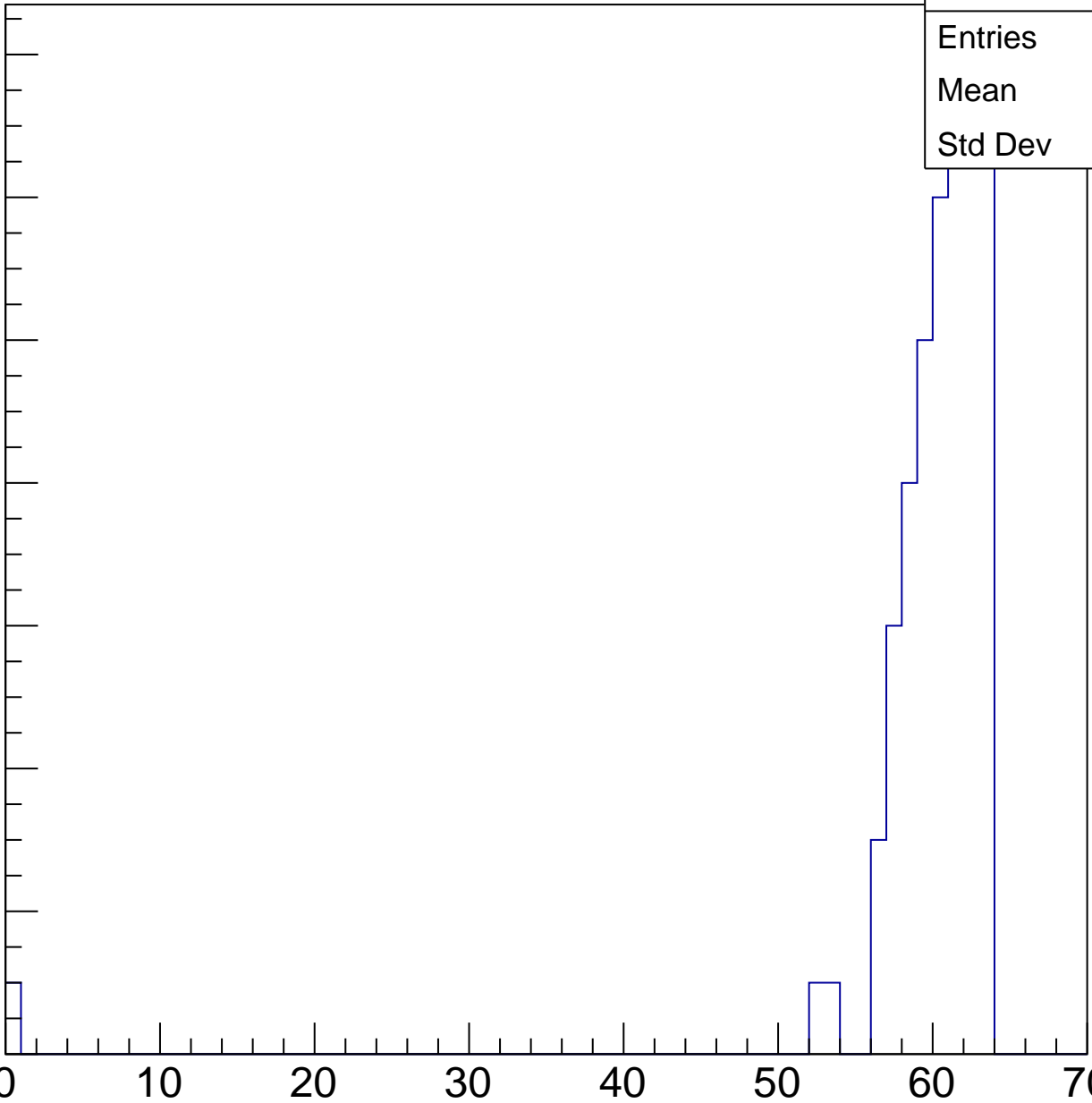
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	59.44
Std Dev	6.924

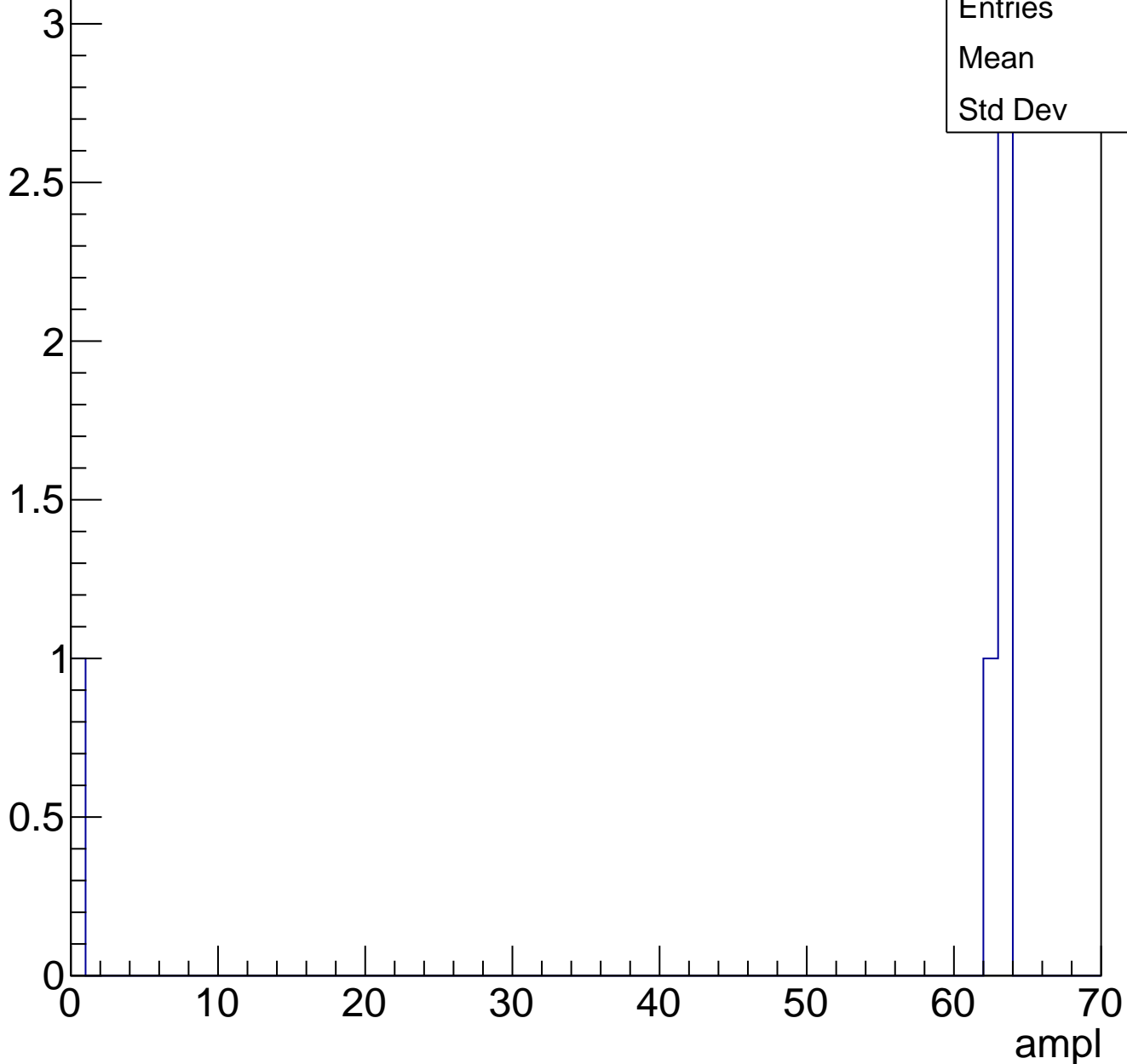
ampl



B0L101S, U20-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



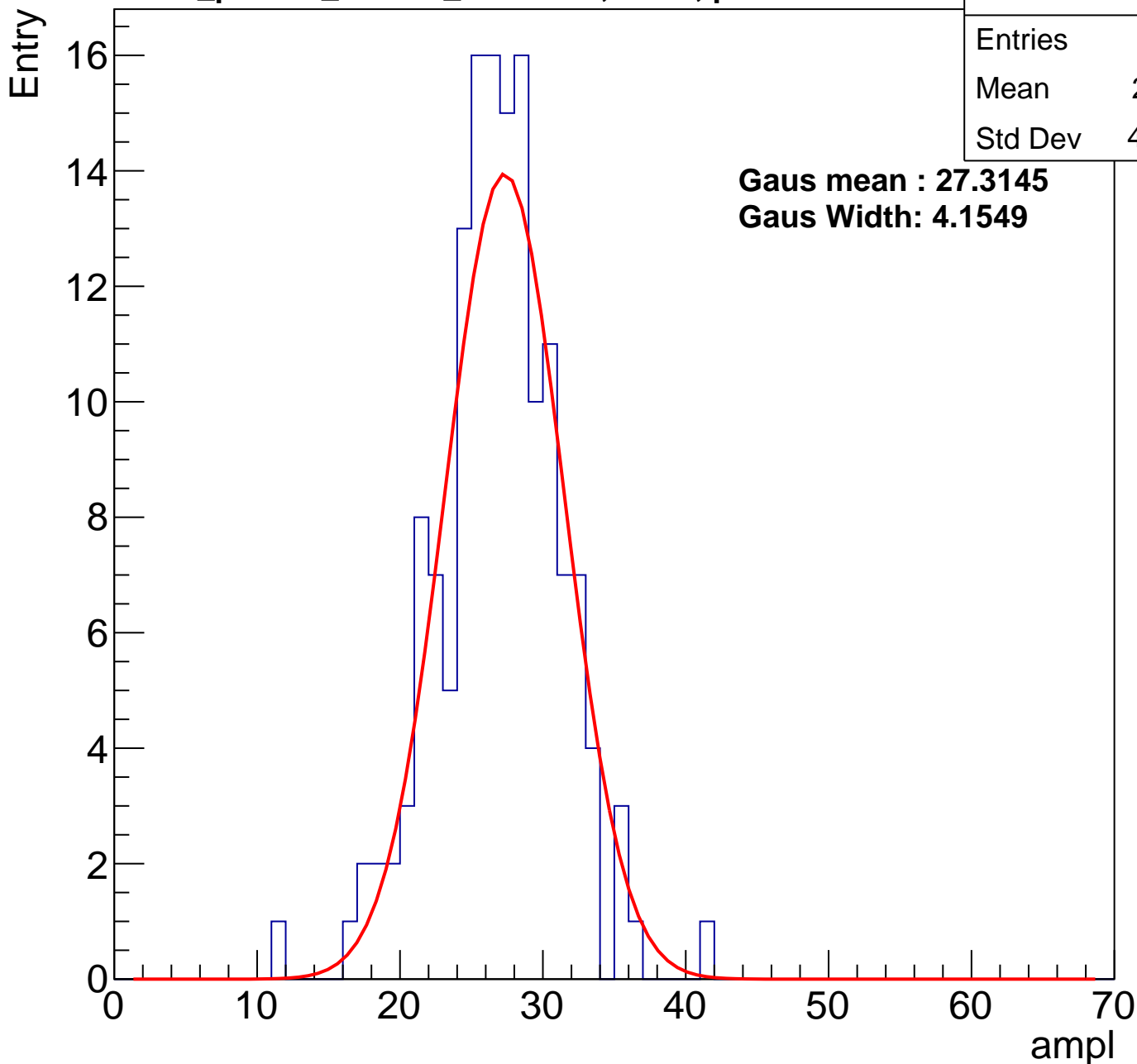
B0L101S, U20-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	26.41
Std Dev	4.273

Gaus mean : 27.3145

Gaus Width: 4.1549



B0L101S, U20-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

149

Mean

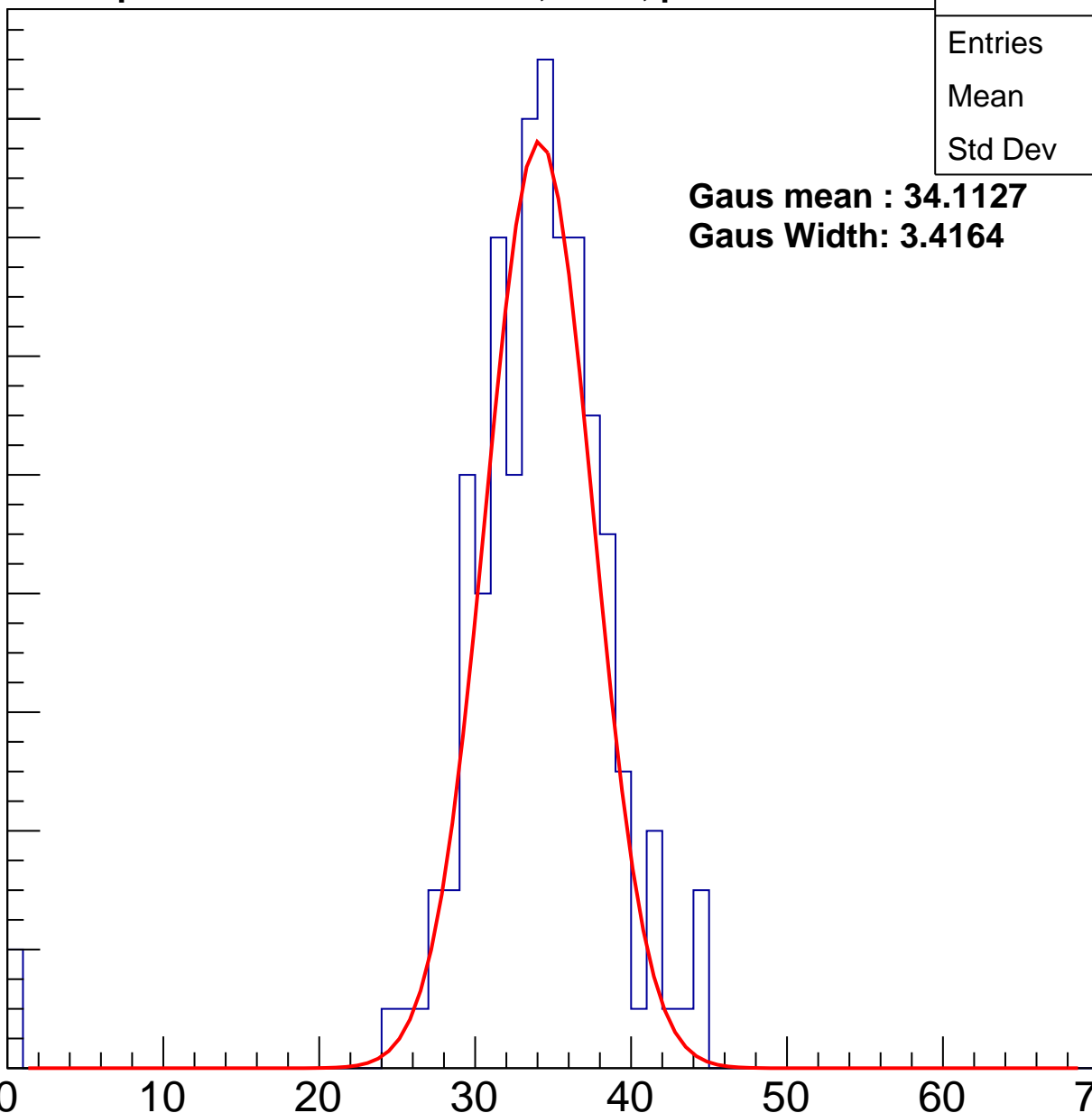
33.48

Std Dev

5.463

Gaus mean : 34.1127

Gaus Width: 3.4164



B0L101S, U20-ch110, adc2

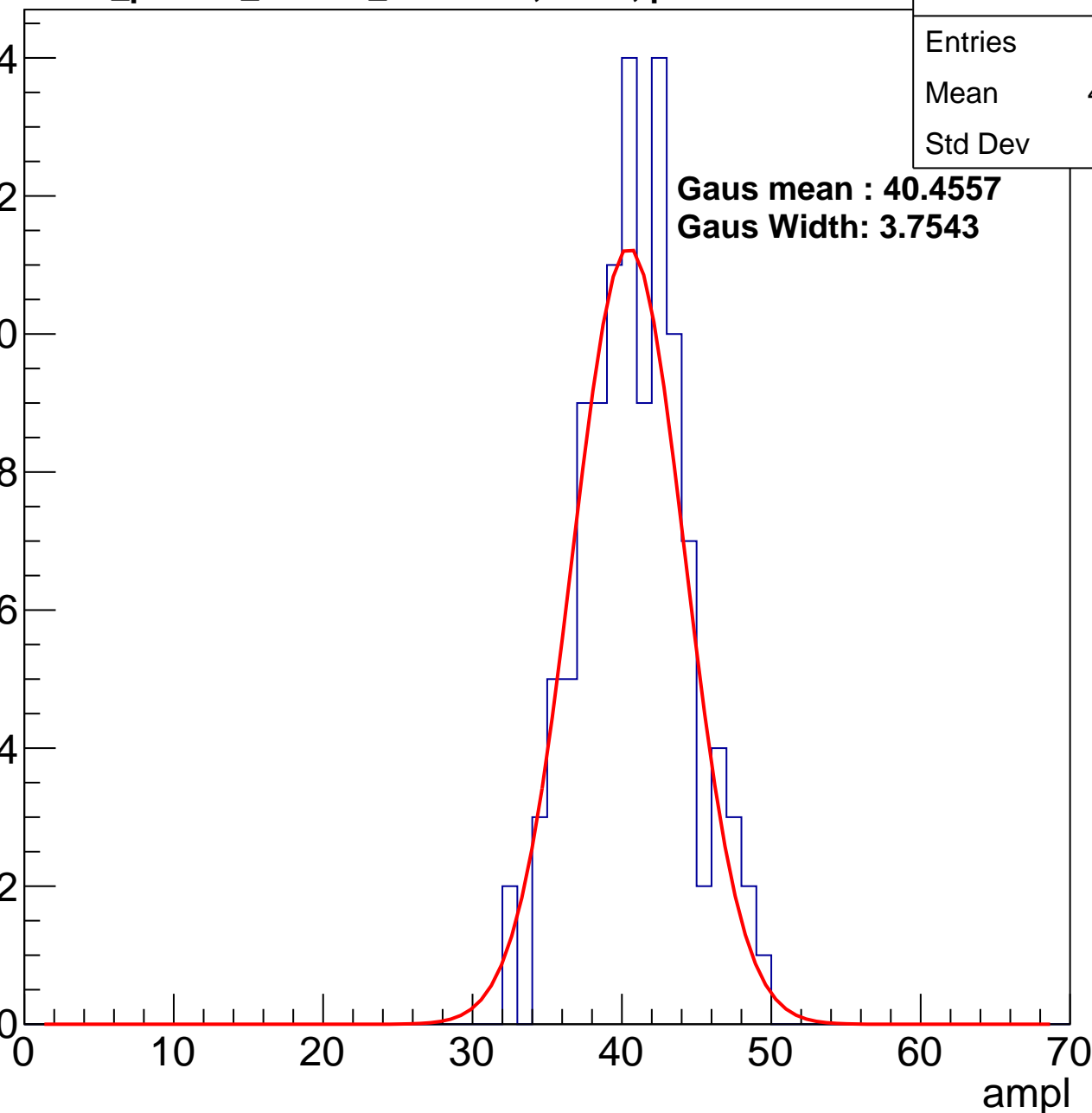
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	40.36
Std Dev	3.531

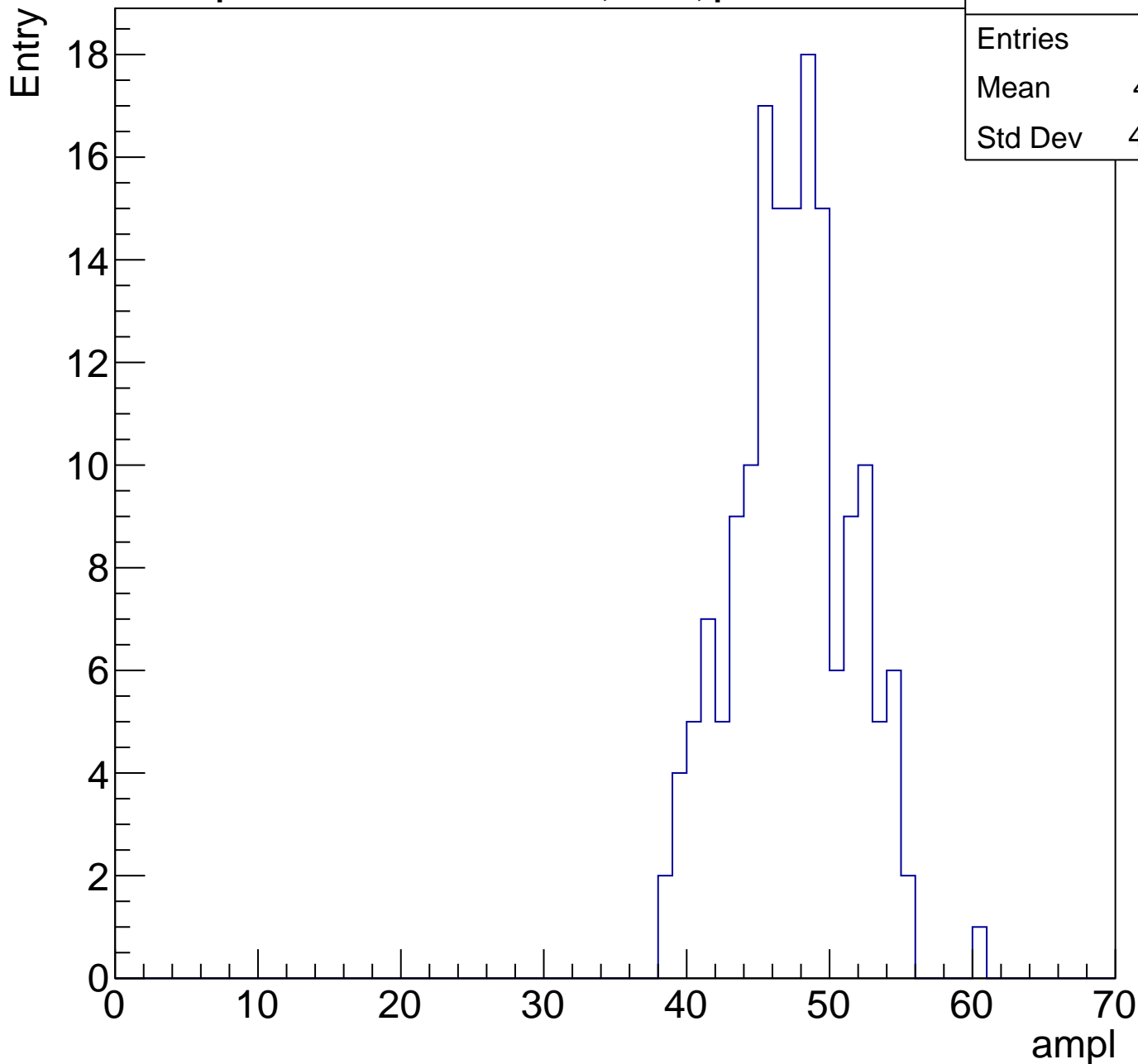
Gaus mean : 40.4557
Gaus Width: 3.7543



B0L101S, U20-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	46.91
Std Dev	4.059



B0L101S, U20-ch110, adc4

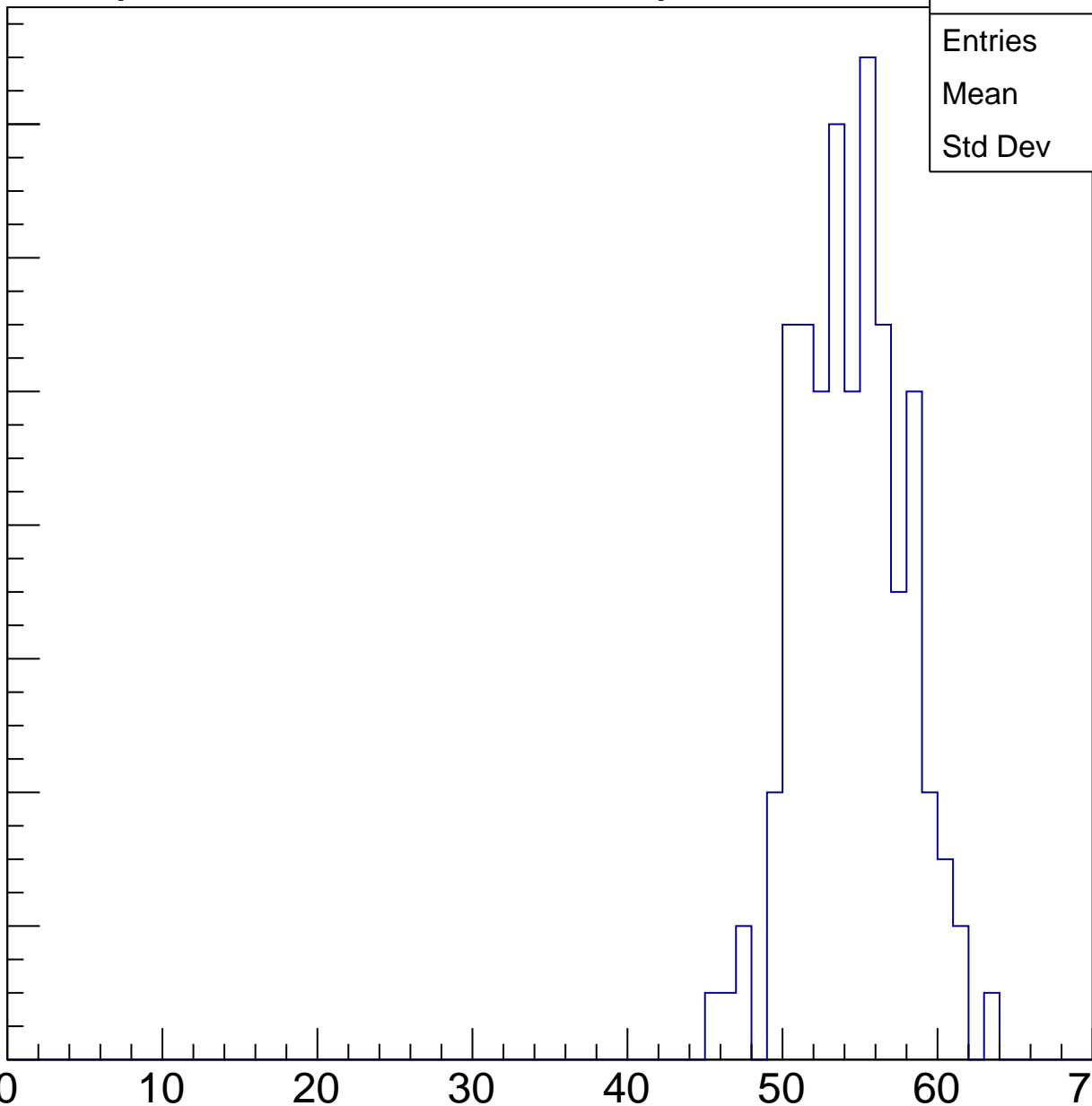
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	53.97
Std Dev	3.385

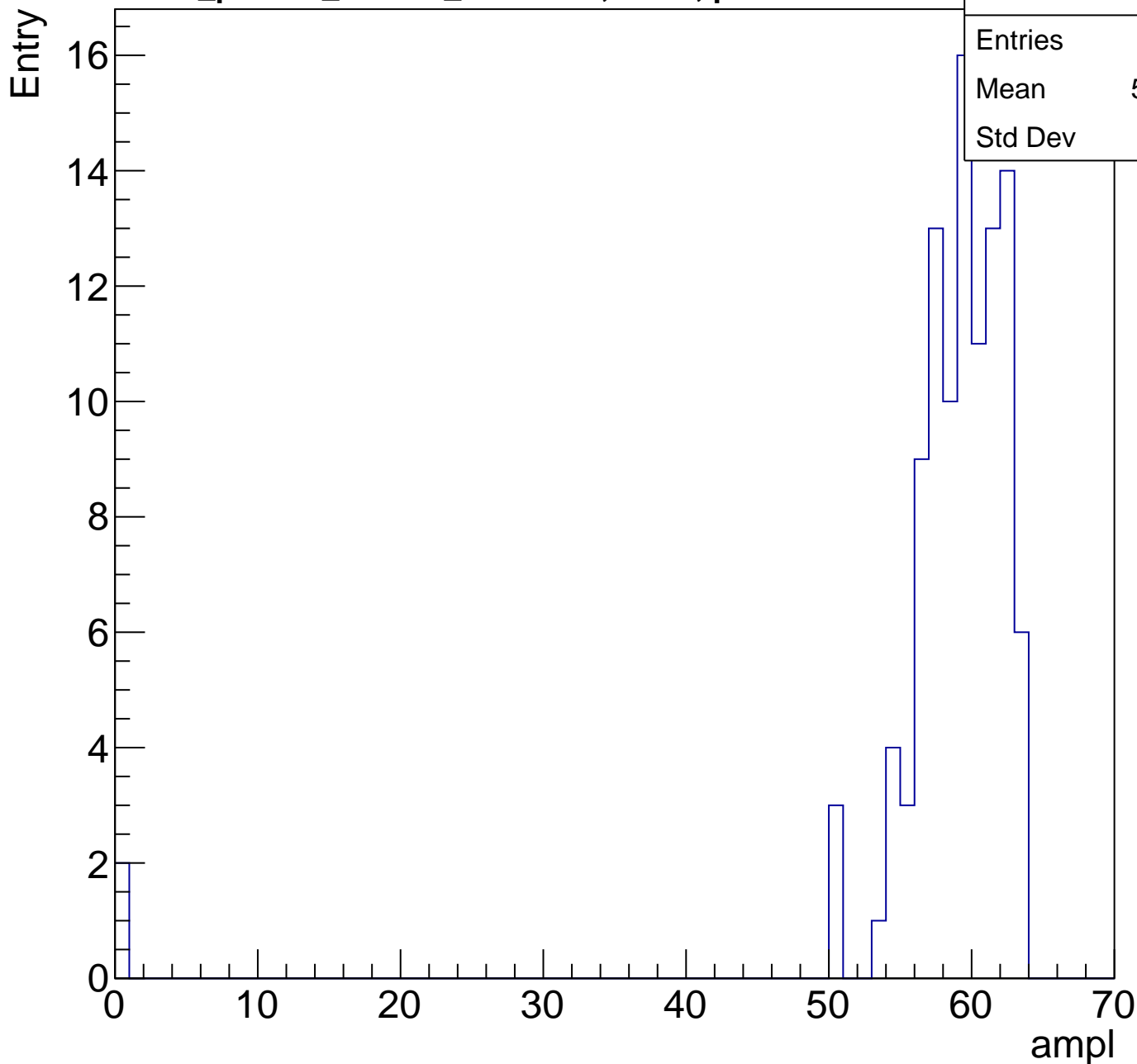
ampl



B0L101S, U20-ch110, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	57.64
Std Dev	8.52



B0L101S, U20-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

27

Mean

61.89

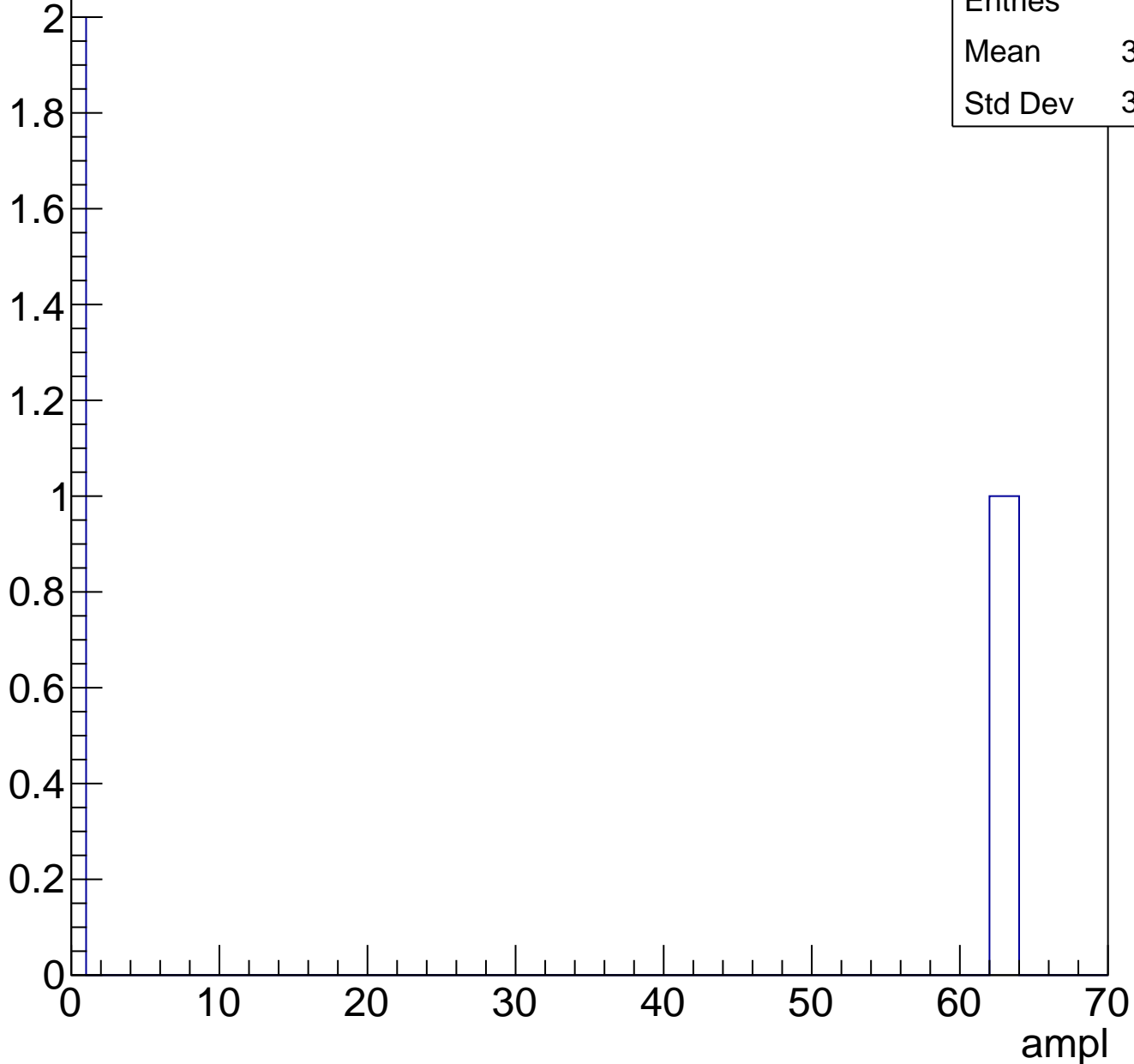
Std Dev

1.165

B0L101S, U20-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch111, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	28.79
Std Dev	5.046

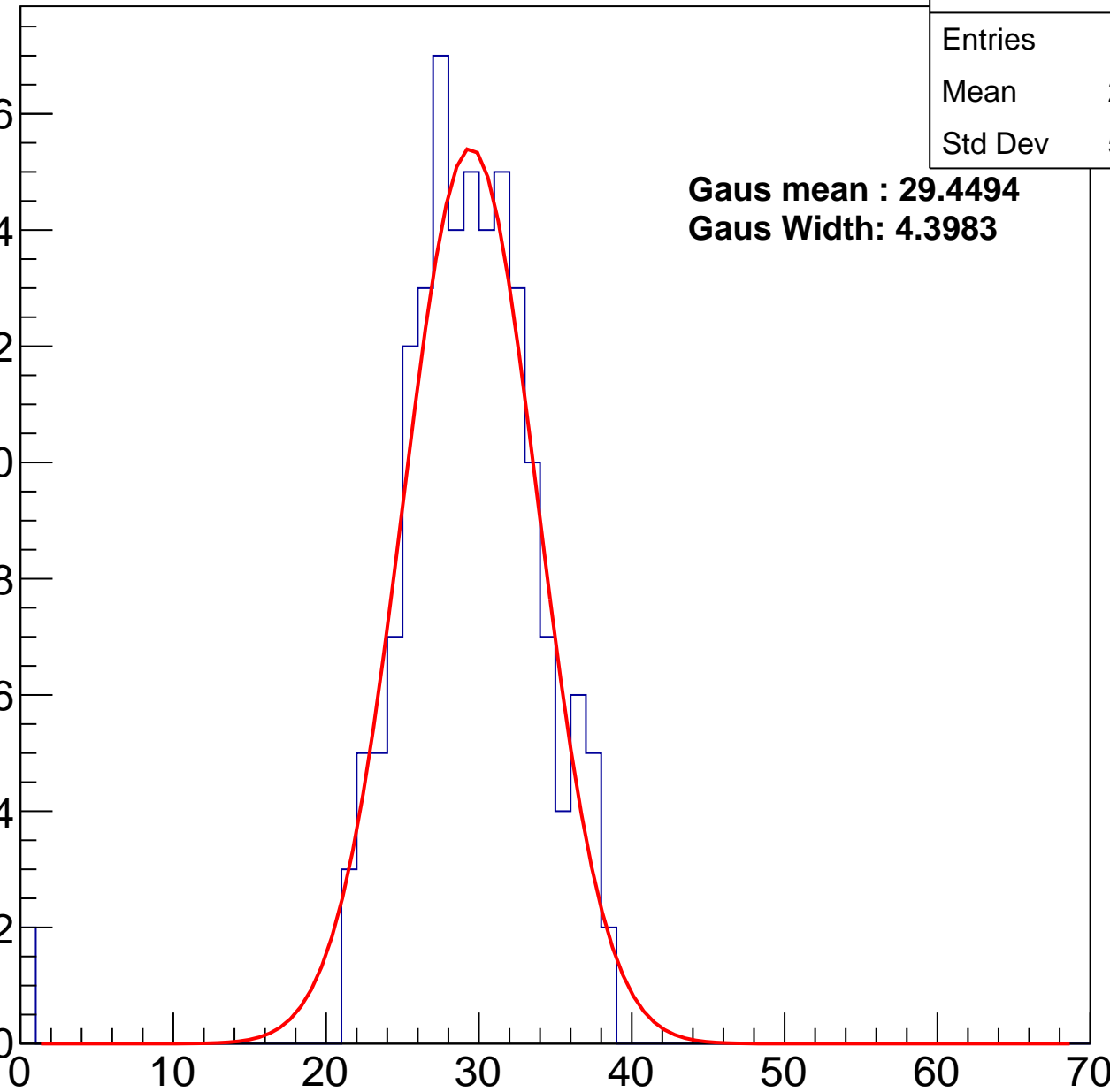
Gaus mean : 29.4494

Gaus Width: 4.3983

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch111, adc1

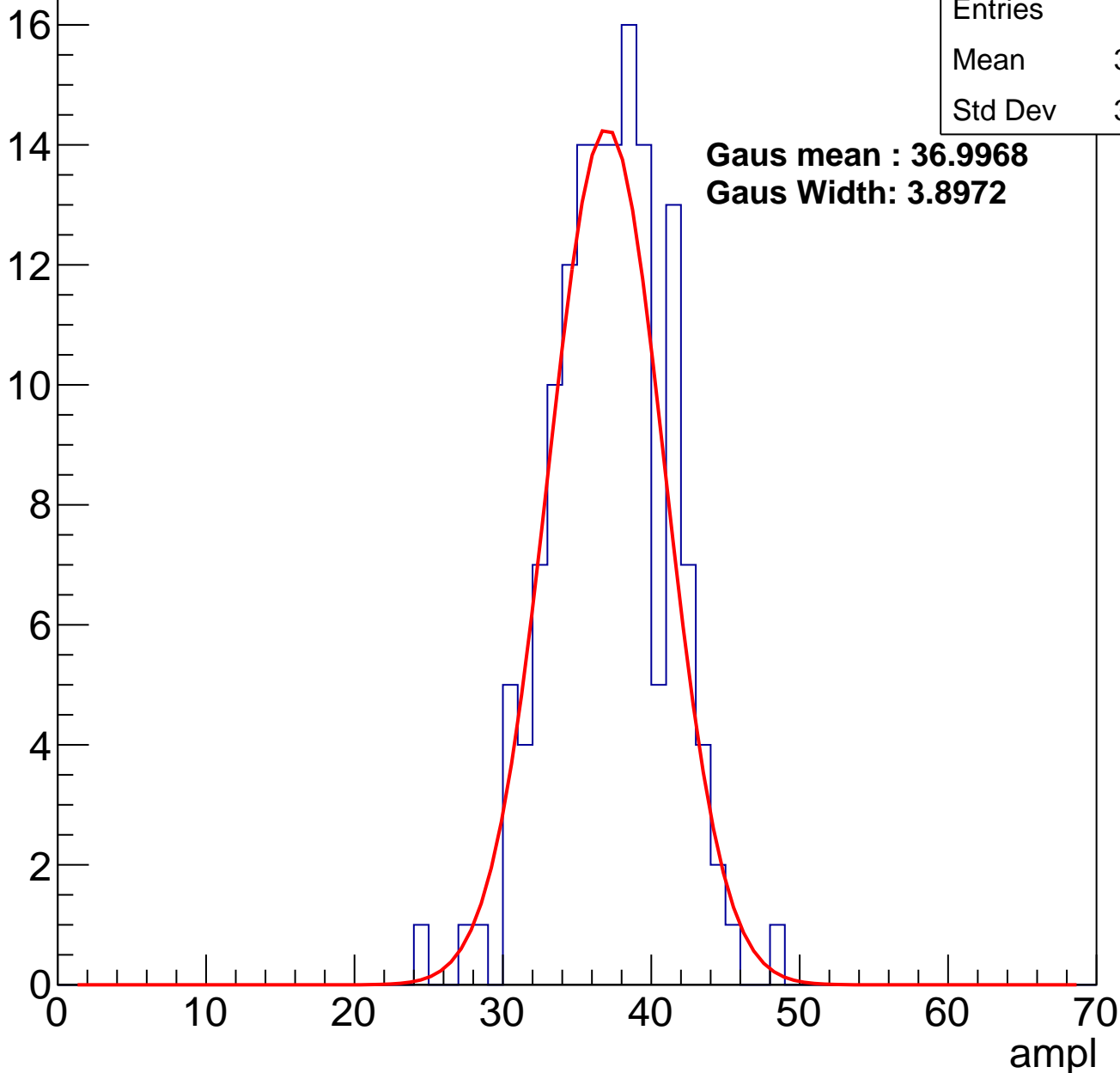
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.72
Std Dev	3.853

Gaus mean : 36.9968

Gaus Width: 3.8972

Entry



B0L101S, U20-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

112

Mean

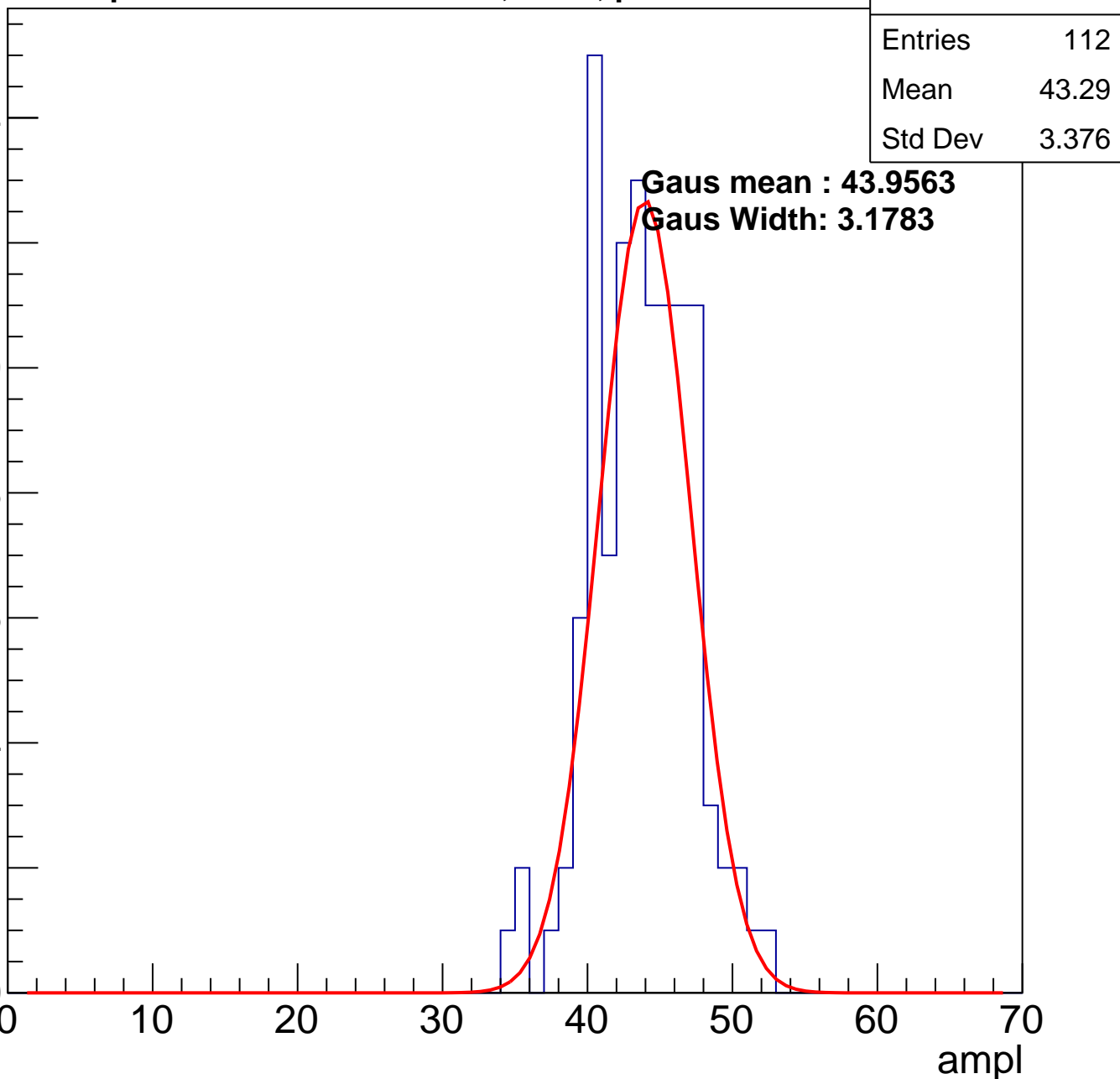
43.29

Std Dev

3.376

Gaus mean : 43.9563

Gaus Width: 3.1783



B0L101S, U20-ch111, adc3

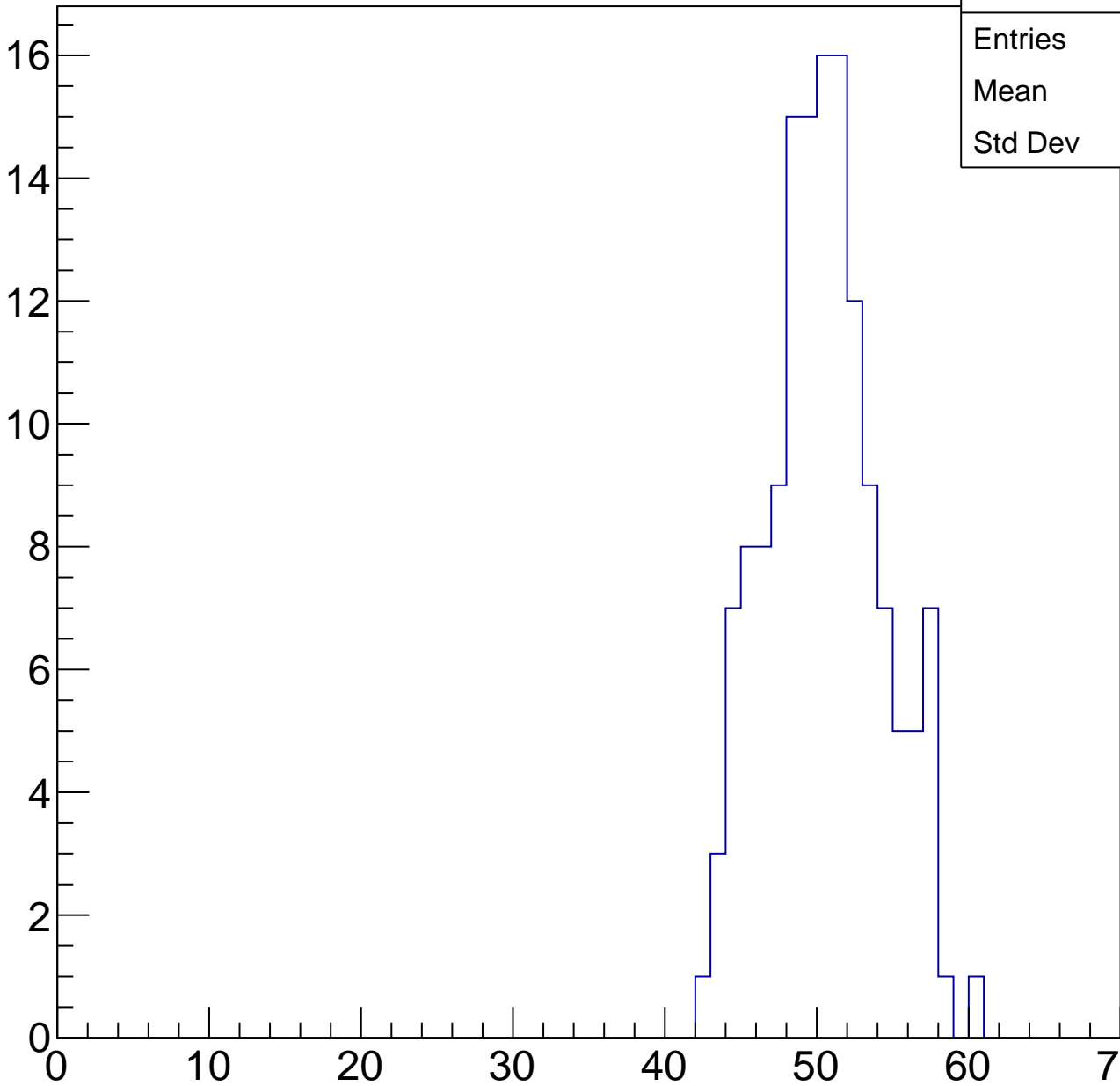
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	145
Mean	50.01
Std Dev	3.729

ampl



B0L101S, U20-ch111, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

106

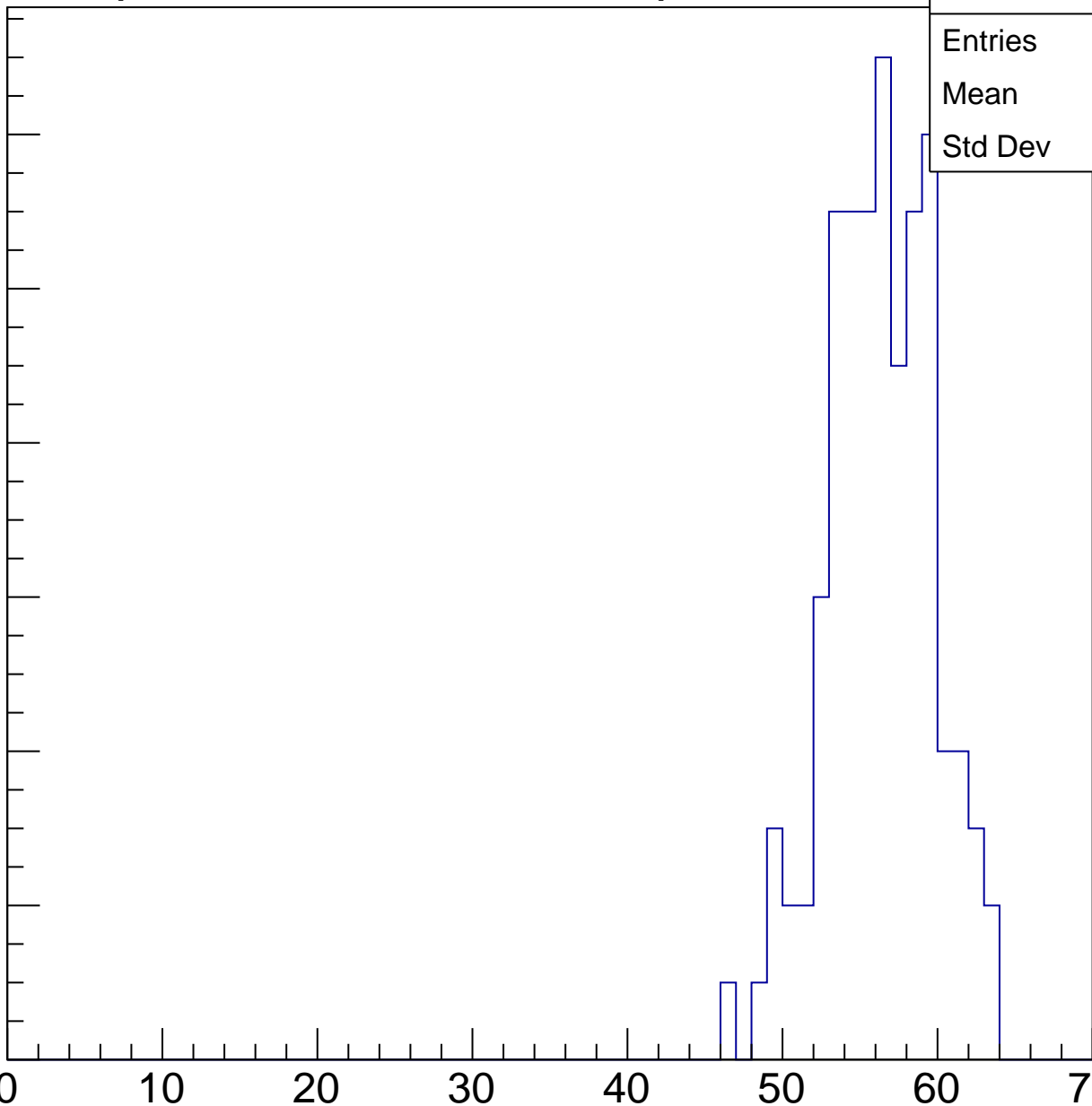
Mean

55.85

Std Dev

3.381

ampl



B0L101S, U20-ch111, adc5

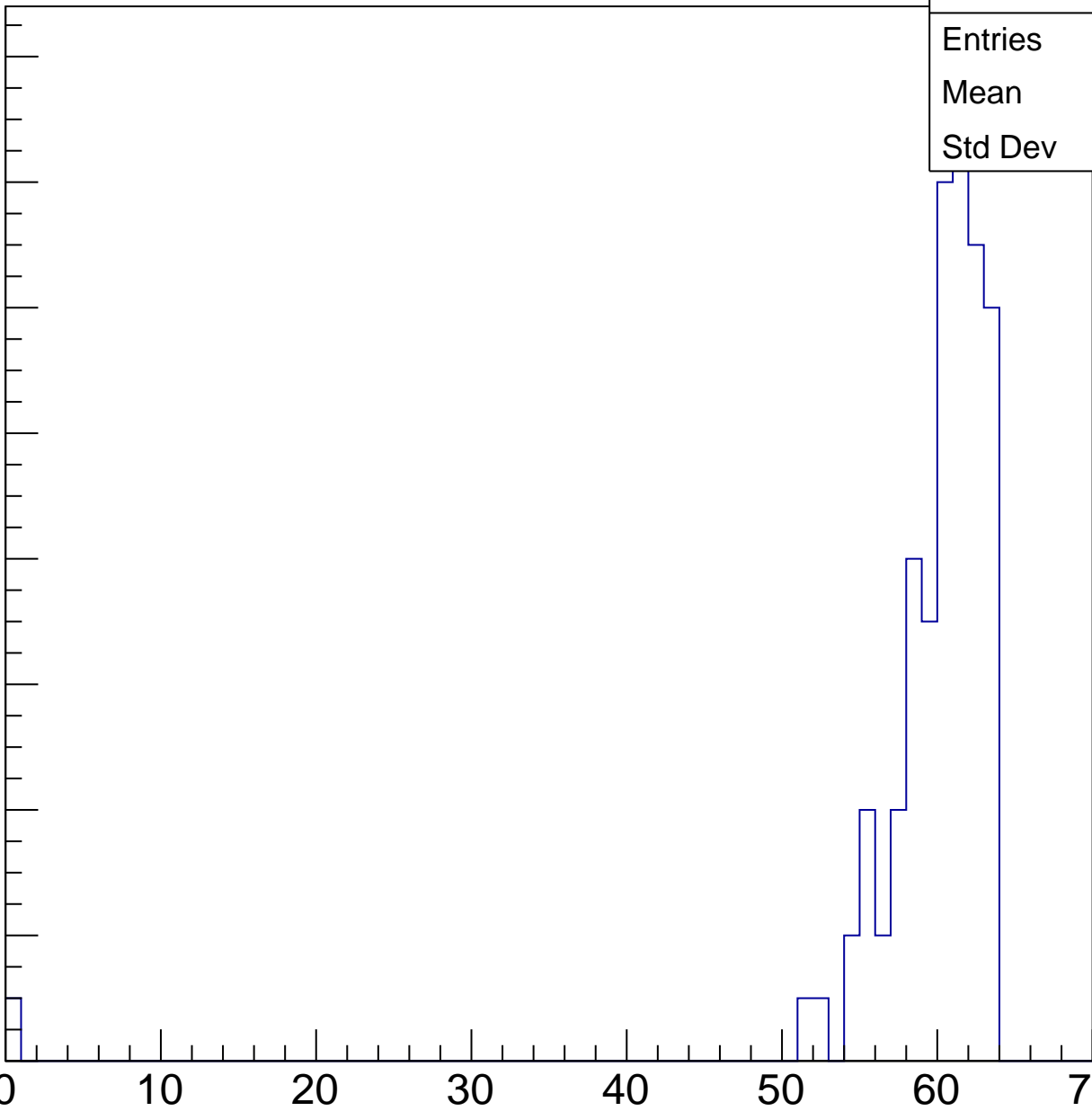
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.13
Std Dev	6.975

ampl

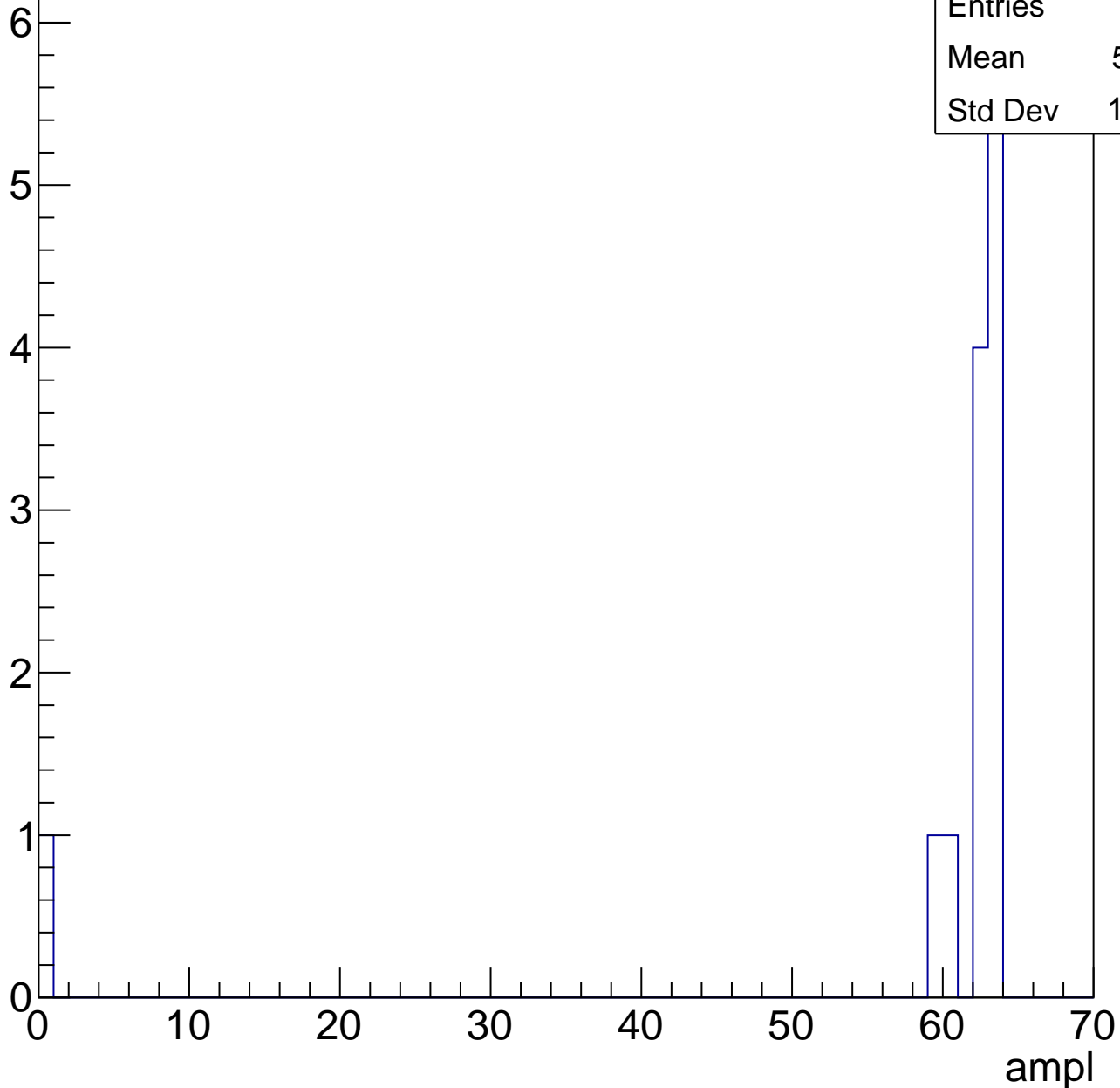


B0L101S, U20-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.31
Std Dev	16.59



B0L101S, U20-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



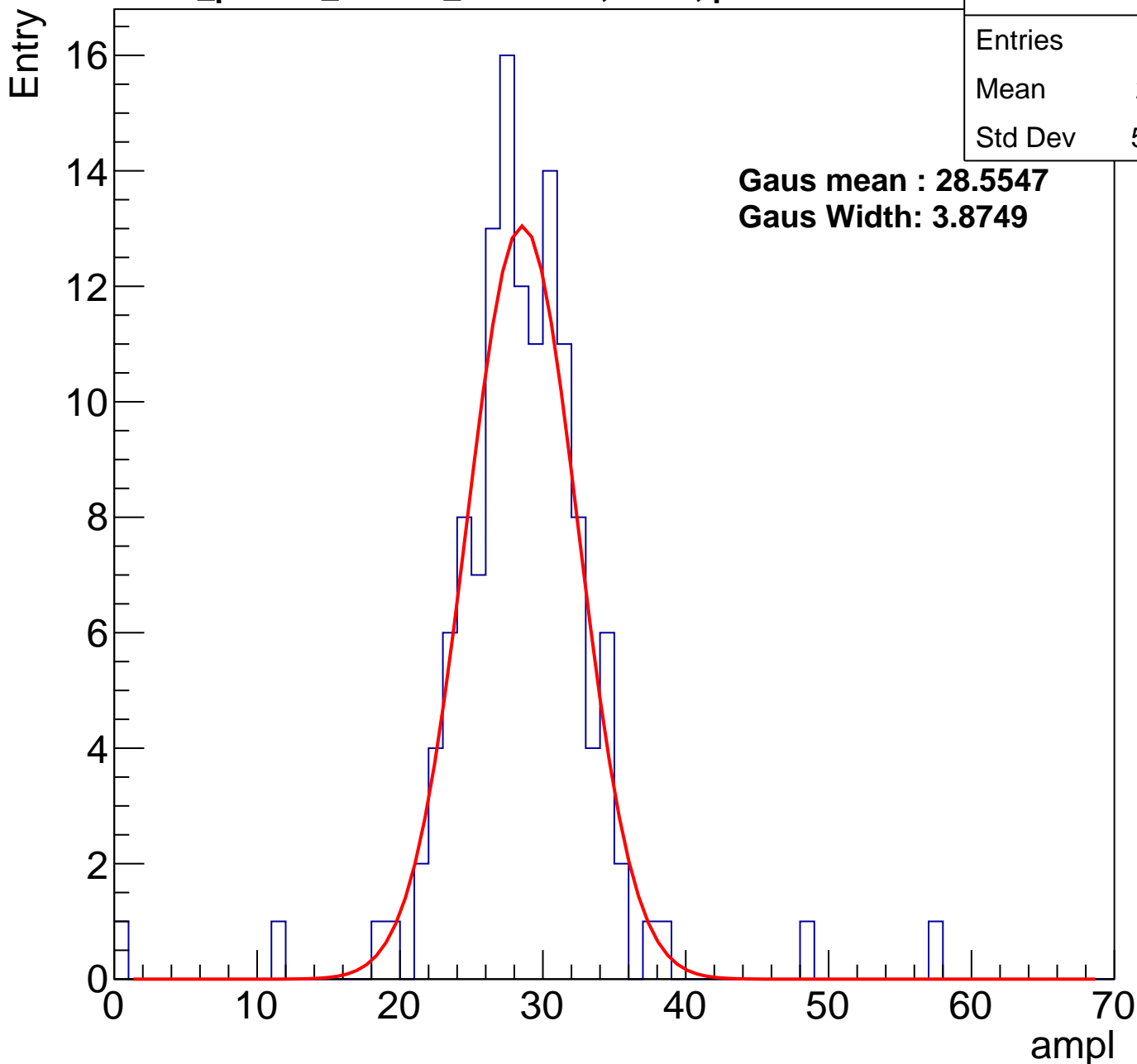
B0L101S, U20-ch112, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	28.11
Std Dev	5.528

Gaus mean : 28.5547

Gaus Width: 3.8749



B0L101S, U20-ch112, adc1

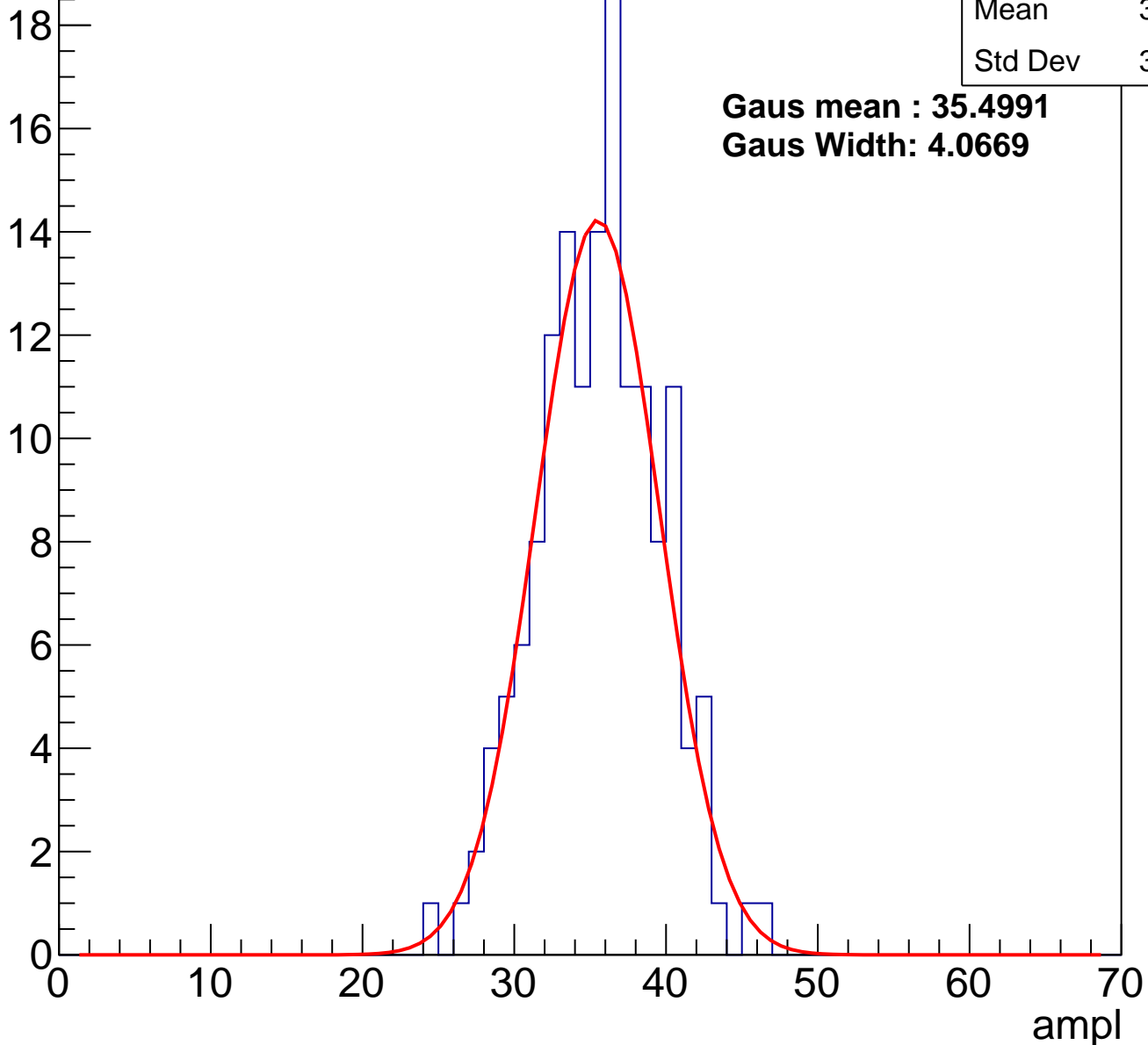
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	35.12
Std Dev	3.997

Gaus mean : 35.4991

Gaus Width: 4.0669

Entry



B0L101S, U20-ch112, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

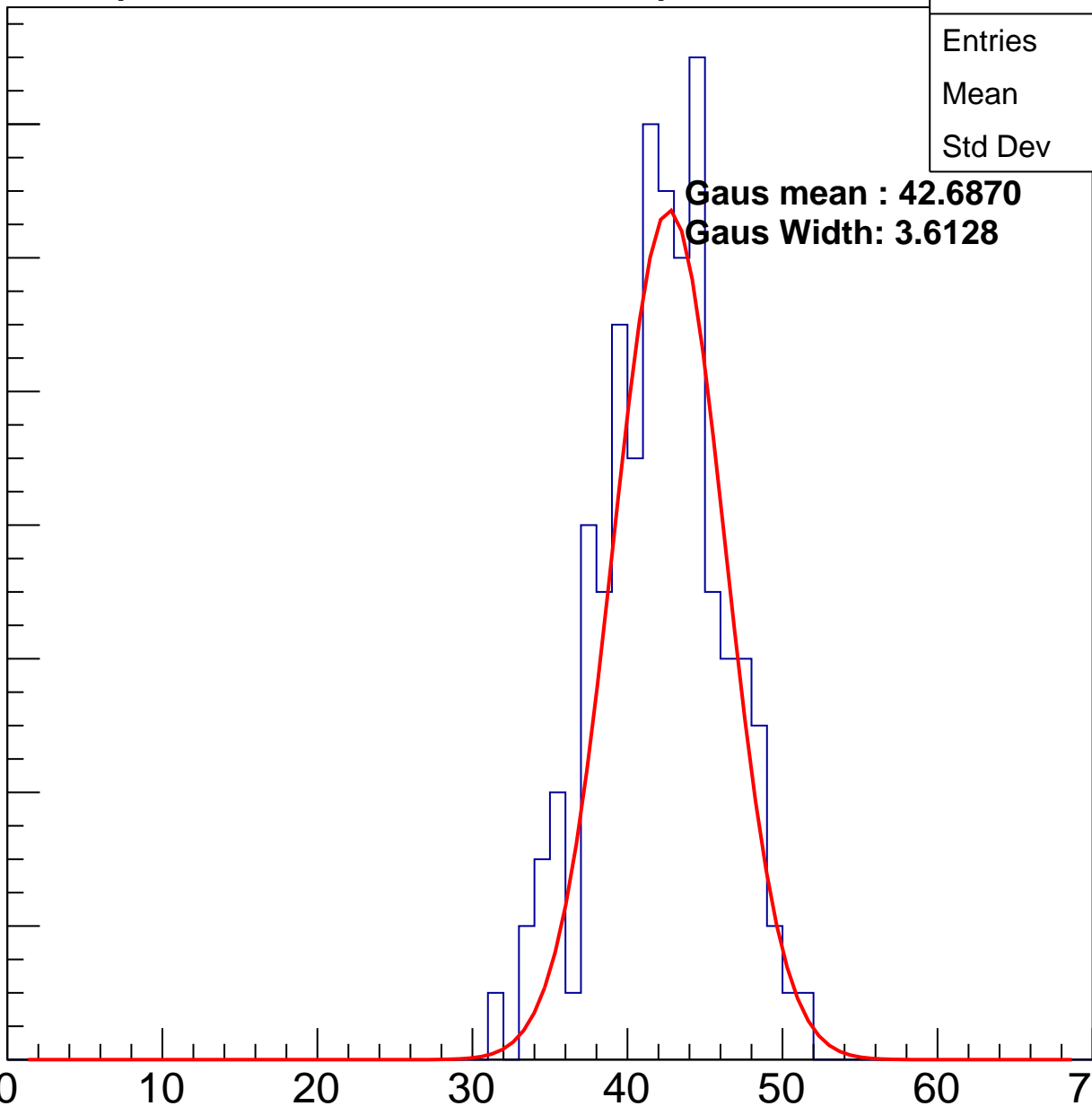
14
12
10
8
6
4
2
0

Entries	128
Mean	41.67
Std Dev	3.911

Gaus mean : 42.6870

Gaus Width: 3.6128

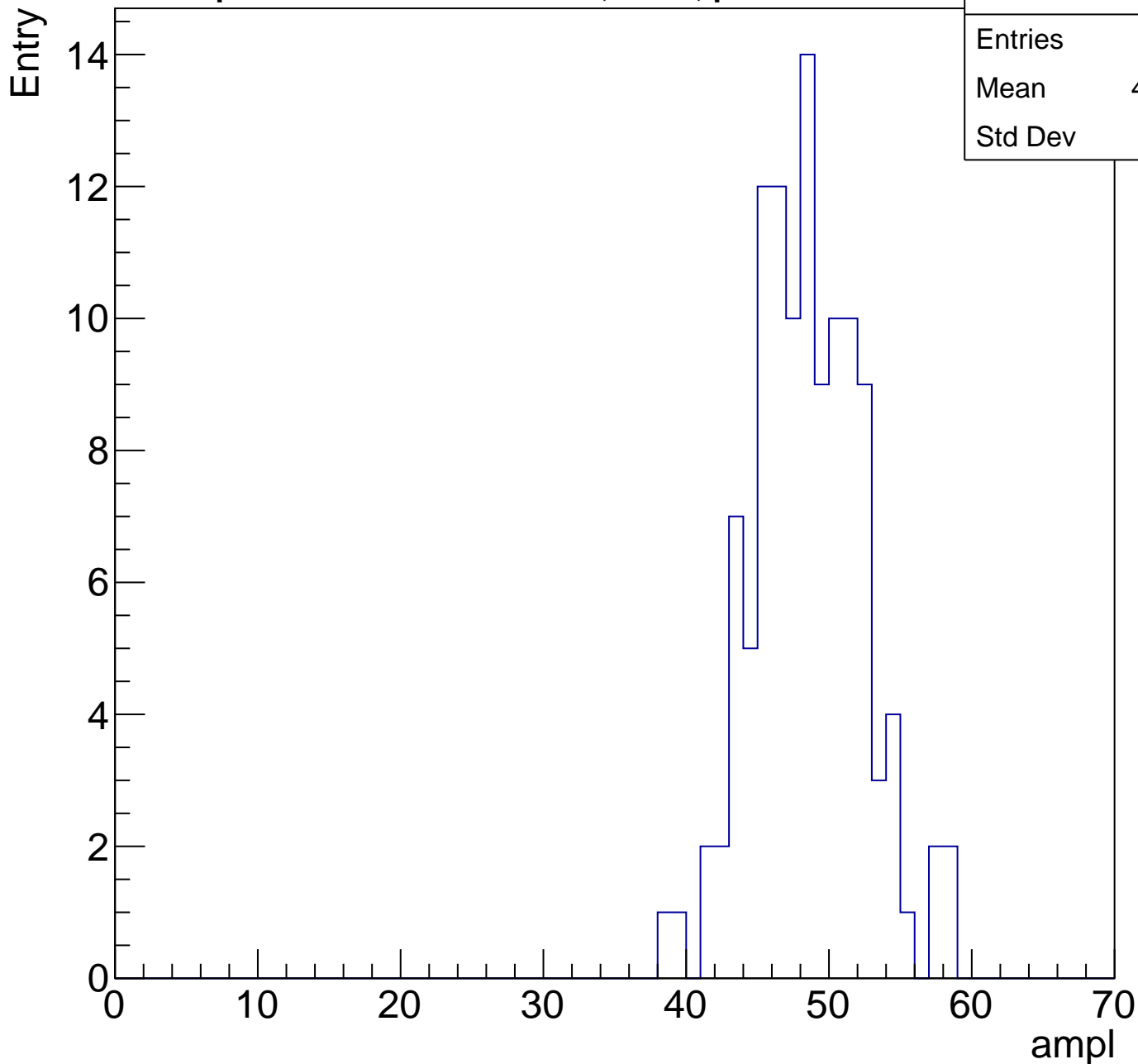
ampl



B0L101S, U20-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

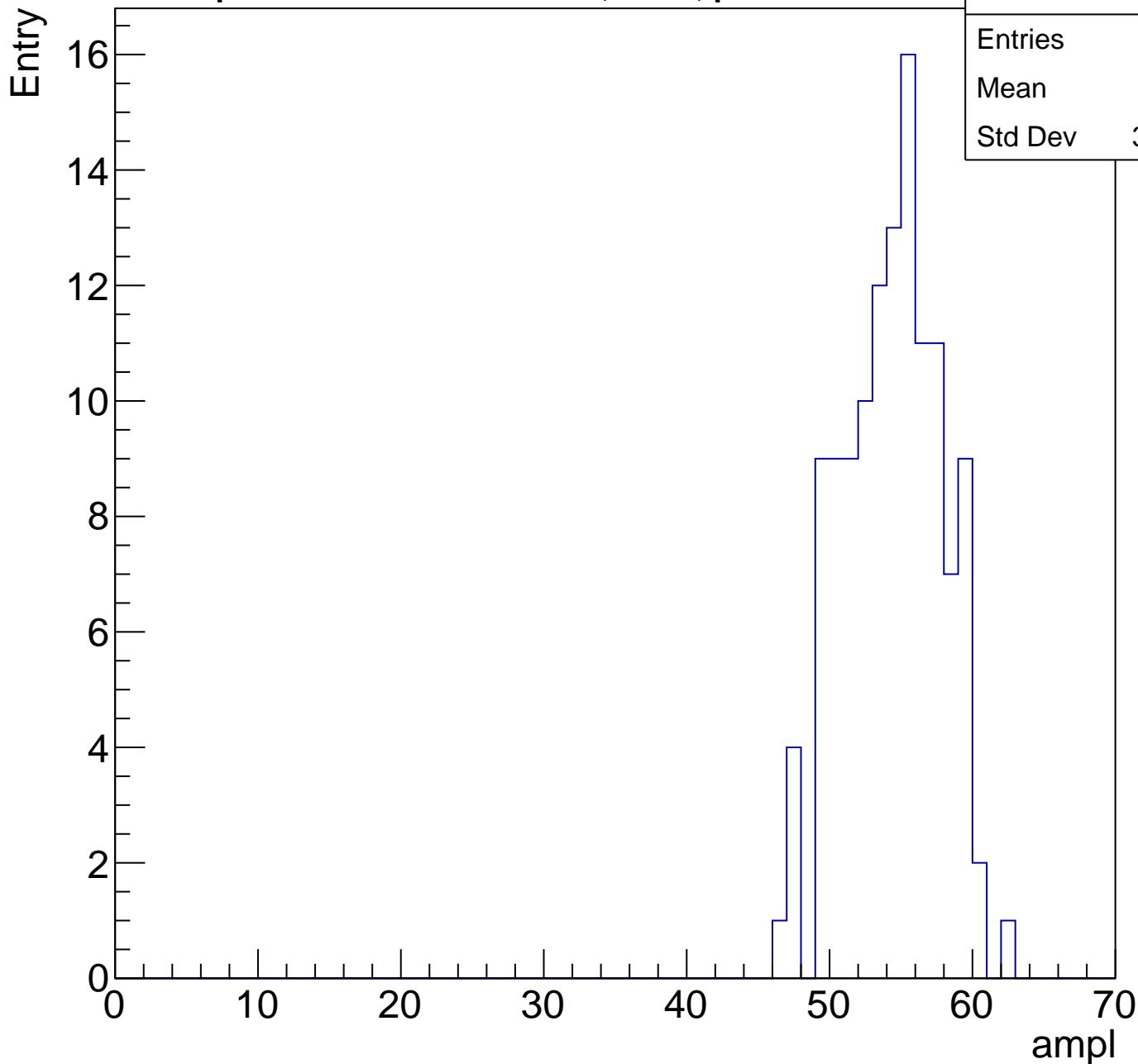
Entries	116
Mean	48.08
Std Dev	3.81



B0L101S, U20-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

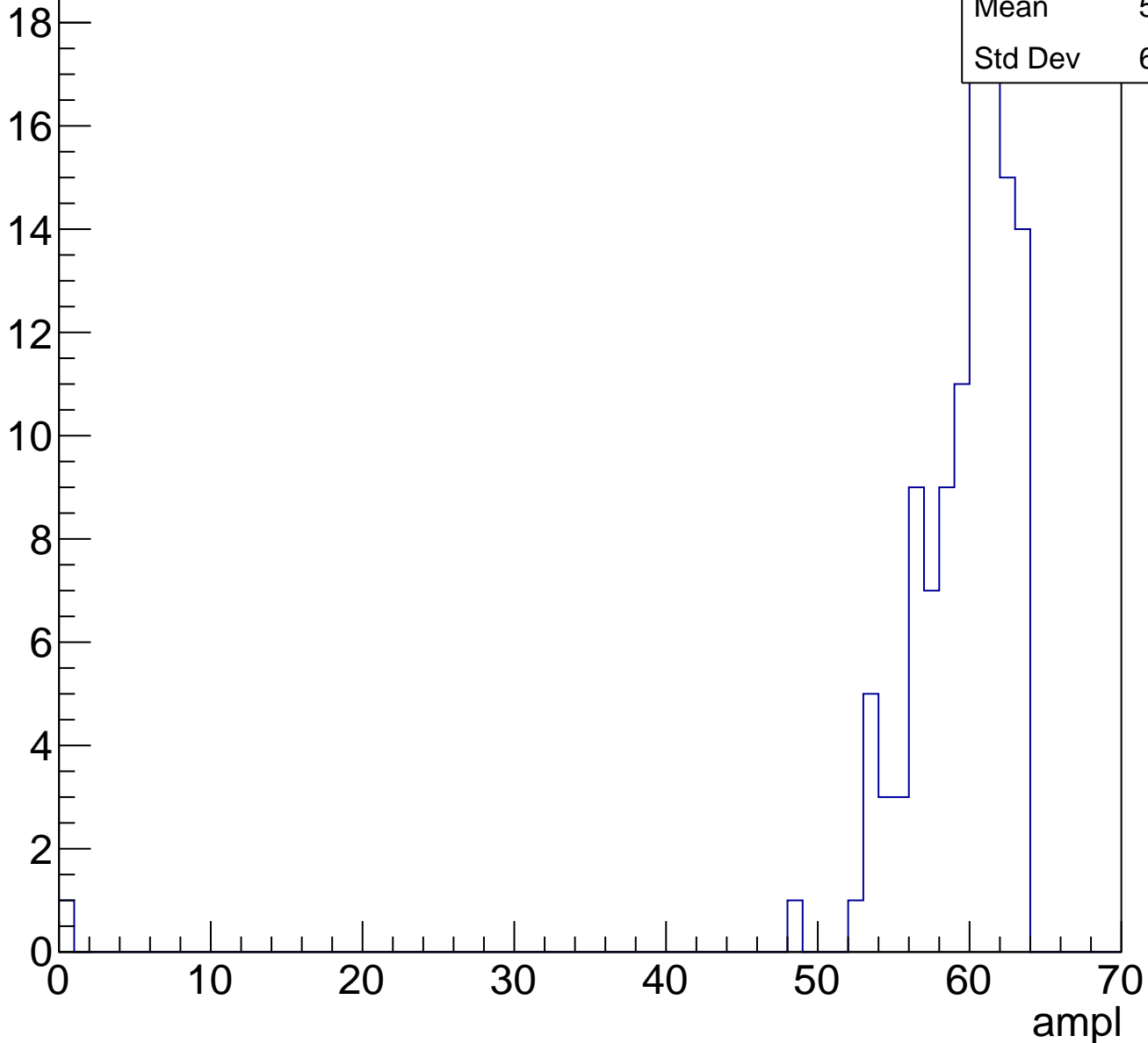
Entries	124
Mean	53.9
Std Dev	3.352



B0L101S, U20-ch112, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

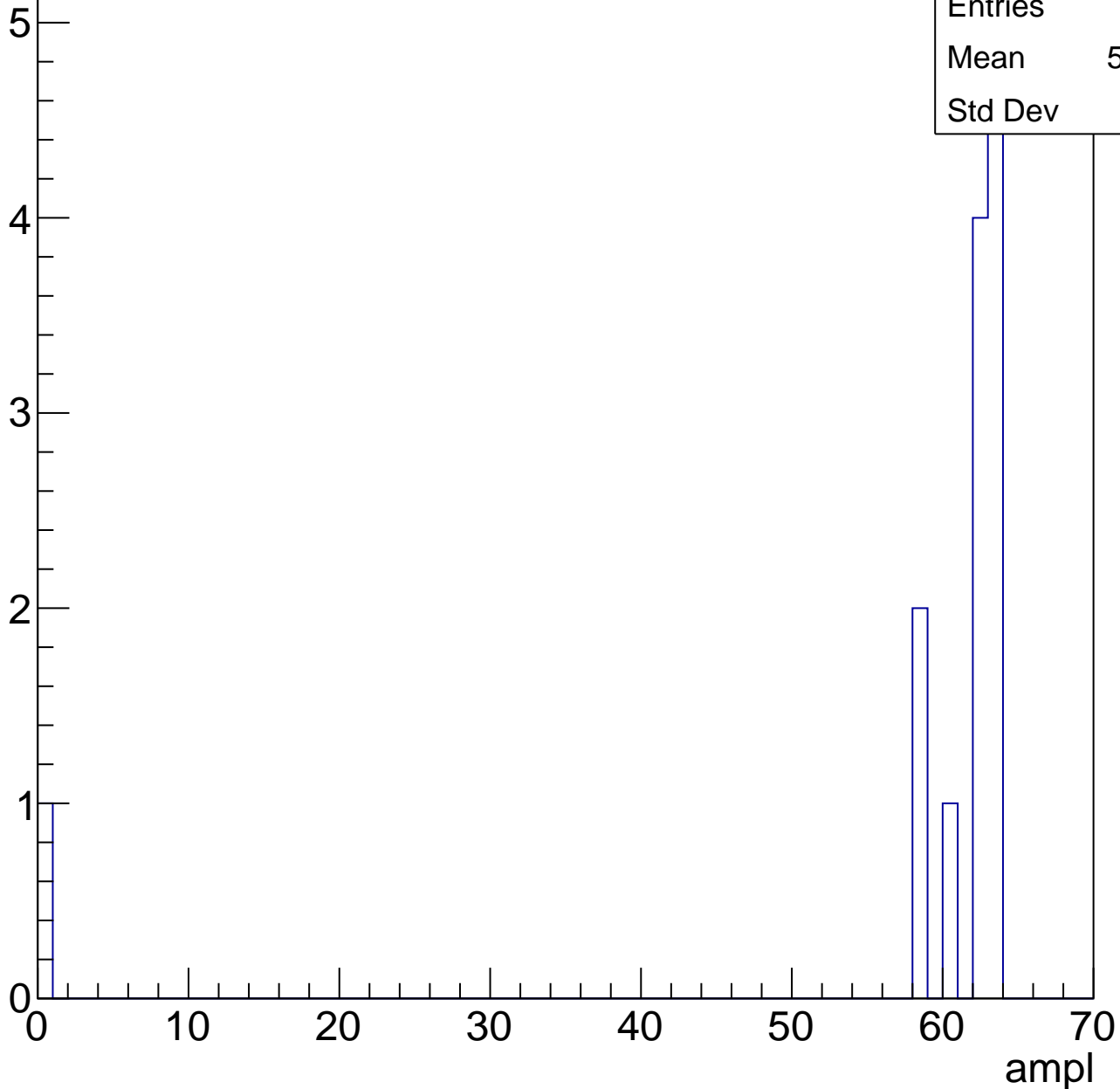


B0L101S, U20-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

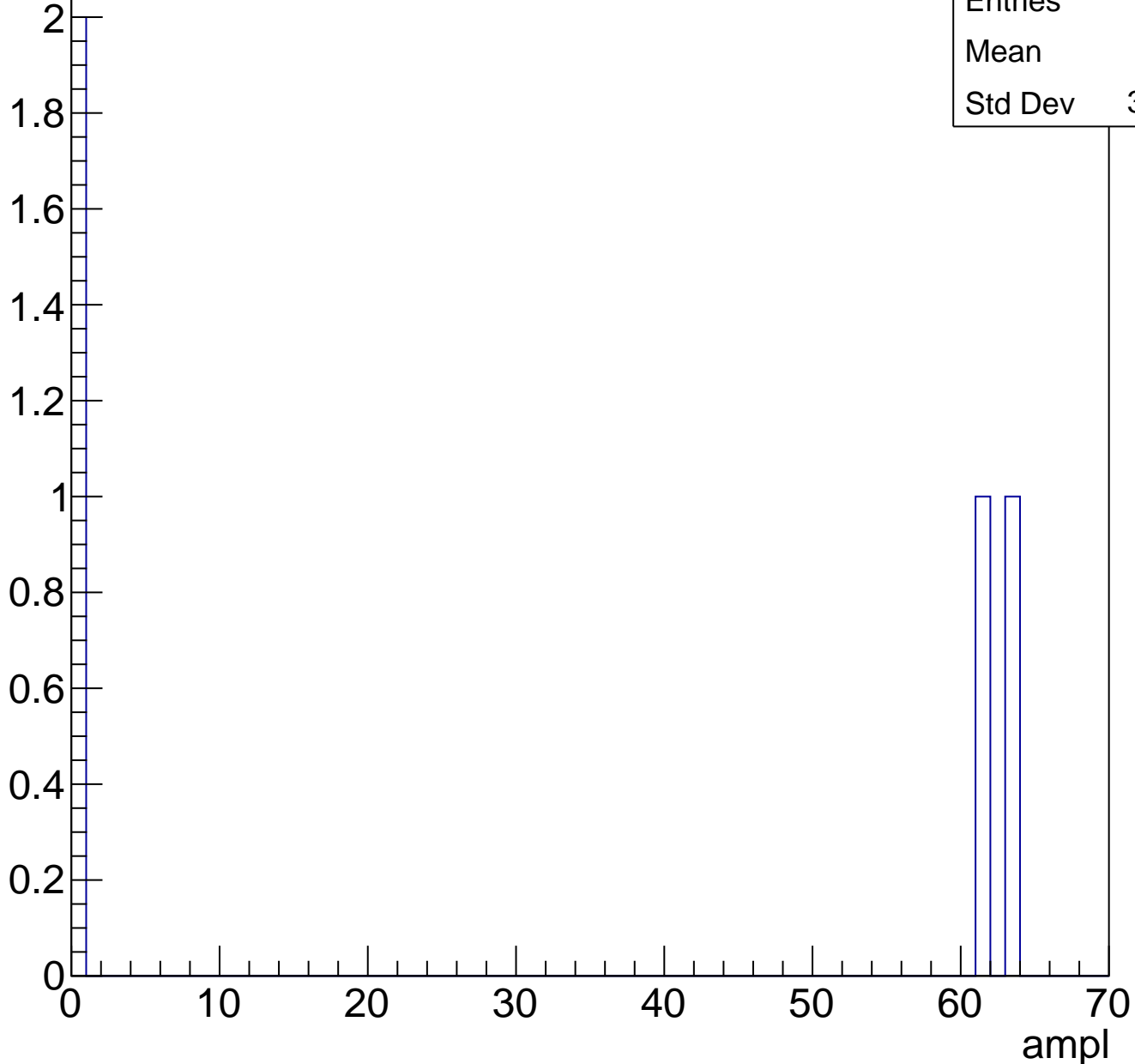
Entries	13
Mean	56.85
Std Dev	16.5



B0L101S, U20-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	31
Std Dev	31.01

B0L101S, U20-ch113, adc0

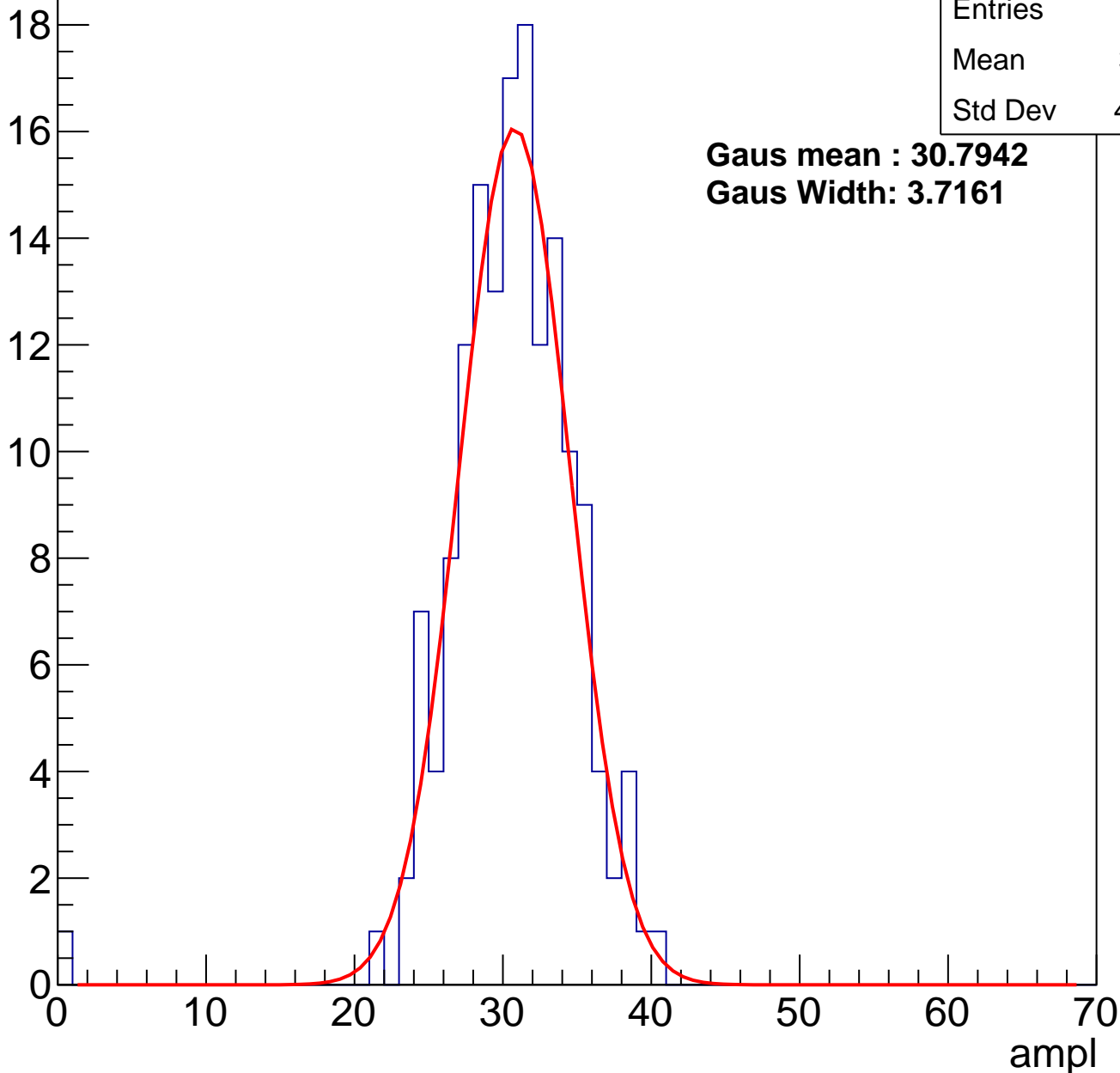
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	30.21
Std Dev	4.392

Gaus mean : 30.7942

Gaus Width: 3.7161

Entry



B0L101S, U20-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	38.15
Std Dev	3.687

Gaus mean : 38.2415

Gaus Width: 3.8896

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

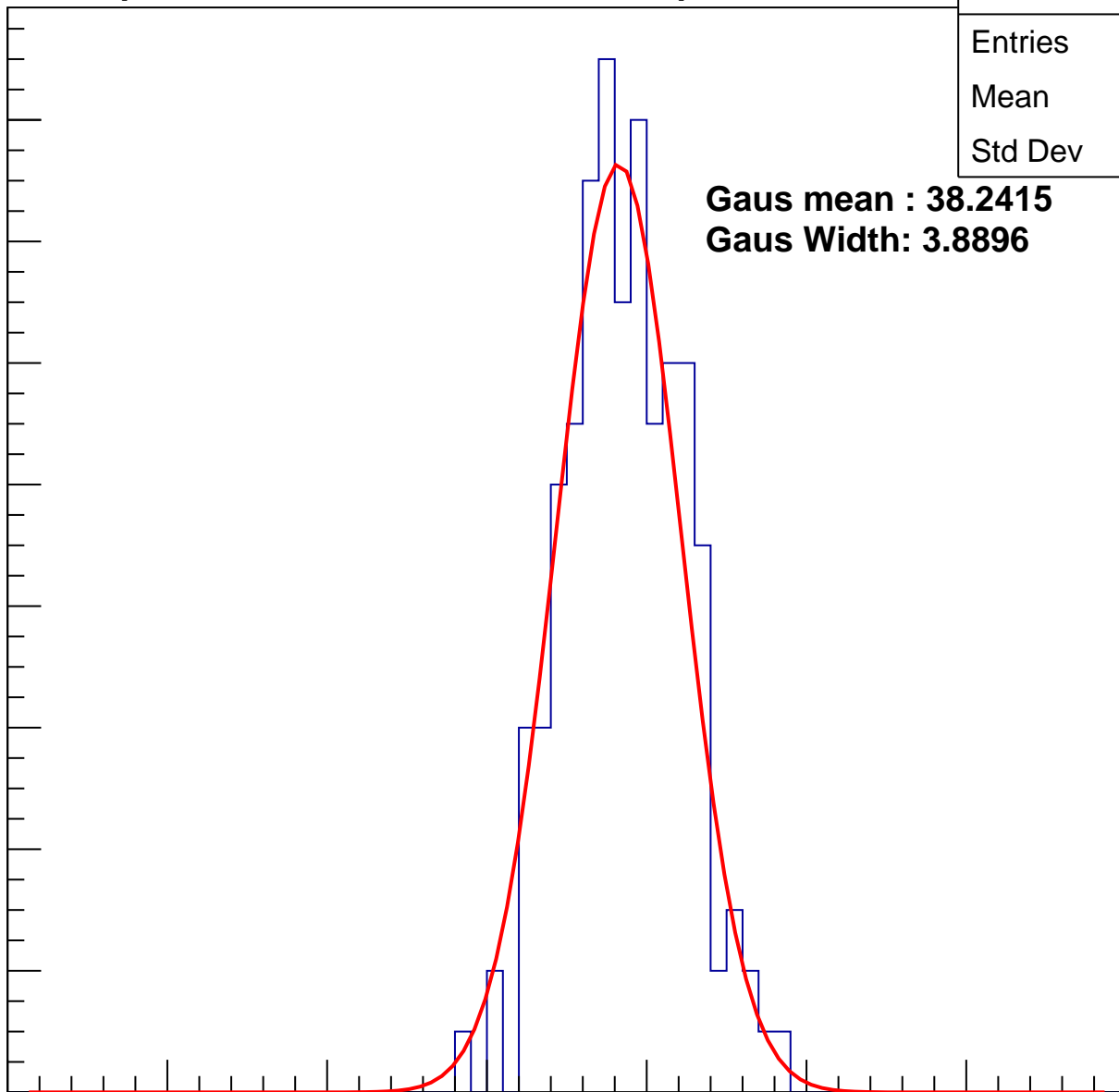
40

50

60

70

ampl



B0L101S, U20-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	45.13
Std Dev	3.642

Gaus mean : 45.6218

Gaus Width: 3.5704

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

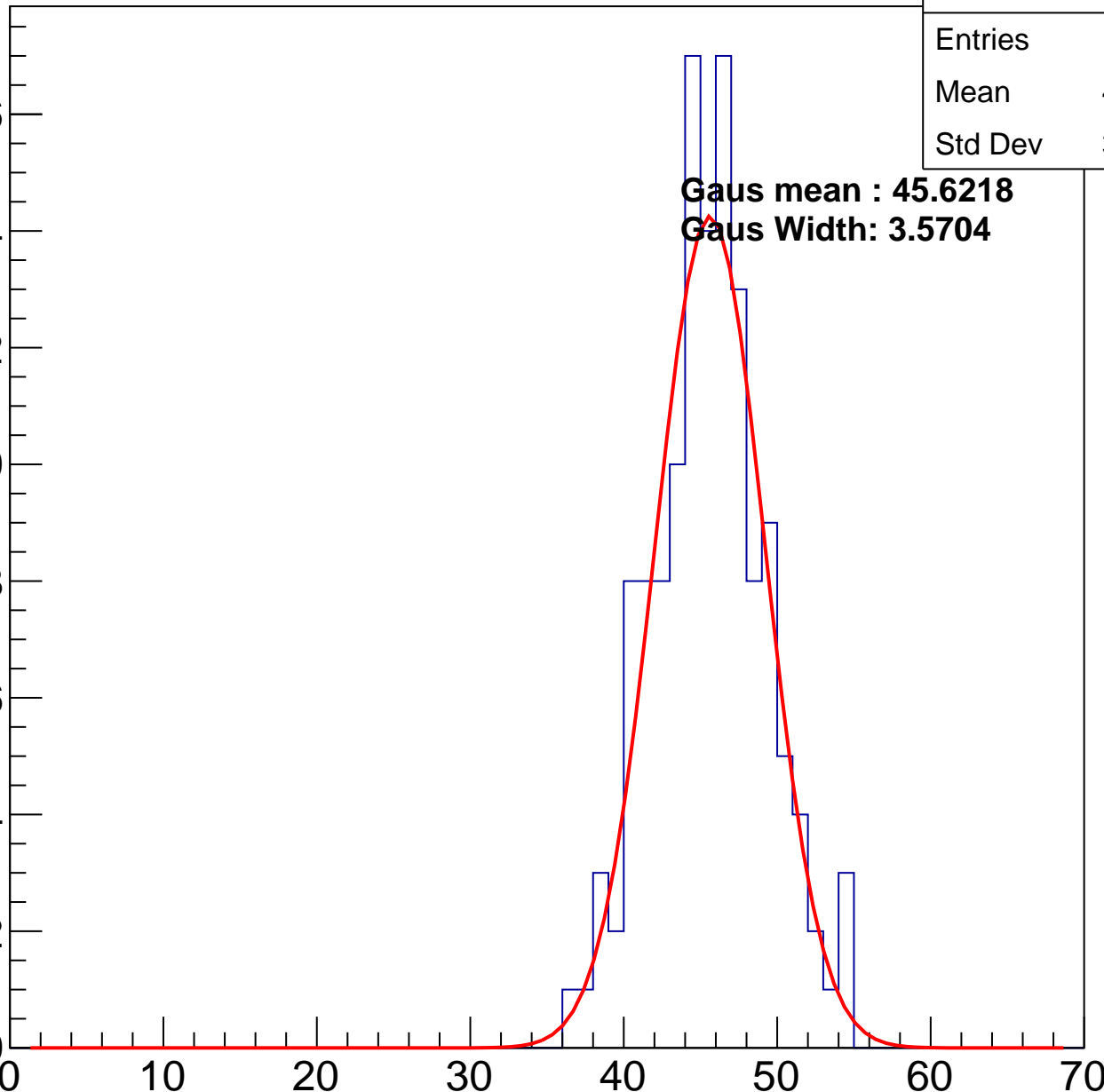
40

50

60

ampl

70



B0L101S, U20-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

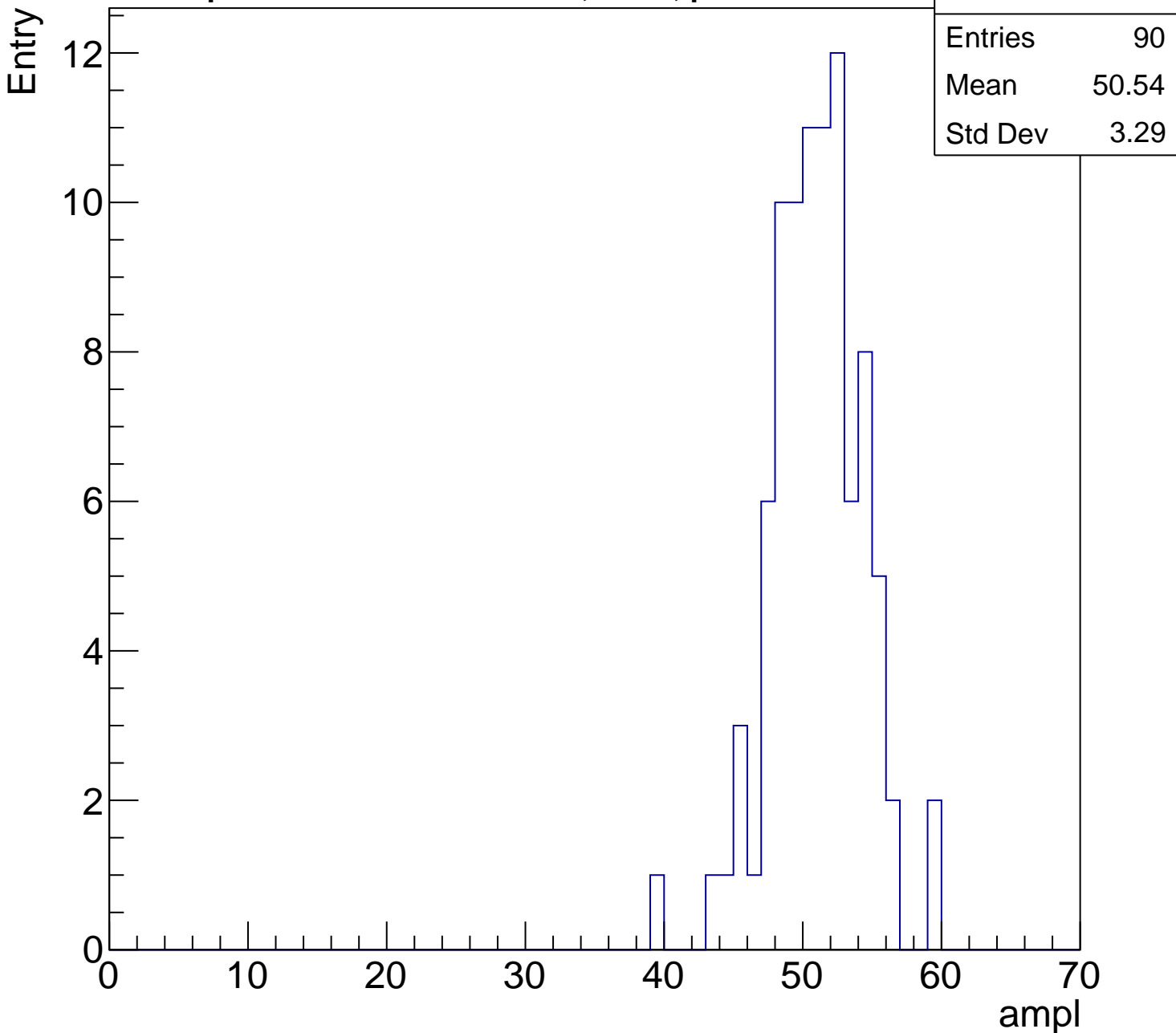
Entries	90
Mean	50.54
Std Dev	3.29

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch113, adc4

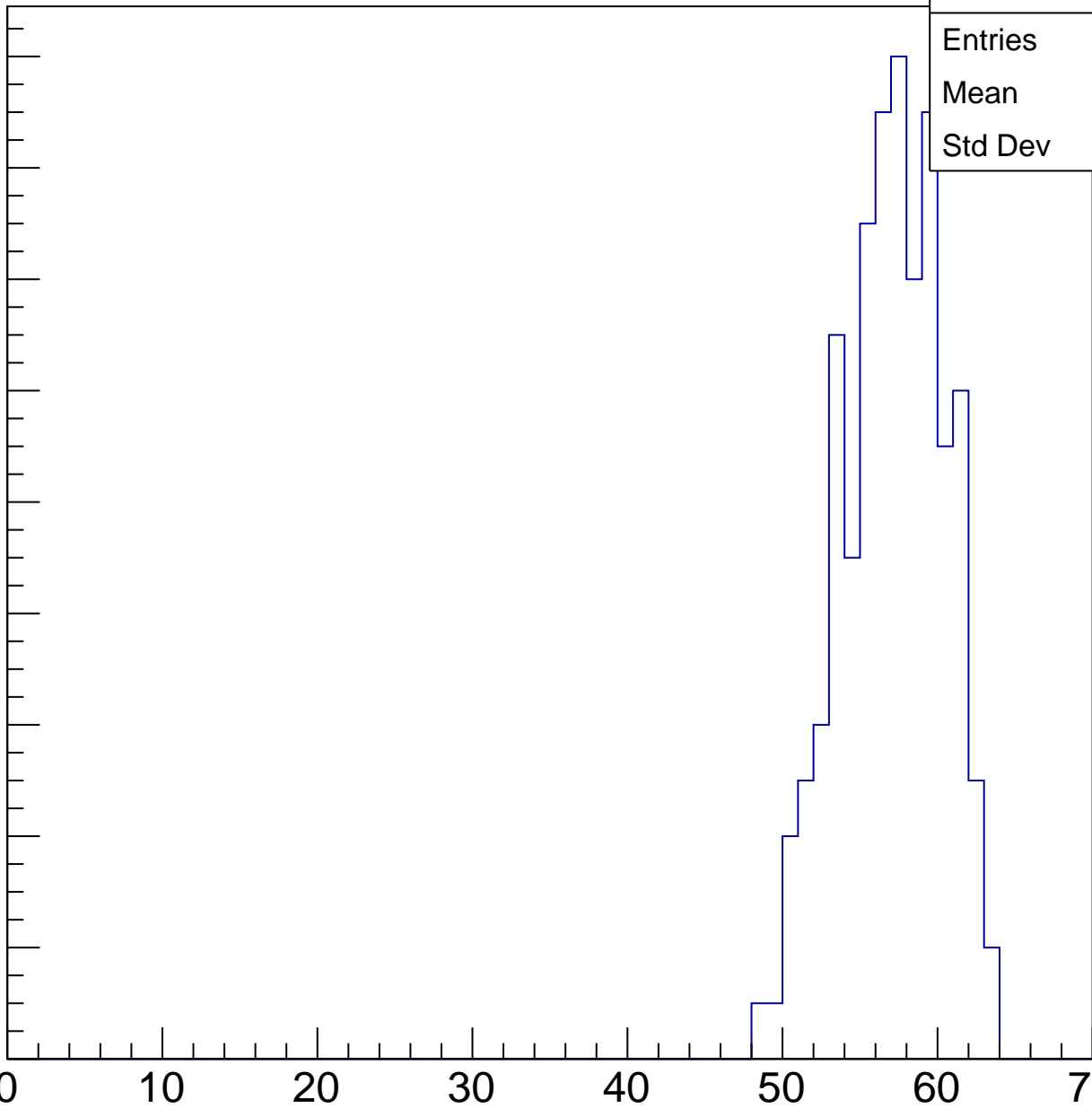
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	150
Mean	56.57
Std Dev	3.244

ampl



B0L101S, U20-ch113, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

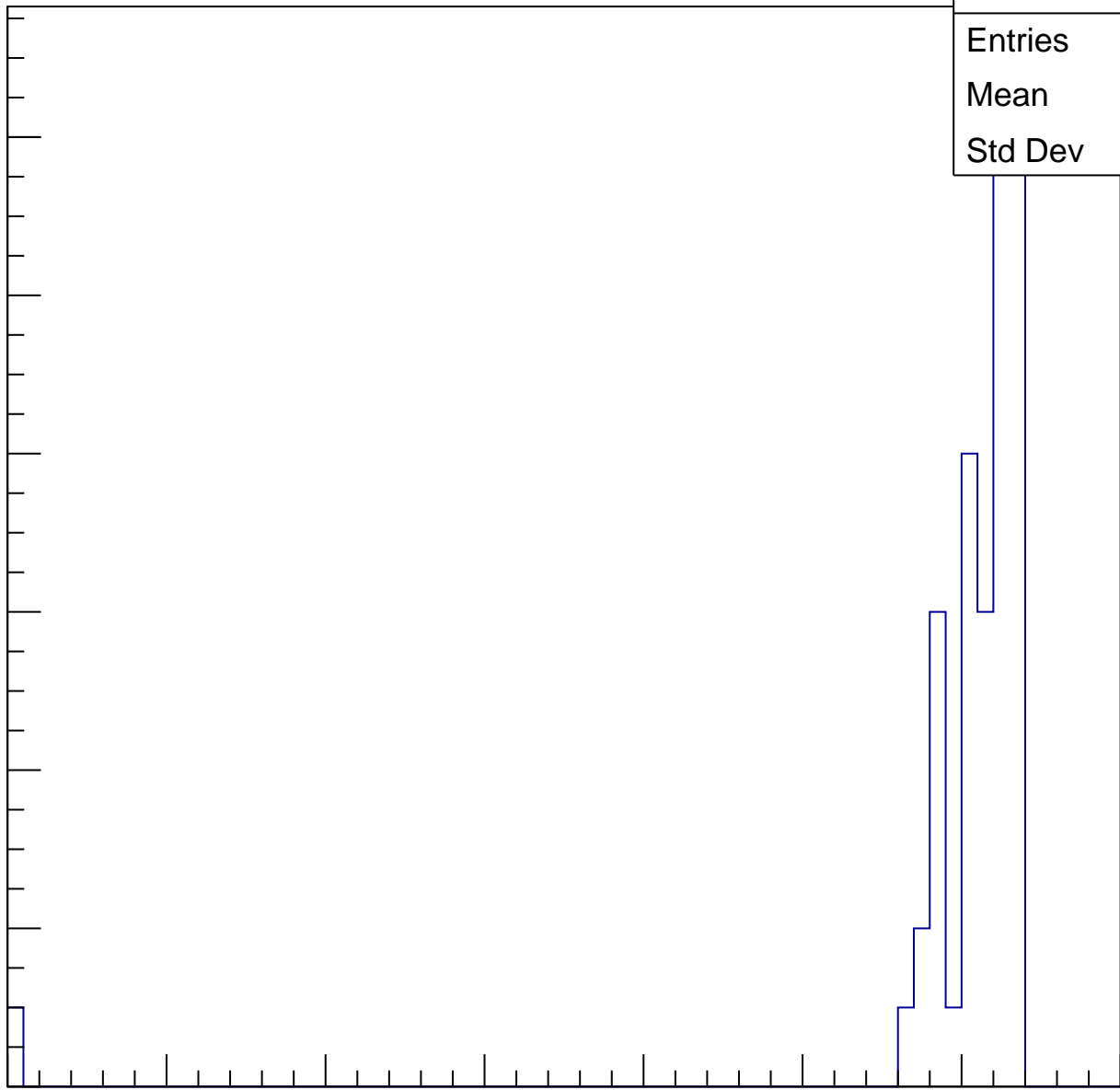
2

0

Entries	50
Mean	59.72
Std Dev	8.748

ampl

0 10 20 30 40 50 60 70

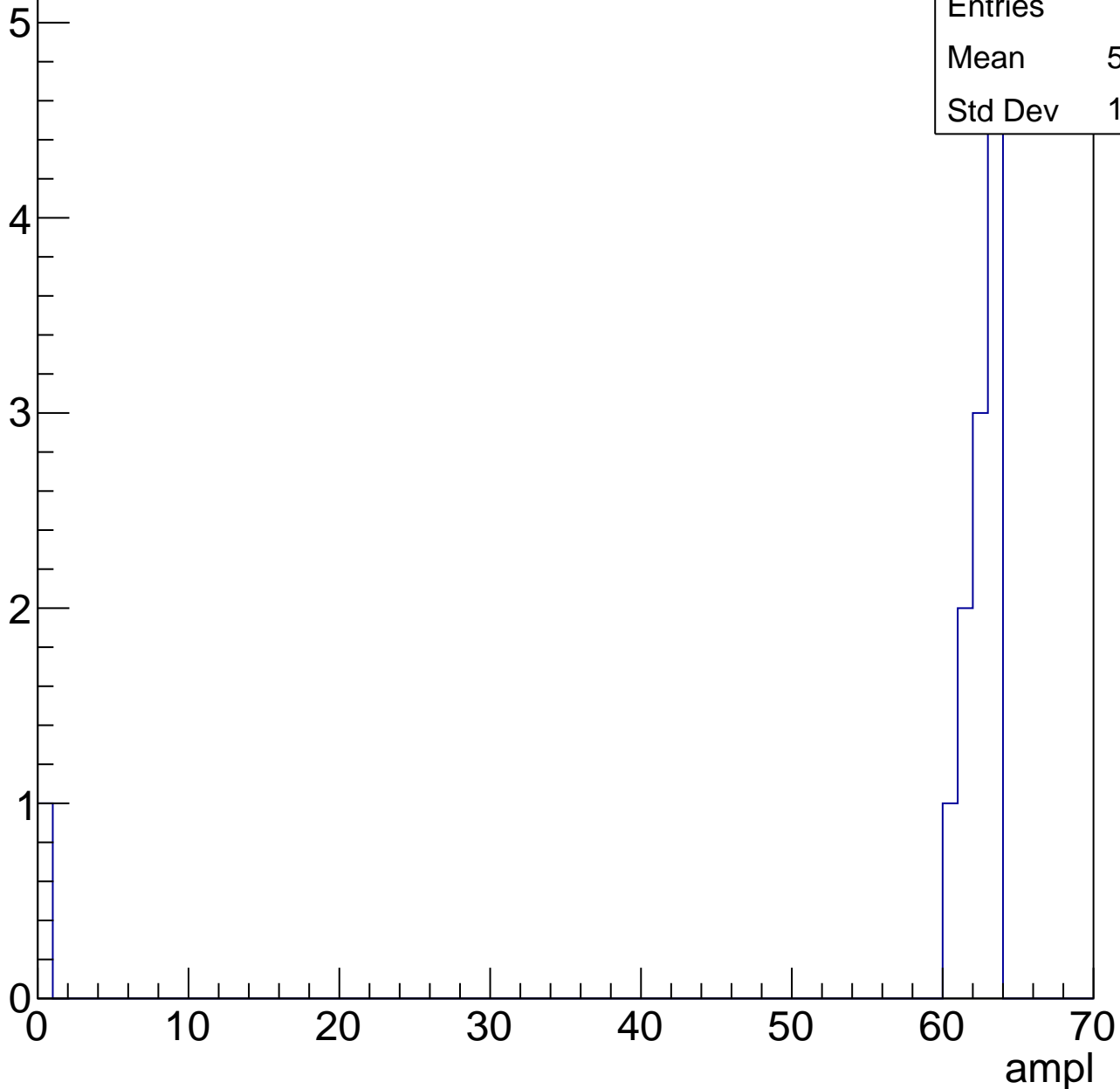


B0L101S, U20-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.92
Std Dev	17.19



B0L101S, U20-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch114, adc0

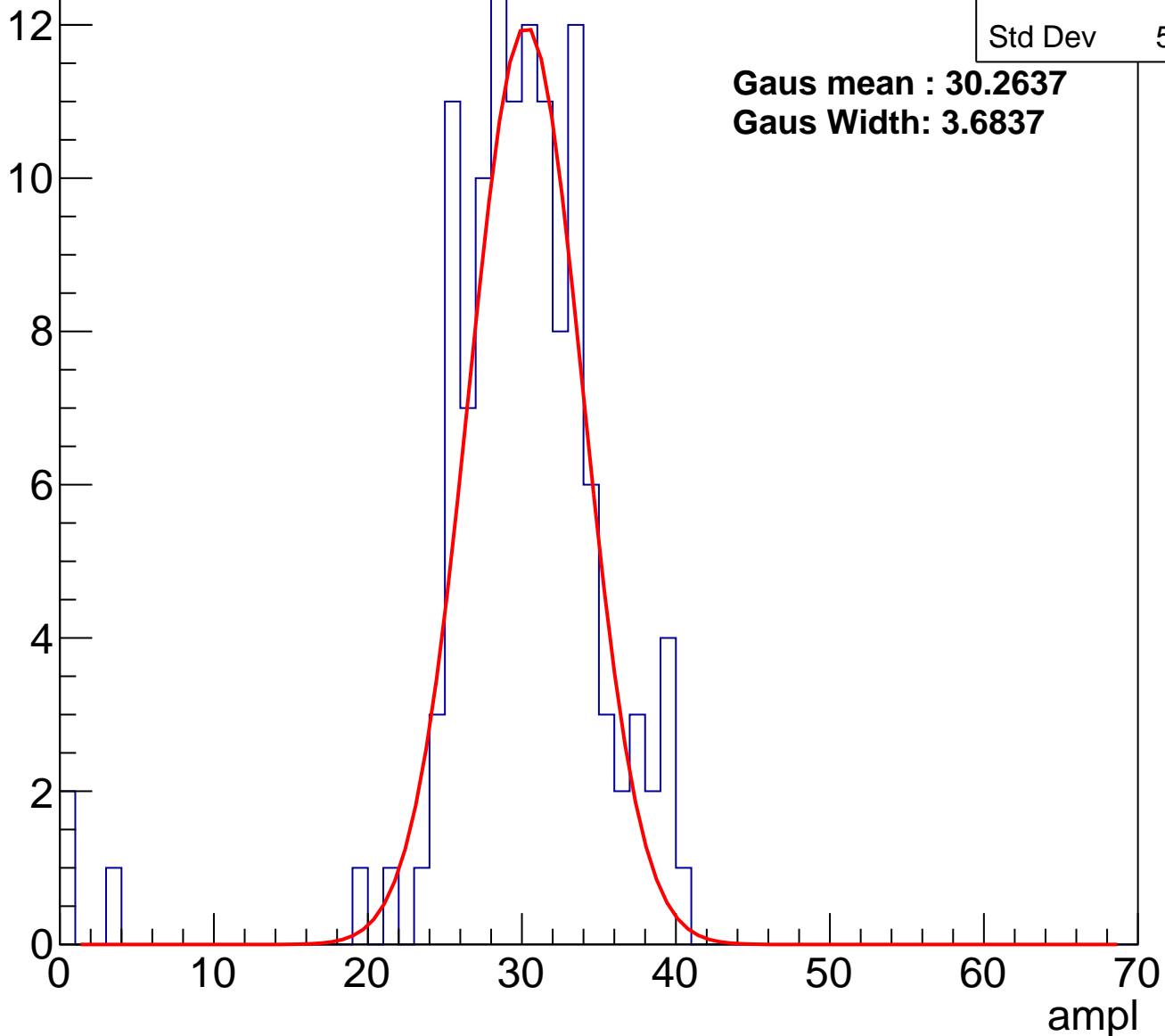
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	29.32
Std Dev	5.975

Gaus mean : 30.2637

Gaus Width: 3.6837

Entry



B0L101S, U20-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.22
Std Dev	3.963

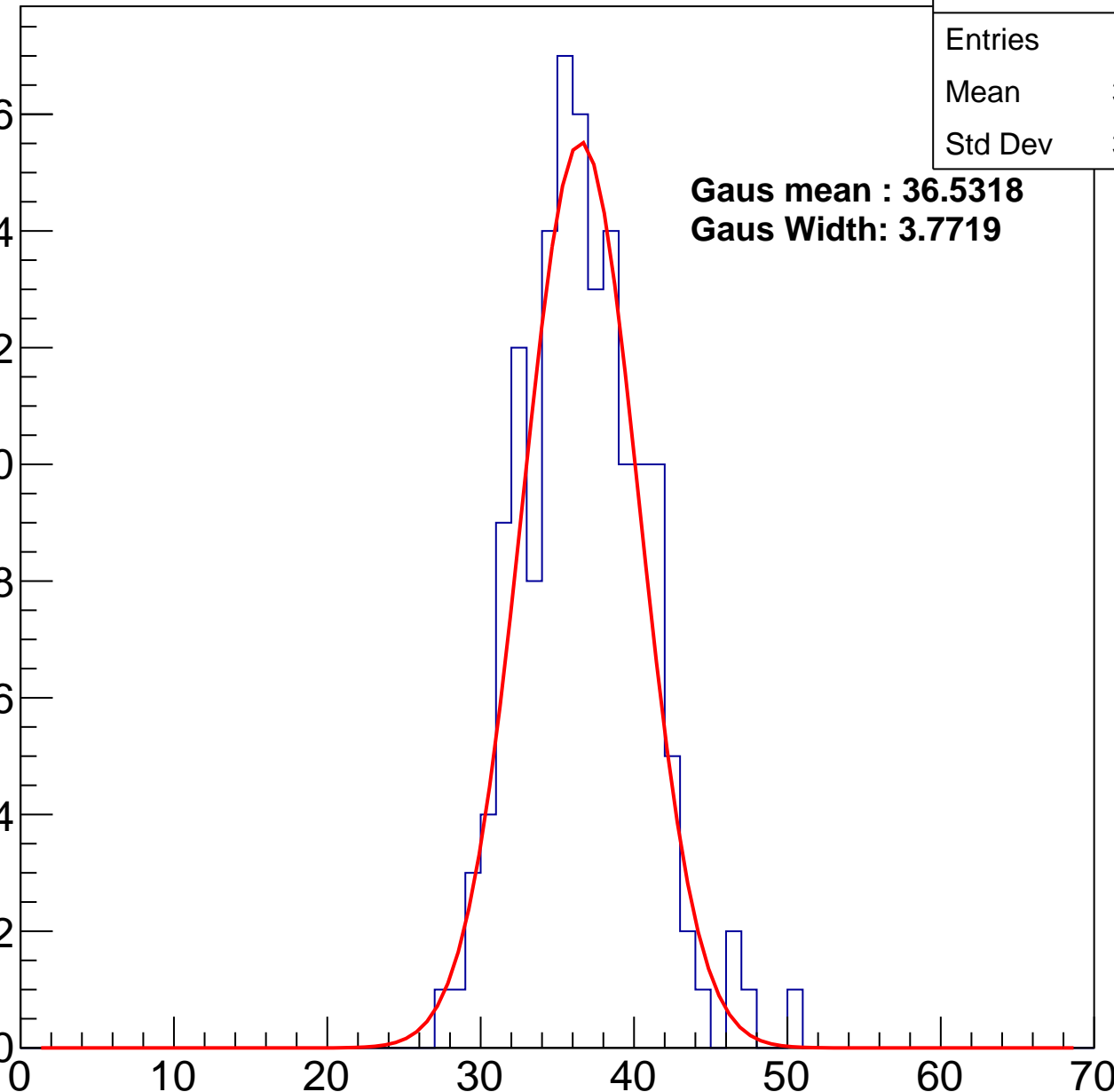
Gaus mean : 36.5318

Gaus Width: 3.7719

Entry

16
14
12
10
8
6
4
2
0

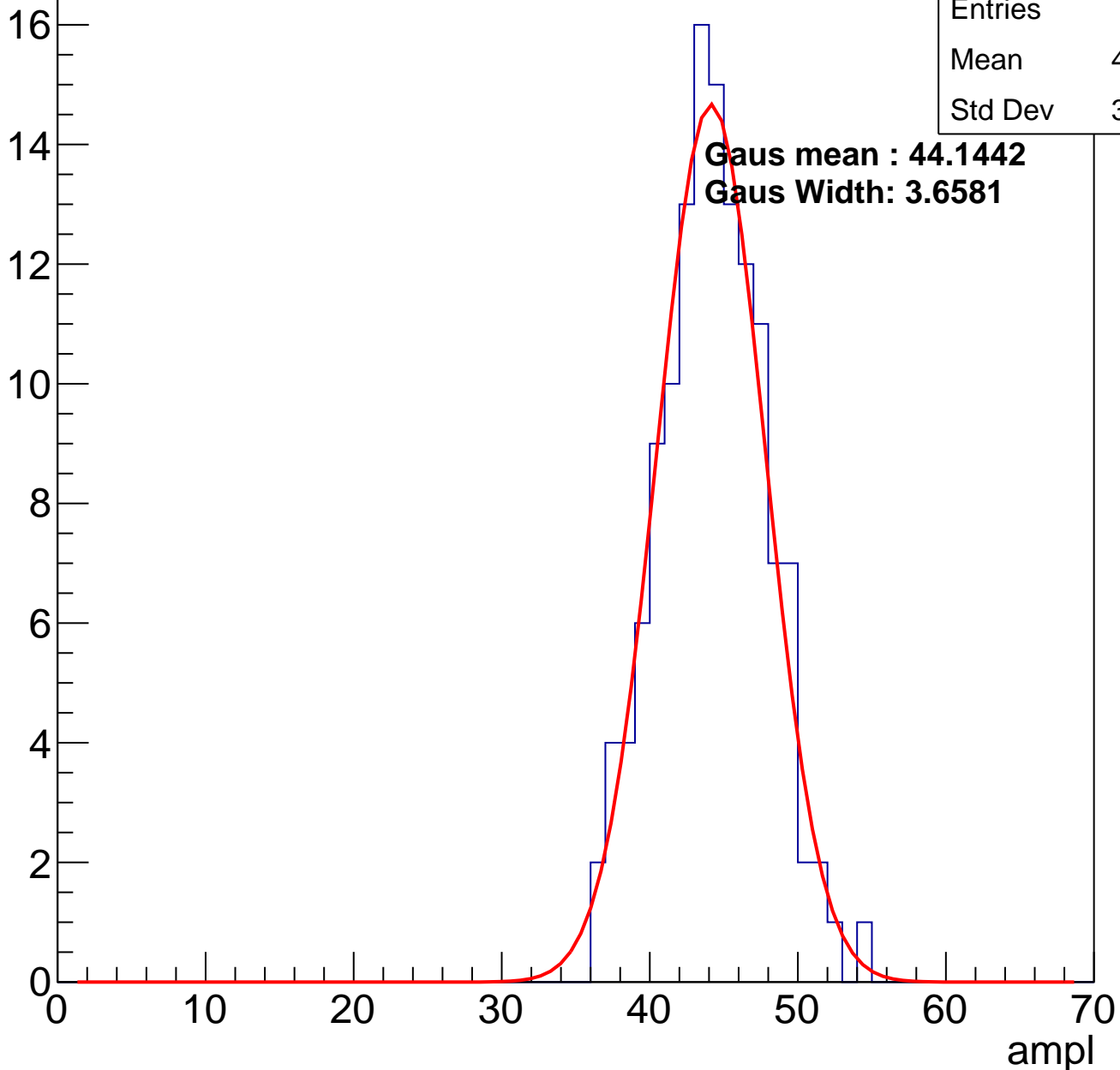
ampl



B0L101S, U20-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

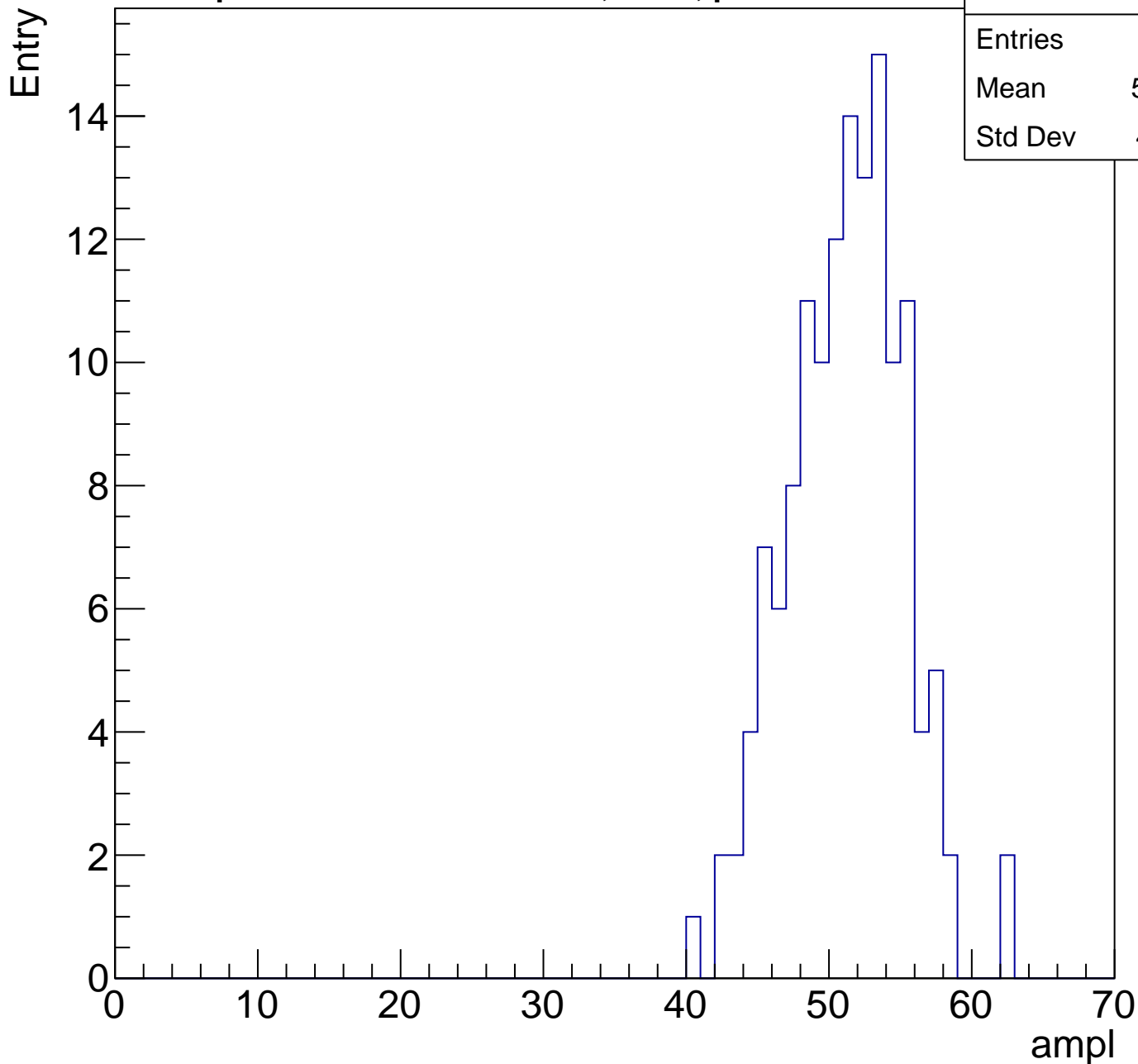
Entry



B0L101S, U20-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.72
Std Dev	4.011



B0L101S, U20-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

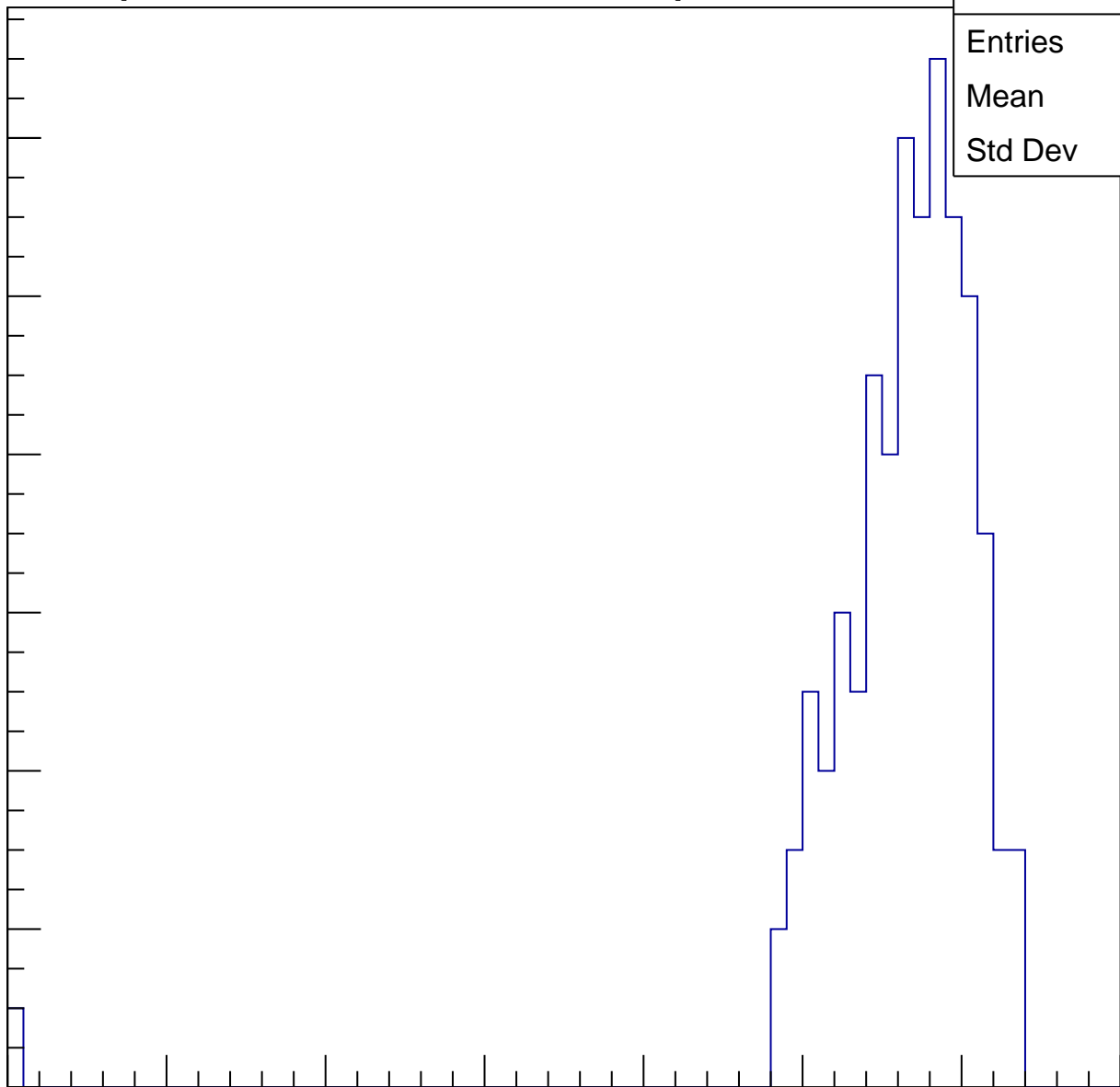
50

60

70

ampl

Entries	113
Mean	55.79
Std Dev	6.396



B0L101S, U20-ch114, adc5

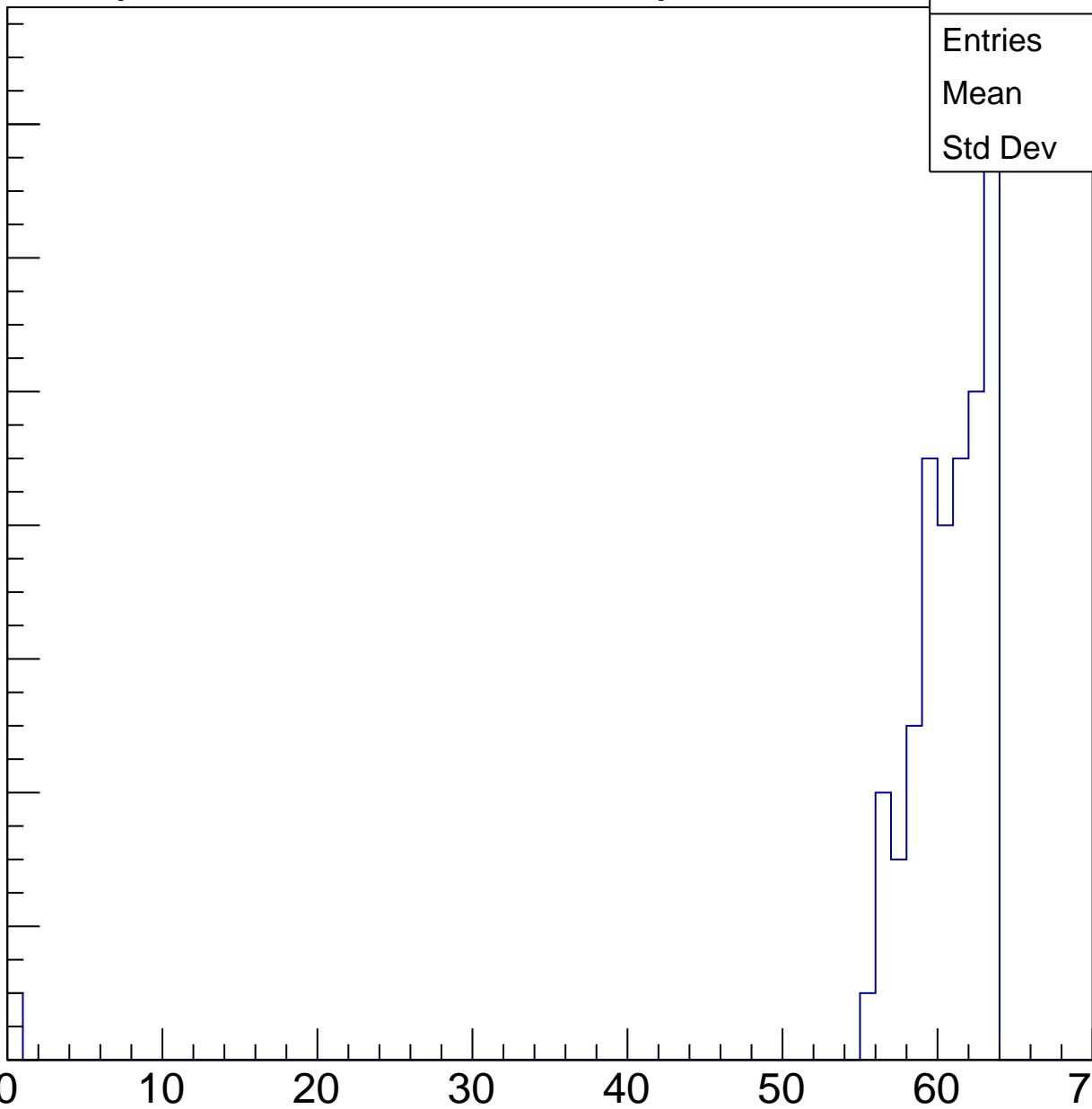
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.46
Std Dev	7.756

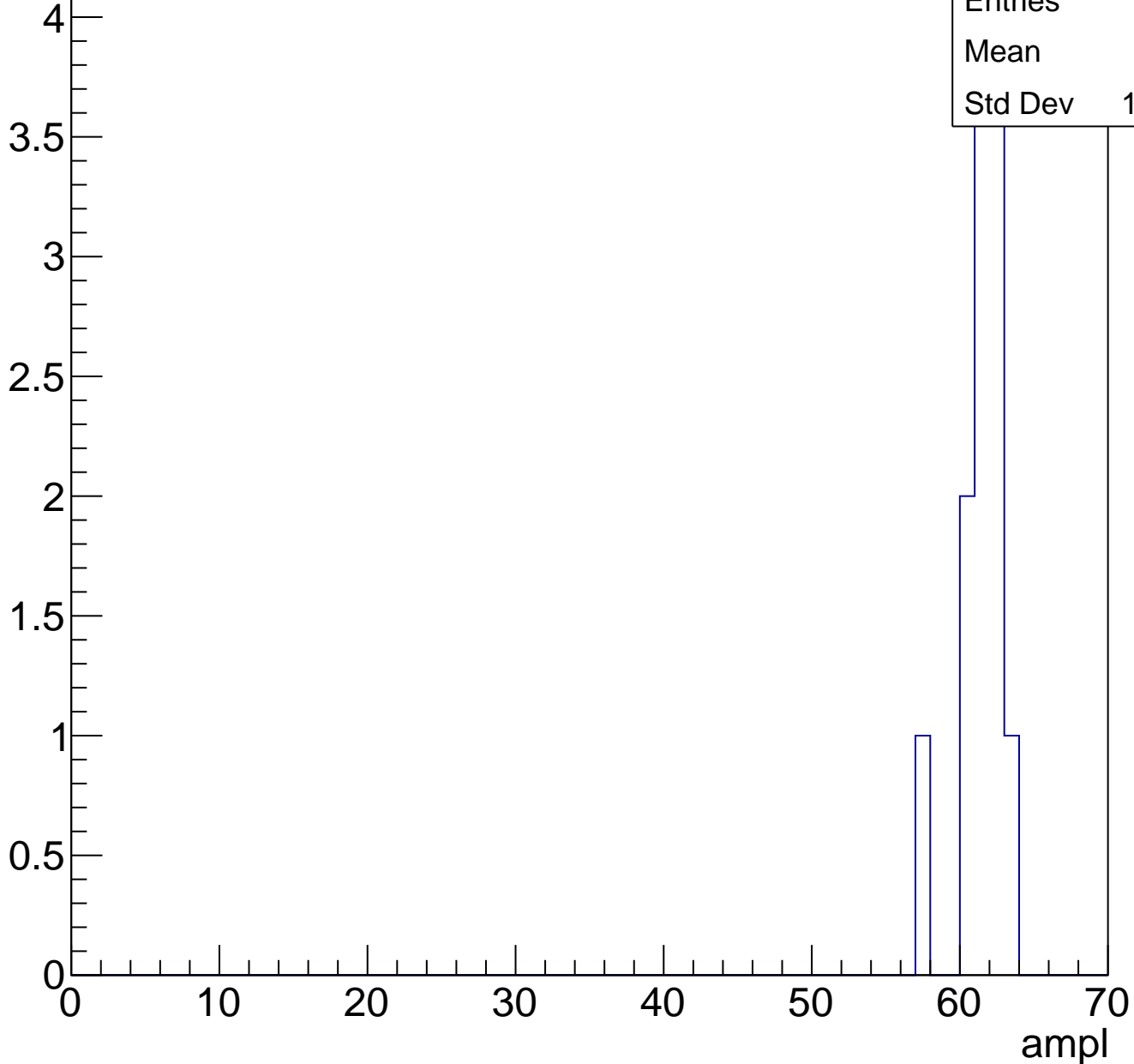
ampl



B0L101S, U20-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

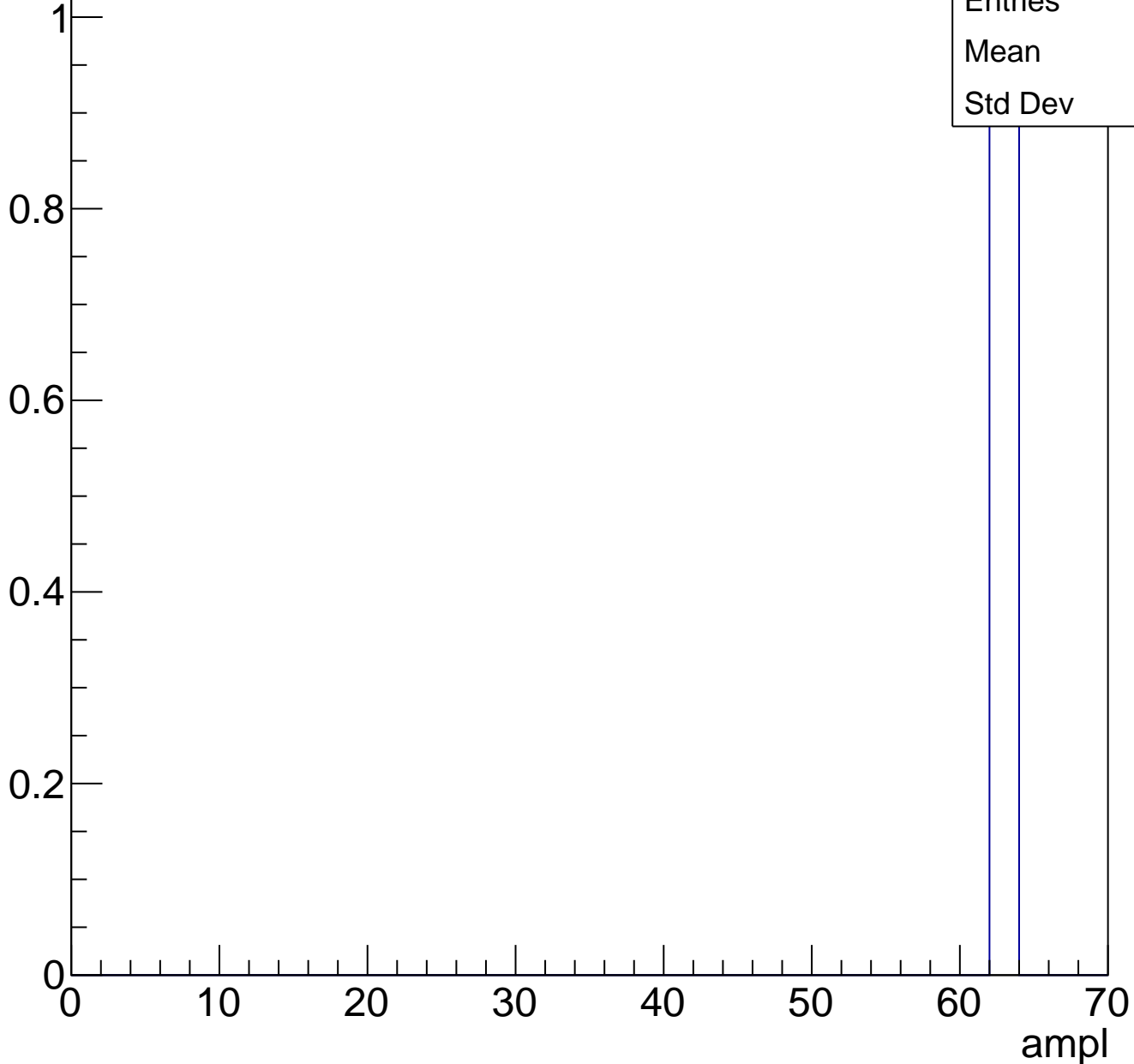
Entry



B0L101S, U20-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch115, adc0

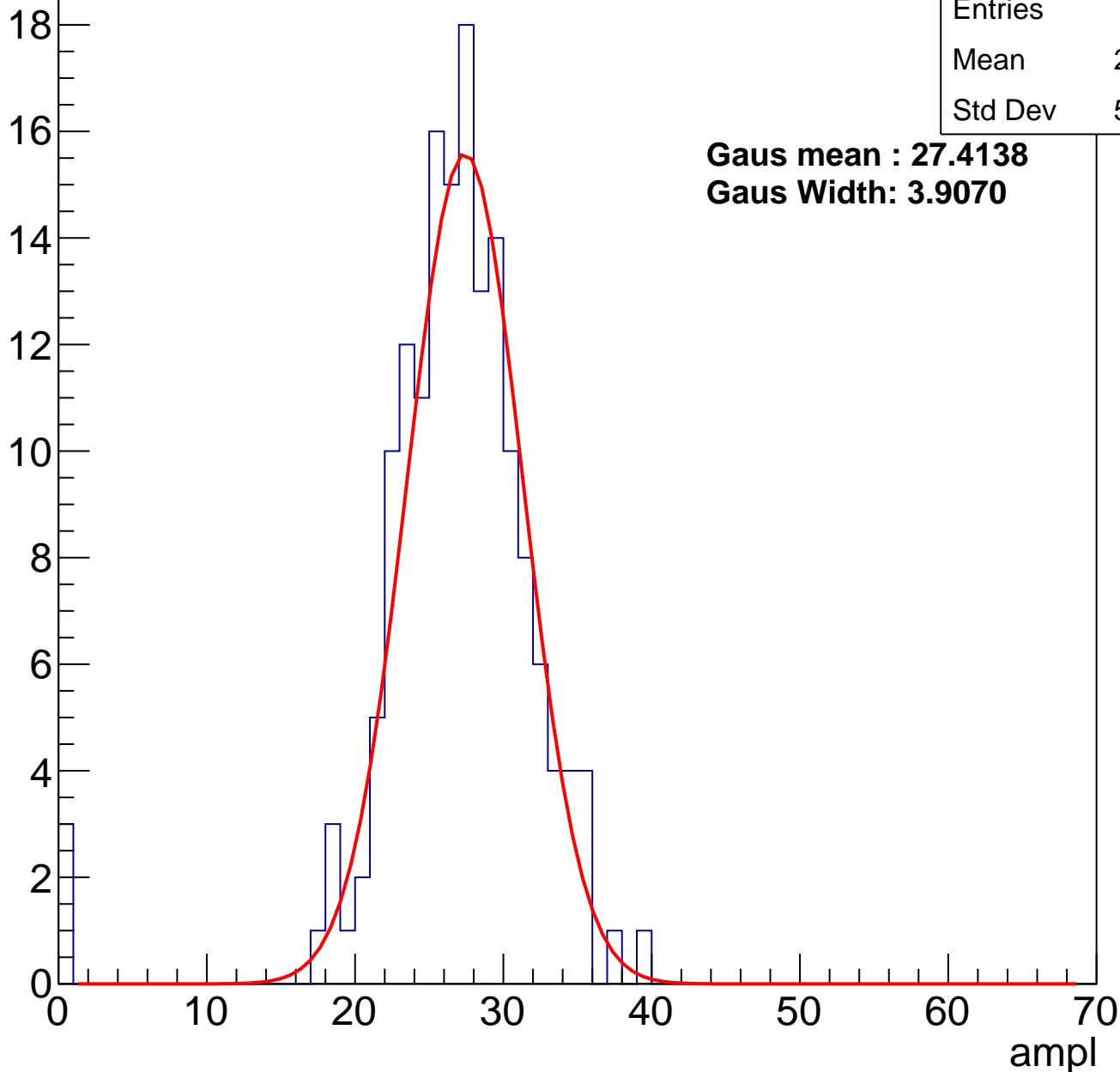
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	26.33
Std Dev	5.384

Gaus mean : 27.4138

Gaus Width: 3.9070

Entry



B0L101S, U20-ch115, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	33.82
Std Dev	3.831

Gaus mean : 34.0368

Gaus Width: 3.8420

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

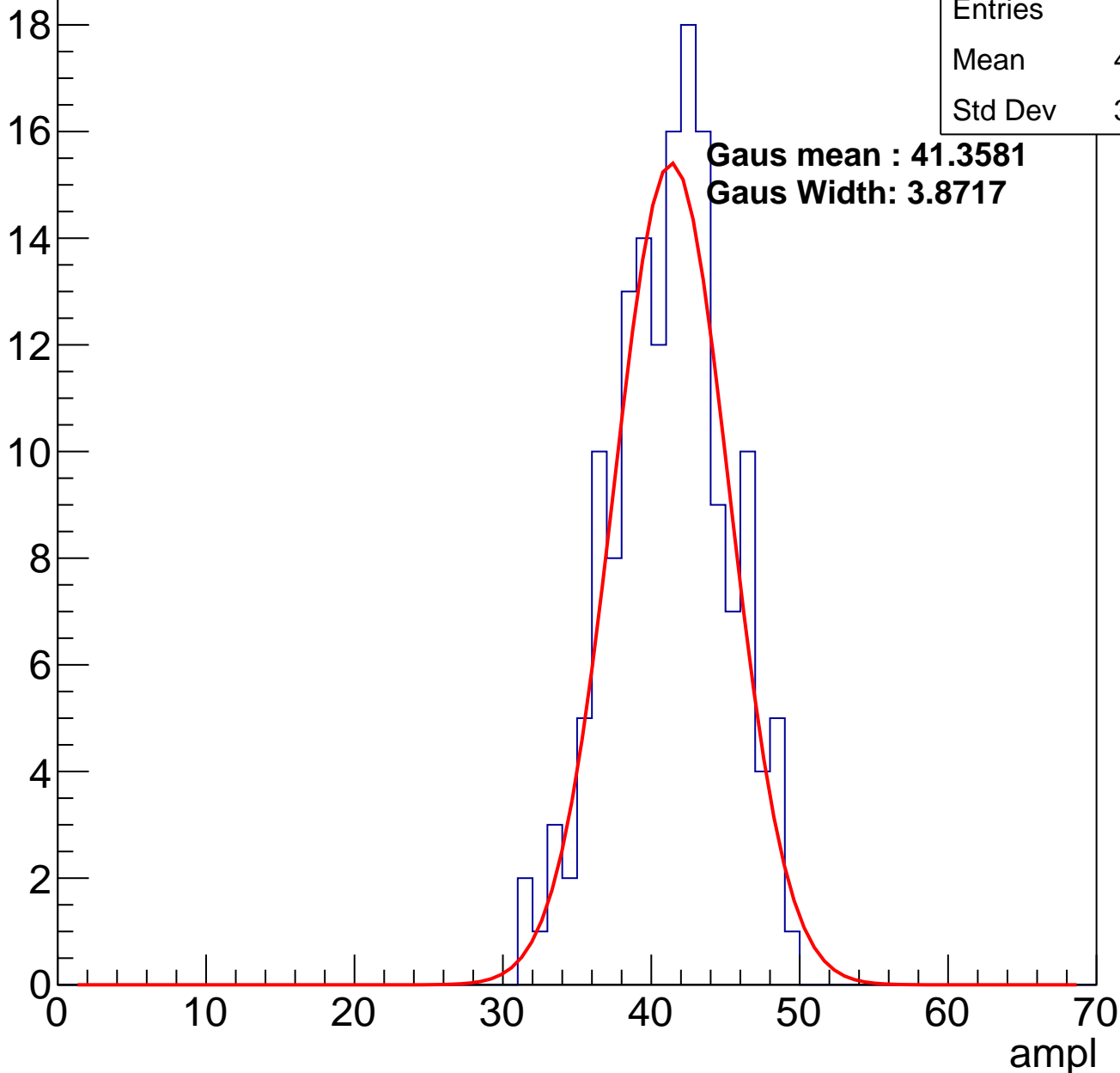
60

70

B0L101S, U20-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

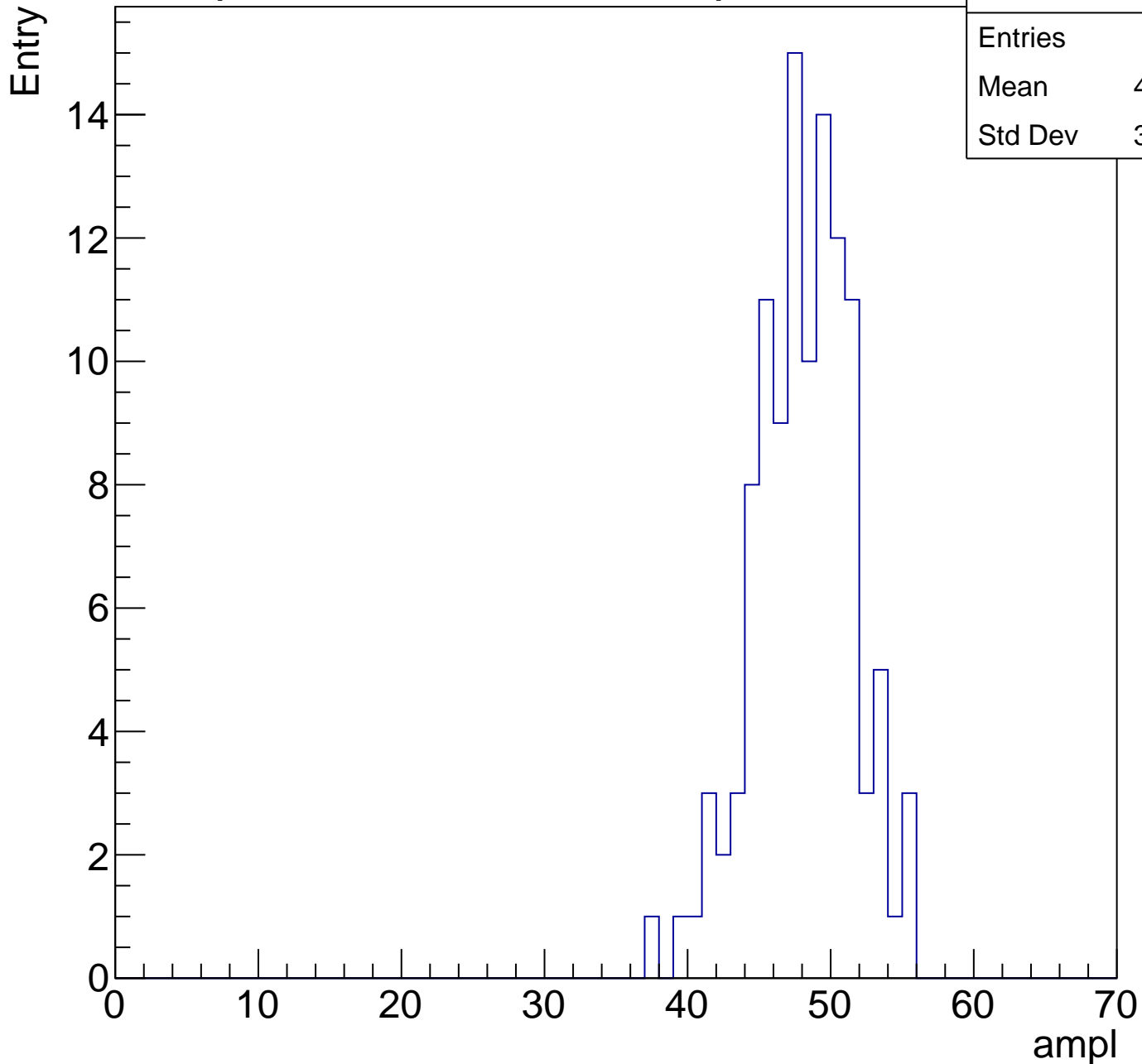
Entry



B0L101S, U20-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

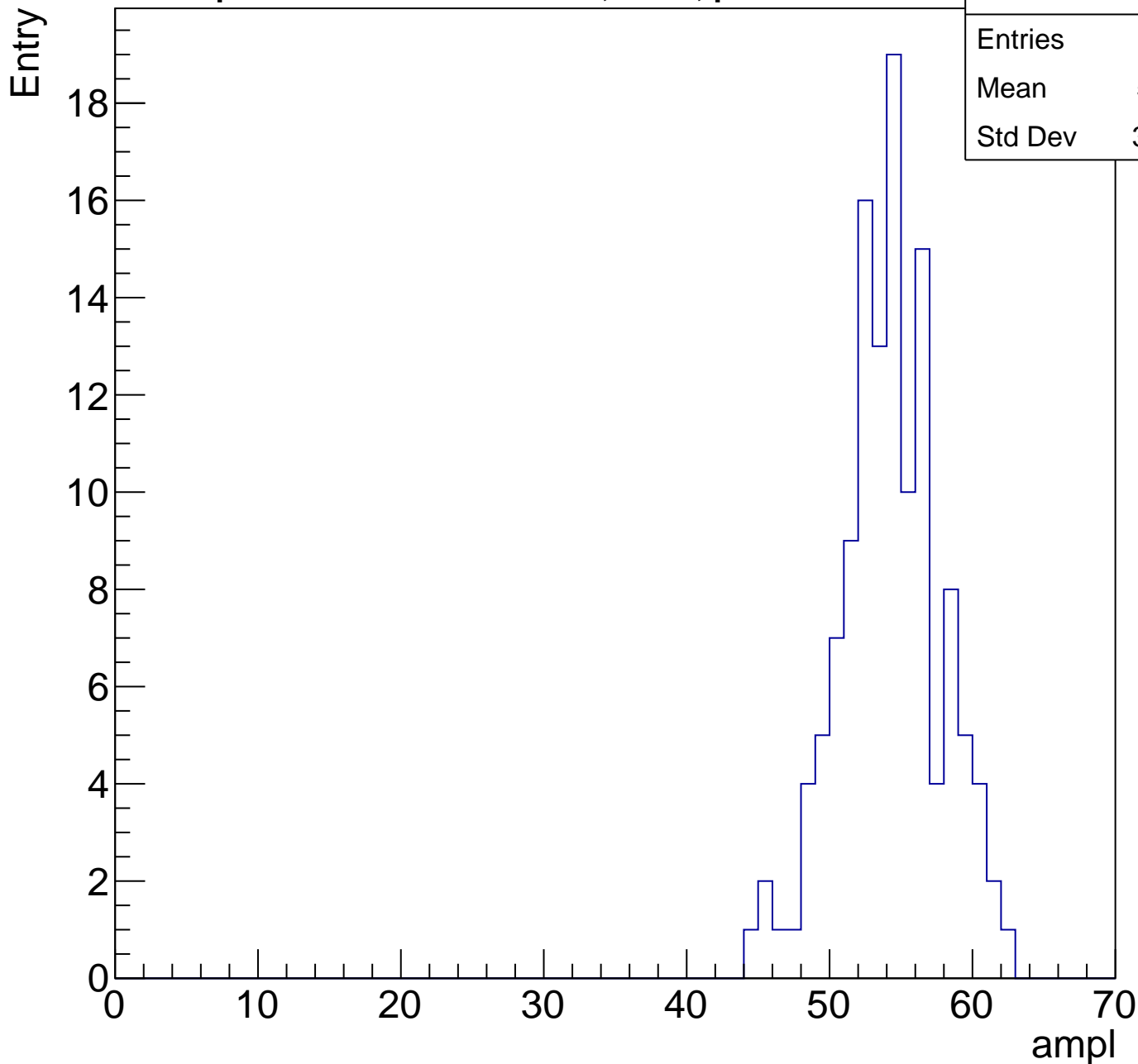
Entries	113
Mean	47.65
Std Dev	3.446



B0L101S, U20-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

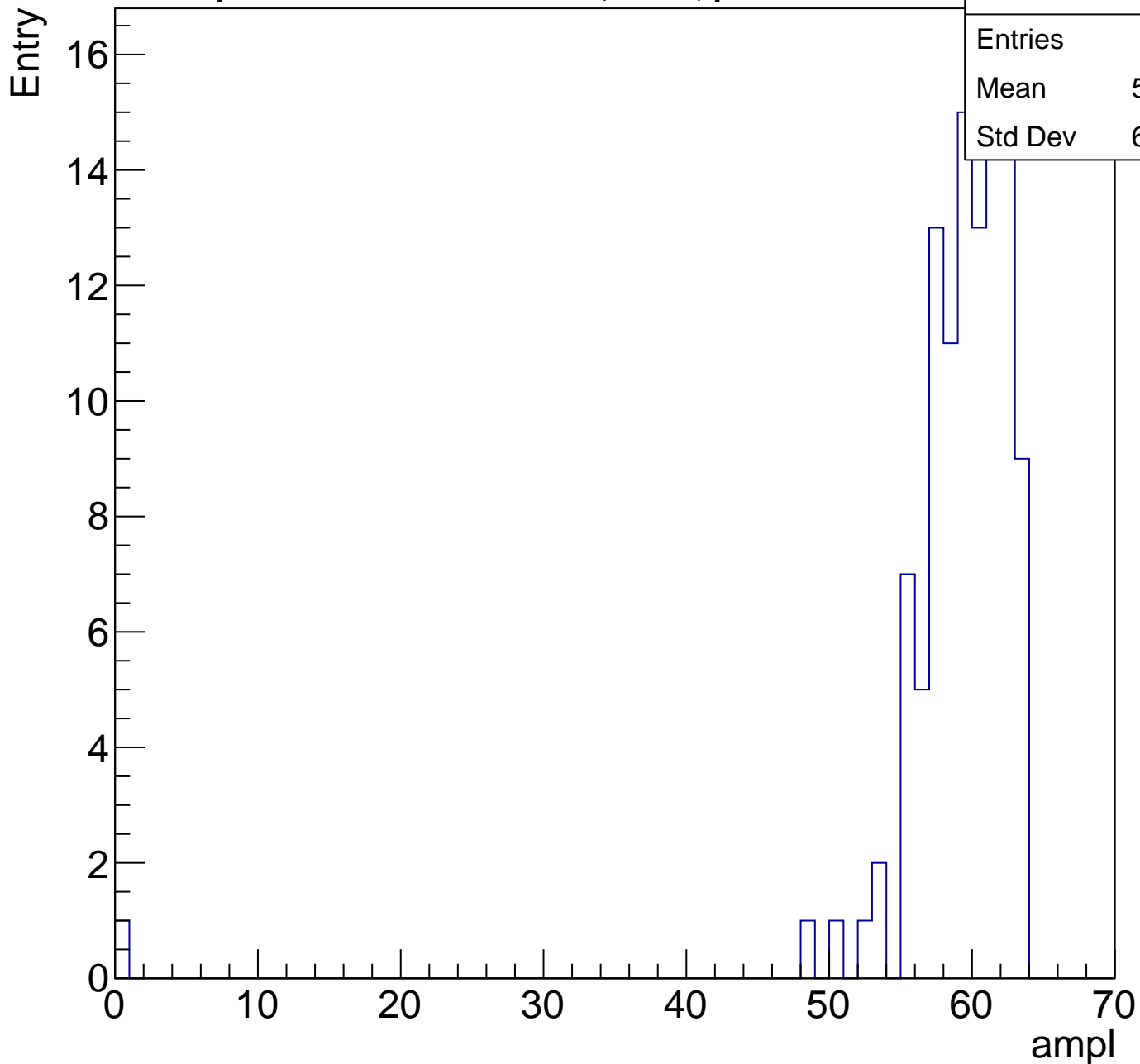
Entries	127
Mean	53.71
Std Dev	3.514



B0L101S, U20-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	58.54
Std Dev	6.297



B0L101S, U20-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	26
Mean	58.77
Std Dev	11.95

B0L101S, U20-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	27.96
Std Dev	7.682

Gaus mean : 27.2514

Gaus Width: 3.8314

Entry

12

10

8

6

4

2

0

0

10

20

30

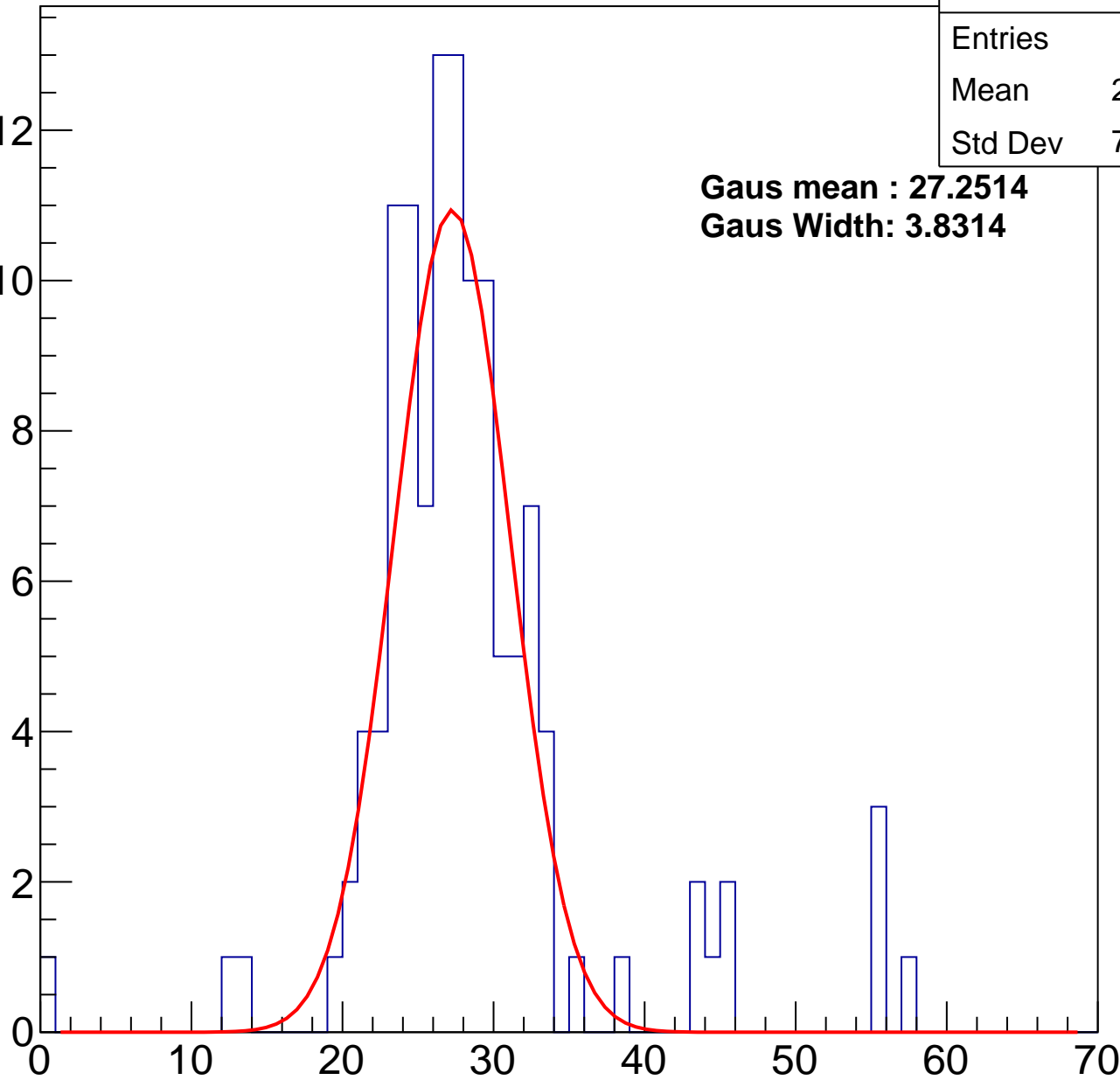
40

50

60

70

ampl



B0L101S, U20-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	32.53
Std Dev	4.544

Gaus mean : 33.2948

Gaus Width: 4.1808

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

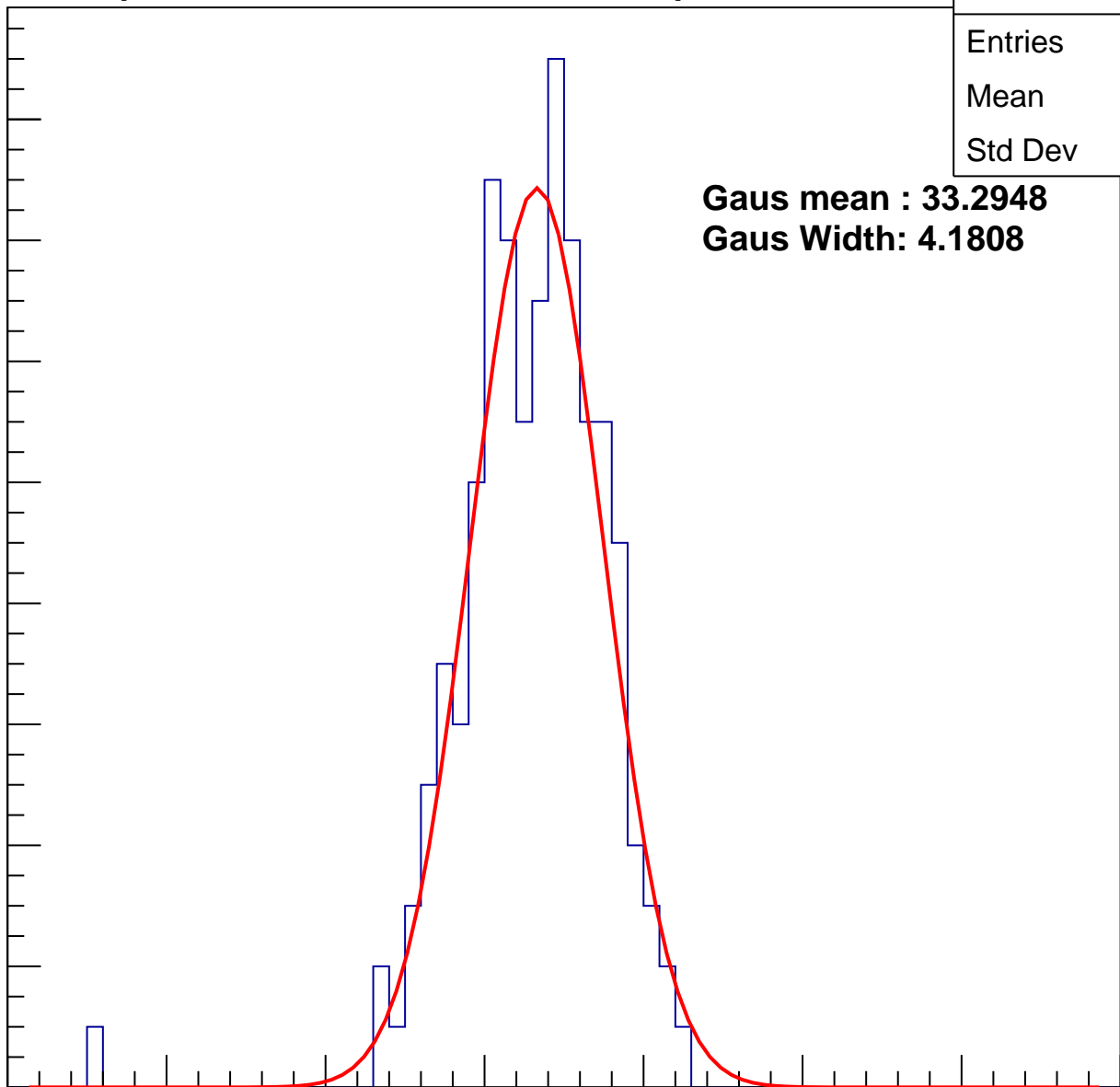
40

50

60

70

ampl



B0L101S, U20-ch116, adc2

calib_packv5_042523_0143.root, FC#1, port C1

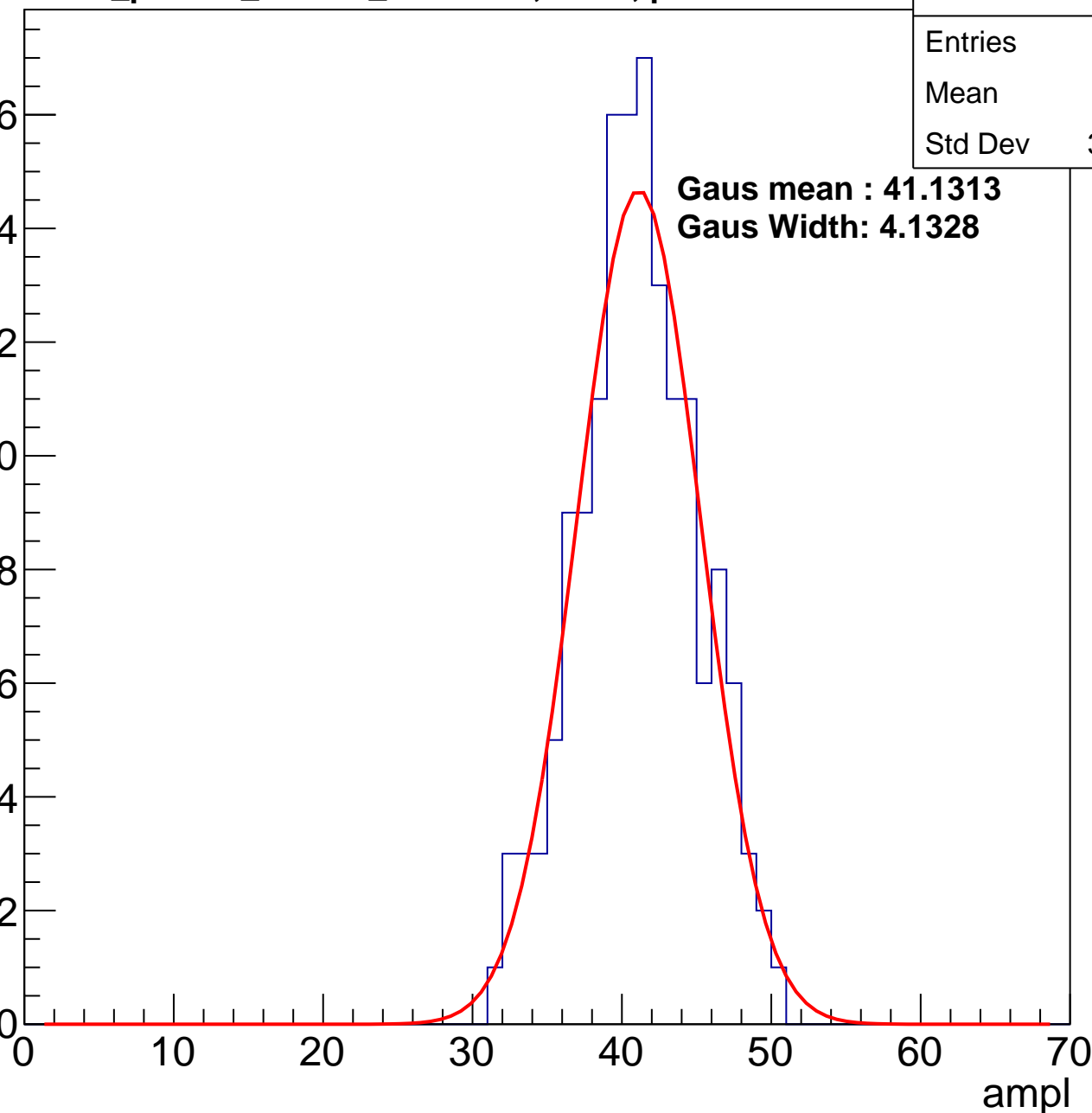
Entry

16
14
12
10
8
6
4
2
0

Entries	154
Mean	40.61
Std Dev	3.956

Gaus mean : 41.1313

Gaus Width: 4.1328



B0L101S, U20-ch116, adc3

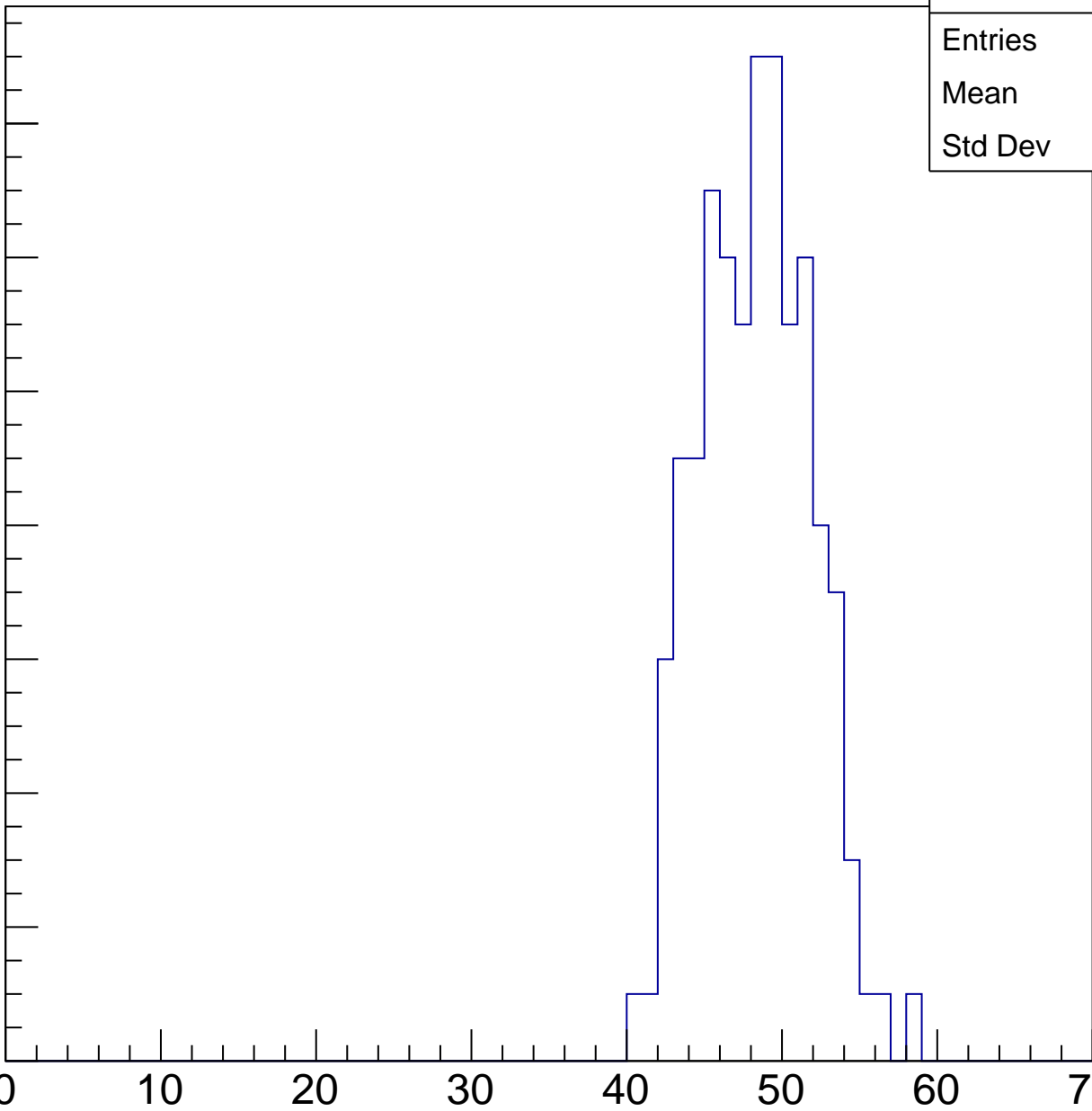
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	136
Mean	47.83
Std Dev	3.495

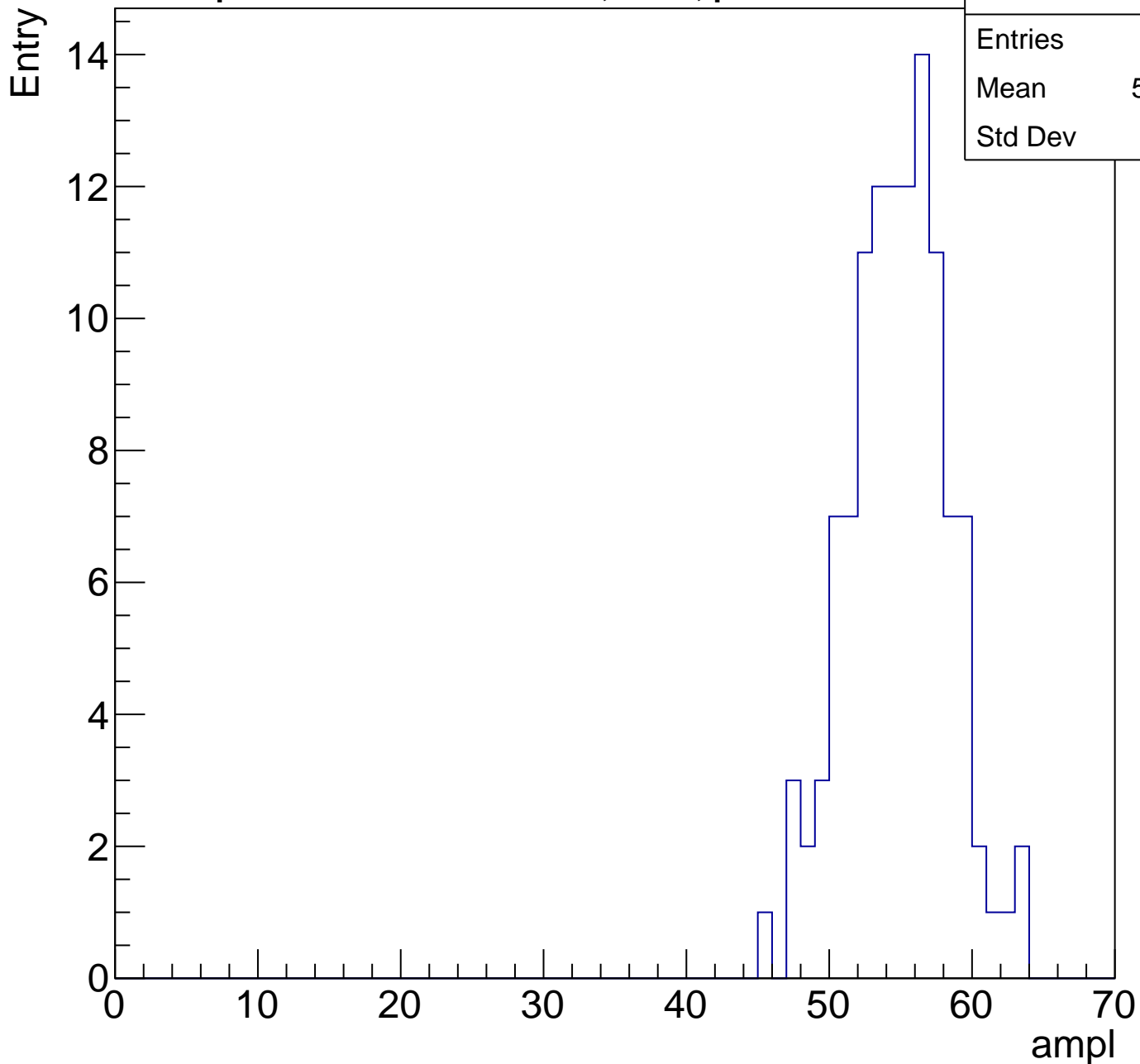
ampl



B0L101S, U20-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	54.36
Std Dev	3.48



B0L101S, U20-ch116, adc5

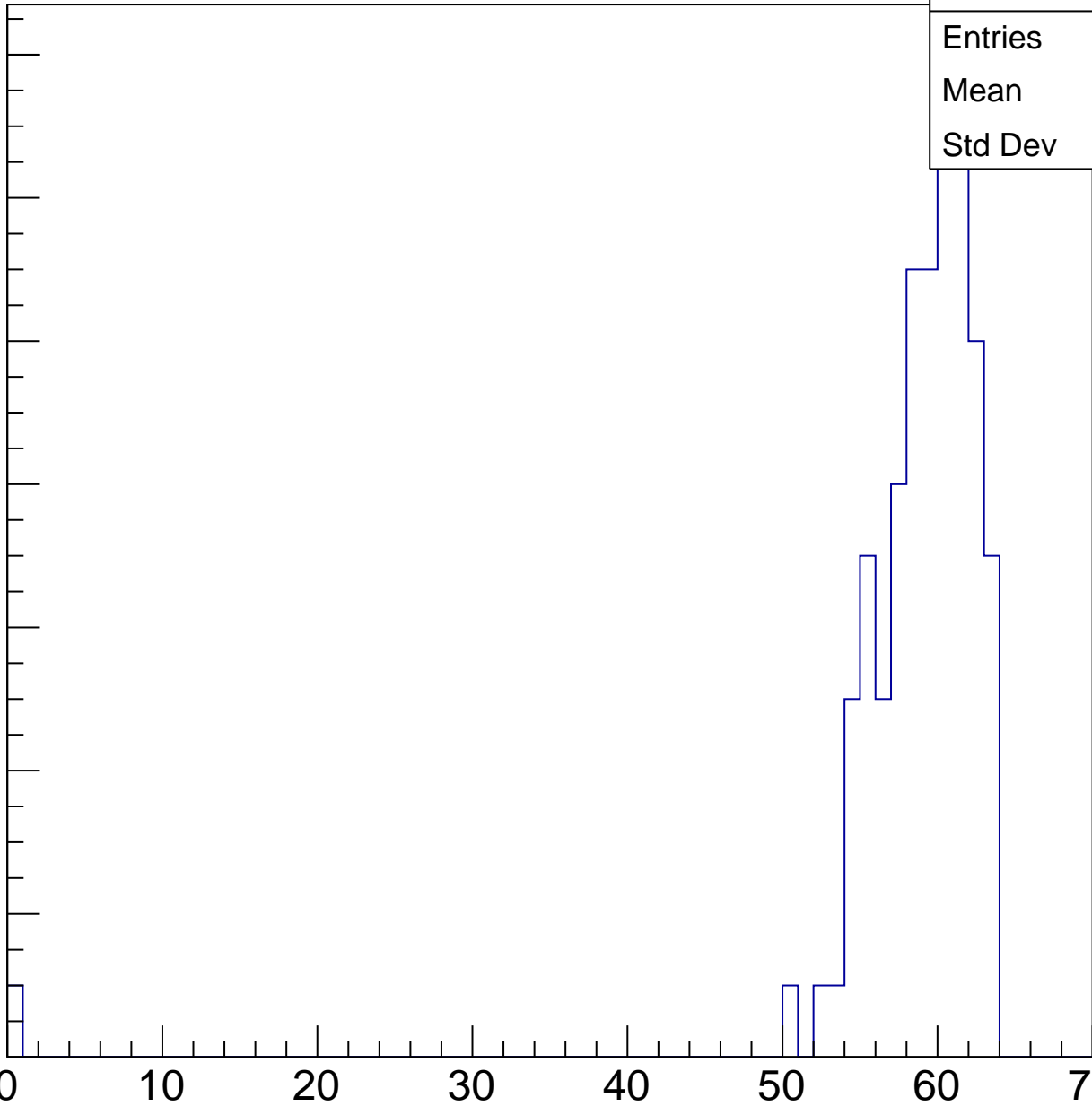
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.18
Std Dev	6.63

ampl

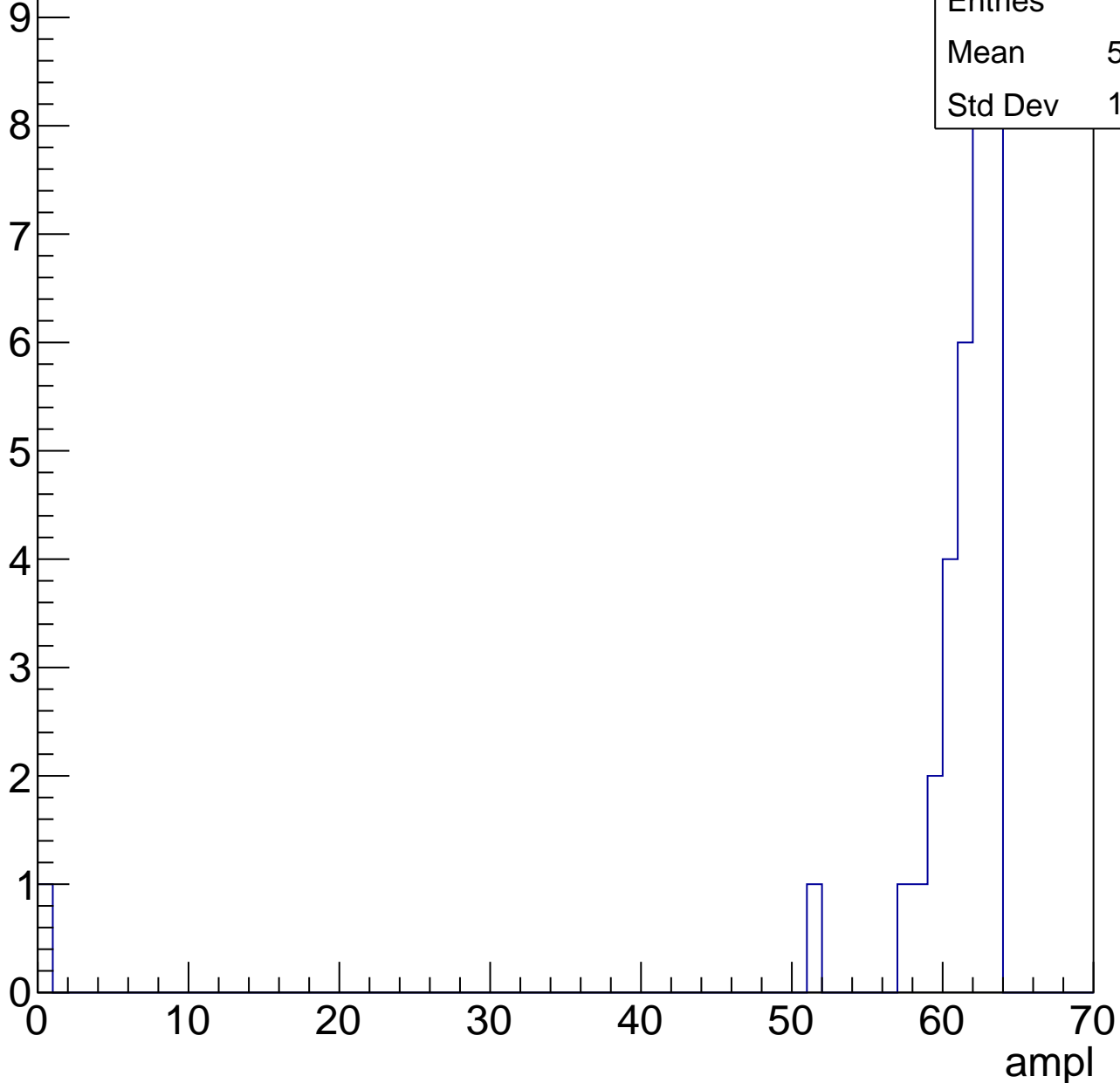


B0L101S, U20-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

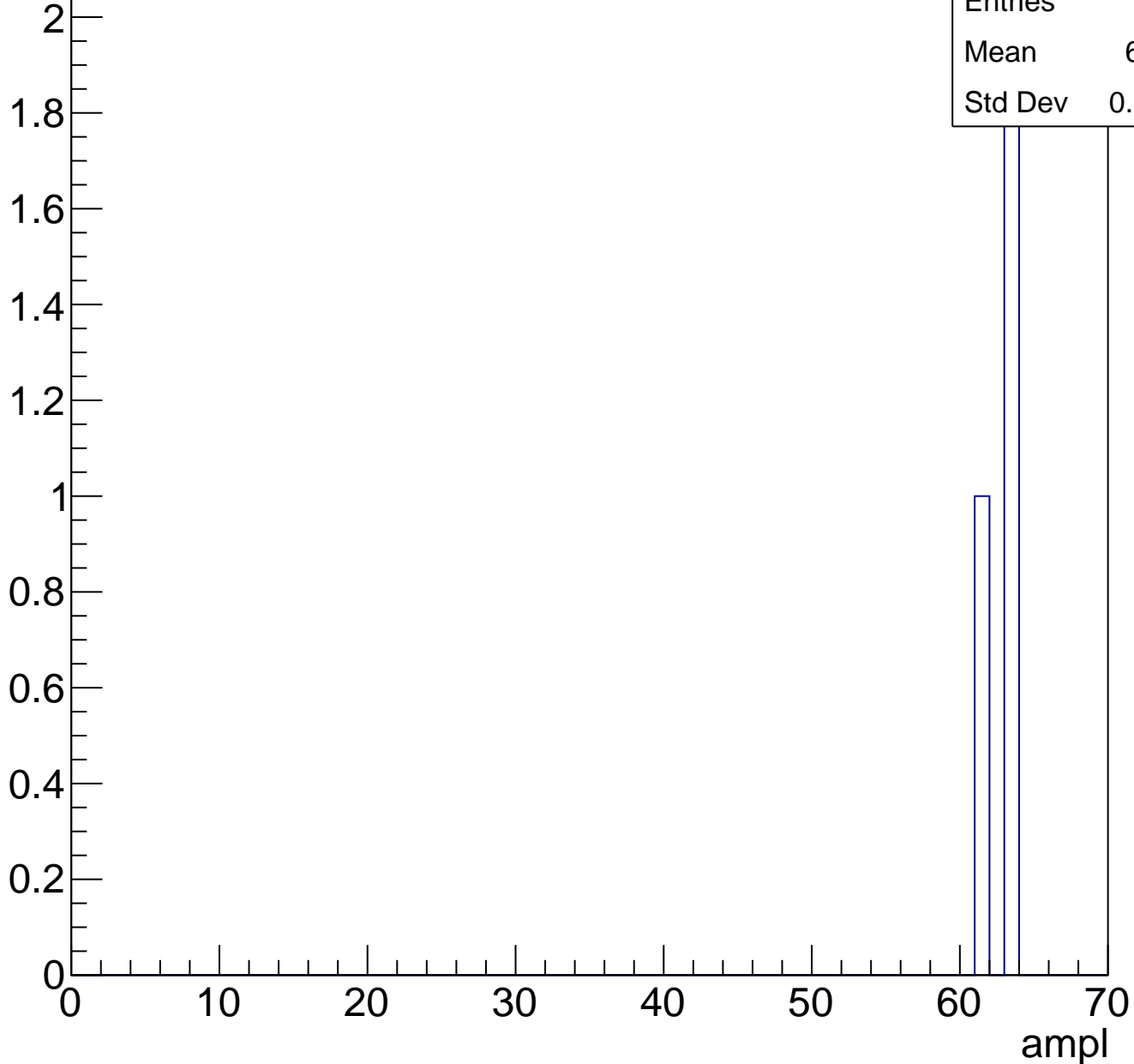
Entries	34
Mean	59.26
Std Dev	10.57



B0L101S, U20-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	28.72
Std Dev	4.768

Gaus mean : 29.4565

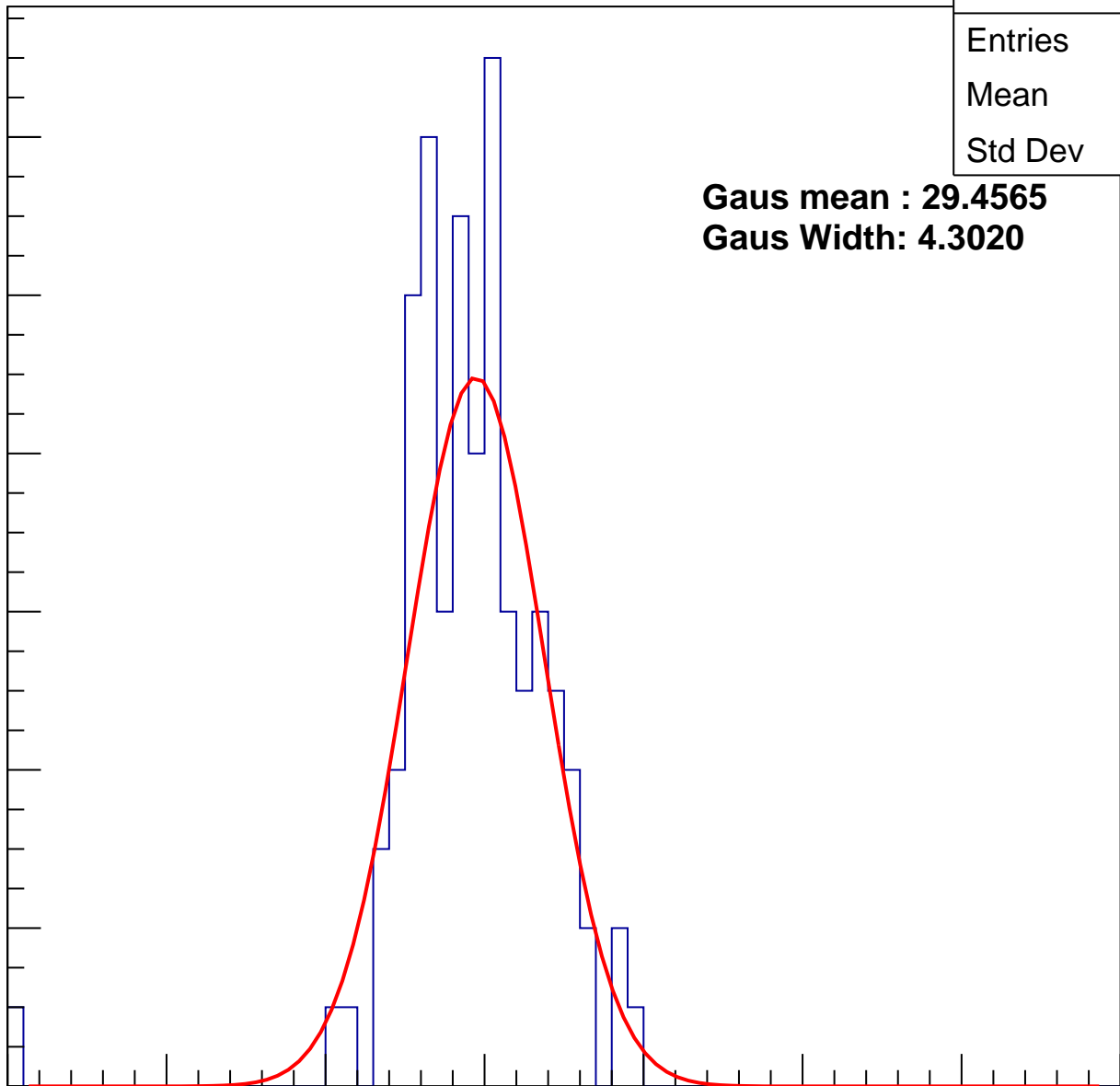
Gaus Width: 4.3020

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch117, adc1

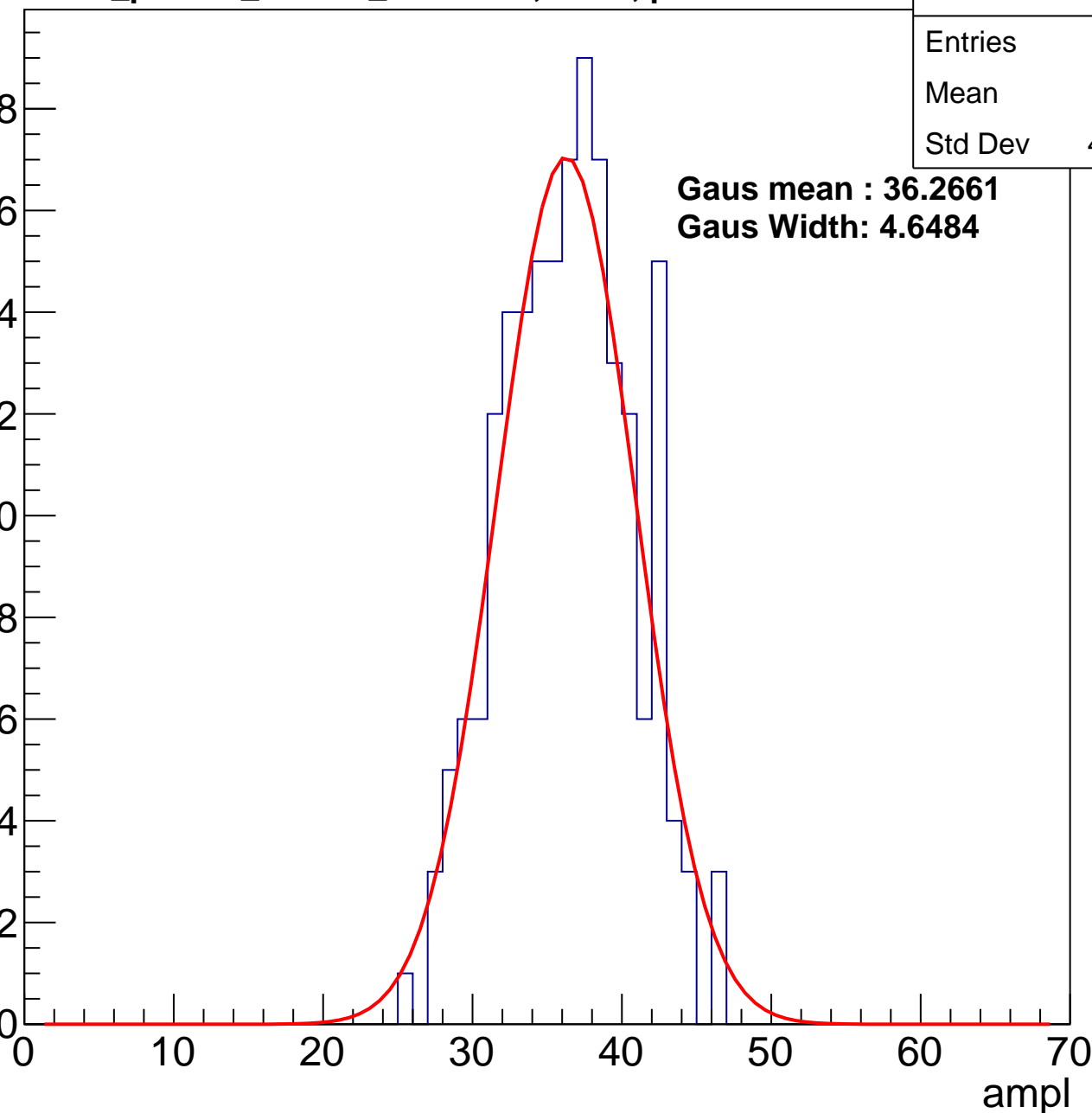
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	200
Mean	35.91
Std Dev	4.296

Gaus mean : 36.2661
Gaus Width: 4.6484



B0L101S, U20-ch117, adc2

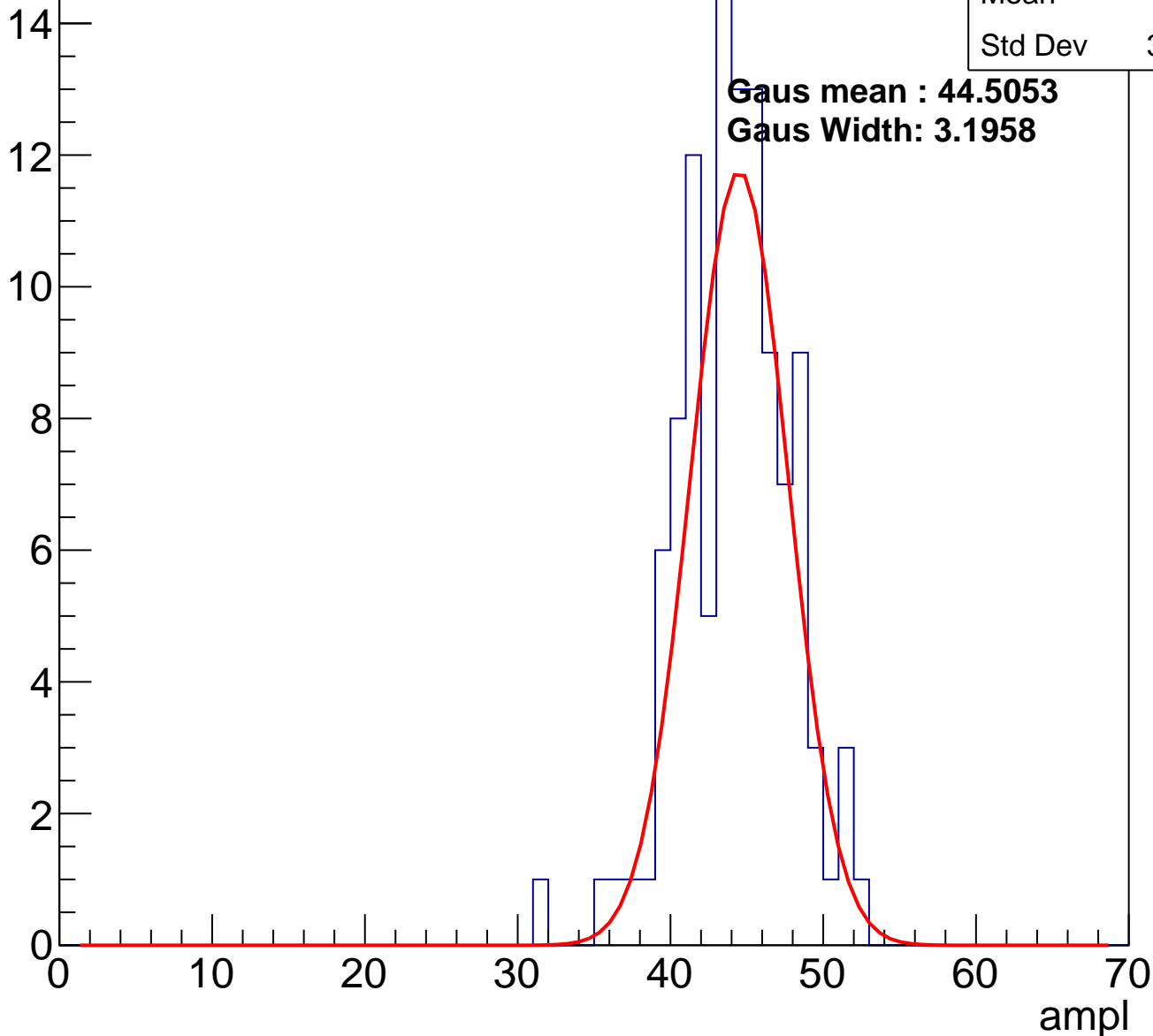
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	110
Mean	43.75
Std Dev	3.556

Gaus mean : 44.5053

Gaus Width: 3.1958



B0L101S, U20-ch117, adc3

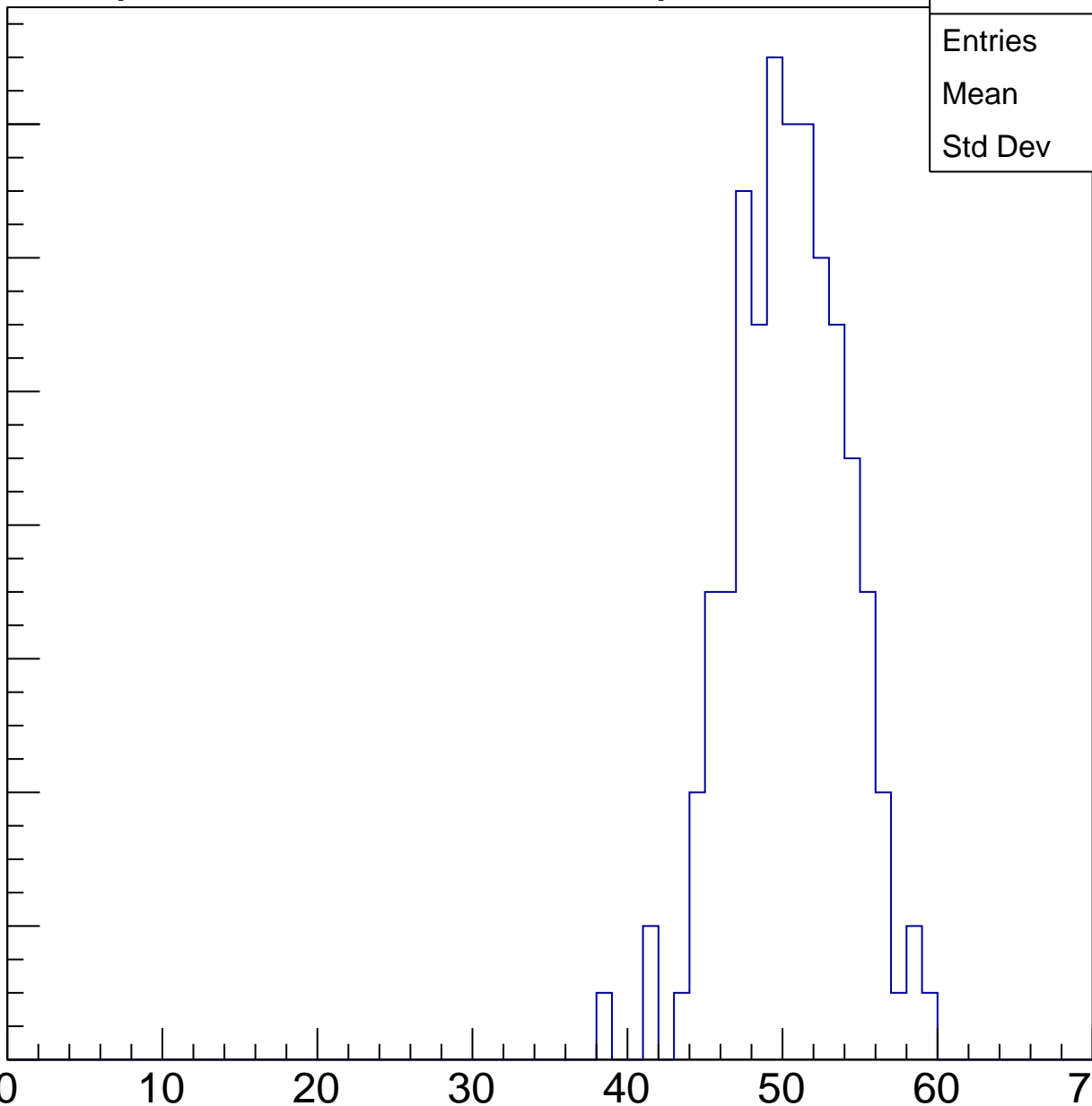
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	136
Mean	49.99
Std Dev	3.682

ampl

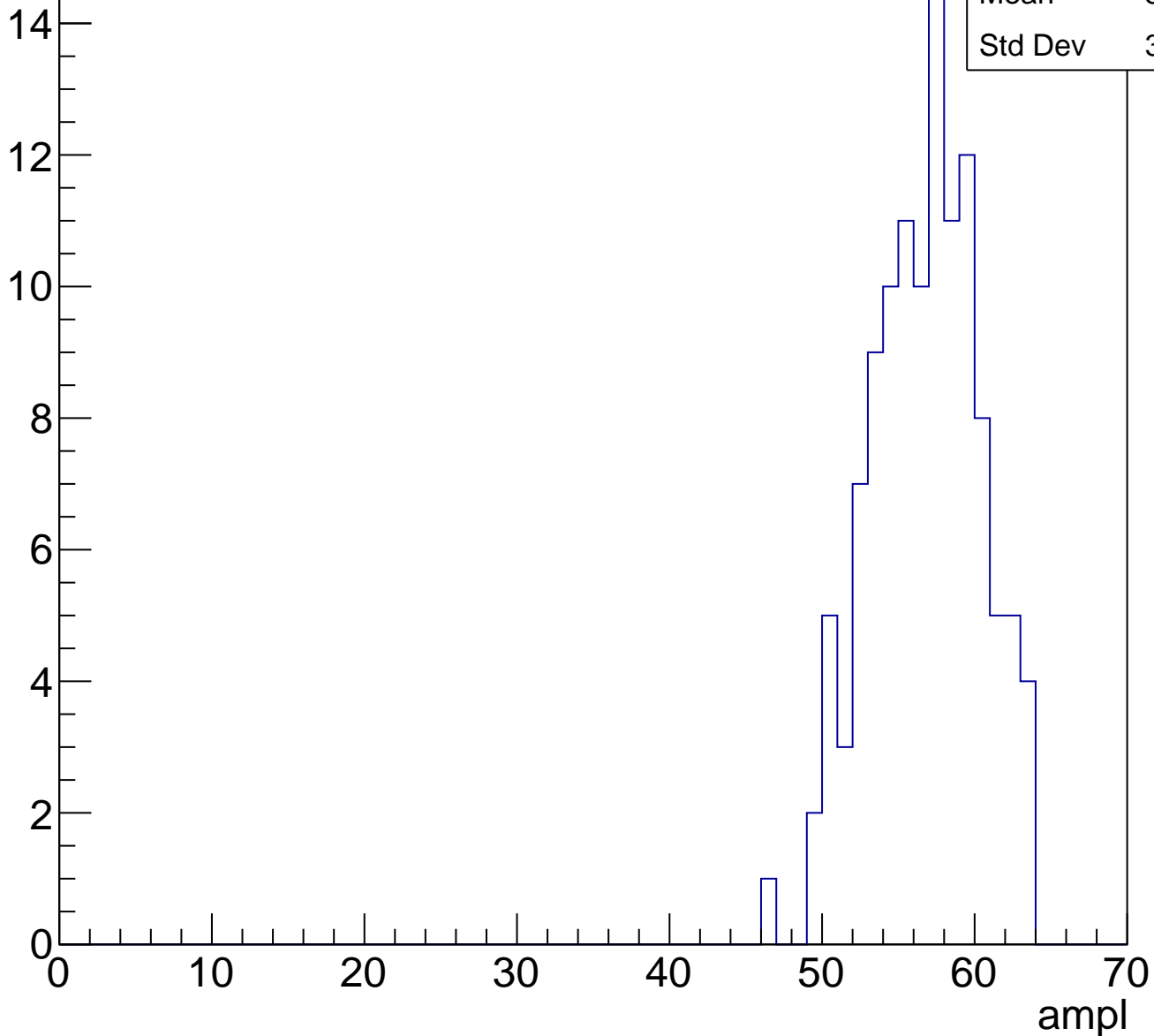


B0L101S, U20-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	56.28
Std Dev	3.546

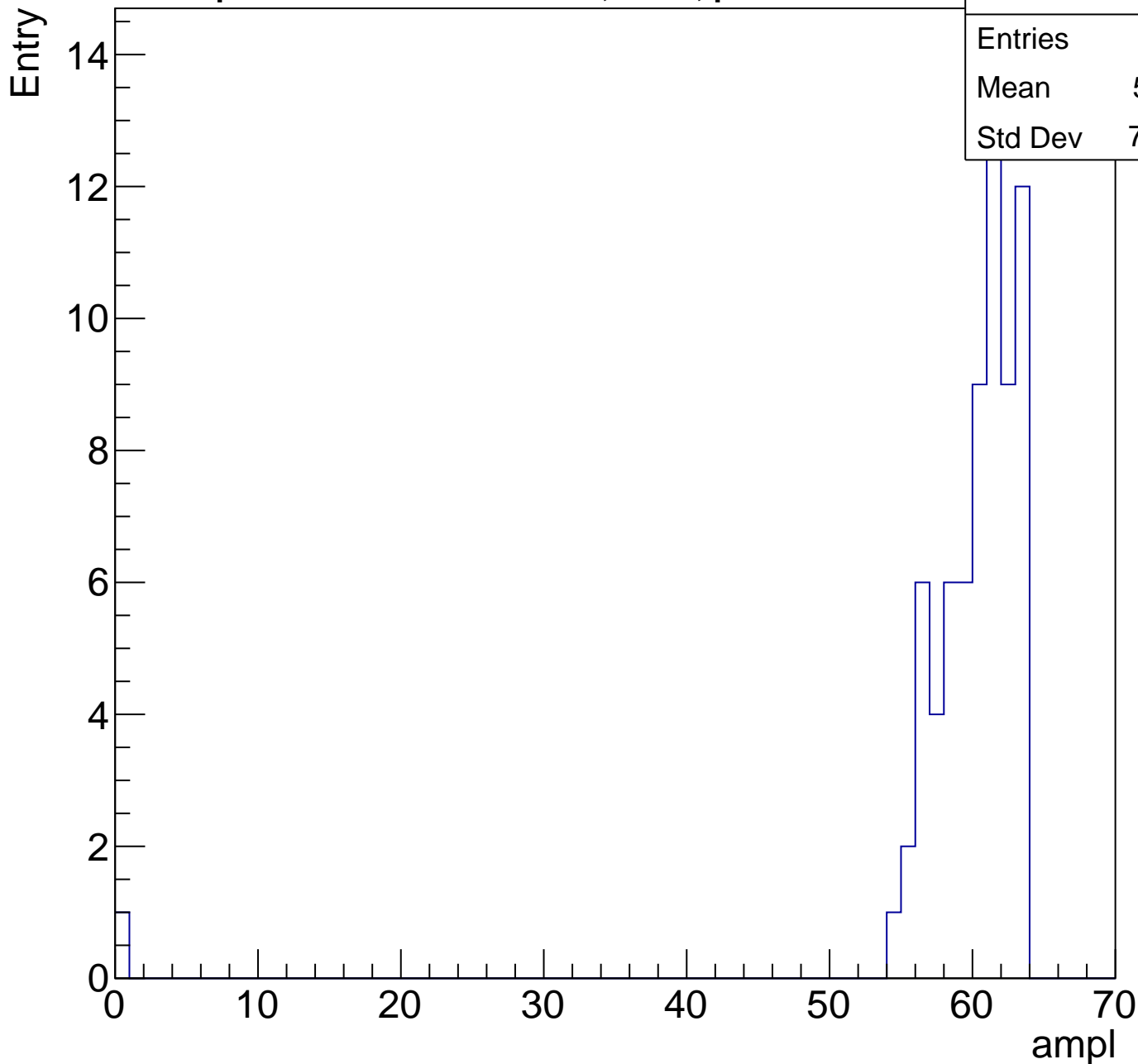
Entry



B0L101S, U20-ch117, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	70
Mean	59.11
Std Dev	7.513

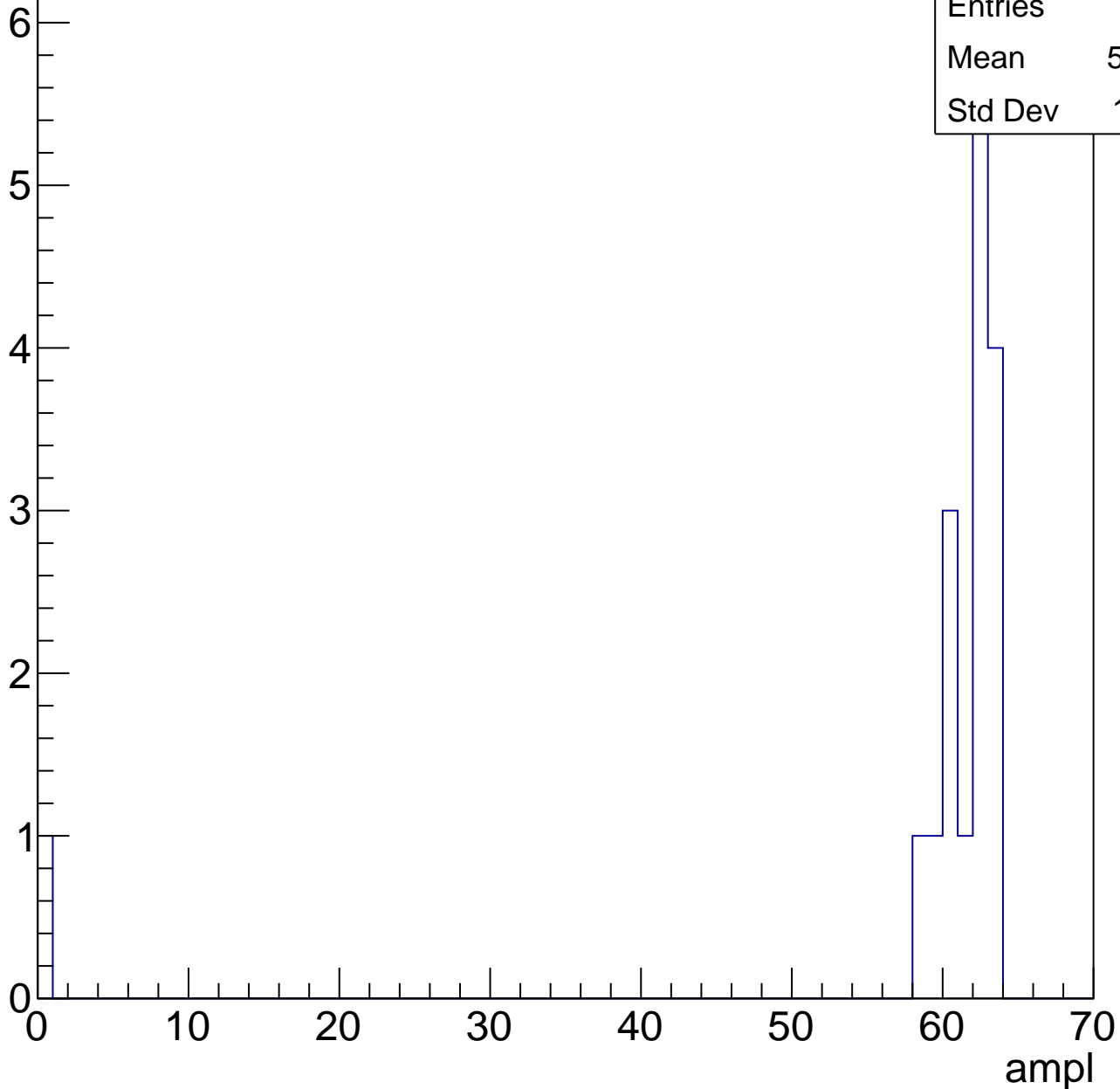


B0L101S, U20-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	57.76
Std Dev	14.51



B0L101S, U20-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch118, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	26.94
Std Dev	6.19

Gaus mean : 27.2648

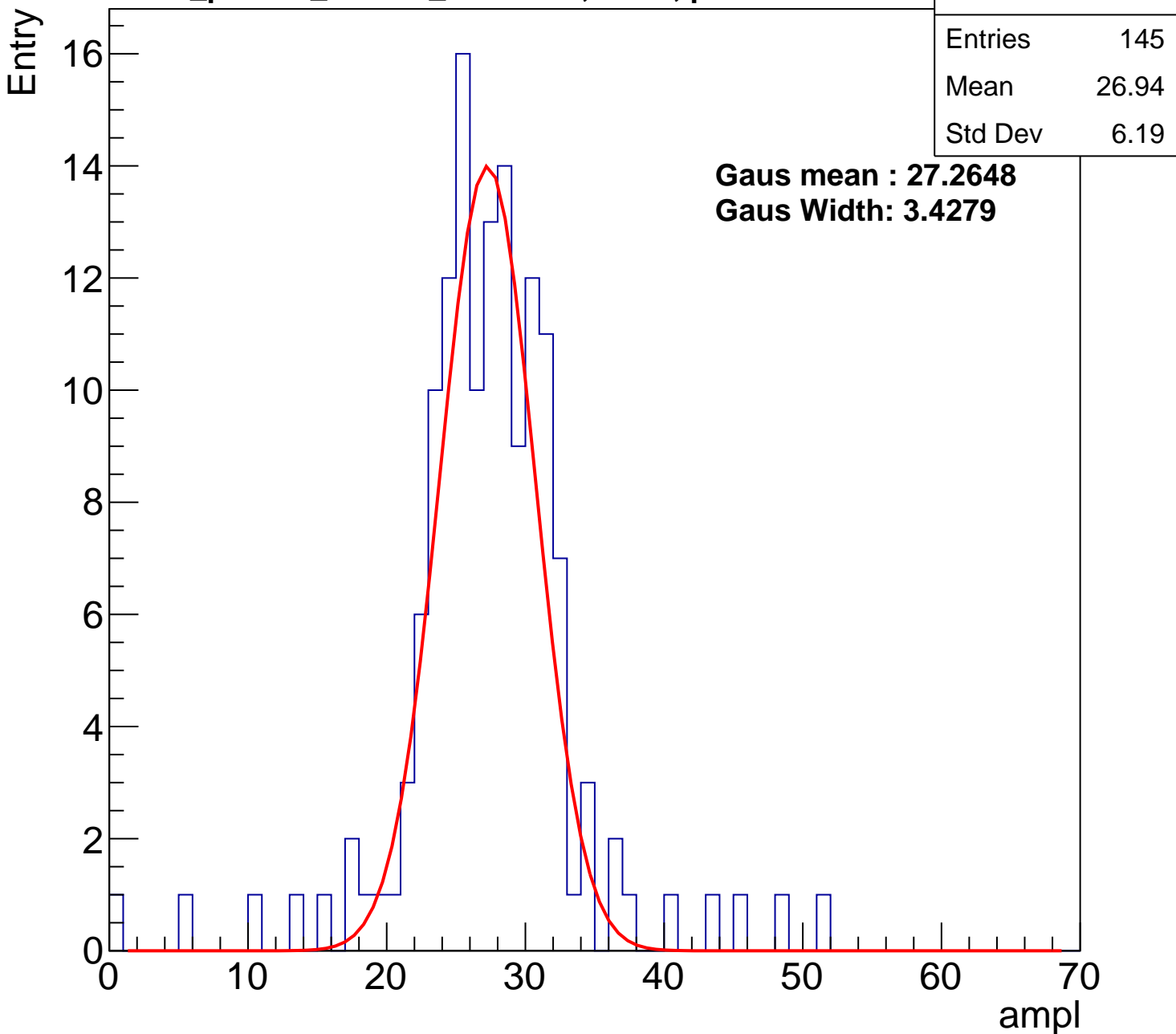
Gaus Width: 3.4279

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	33.39
Std Dev	4.465

Gaus mean : 33.8149

Gaus Width: 4.4088

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

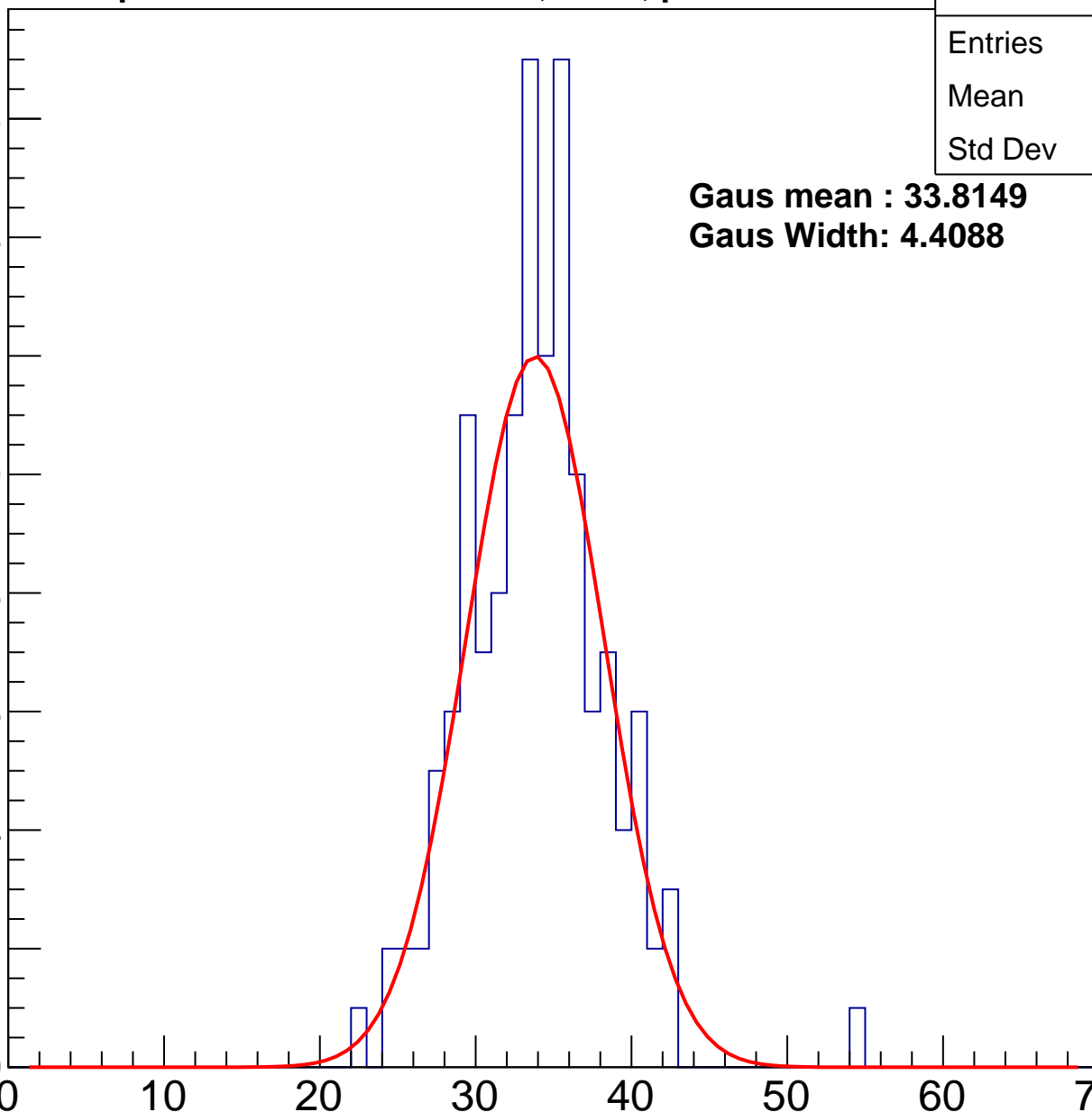
40

50

60

70

ampl



B0L101S, U20-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

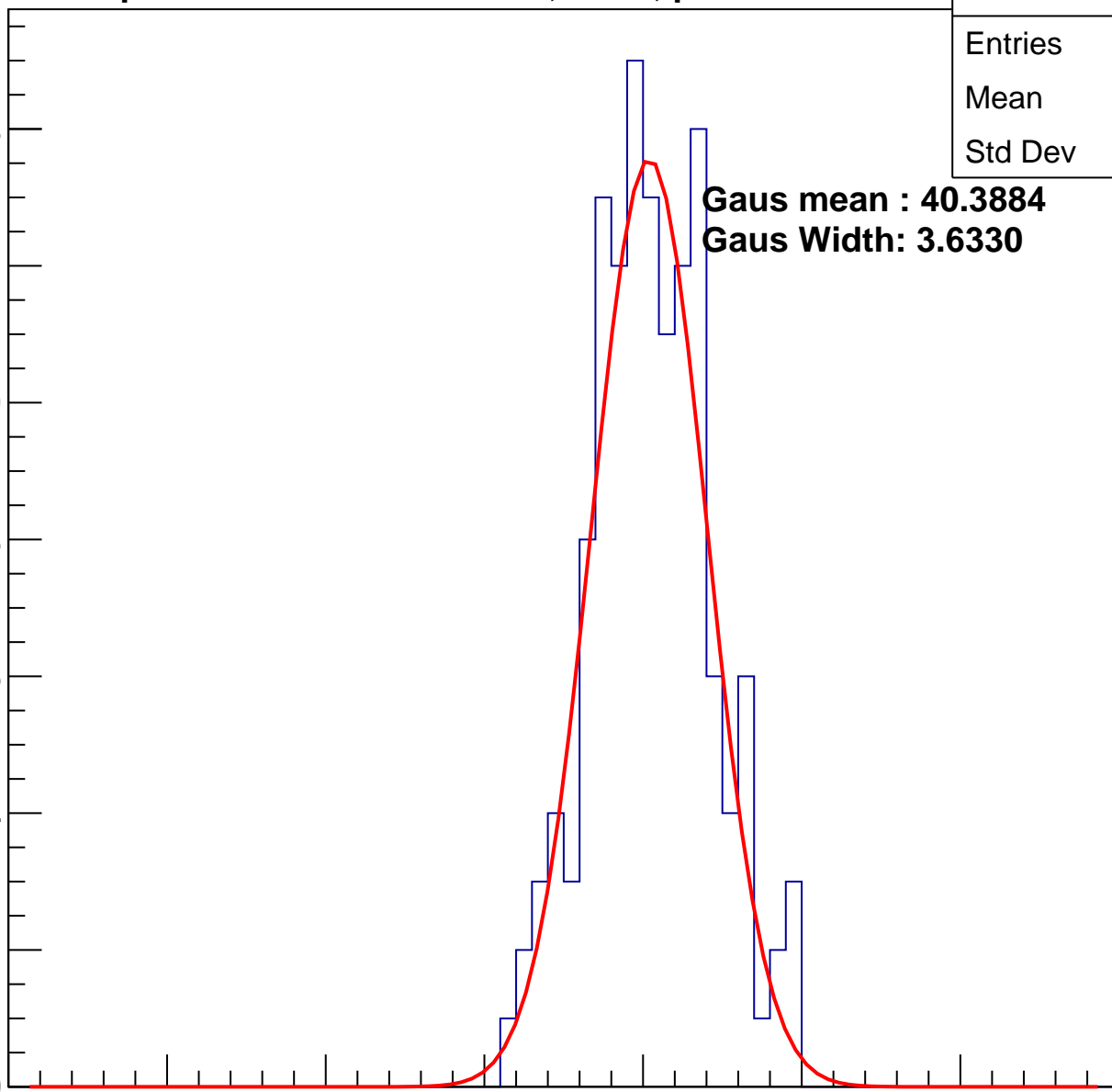
70

ampl

Entries	133
Mean	40.09
Std Dev	3.779

Gaus mean : 40.3884

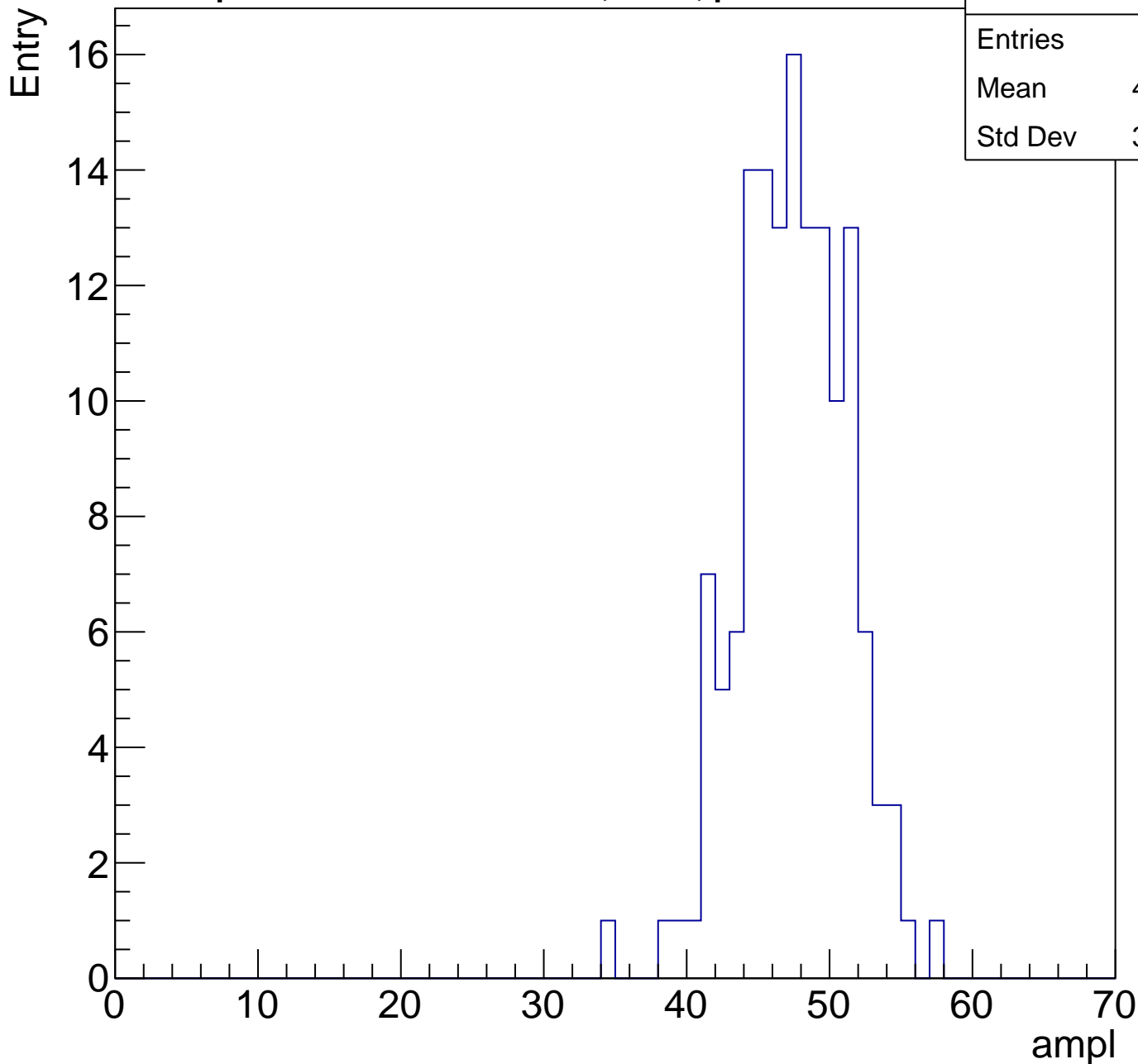
Gaus Width: 3.6330



B0L101S, U20-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

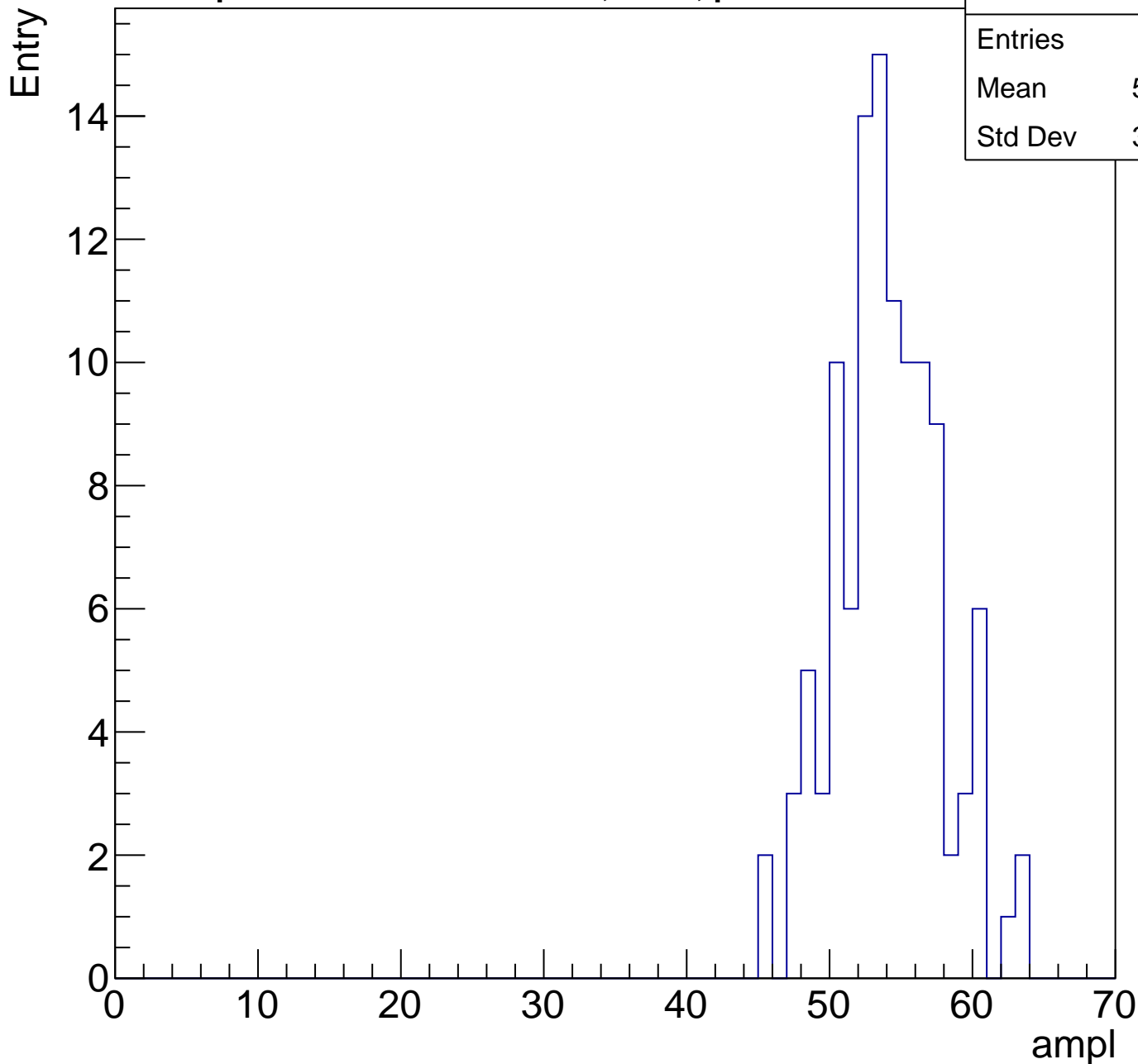
Entries	142
Mean	46.98
Std Dev	3.718



B0L101S, U20-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	53.62
Std Dev	3.675



B0L101S, U20-ch118, adc5

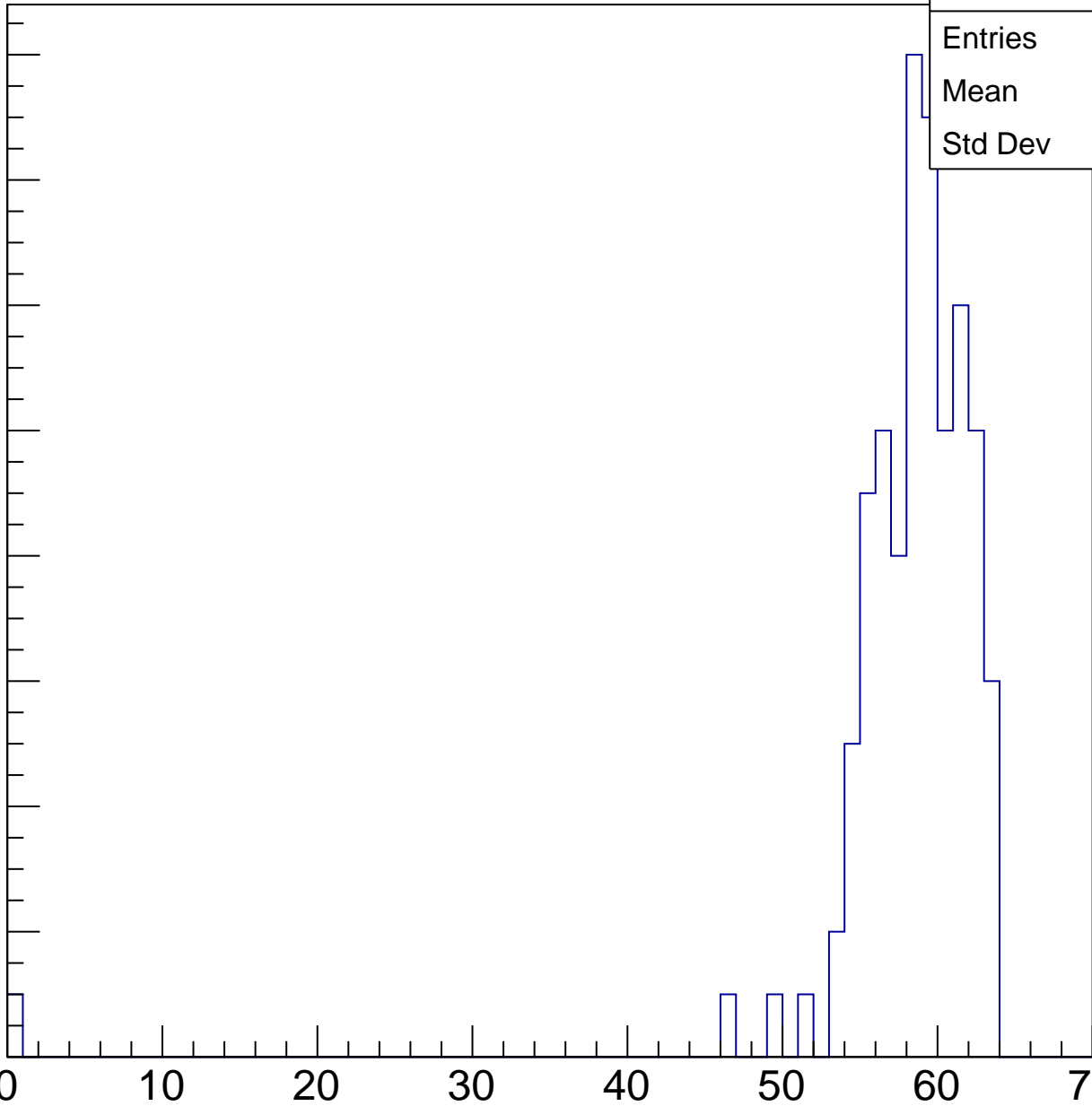
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	57.72
Std Dev	6.385

ampl



B0L101S, U20-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	40
Mean	59.52
Std Dev	9.744

0

2

4

6

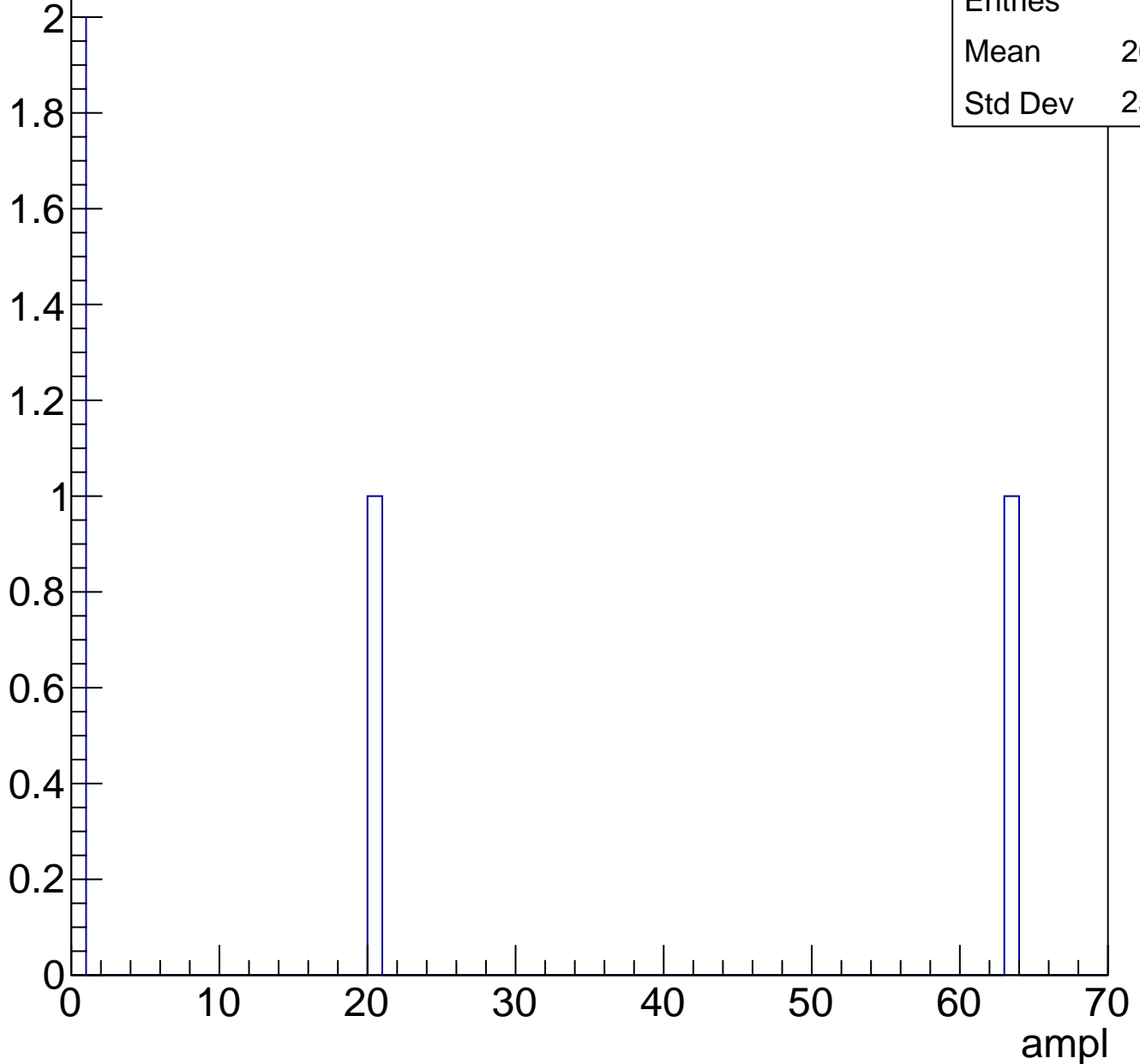
8

10

B0L101S, U20-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	20.75
Std Dev	25.72

B0L101S, U20-ch119, adc0

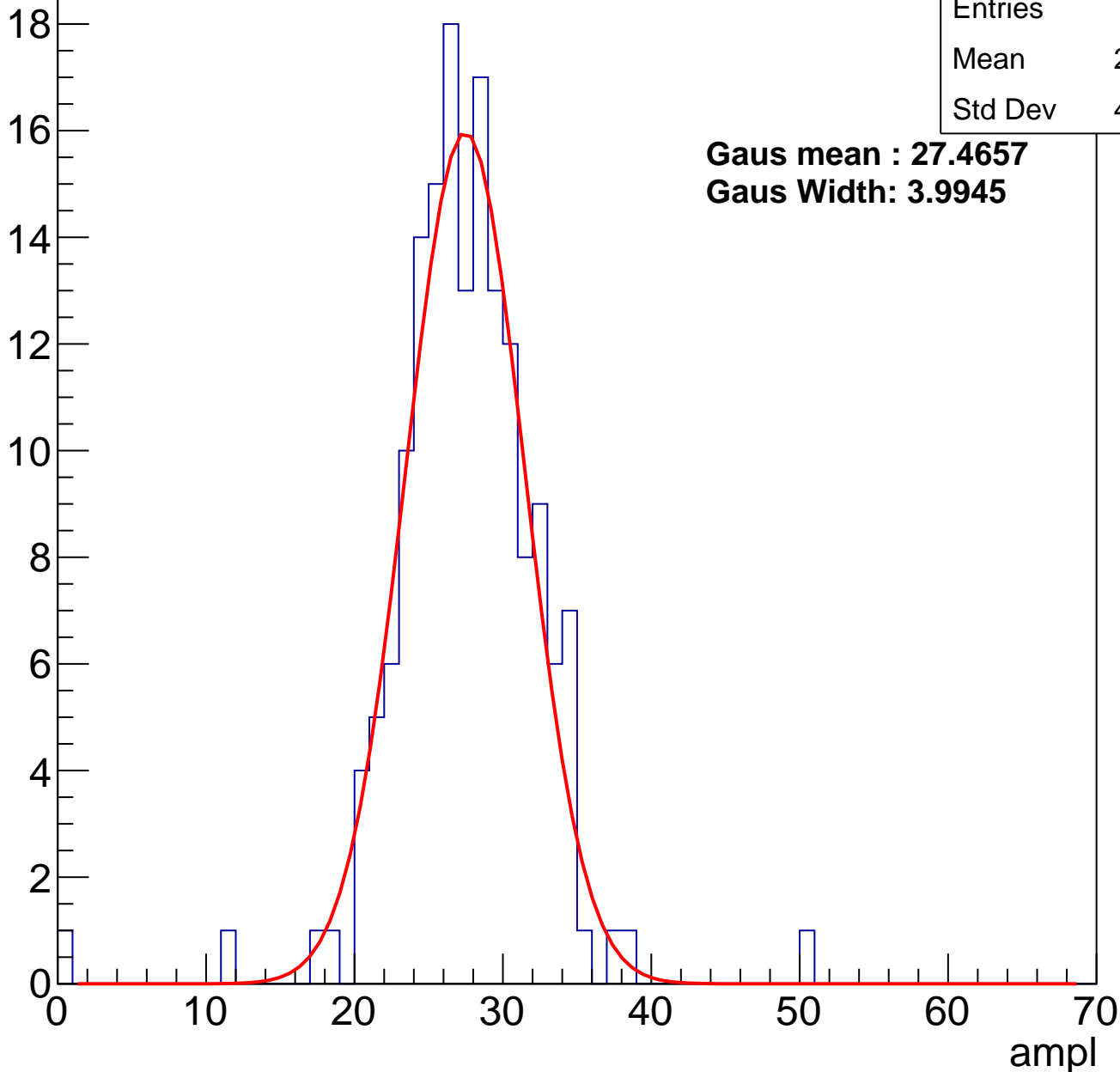
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	27.08
Std Dev	4.898

Gaus mean : 27.4657

Gaus Width: 3.9945

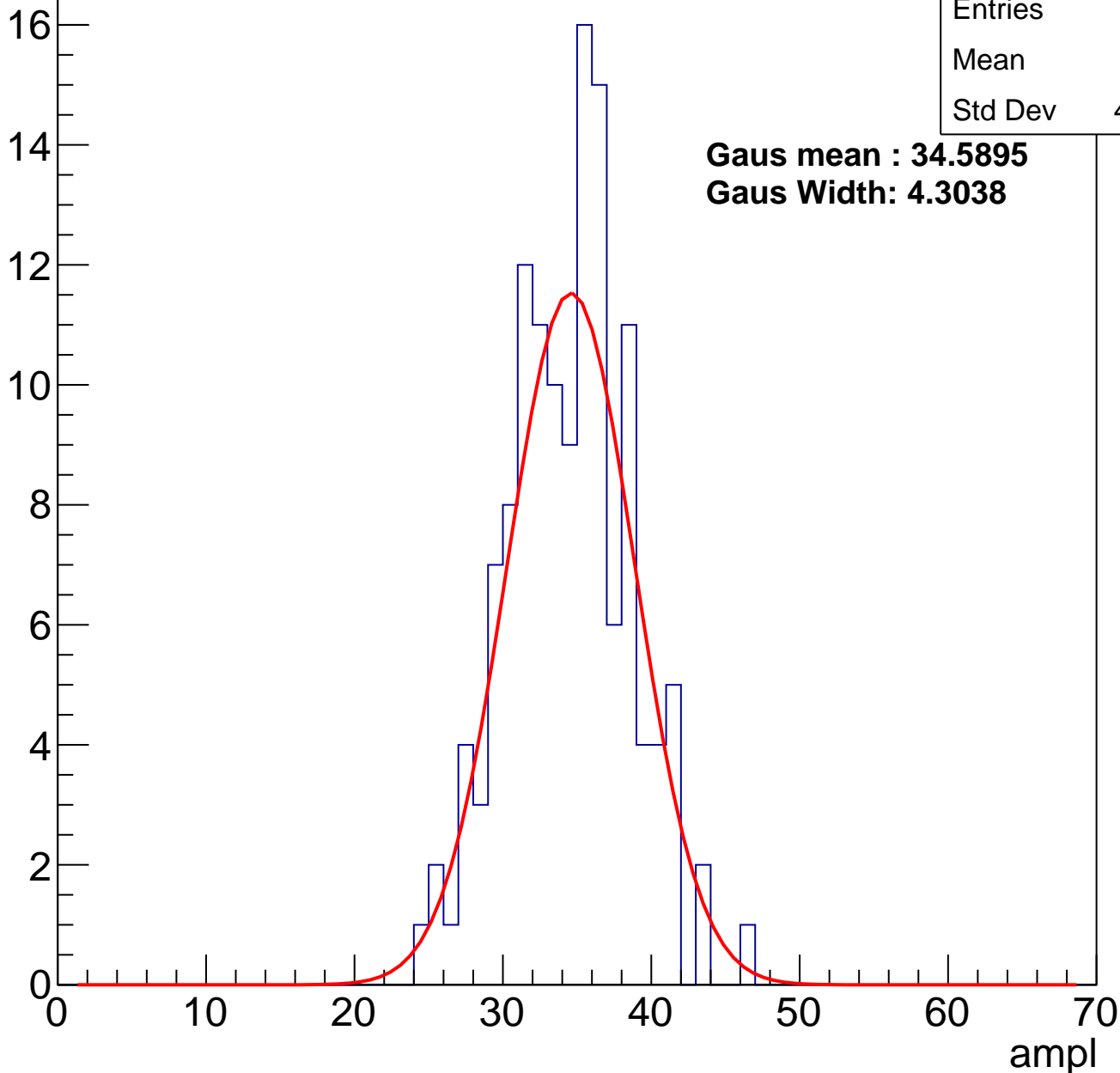
Entry



B0L101S, U20-ch119, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

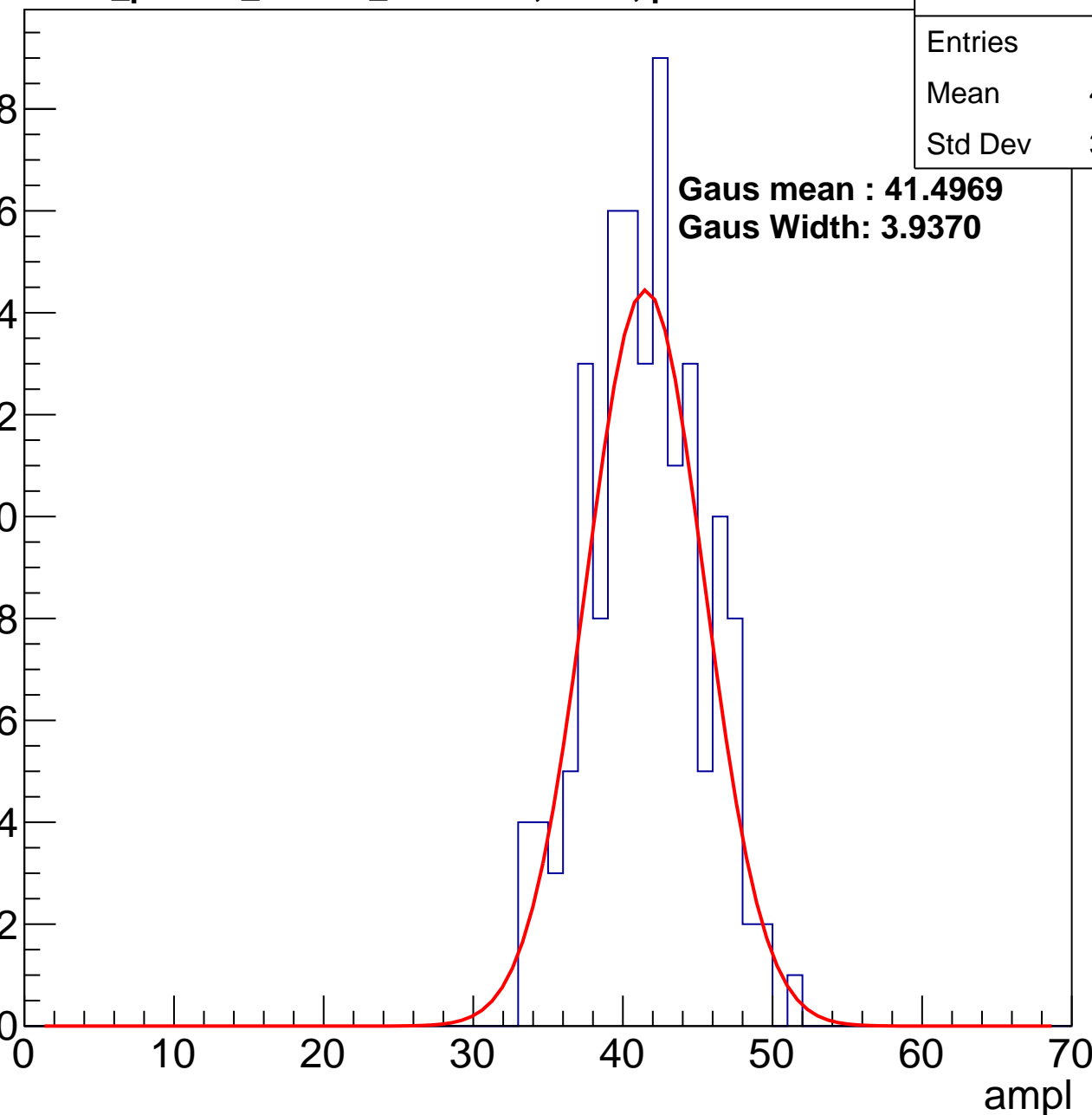
Entry

18
16
14
12
10
8
6
4
2
0

Entries	153
Mean	41.07
Std Dev	3.764

Gaus mean : 41.4969

Gaus Width: 3.9370



B0L101S, U20-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

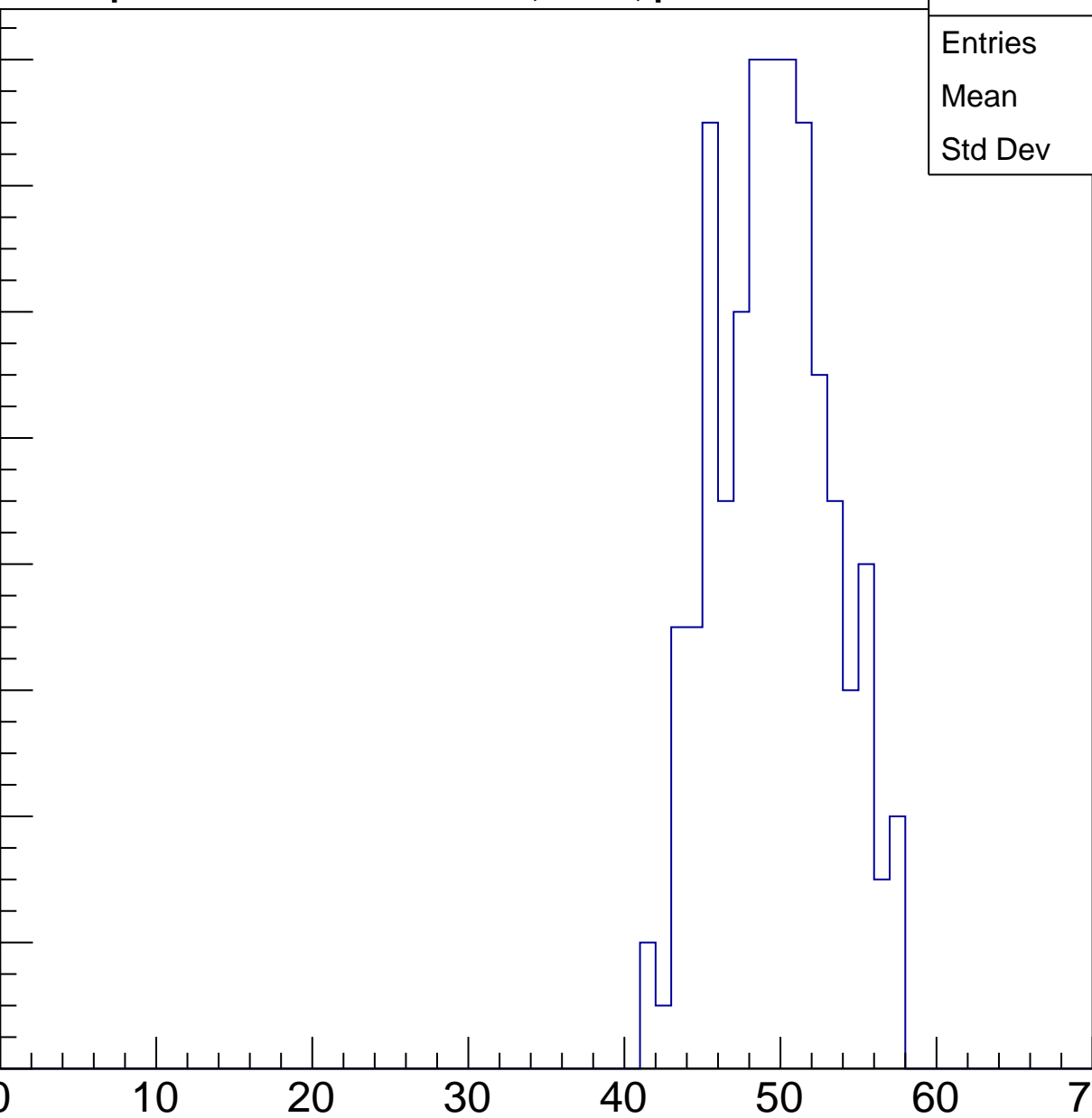
Entries	157
Mean	49.12
Std Dev	3.698

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

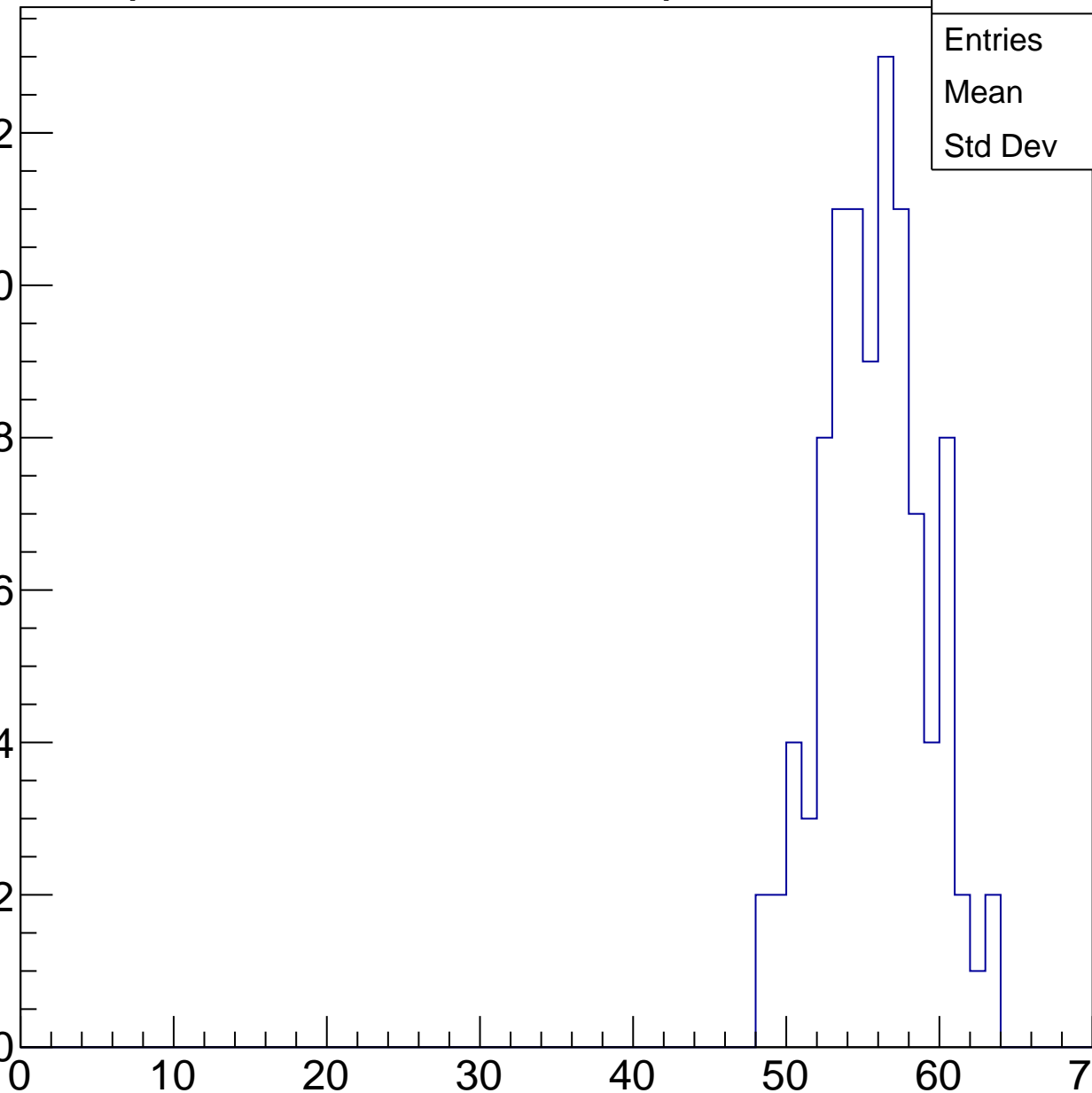
4

2

0

Entries	98
Mean	55.33
Std Dev	3.301

ampl



B0L101S, U20-ch119, adc5

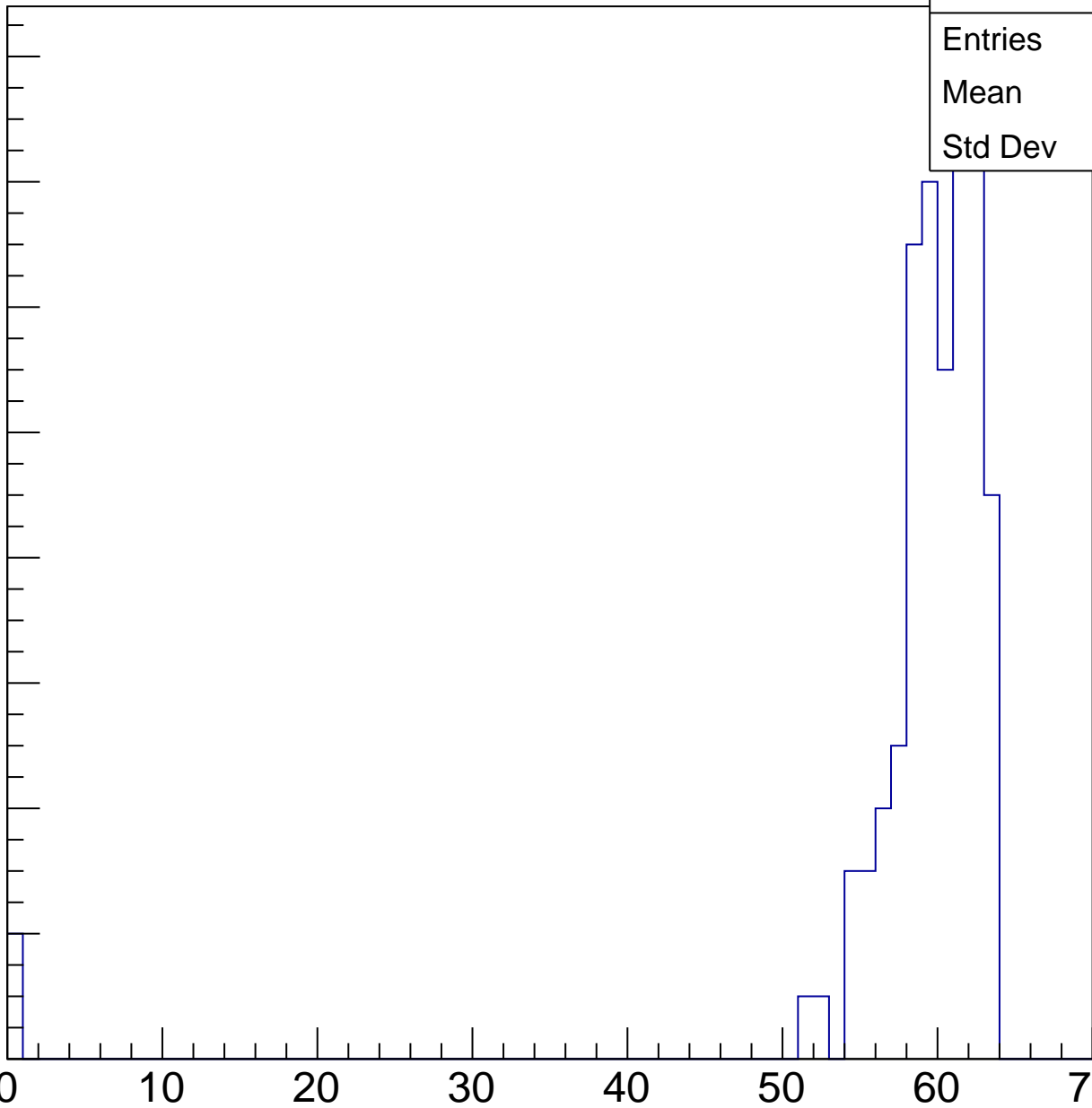
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.28
Std Dev	8.837

ampl

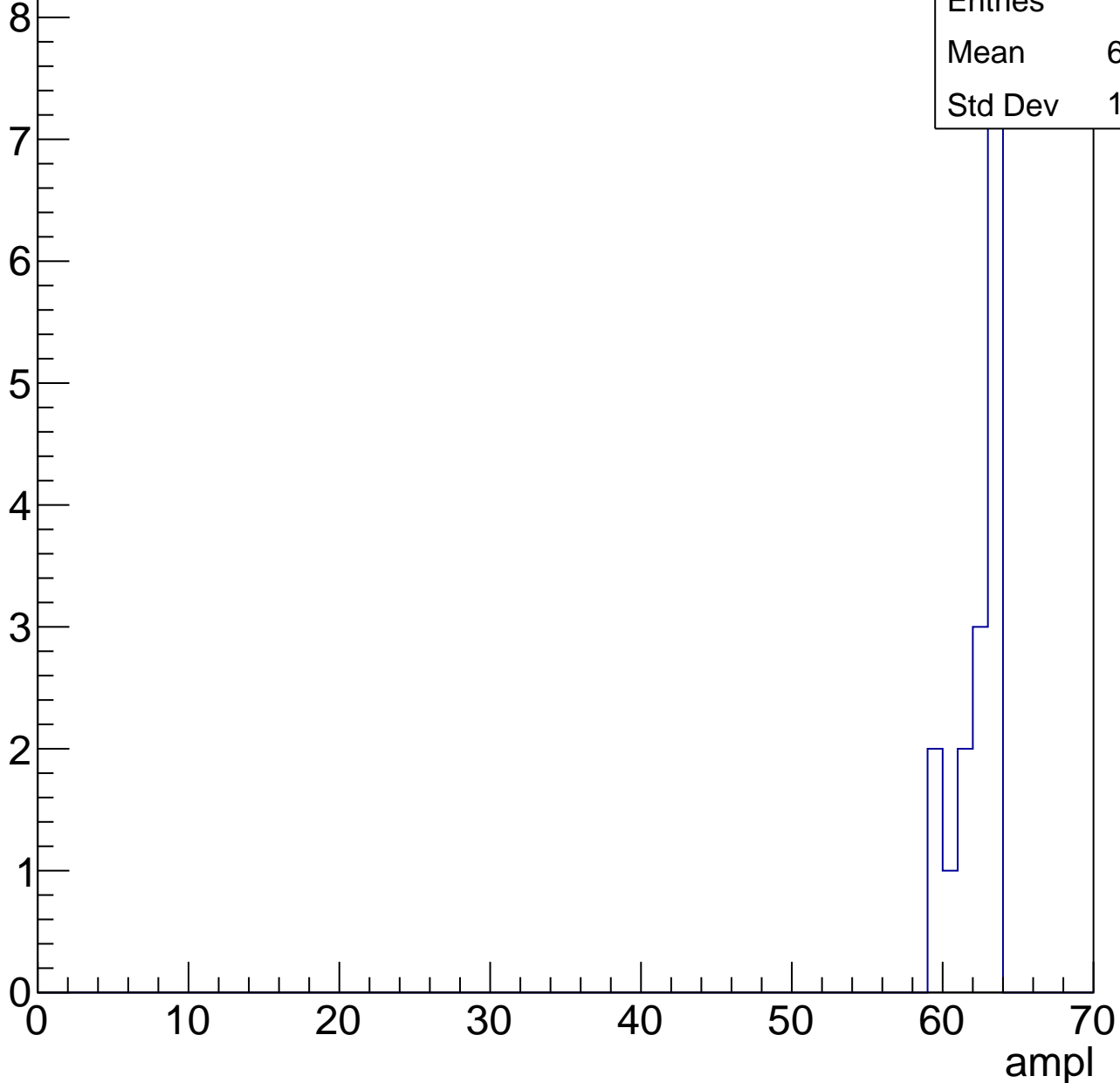


B0L101S, U20-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

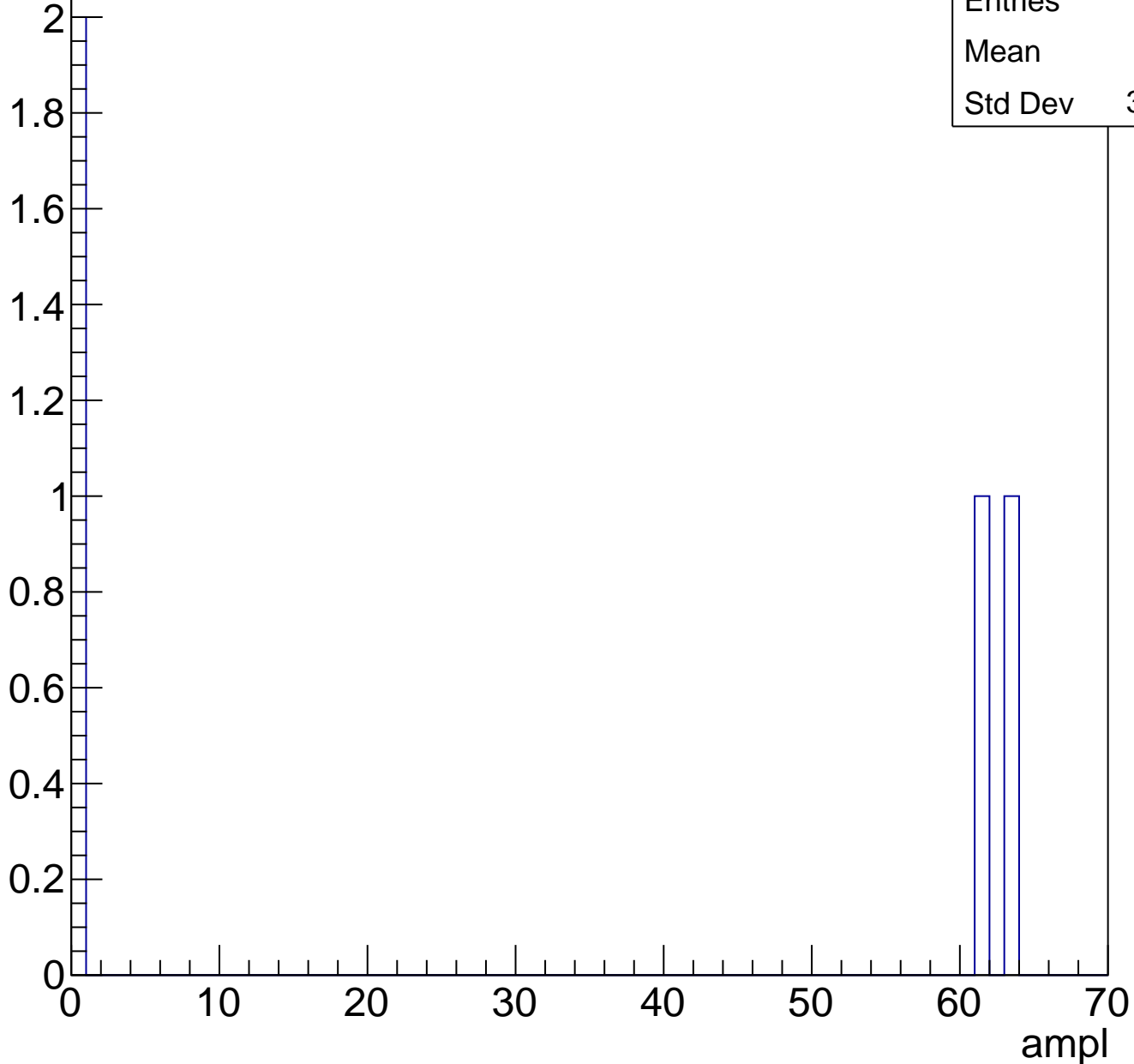
Entries	16
Mean	61.88
Std Dev	1.409



B0L101S, U20-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	31
Std Dev	31.01

B0L101S, U20-ch120, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	27.31
Std Dev	6.271

Gaus mean : 28.1288

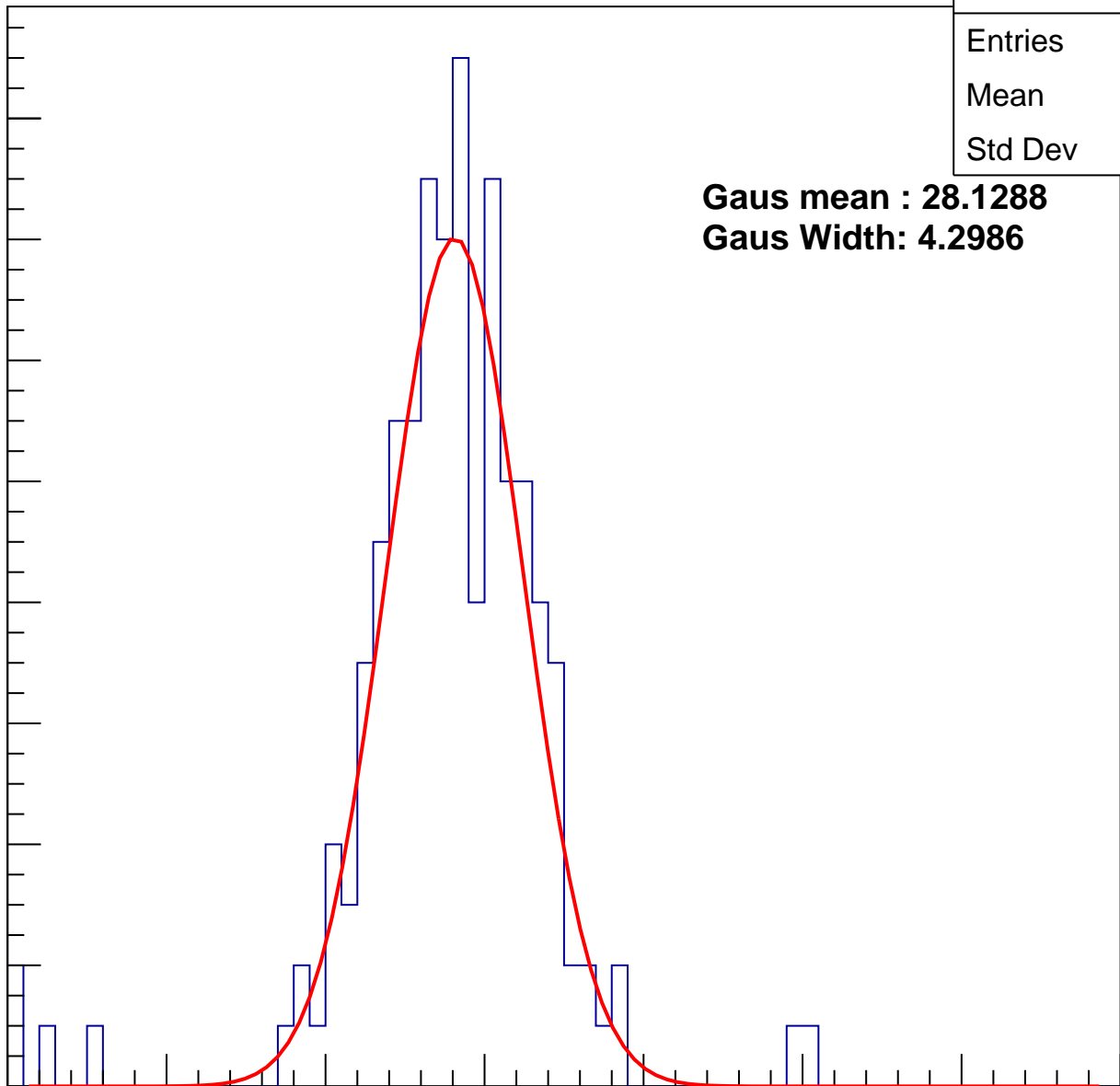
Gaus Width: 4.2986

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch120, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	34.77
Std Dev	4.33

Gaus mean : 35.2731

Gaus Width: 4.0594

Entry

12

10

8

6

4

2

0

0

10

20

30

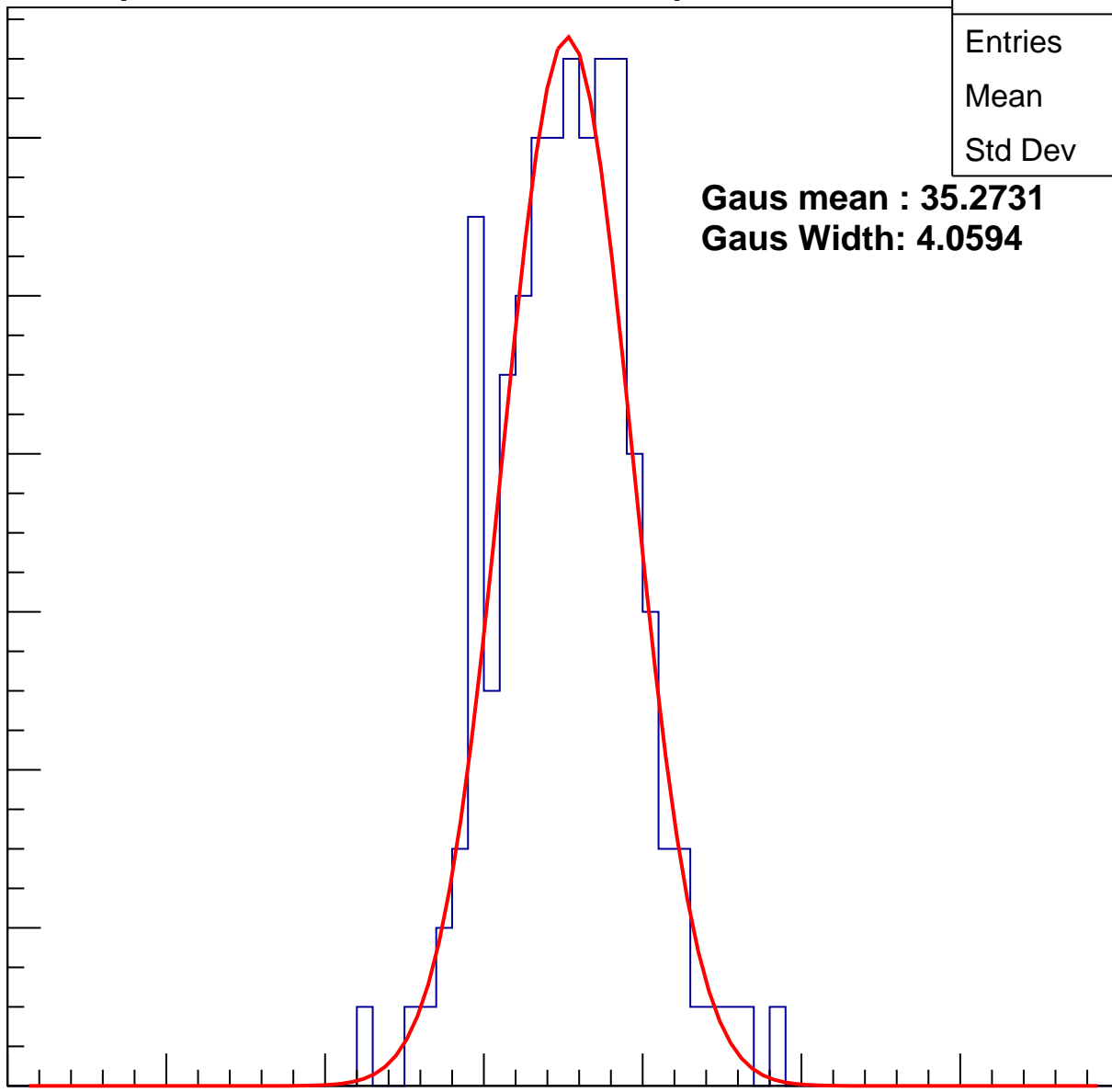
40

50

60

70

ampl



B0L101S, U20-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

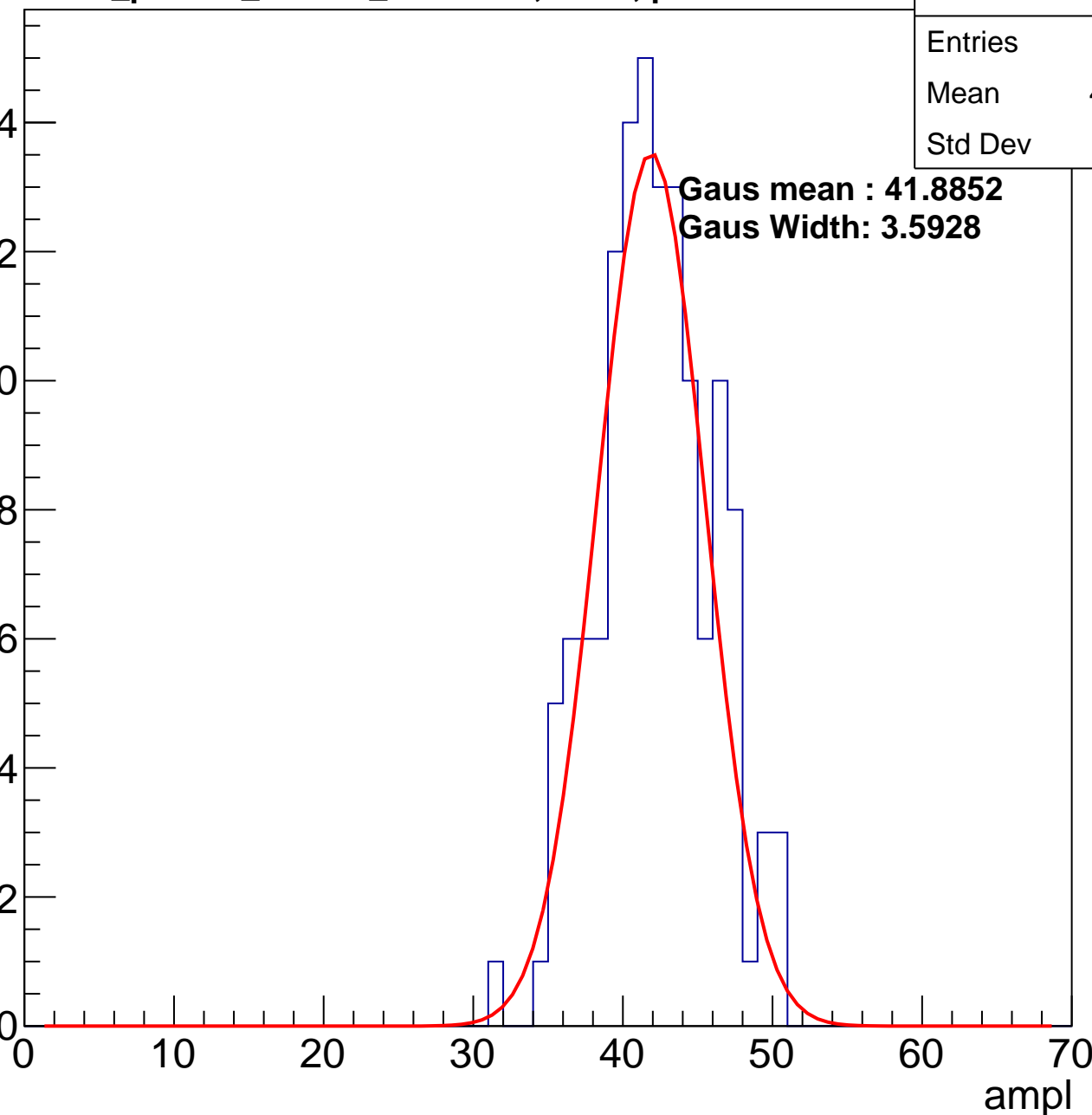
Entry

14
12
10
8
6
4
2
0

Entries	133
Mean	41.69
Std Dev	3.78

Gaus mean : 41.8852

Gaus Width: 3.5928



ampl

B0L101S, U20-ch120, adc3

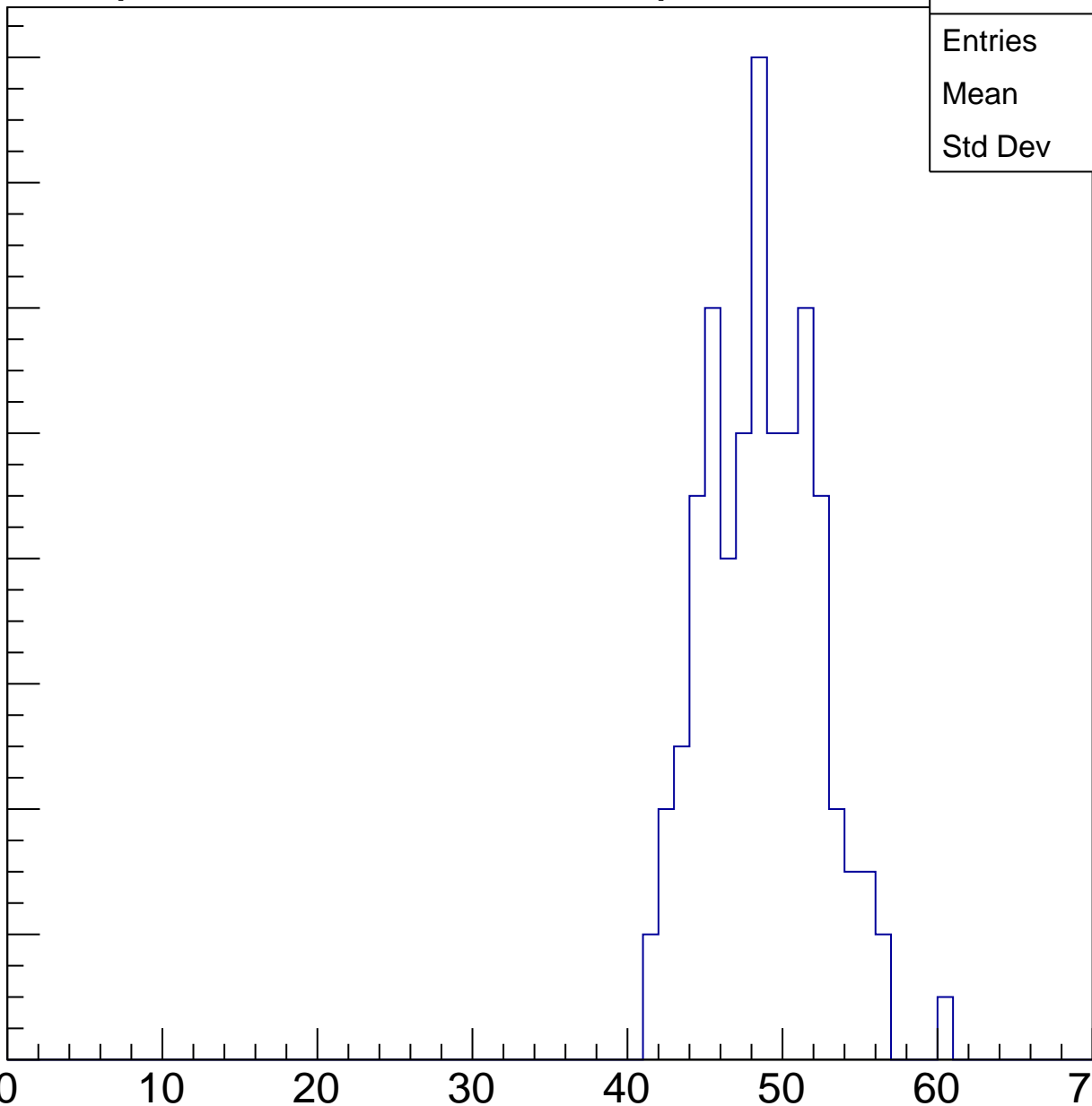
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	48.23
Std Dev	3.633

ampl



B0L101S, U20-ch120, adc4

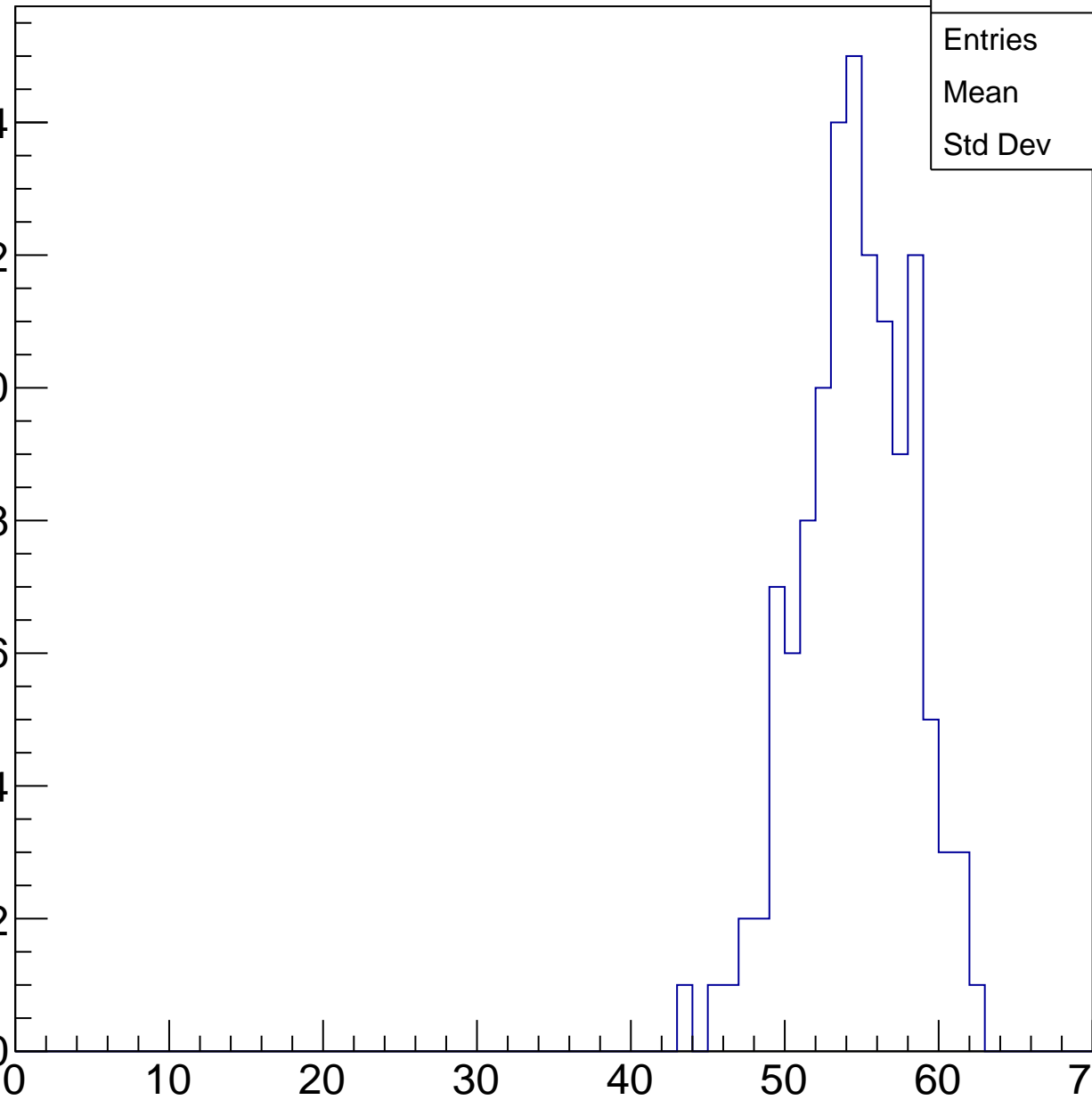
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	54.08
Std Dev	3.613

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch120, adc5

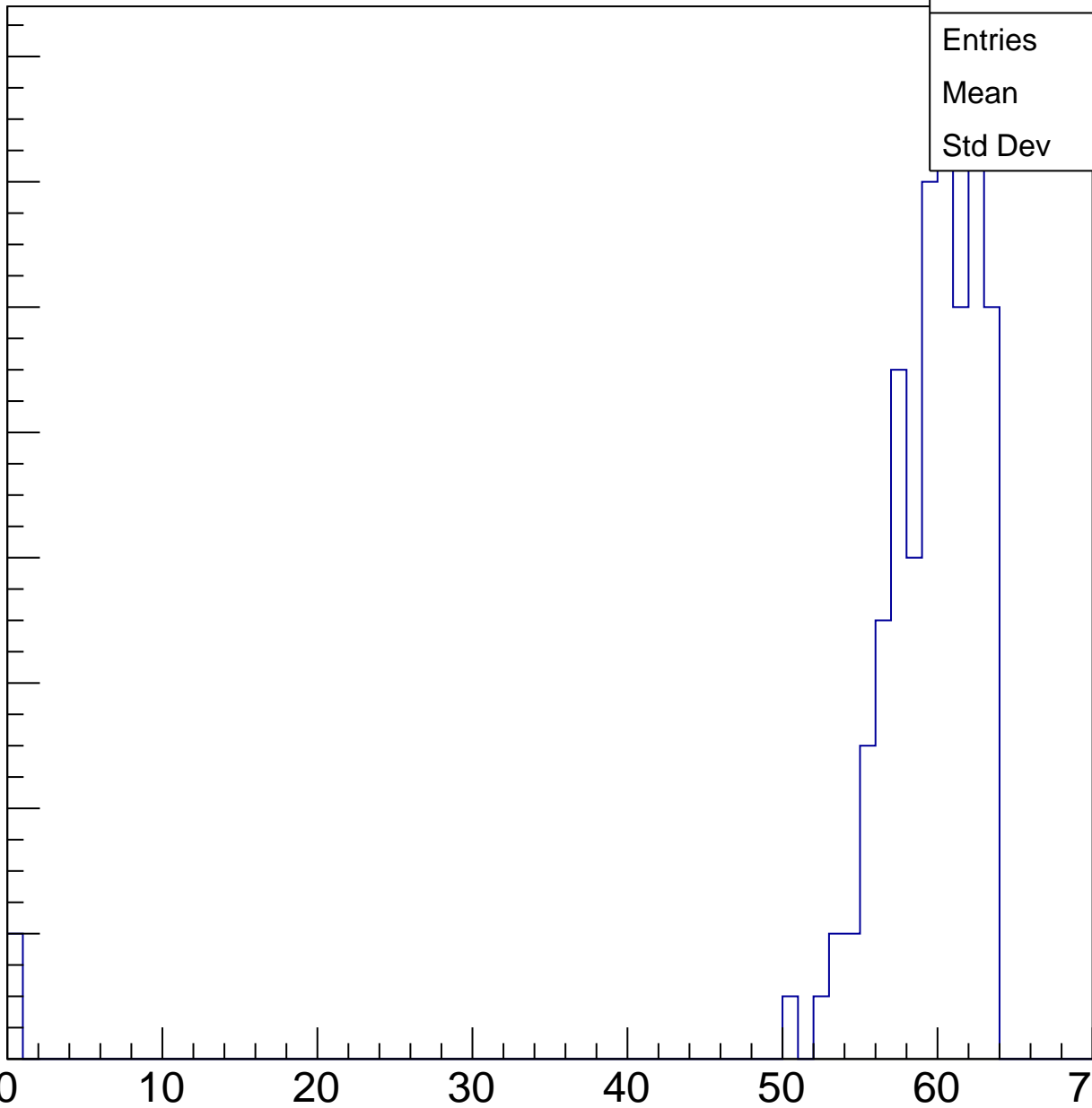
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	58.15
Std Dev	8.46

ampl

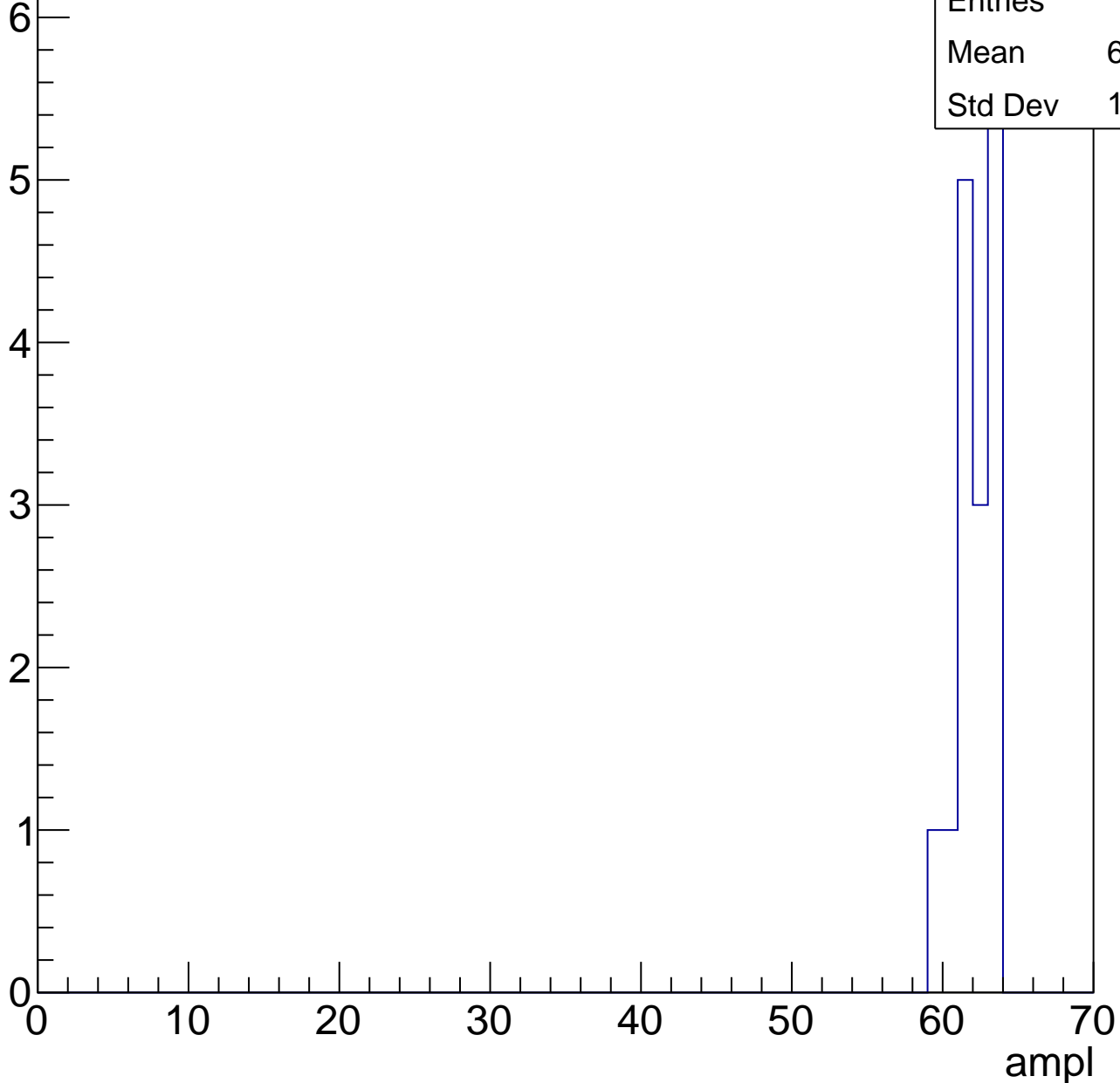


B0L101S, U20-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.75
Std Dev	1.199



B0L101S, U20-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch121, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	29
Std Dev	3.506

Gaus mean : 29.6068

Gaus Width: 3.5419

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

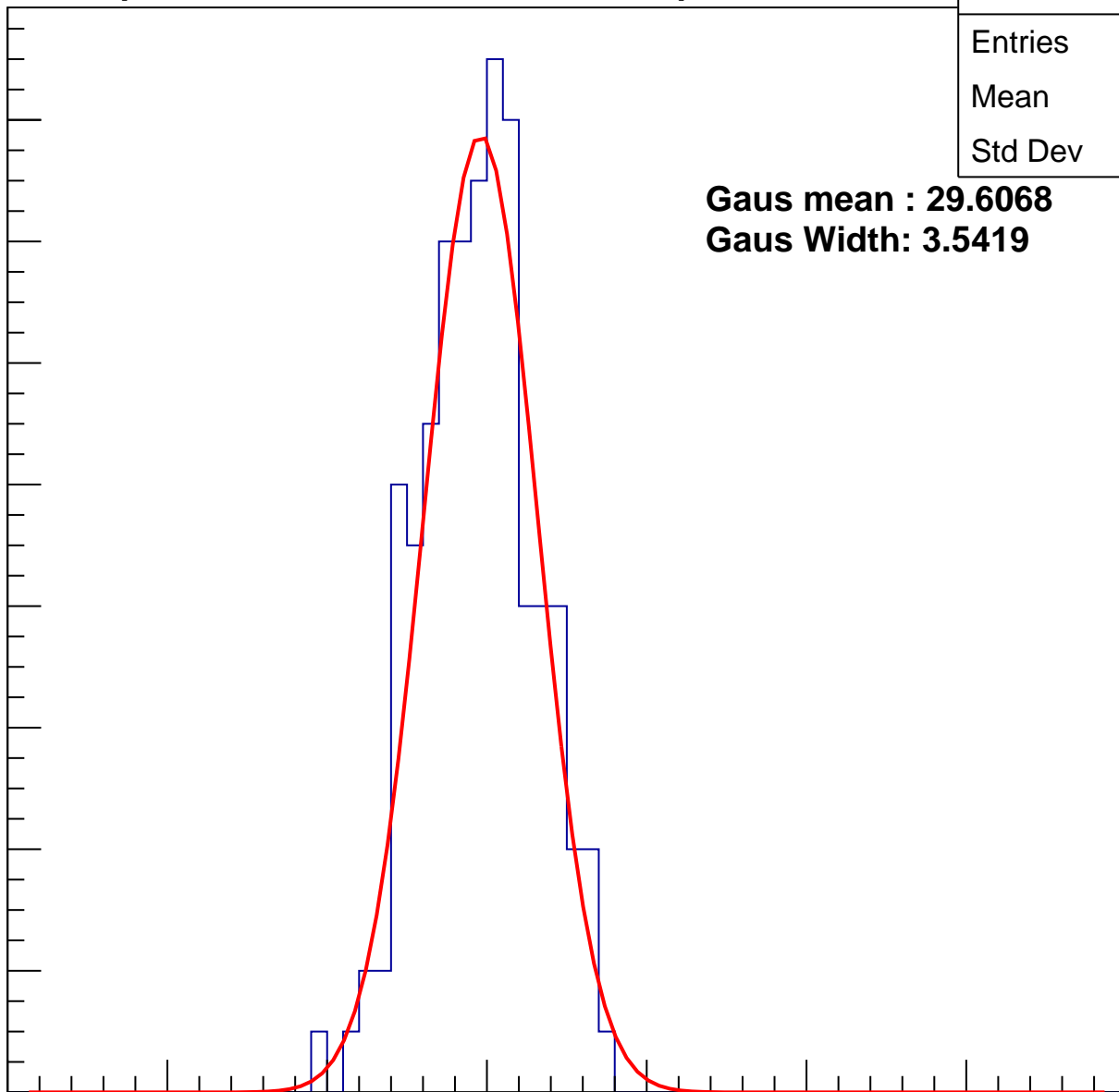
40

50

60

70

ampl



B0L101S, U20-ch121, adc1

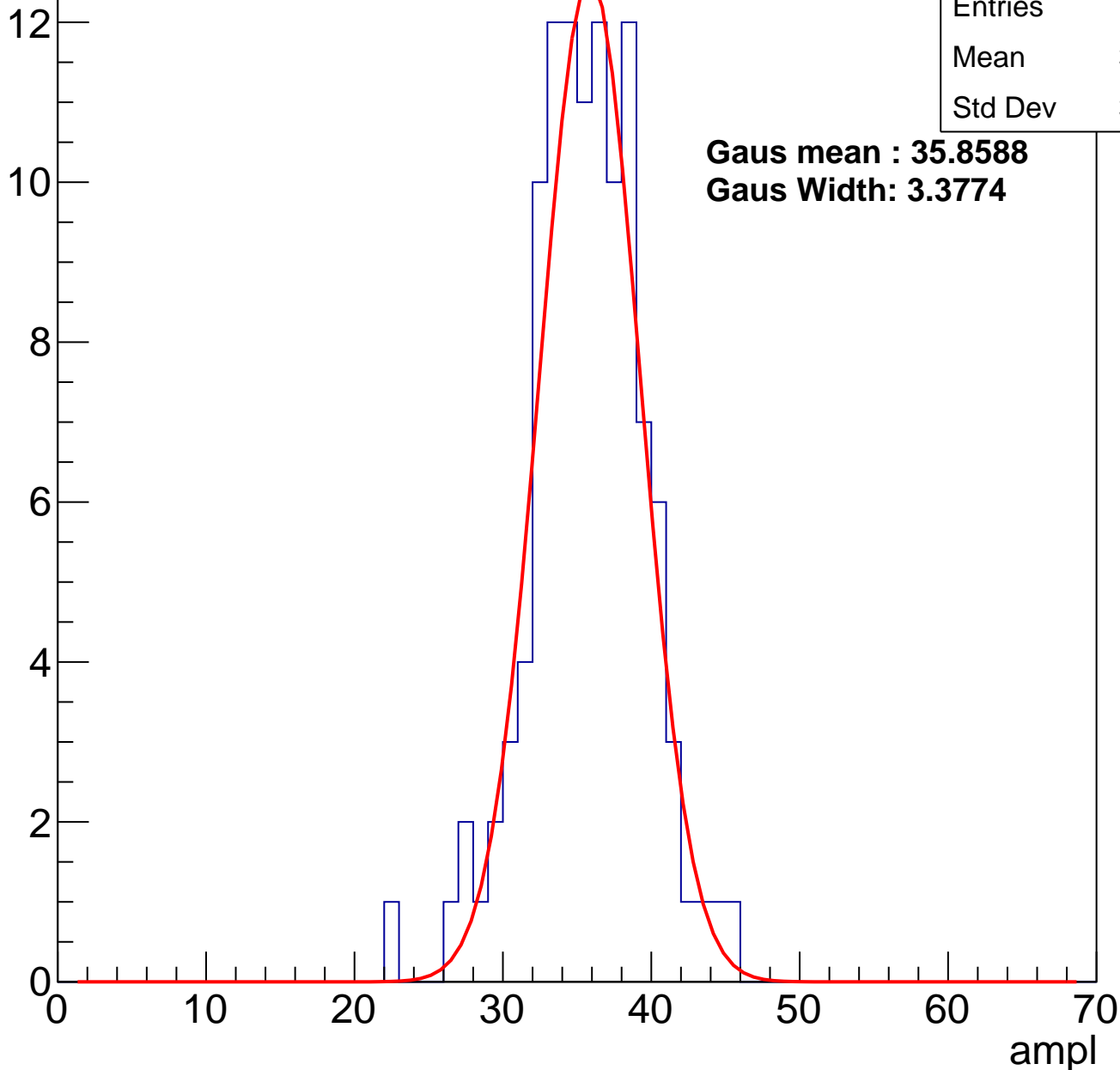
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.21
Std Dev	3.771

Gaus mean : 35.8588

Gaus Width: 3.3774

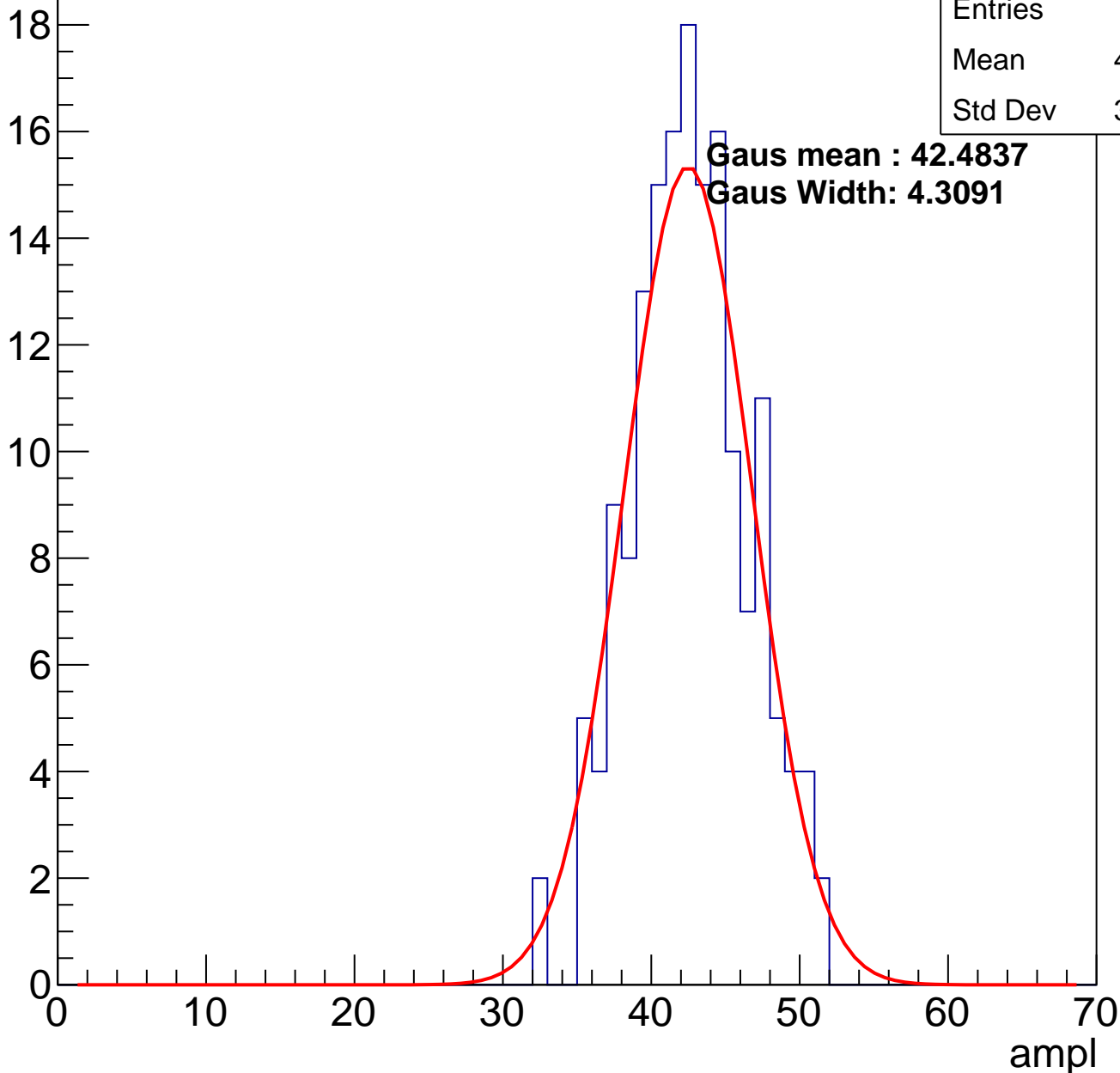
Entry



B0L101S, U20-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch121, adc3

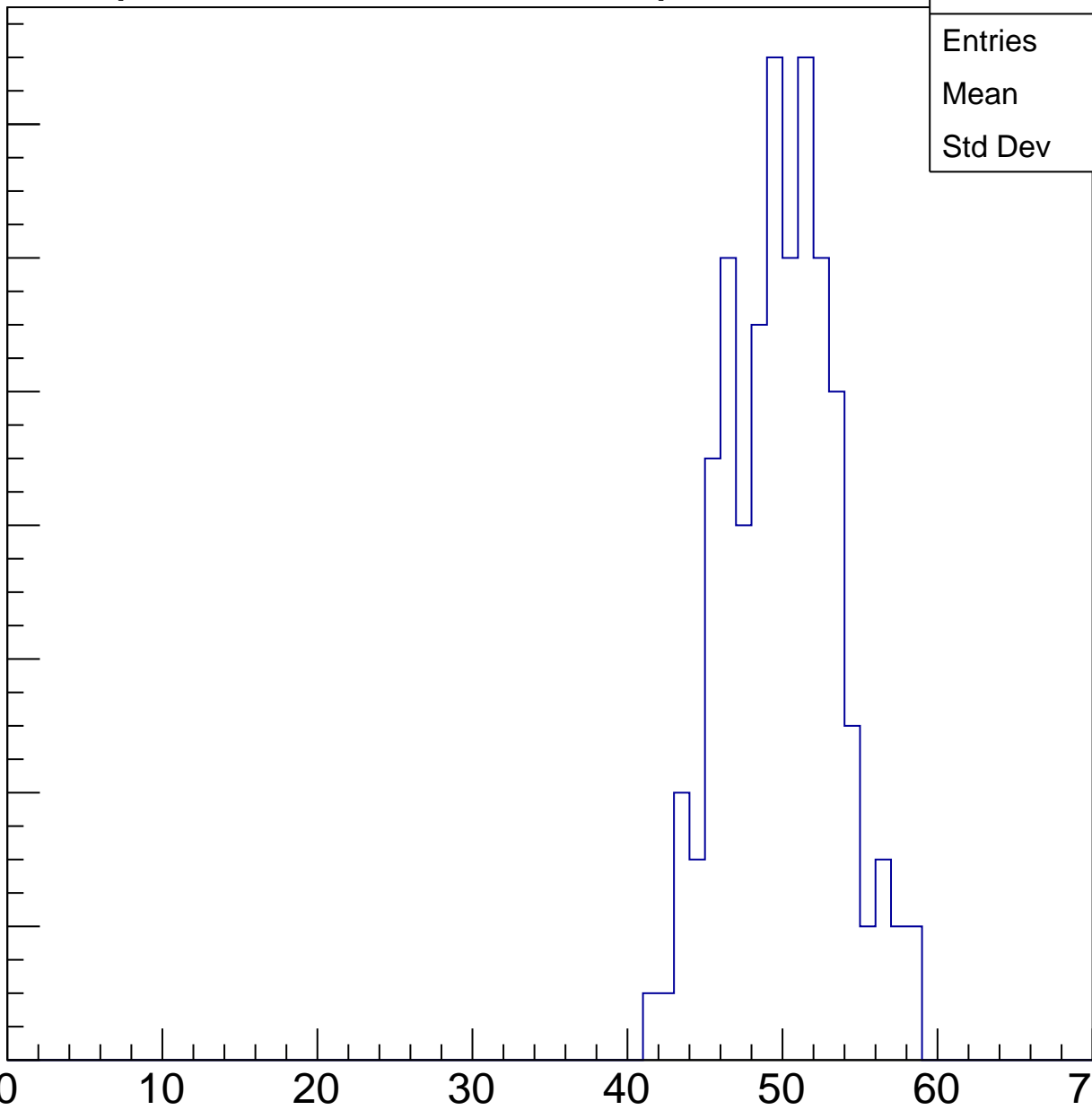
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	49.45
Std Dev	3.522

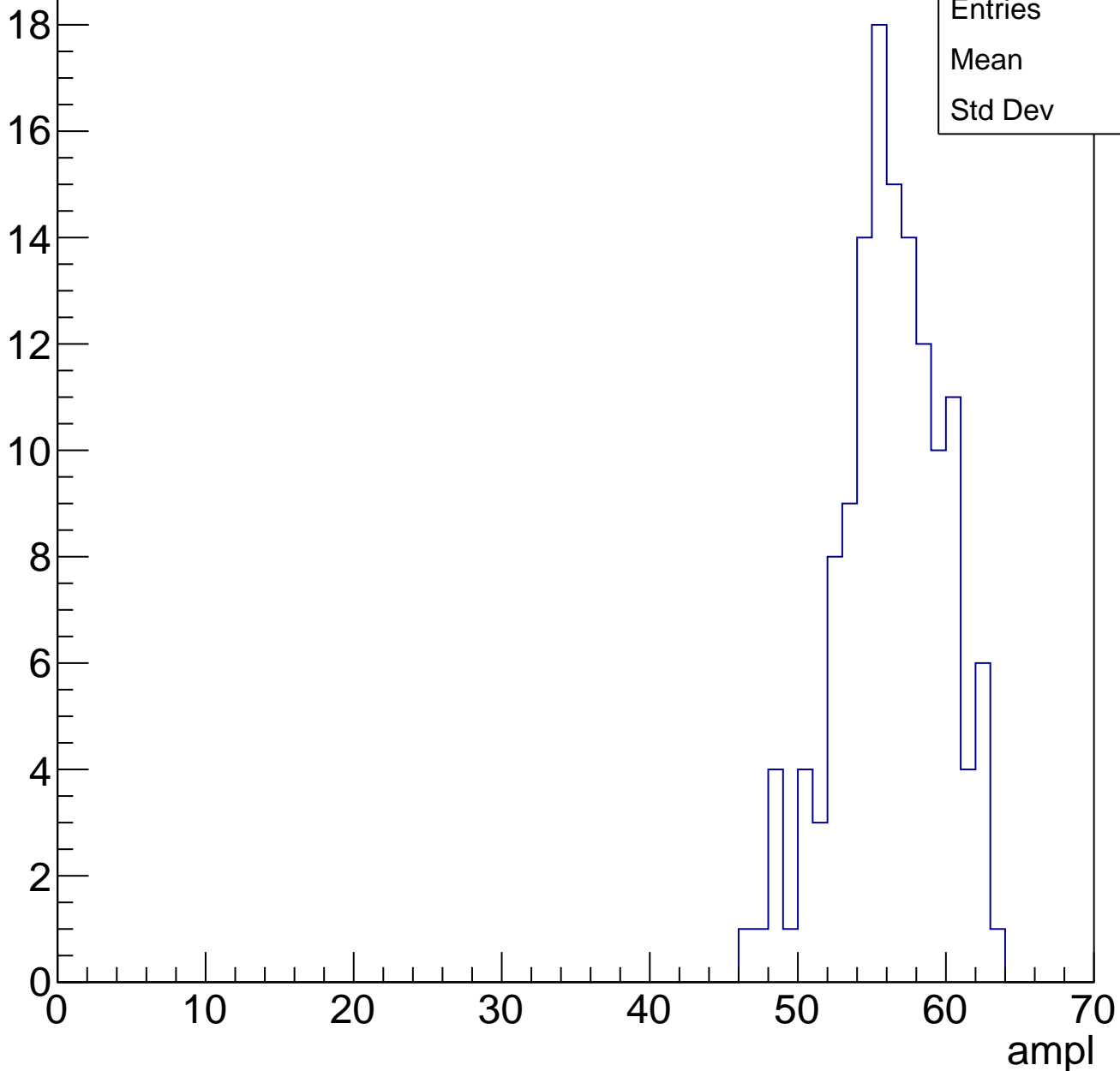
ampl



B0L101S, U20-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch121, adc5

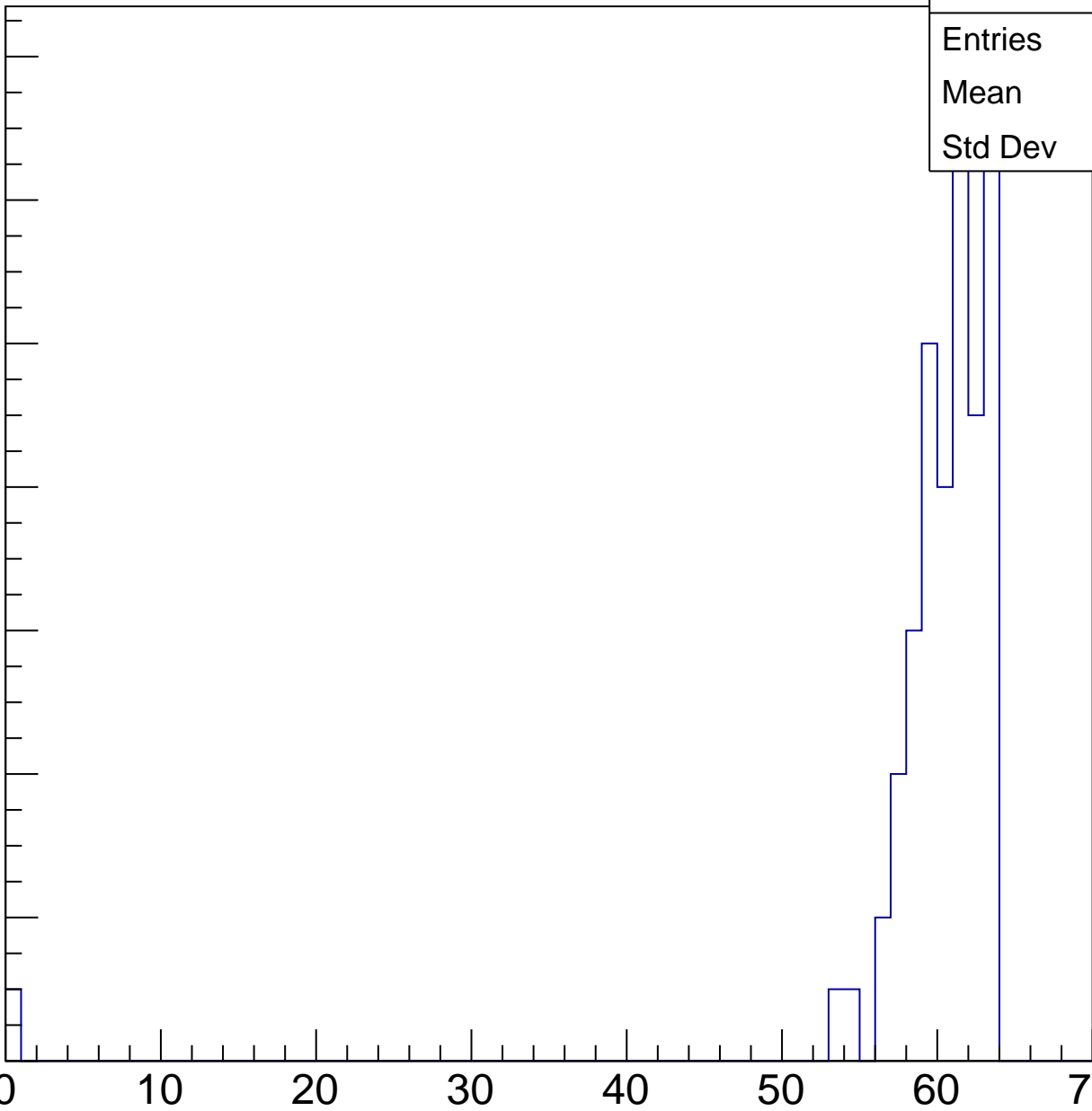
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.41
Std Dev	7.501

ampl

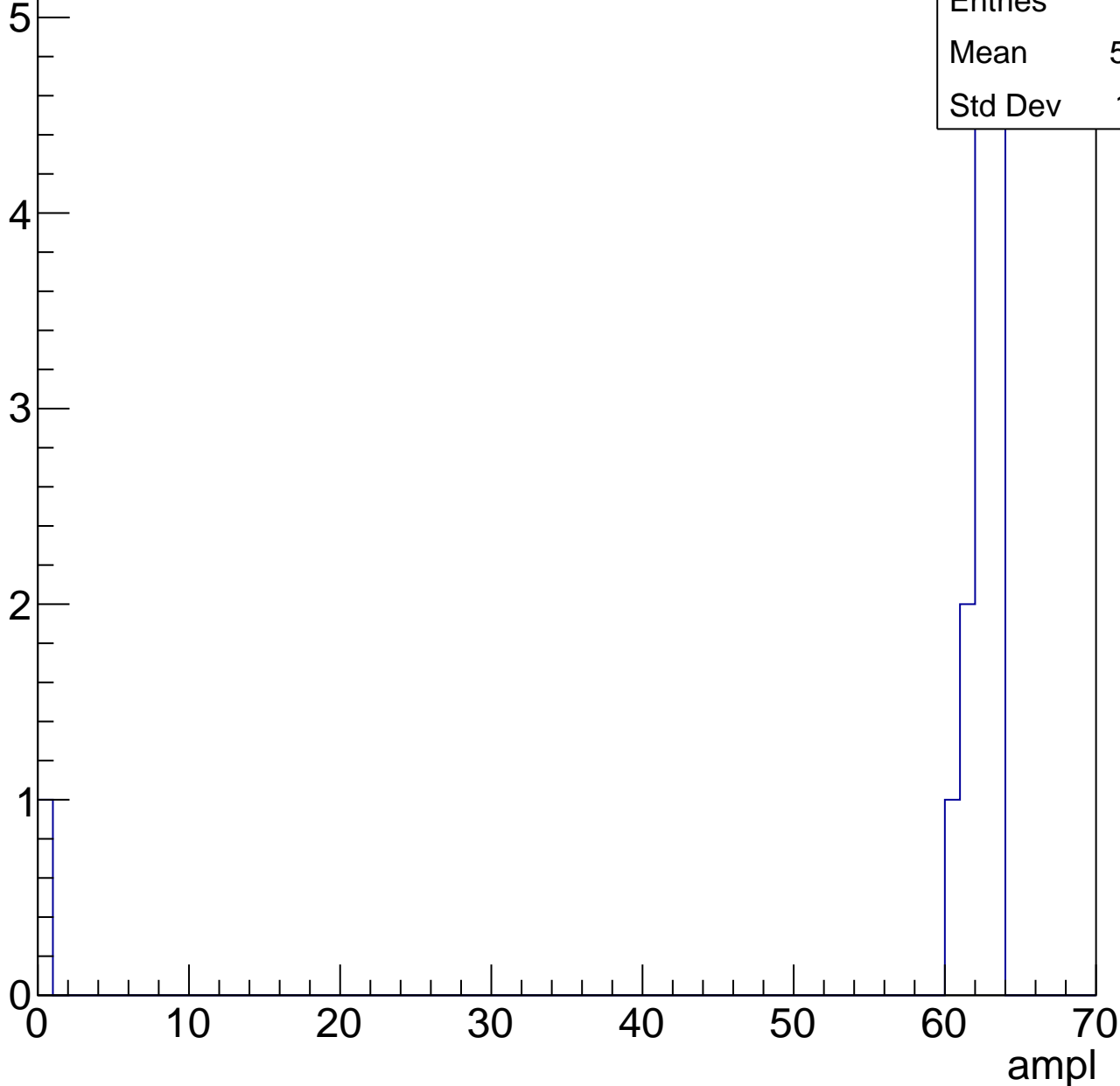


B0L101S, U20-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

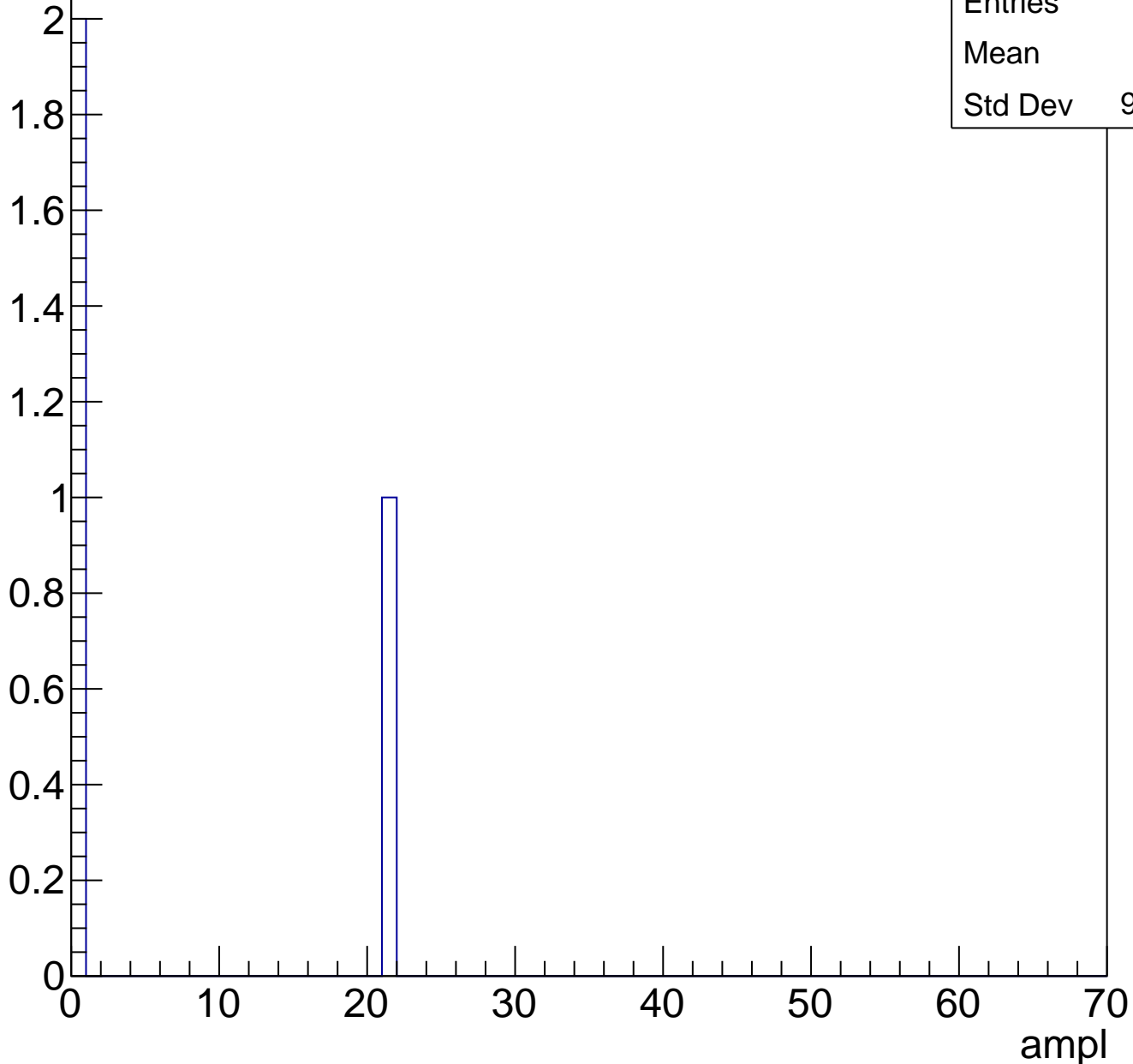
Entries	14
Mean	57.64
Std Dev	16.01



B0L101S, U20-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

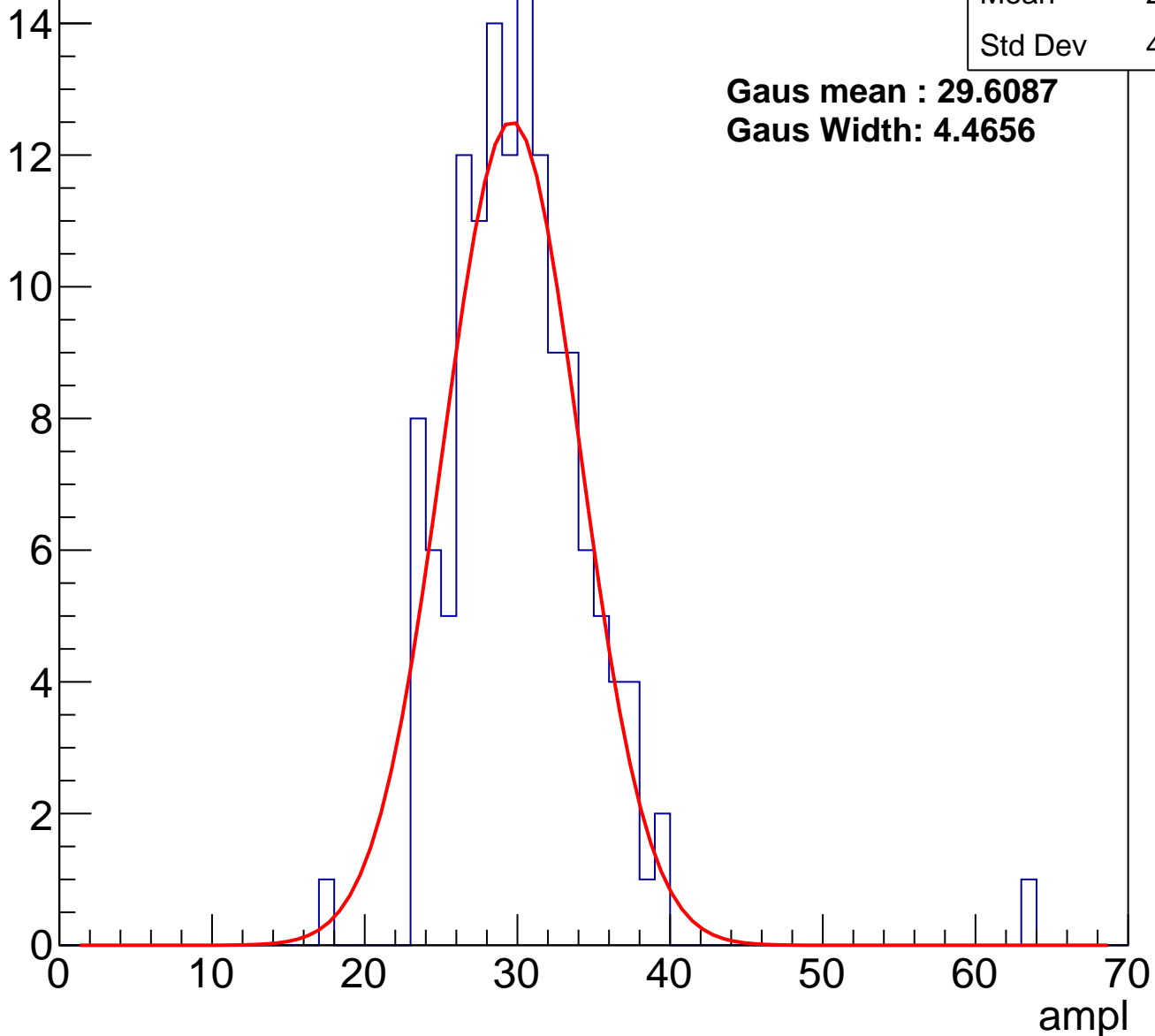
Entry



B0L101S, U20-ch122, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch122, adc1

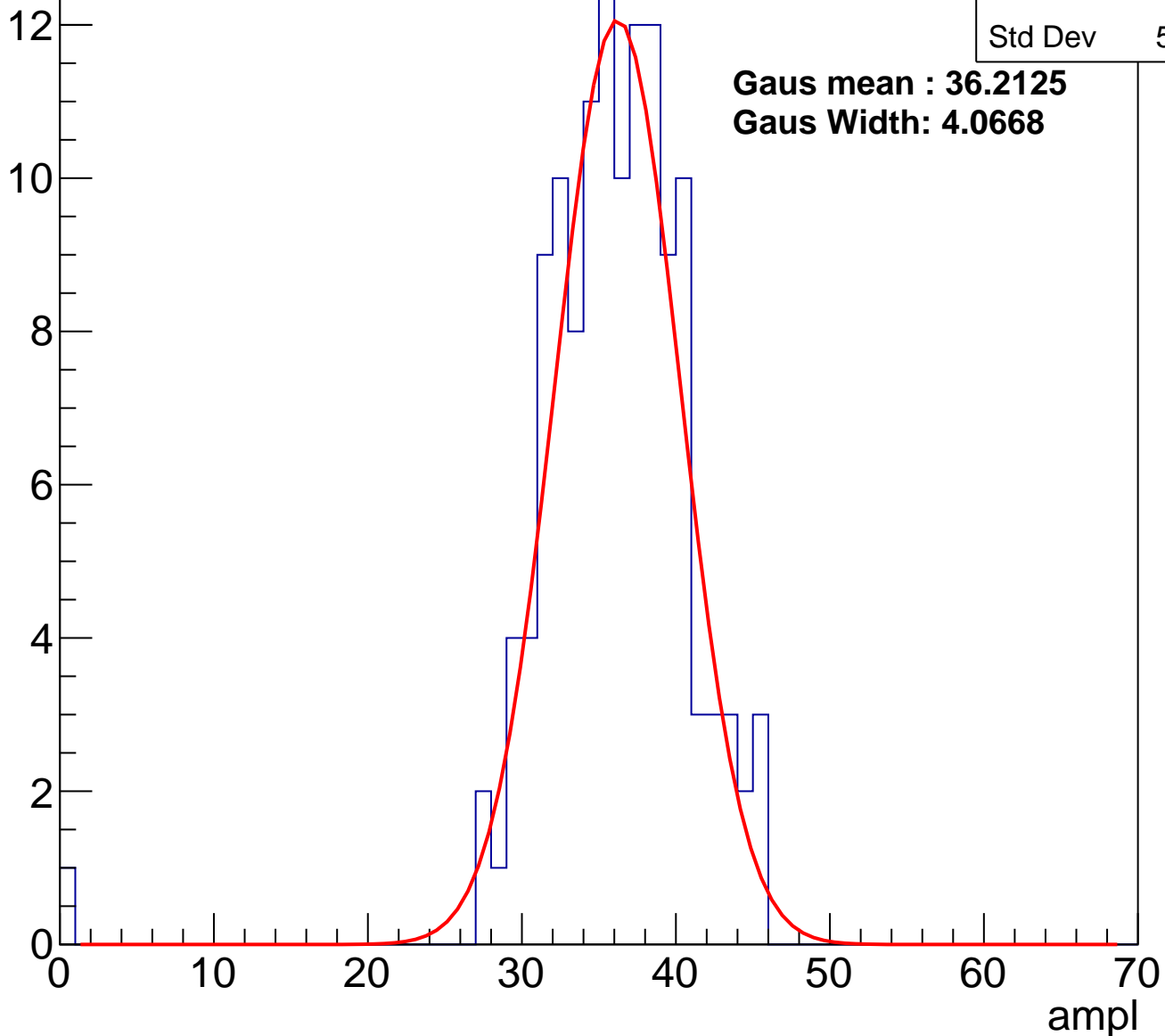
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	35.55
Std Dev	5.078

Gaus mean : 36.2125

Gaus Width: 4.0668

Entry



B0L101S, U20-ch122, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	42.02
Std Dev	4.06

Gaus mean : 42.7043

Gaus Width: 3.9664

Entry

12

10

8

6

4

2

0

0

10

20

30

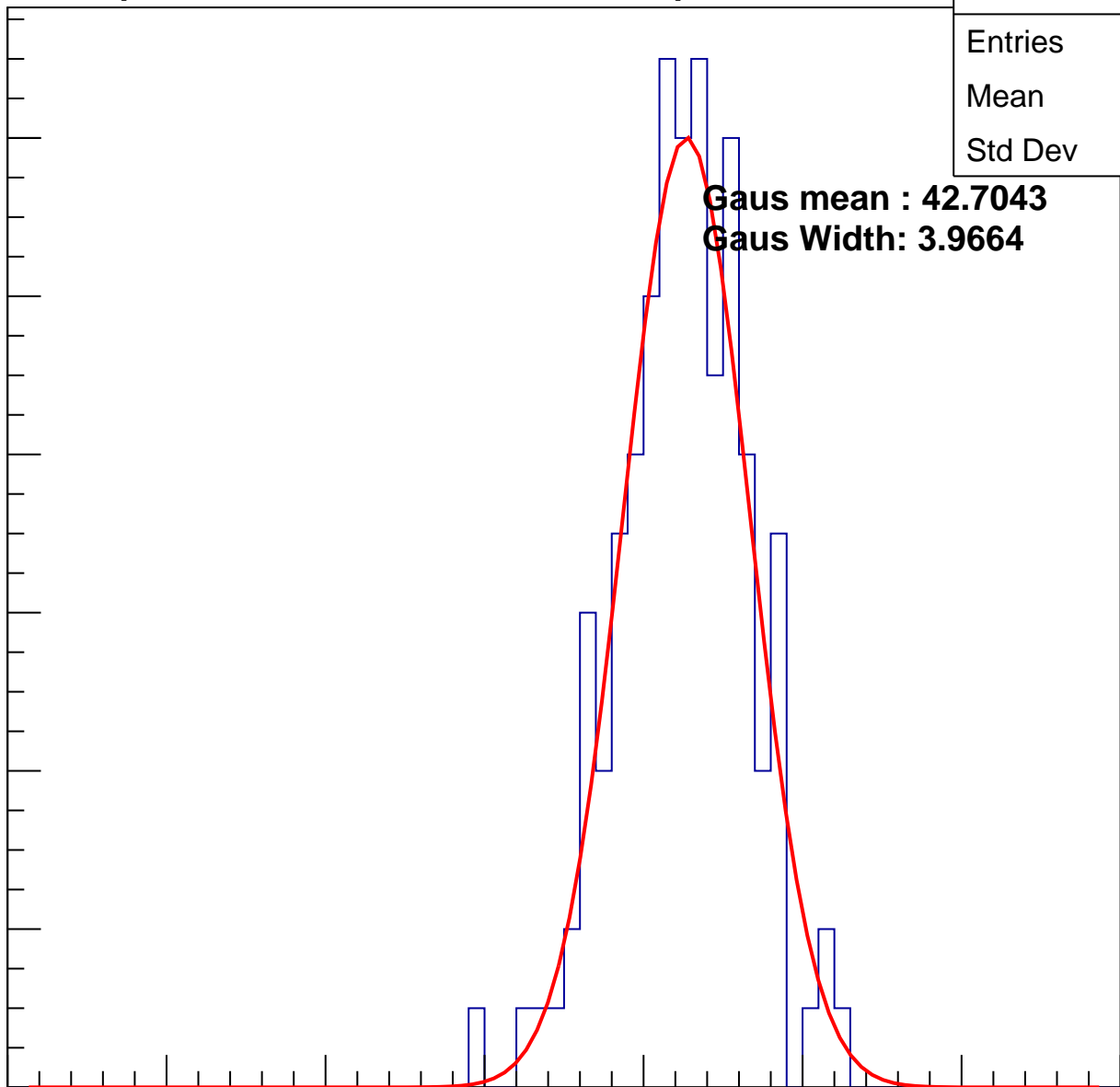
40

50

60

70

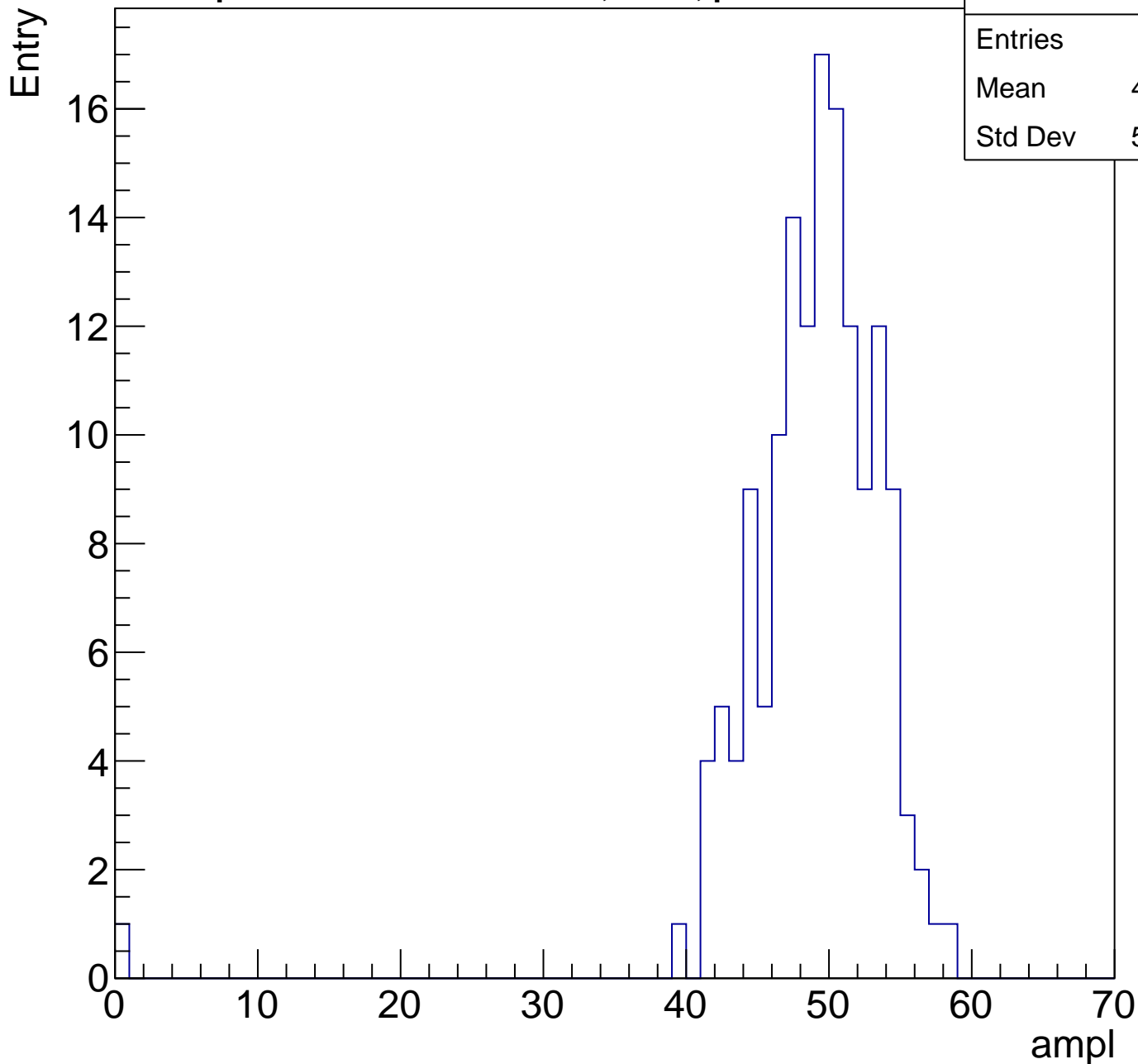
ampl



B0L101S, U20-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	48.48
Std Dev	5.522



B0L101S, U20-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	55.38
Std Dev	3.759

Entry

12

10

8

6

4

2

0

0

10

20

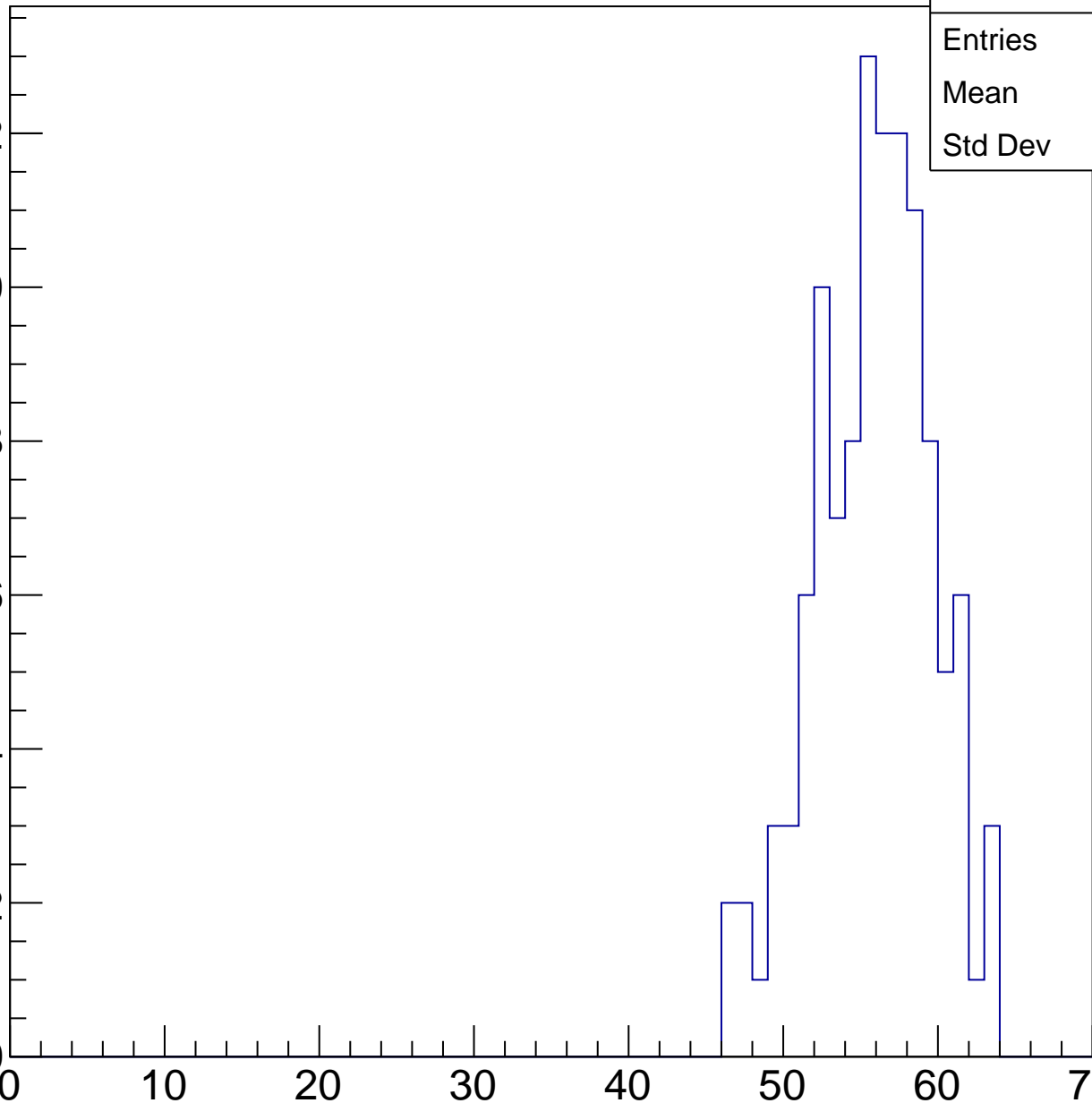
30

40

ampl

60

70



B0L101S, U20-ch122, adc5

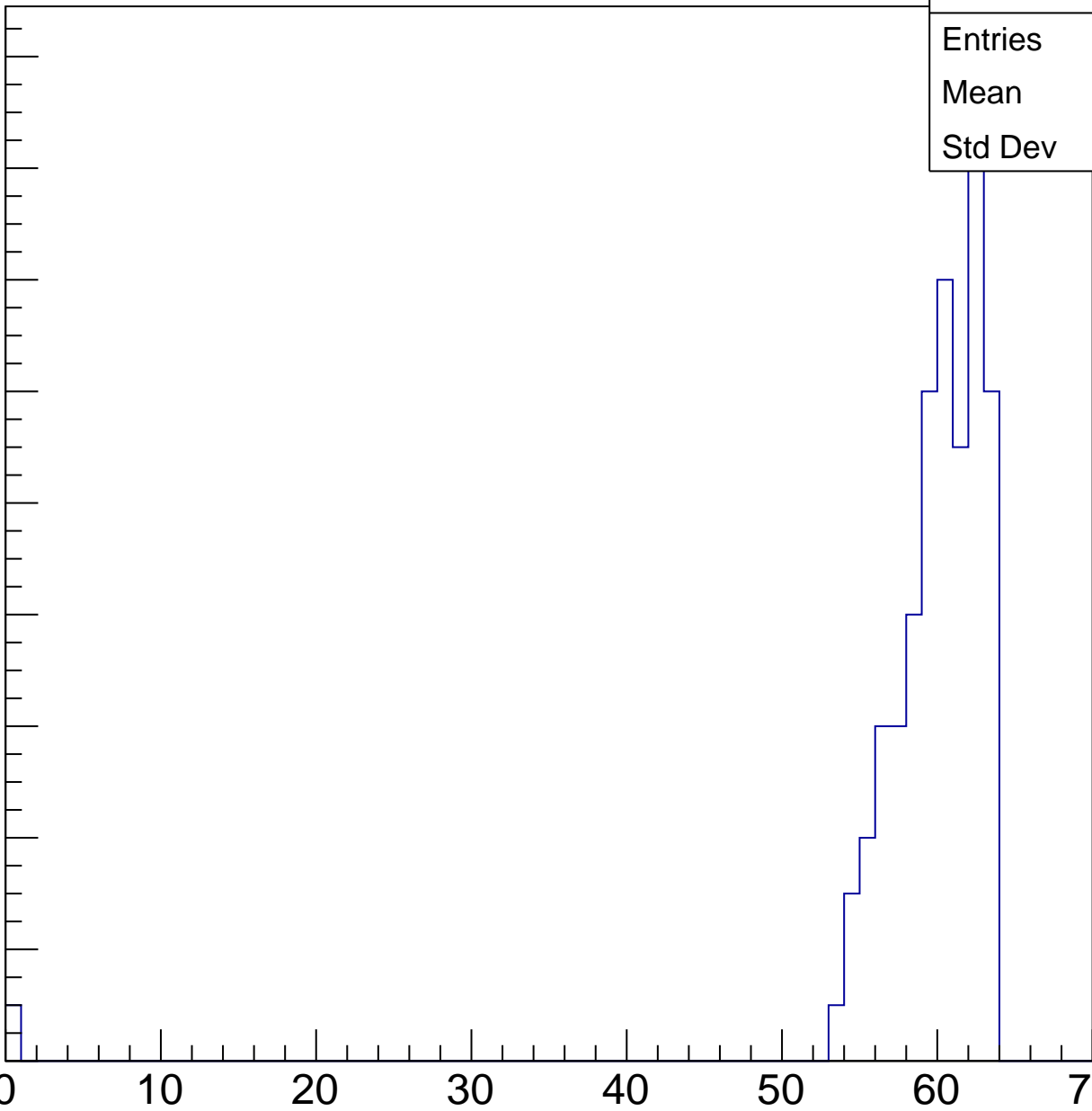
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.04
Std Dev	6.578

ampl

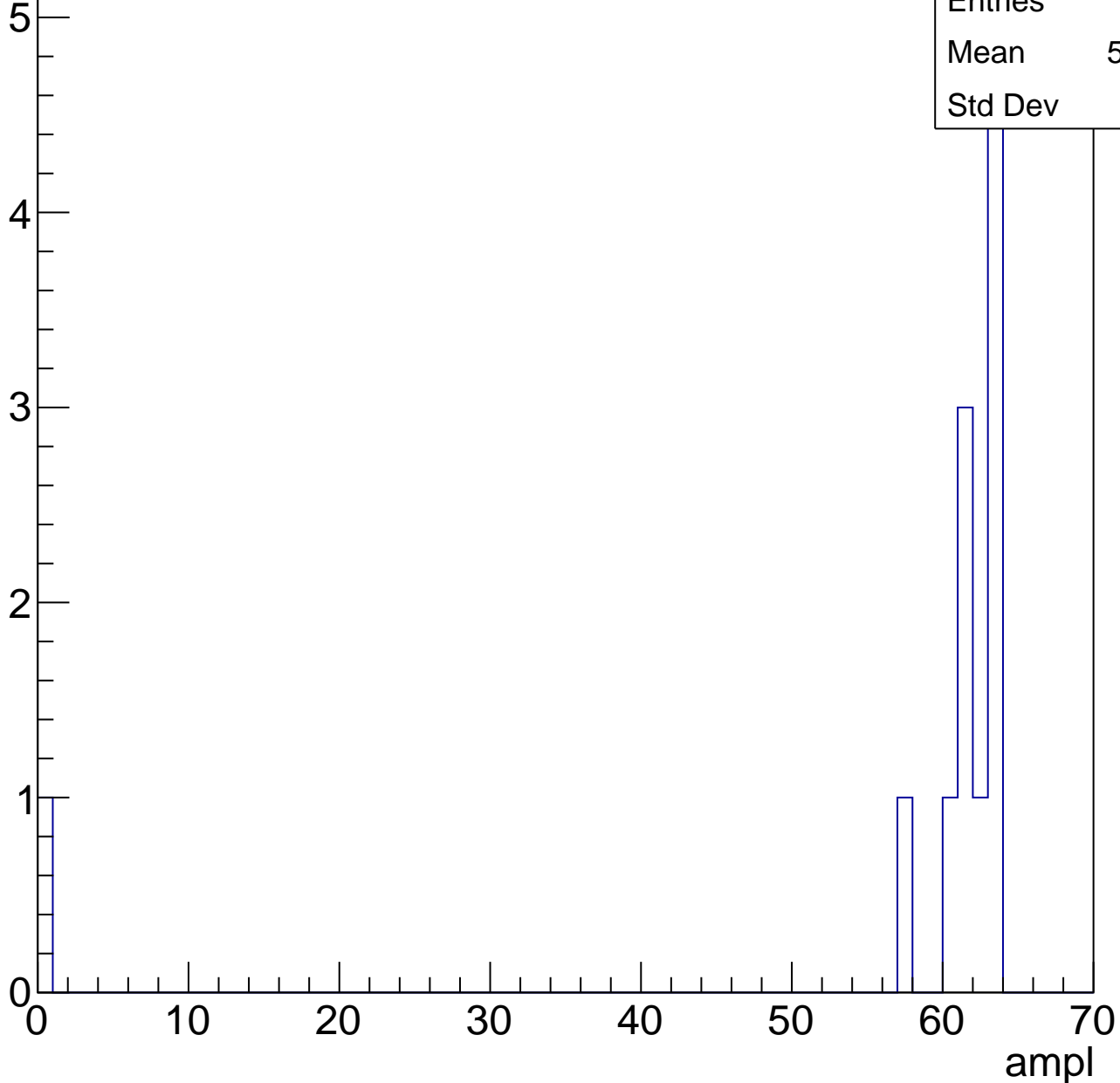


B0L101S, U20-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.42
Std Dev	17.1



B0L101S, U20-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U20-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	27.96
Std Dev	4.263

Gaus mean : 28.2790

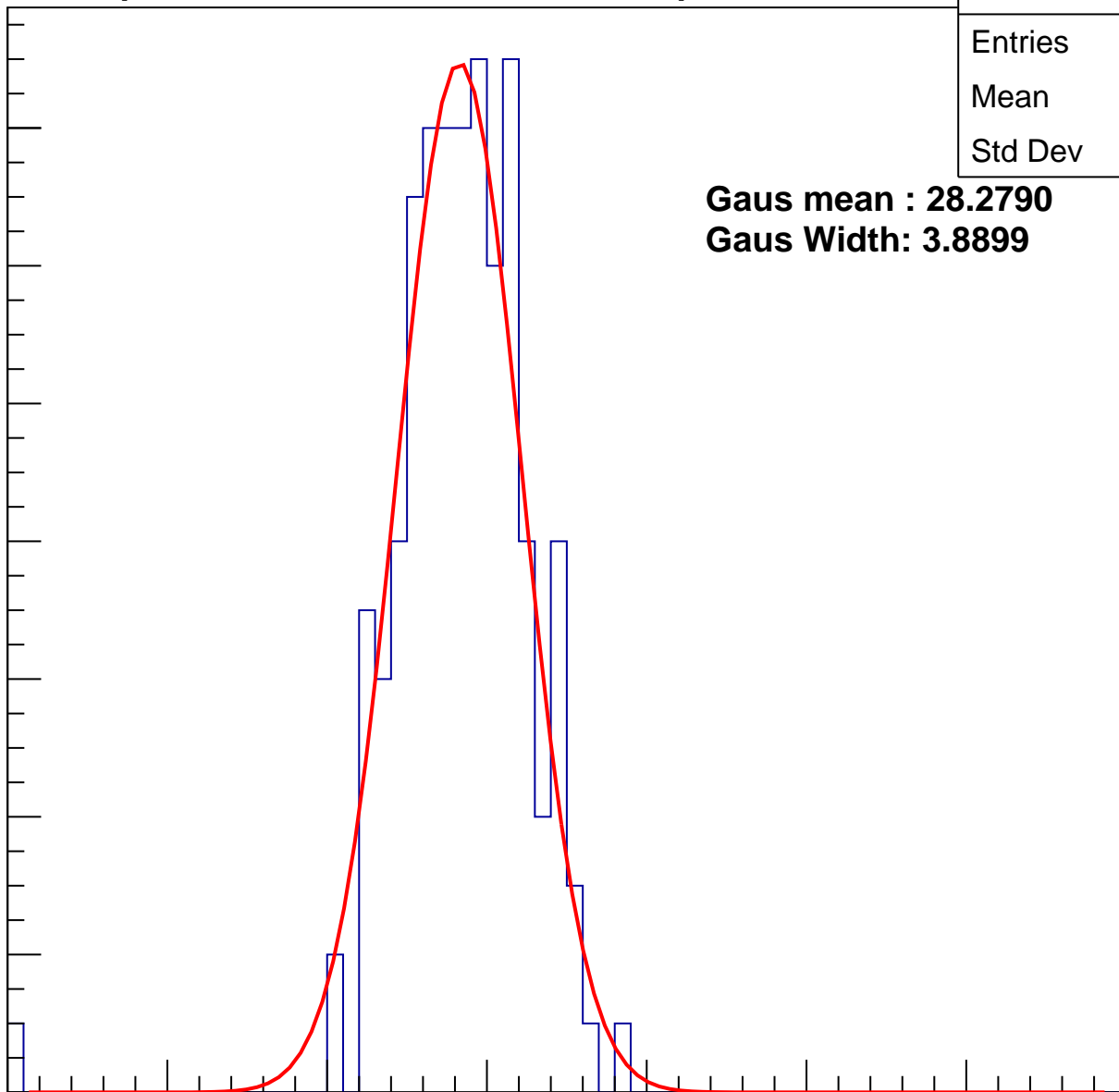
Gaus Width: 3.8899

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U20-ch123, adc1

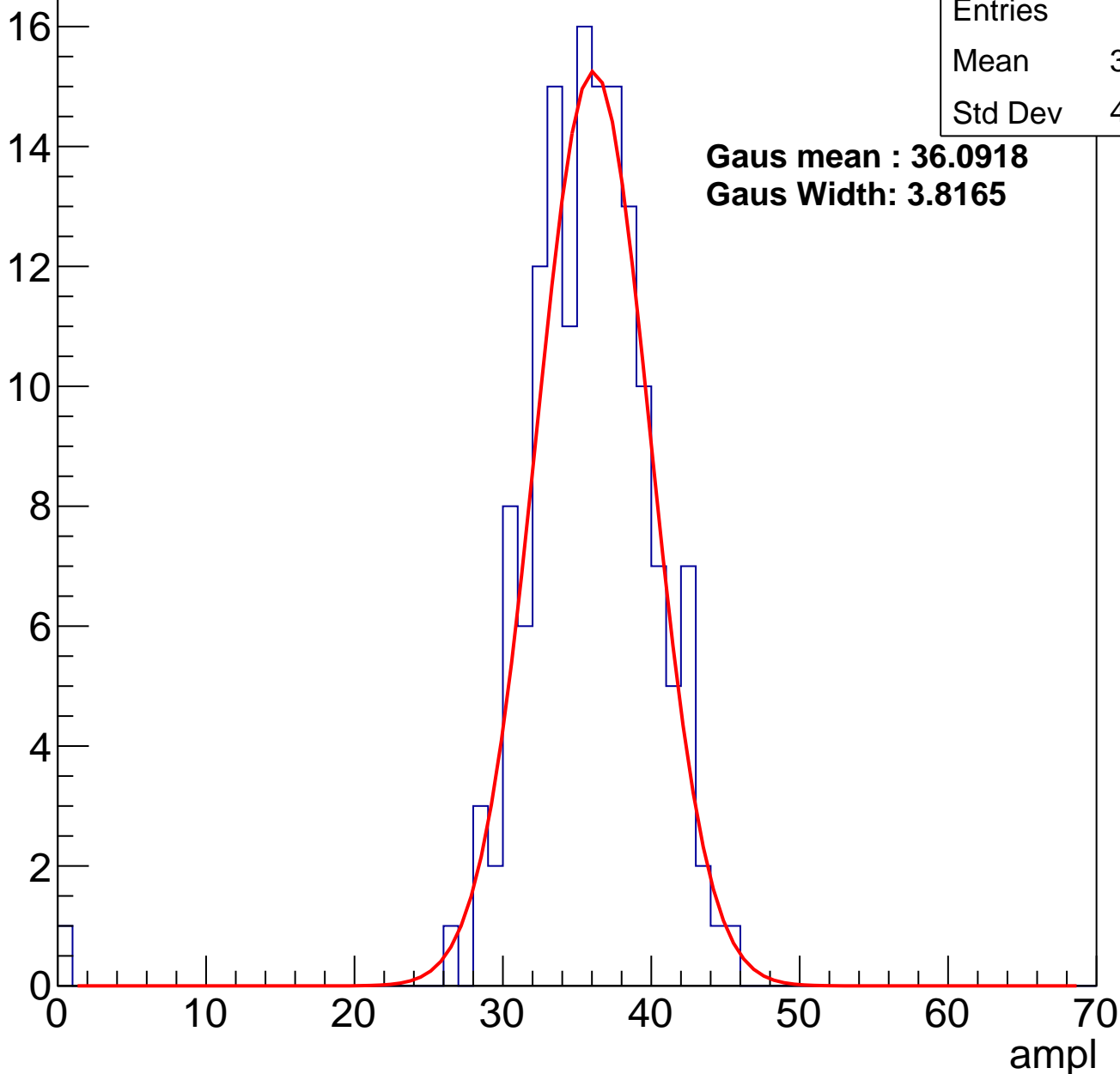
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	35.36
Std Dev	4.702

Gaus mean : 36.0918

Gaus Width: 3.8165

Entry



B0L101S, U20-ch123, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

42.47

Std Dev

3.583

Gaus mean : 43.0121

Gaus Width: 3.6962

0

10

20

30

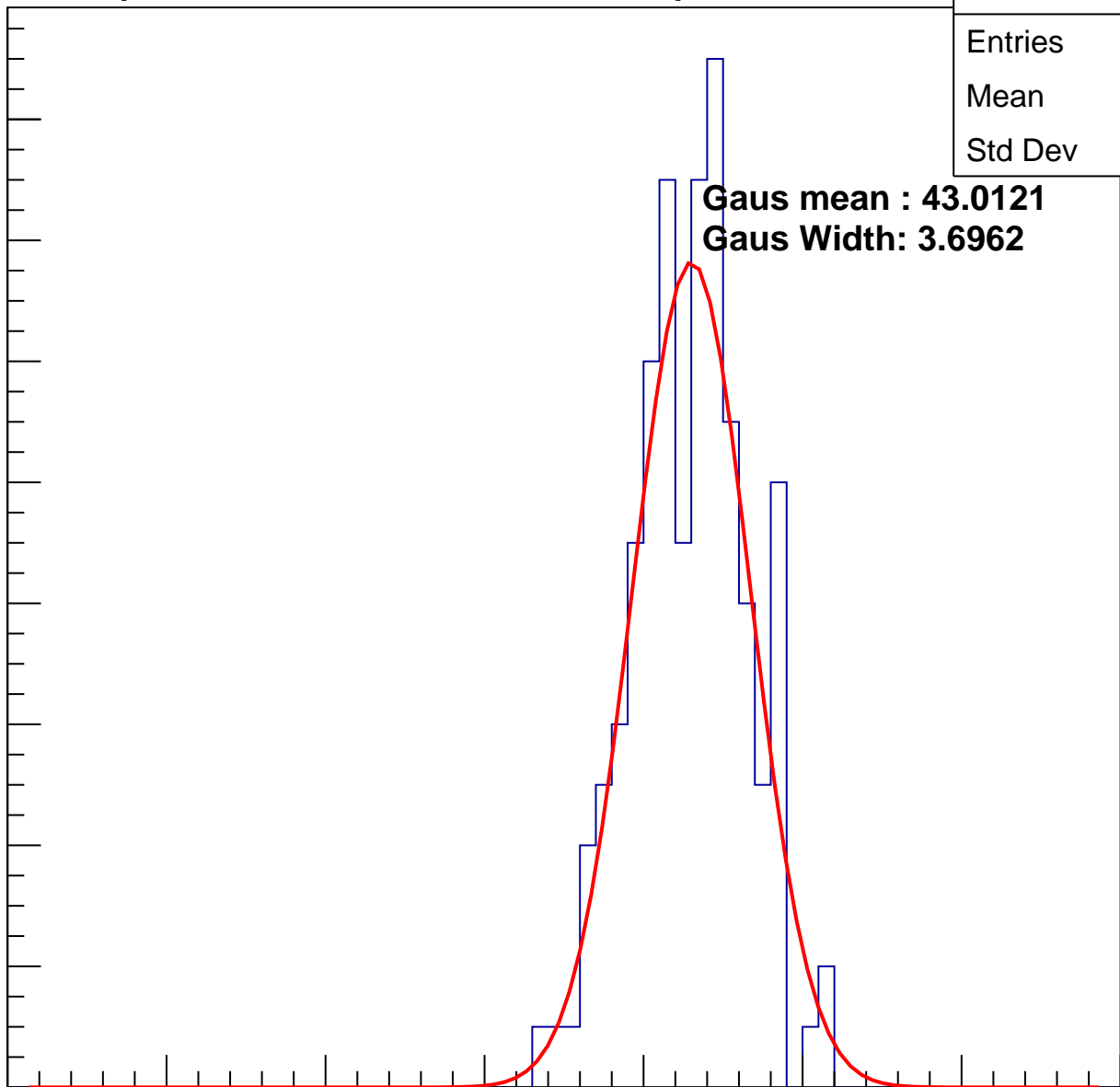
40

50

60

70

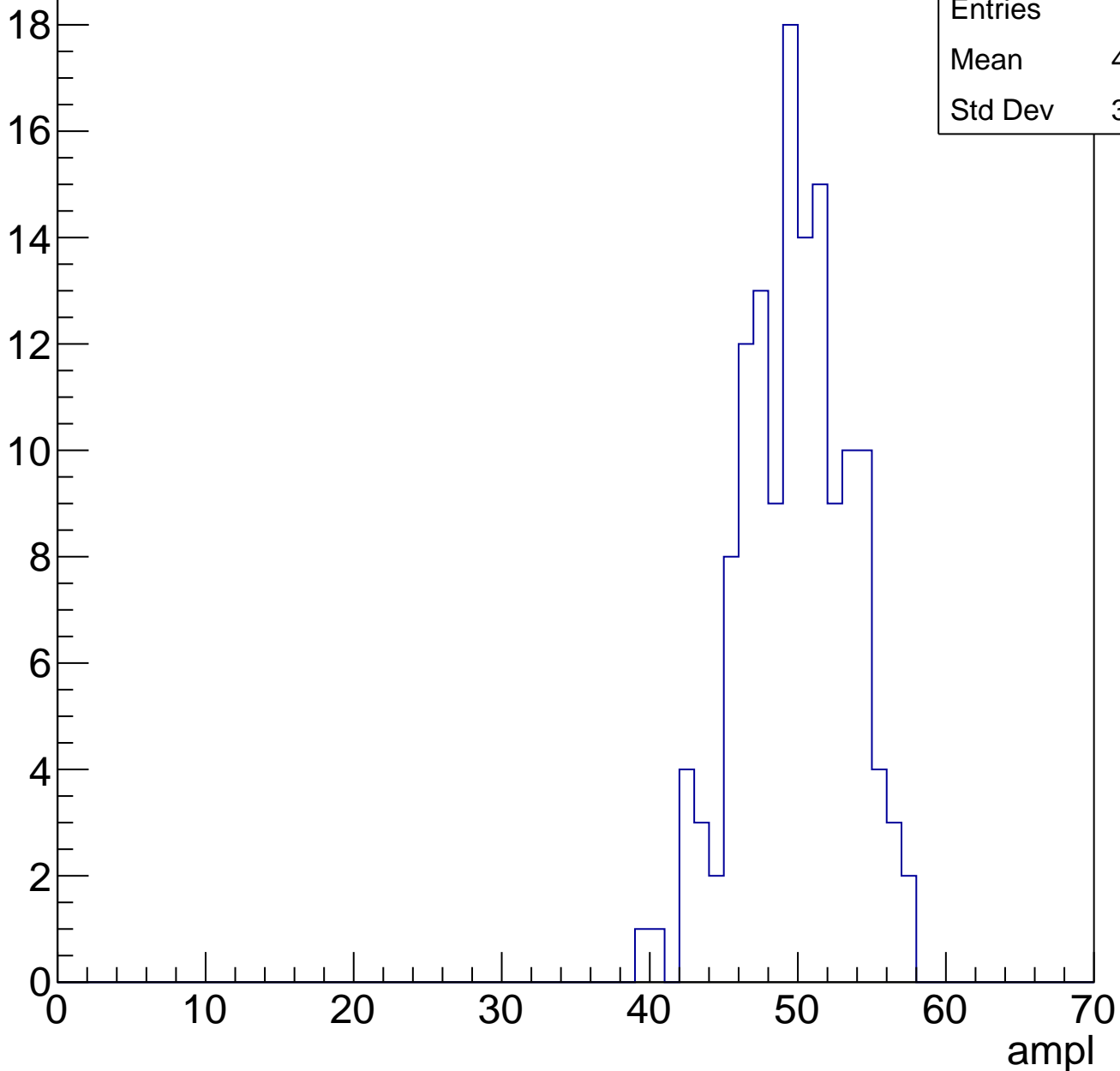
ampl



B0L101S, U20-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

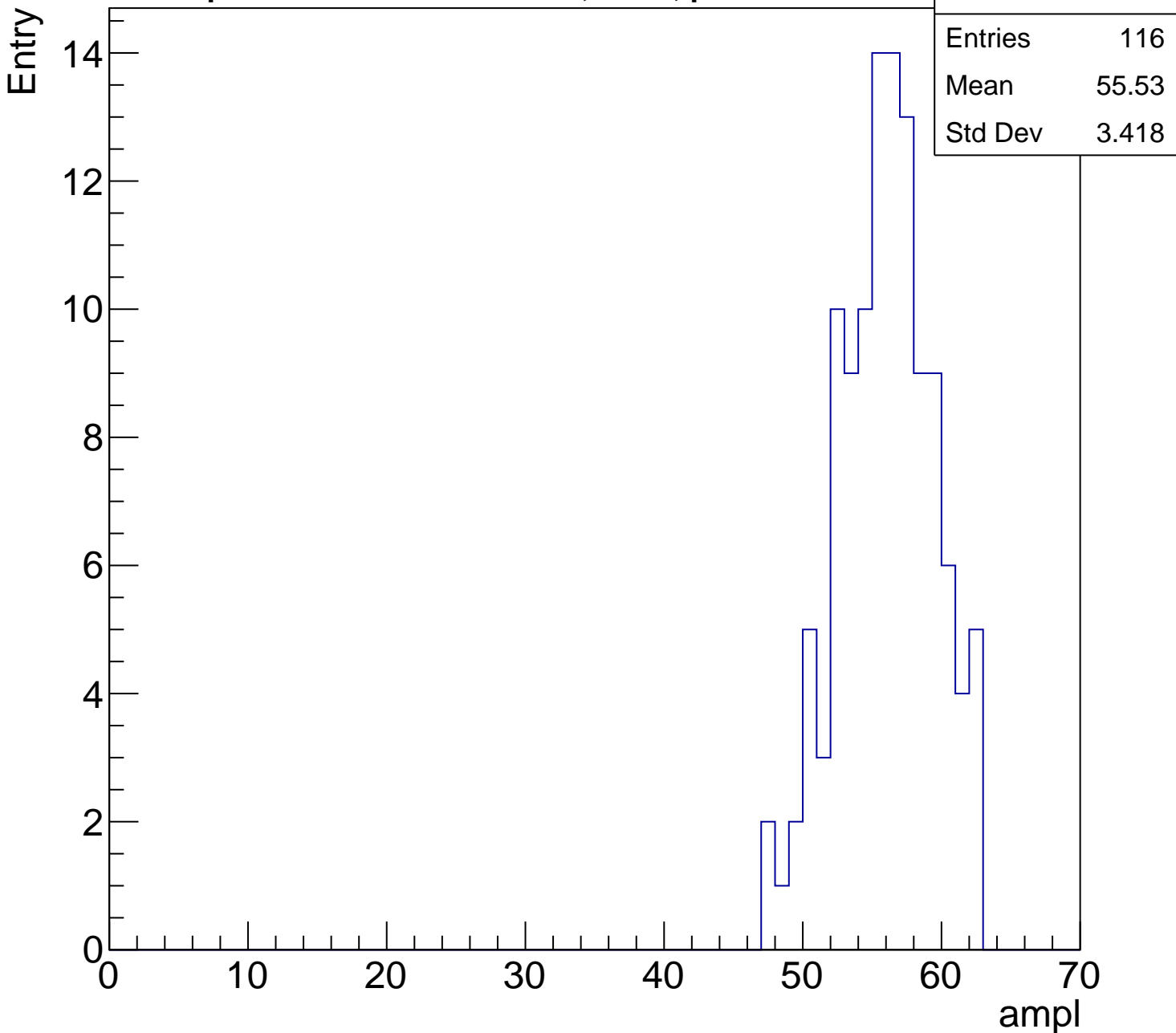
Entries	116
Mean	55.53
Std Dev	3.418

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U20-ch123, adc5

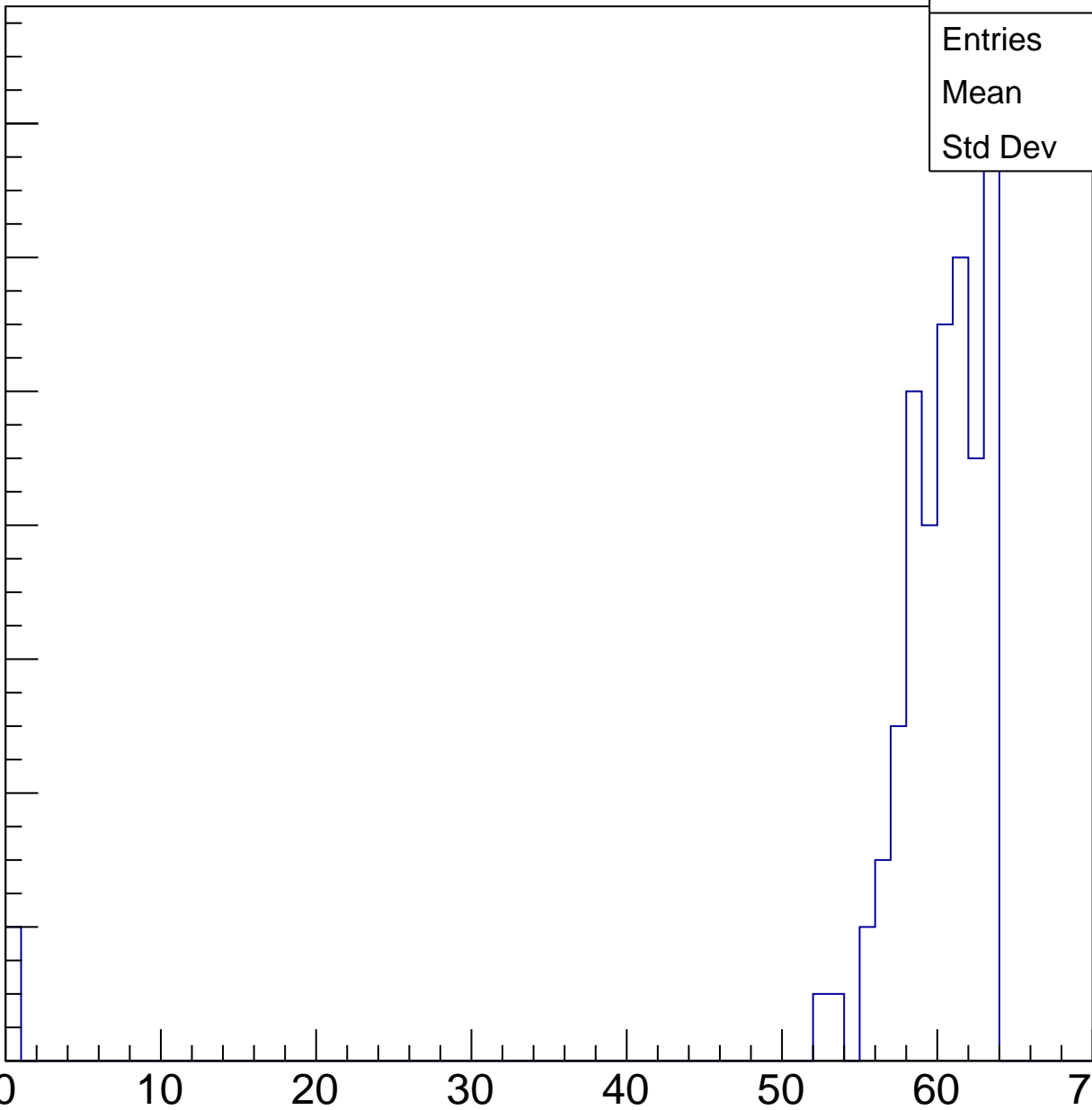
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	58.42
Std Dev	9.737

ampl

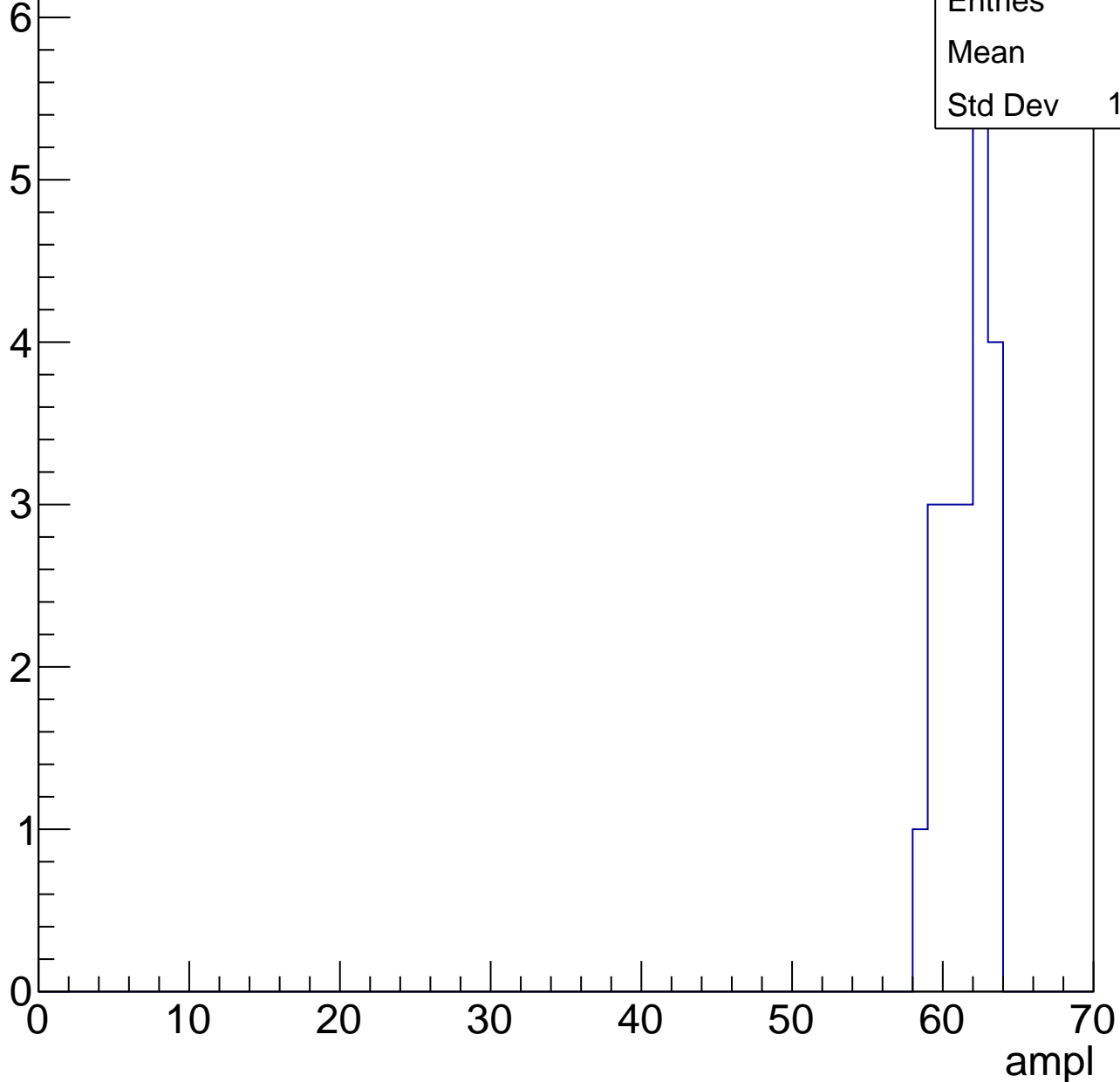


B0L101S, U20-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.1
Std Dev	1.513



B0L101S, U20-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



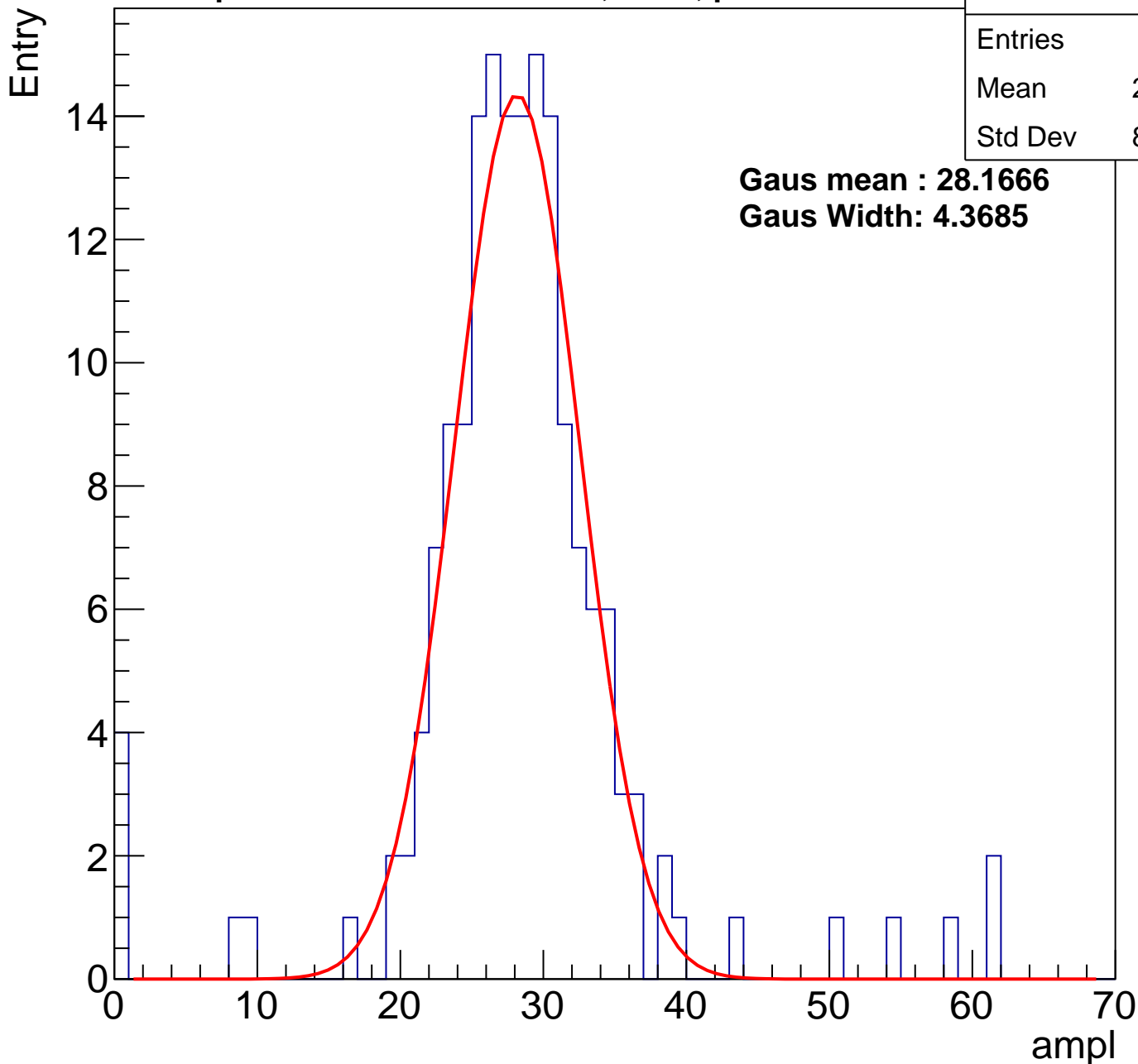
B0L101S, U20-ch124, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	27.79
Std Dev	8.102

Gaus mean : 28.1666

Gaus Width: 4.3685



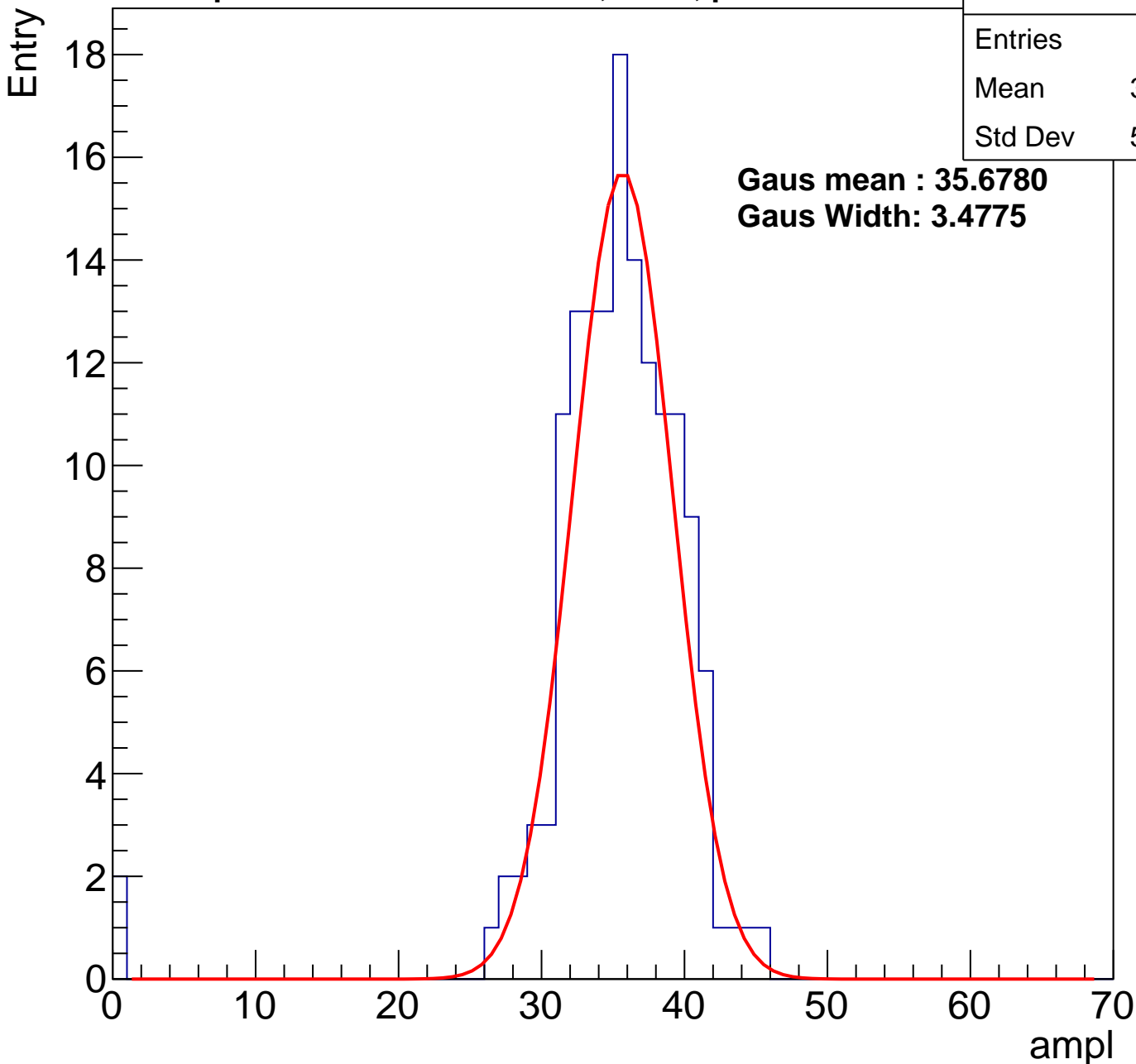
B0L101S, U20-ch124, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	34.77
Std Dev	5.429

Gaus mean : 35.6780

Gaus Width: 3.4775



B0L101S, U20-ch124, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

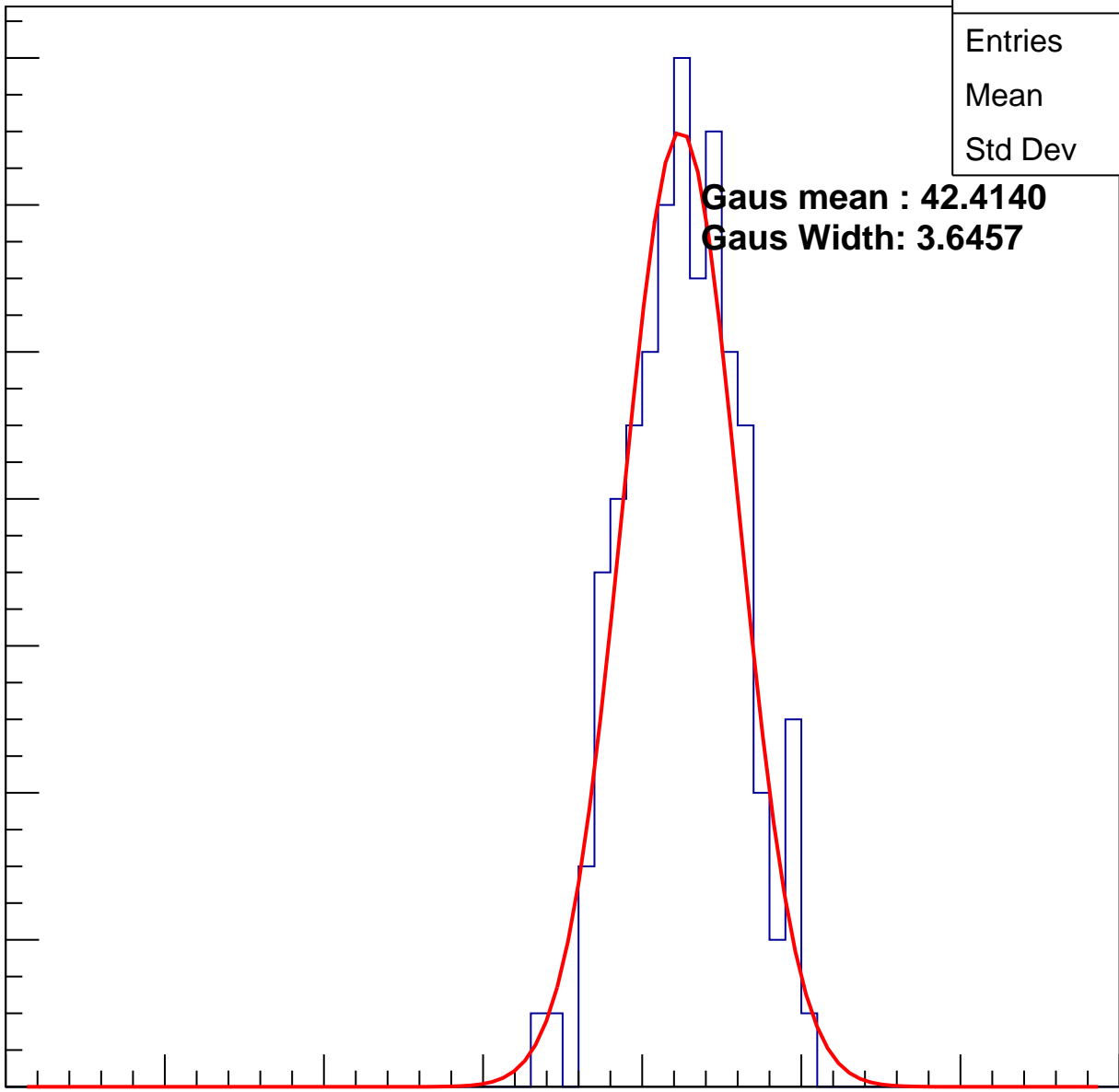
14
12
10
8
6
4
2
0

Entries	120
Mean	42.14
Std Dev	3.494

Gaus mean : 42.4140
Gaus Width: 3.6457

ampl

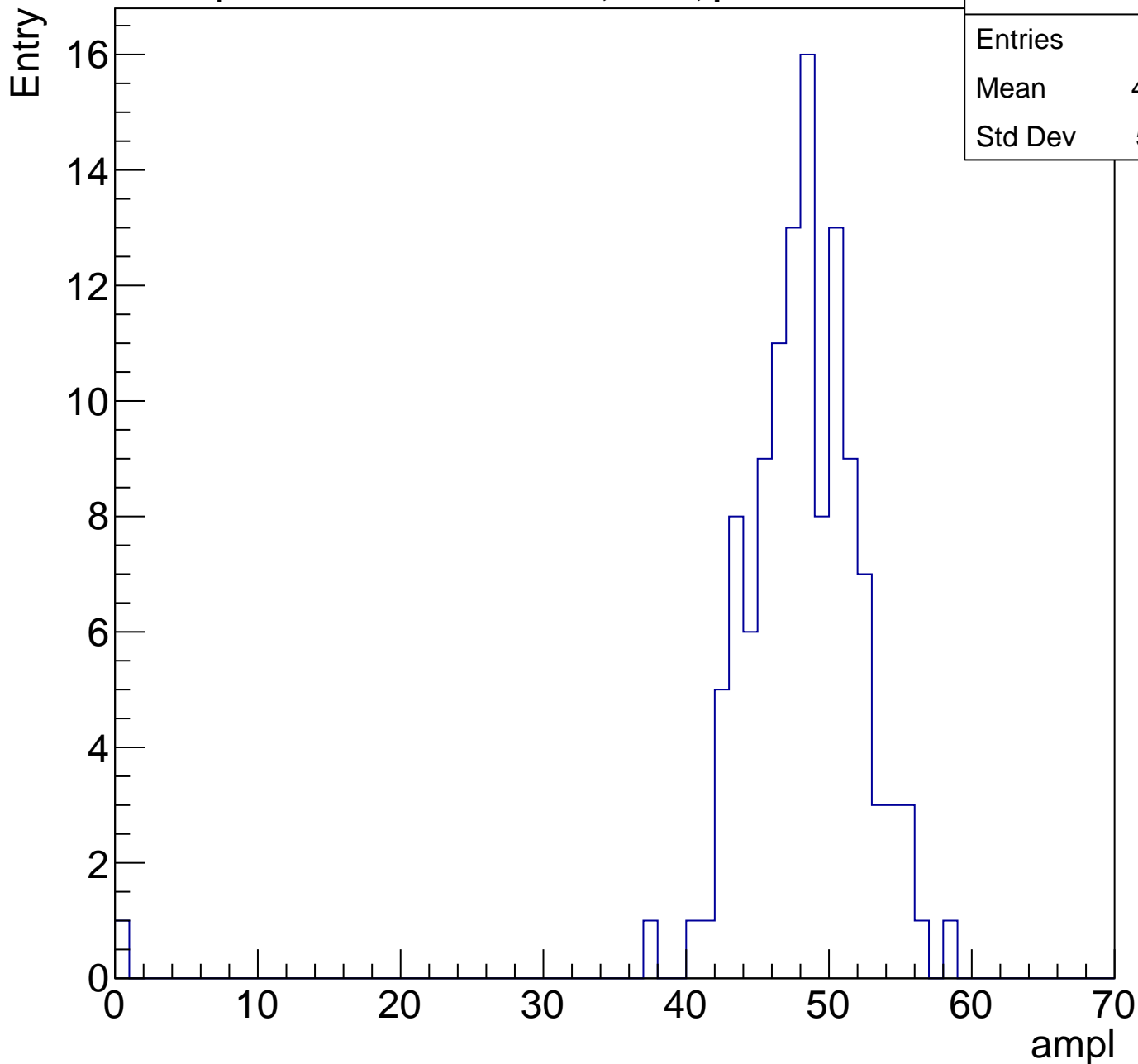
0 10 20 30 40 50 60 70



B0L101S, U20-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	47.42
Std Dev	5.671

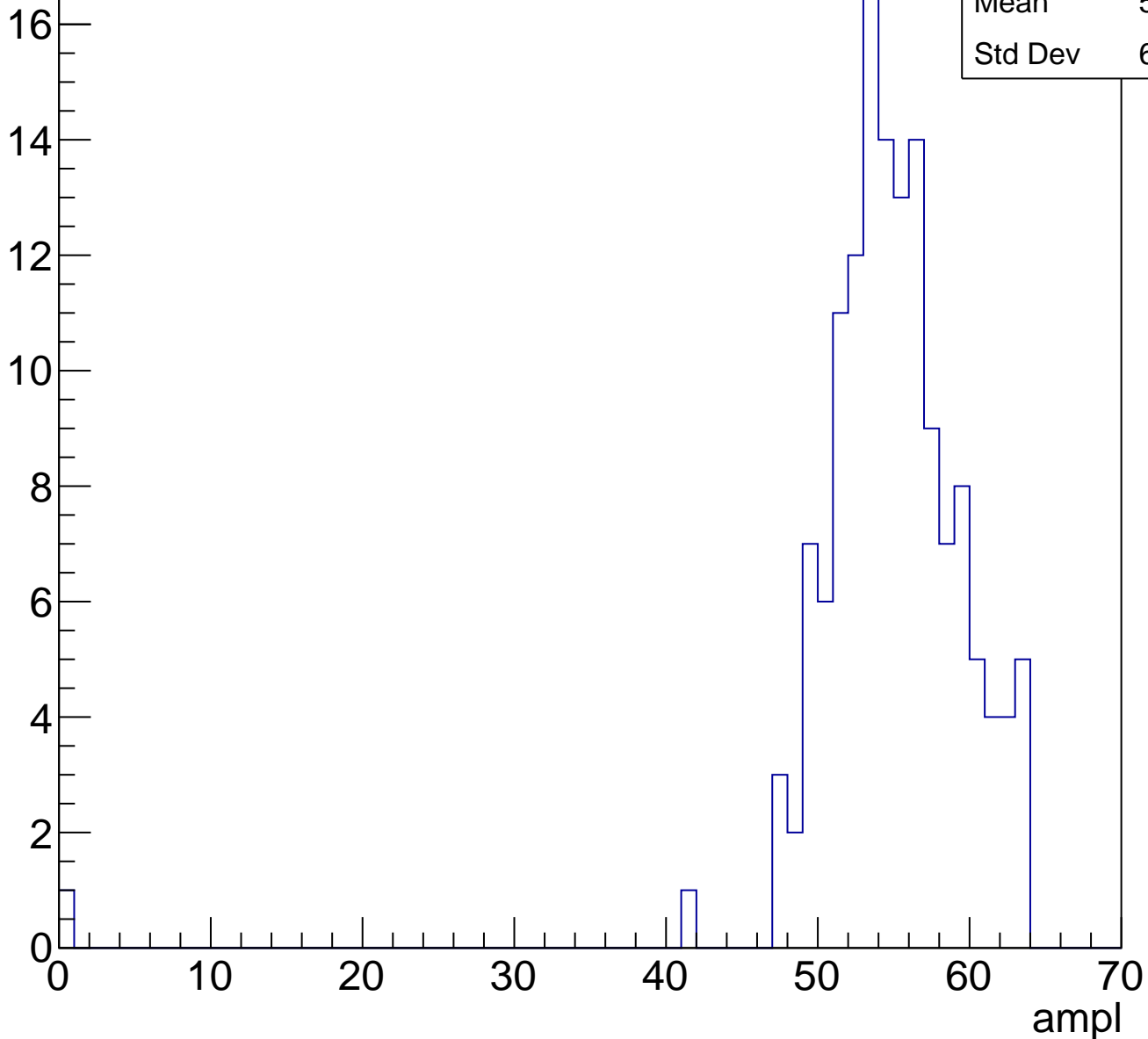


B0L101S, U20-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	143
Mean	54.27
Std Dev	6.046



B0L101S, U20-ch124, adc5

calib_packv5_042523_0143.root, FC#1, port C1

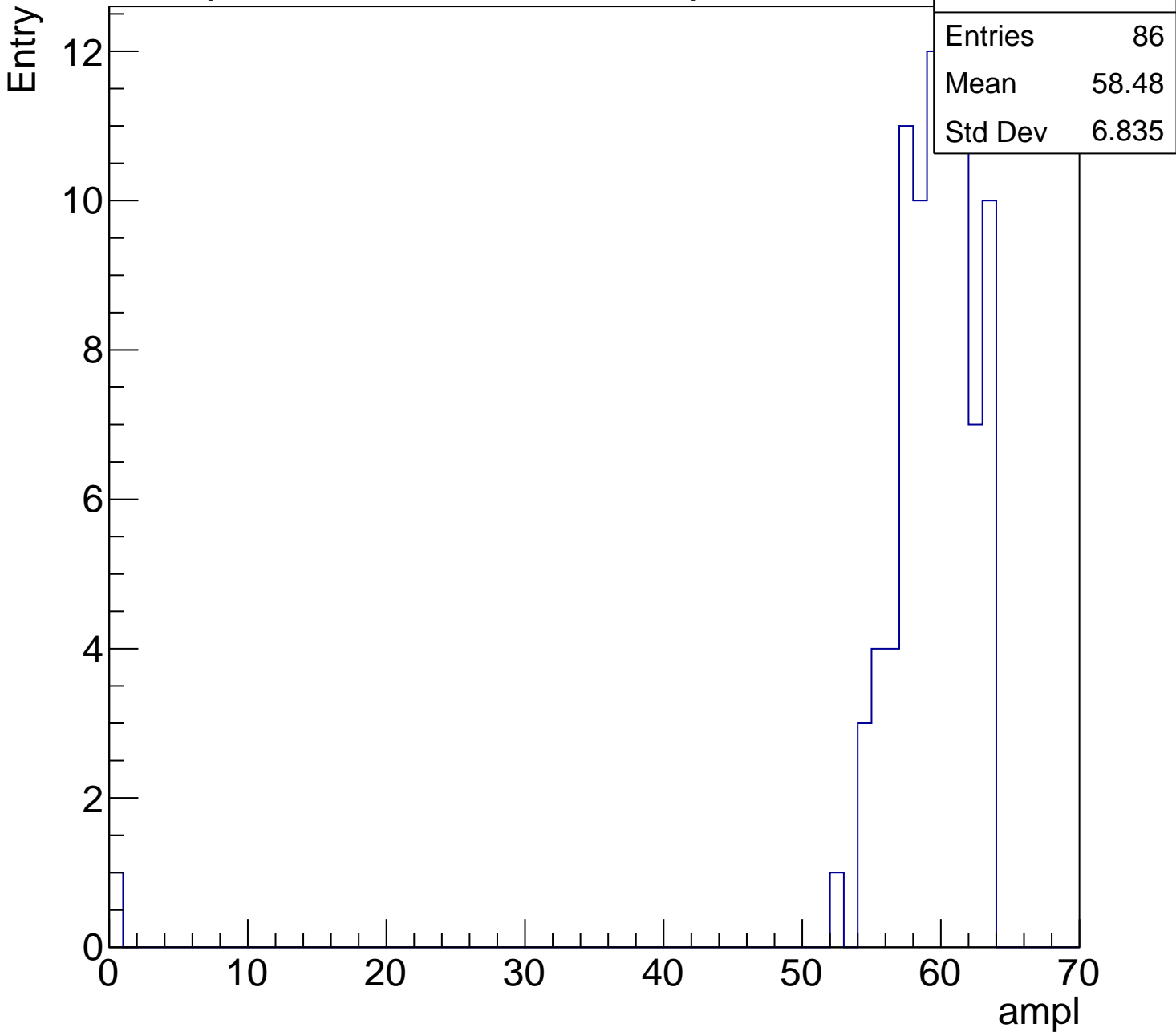
Entry

12
10
8
6
4
2
0

Entries	86
Mean	58.48
Std Dev	6.835

ampl

0 10 20 30 40 50 60 70

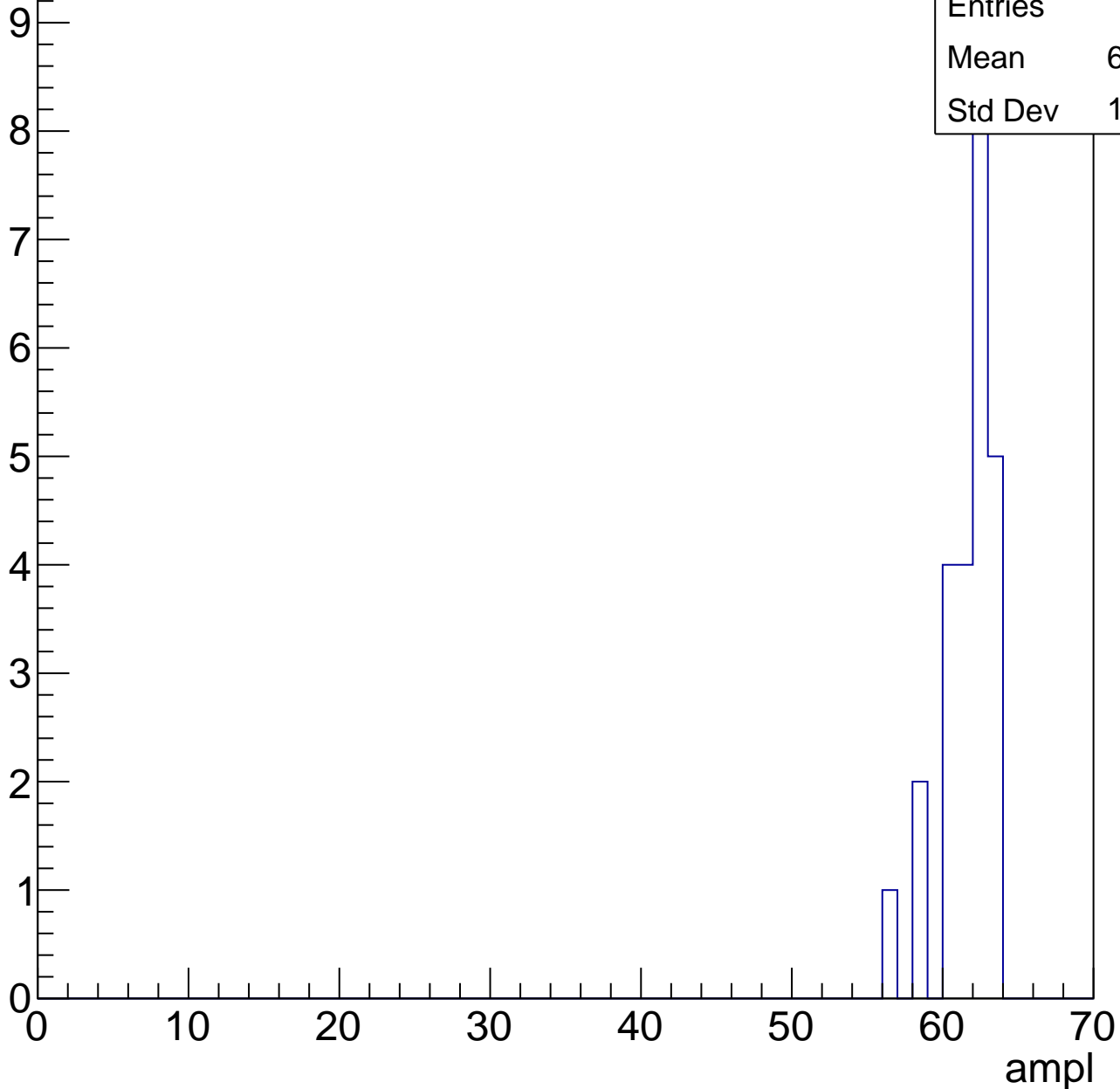


B0L101S, U20-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	25
Mean	61.16
Std Dev	1.736



B0L101S, U20-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch125, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	28.72
Std Dev	3.415

Gaus mean : 28.7697

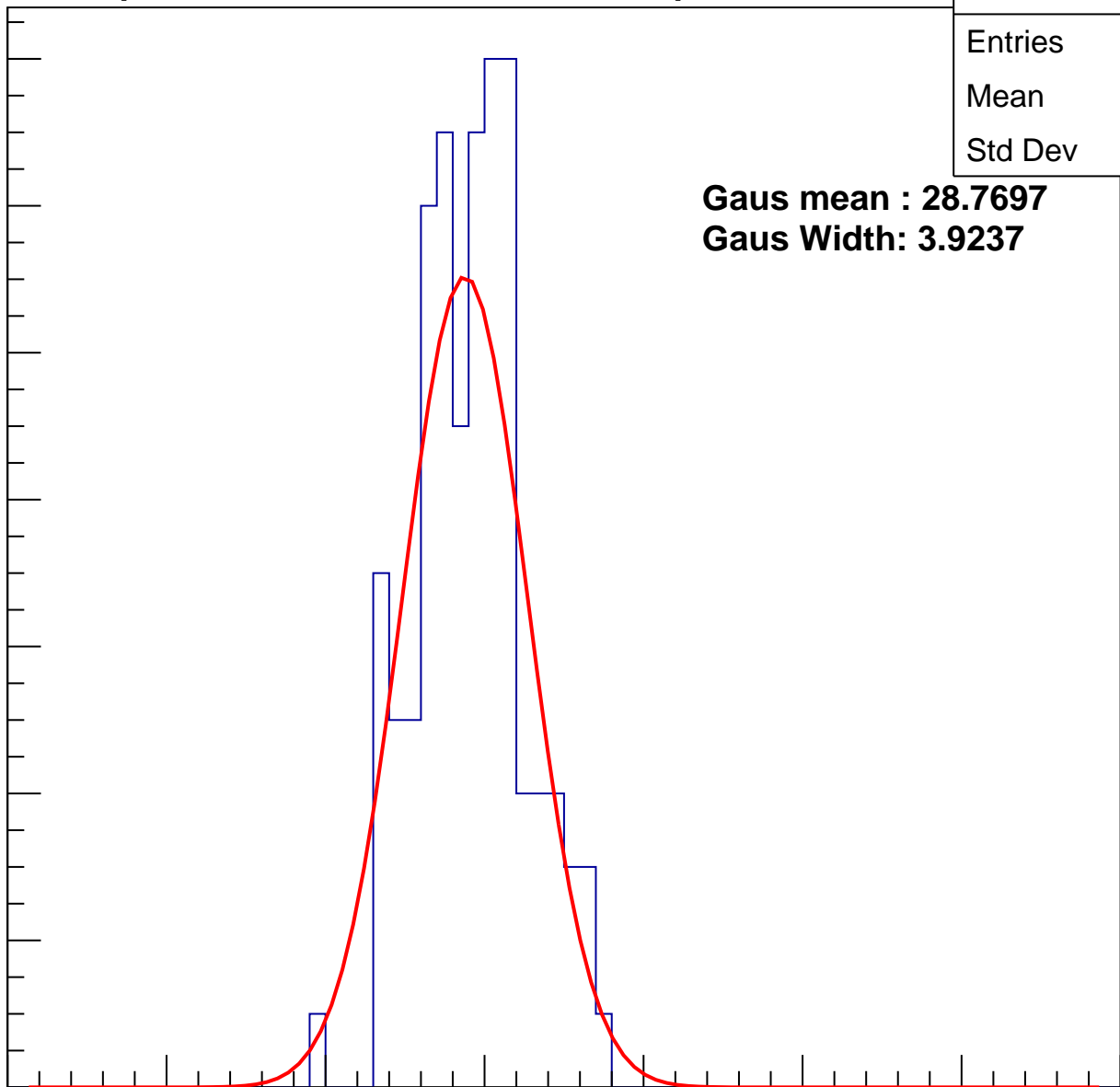
Gaus Width: 3.9237

Entry

14
12
10
8
6
4
2
0

ampl

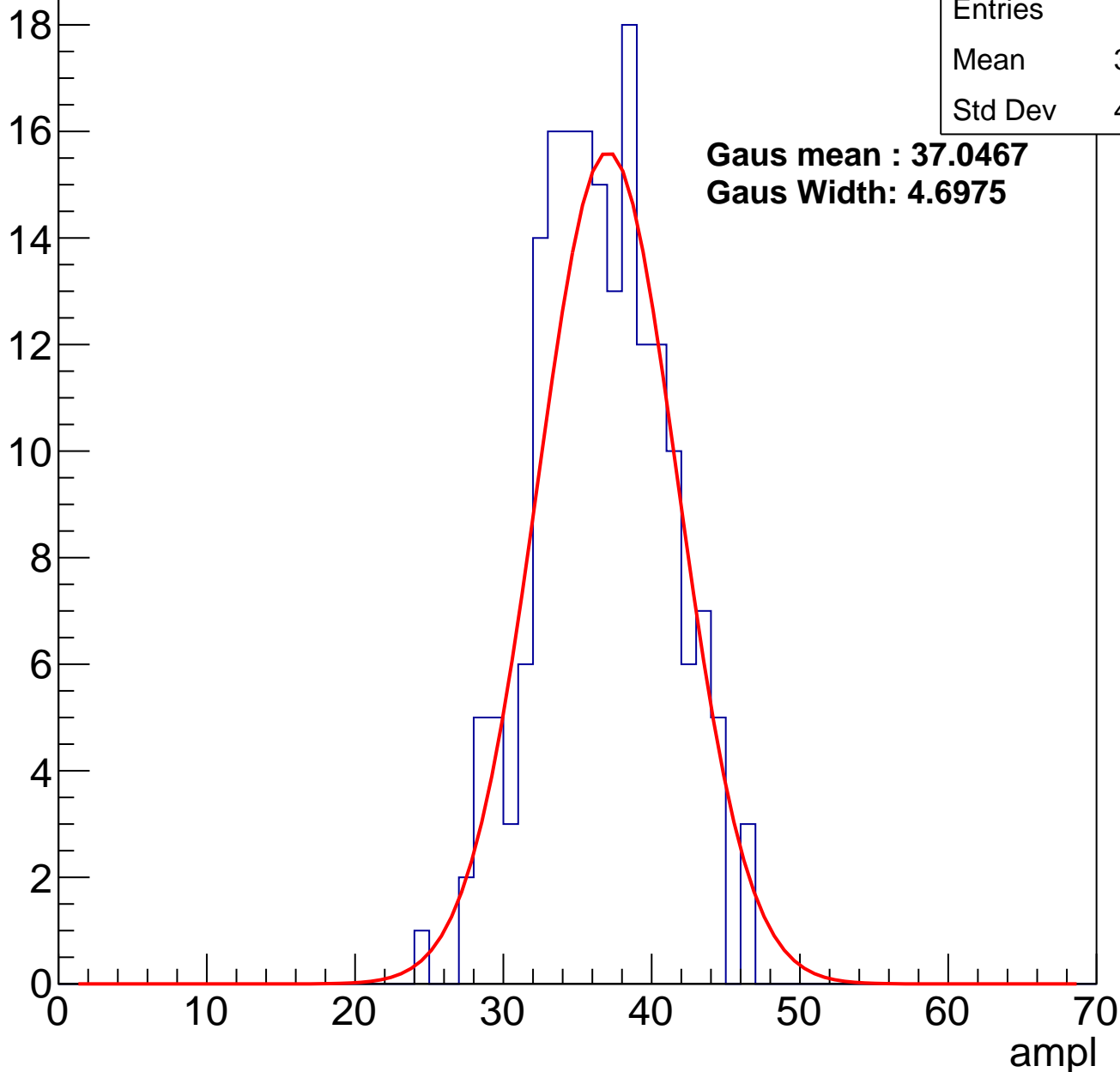
0 10 20 30 40 50 60 70



B0L101S, U20-ch125, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

43.73

Std Dev

3.59

Gaus mean : 44.2649

Gaus Width: 3.4014

0

10

20

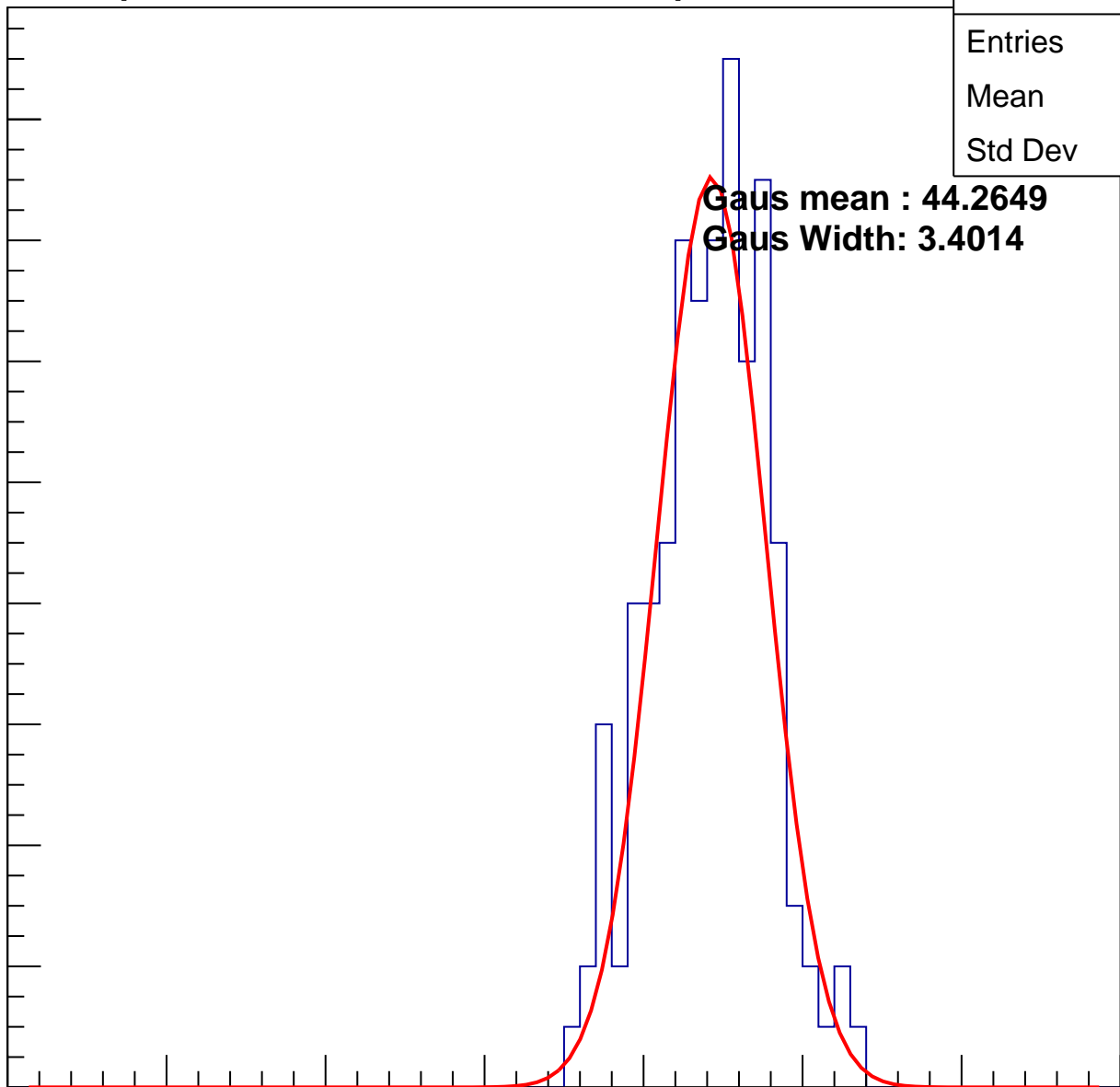
30

40

50

60

ampl



B0L101S, U20-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	51.35
Std Dev	3.45

Entry

16

14

12

10

8

6

4

2

0

0

10

20

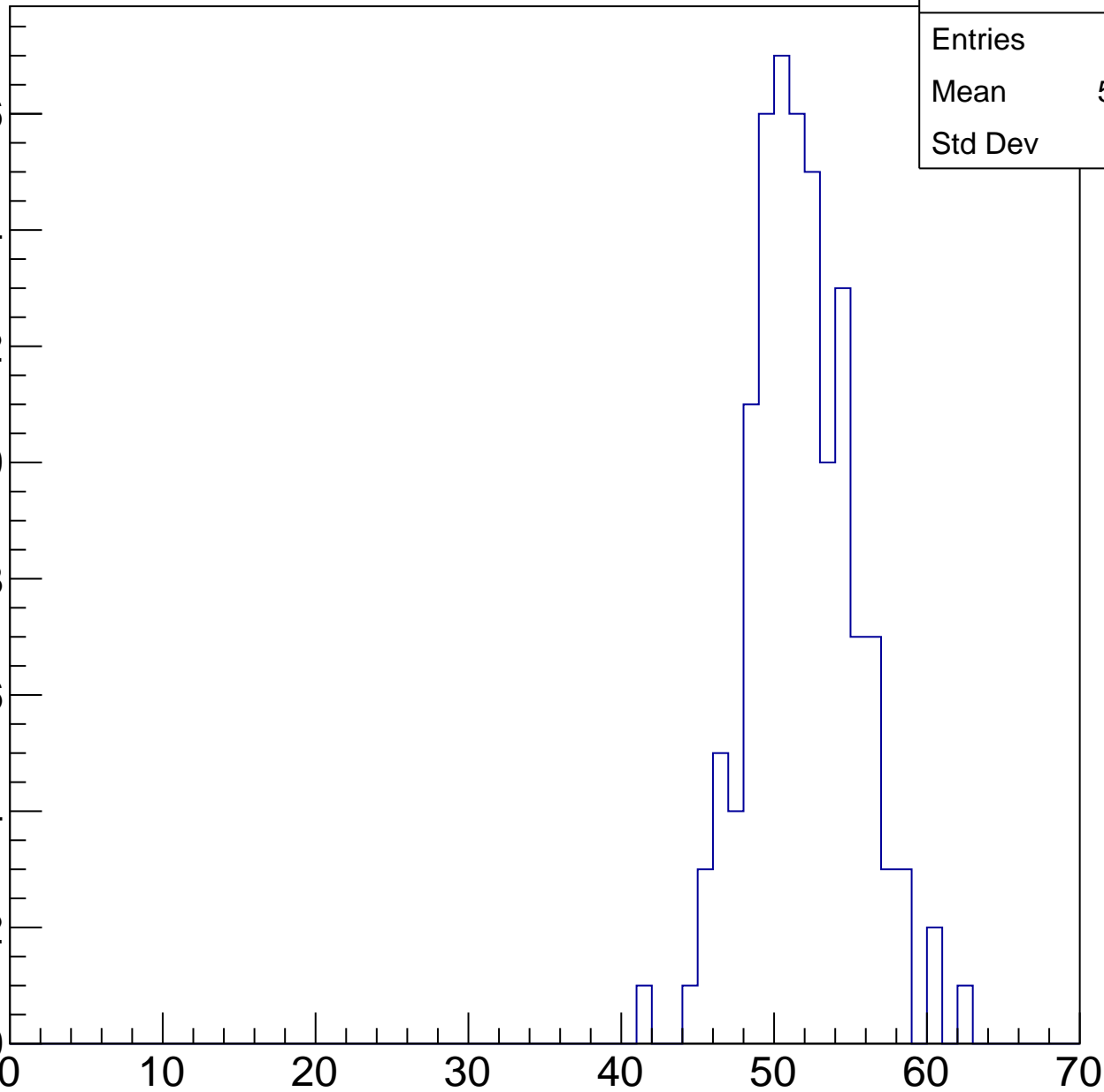
30

40

50

60

ampl



B0L101S, U20-ch125, adc4

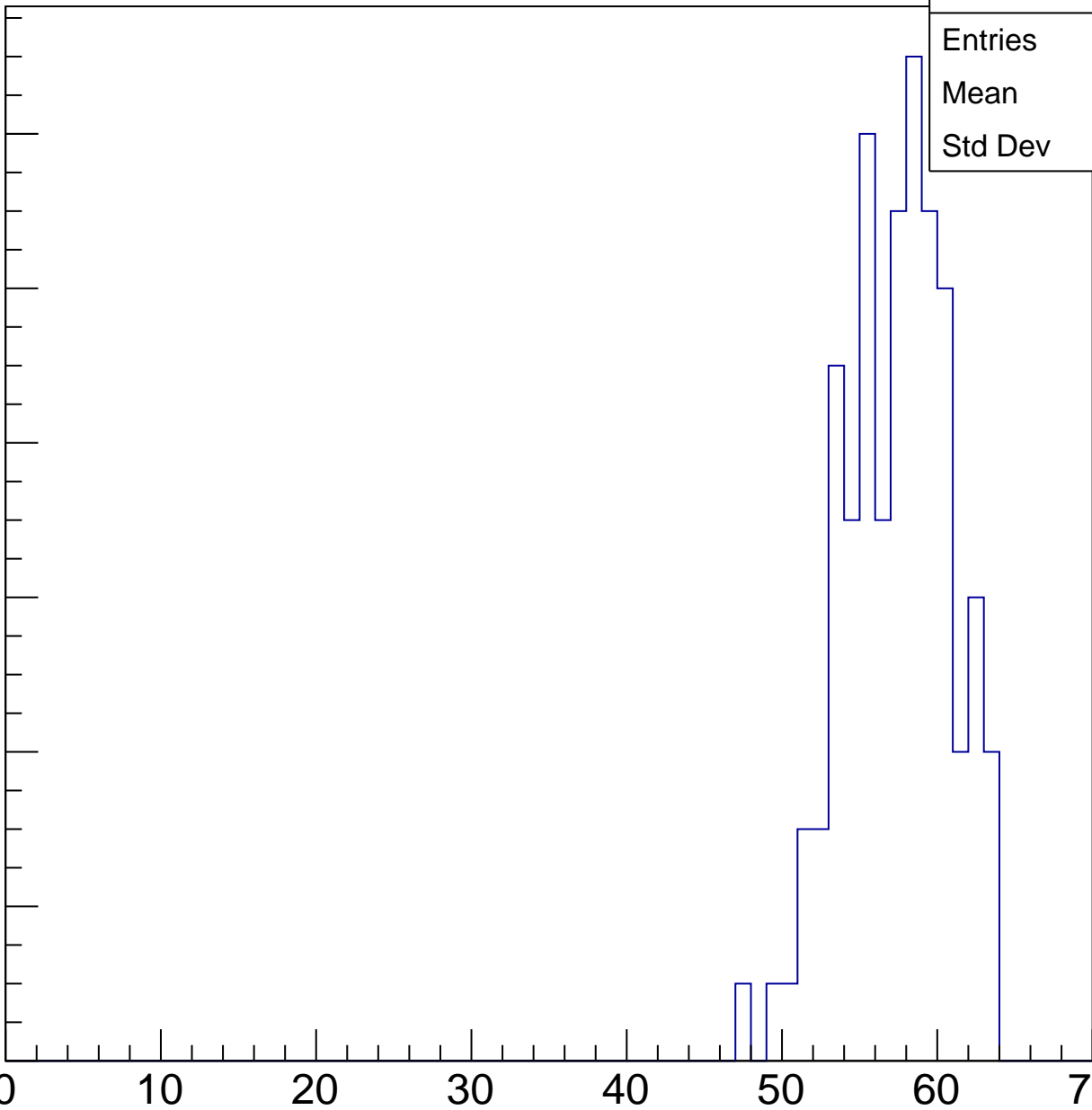
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	56.89
Std Dev	3.359

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U20-ch125, adc5

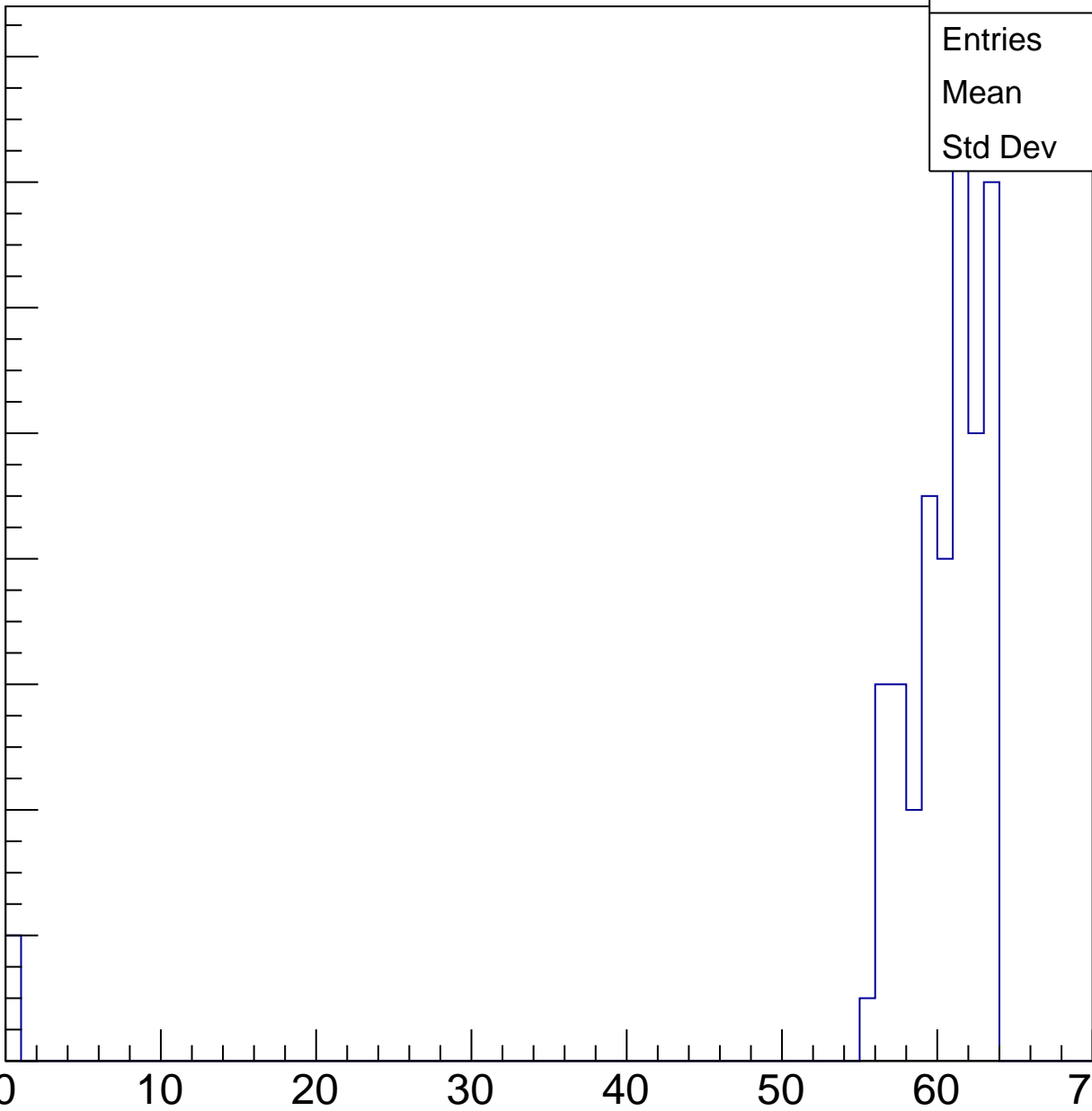
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	58.61
Std Dev	9.89

ampl



B0L101S, U20-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

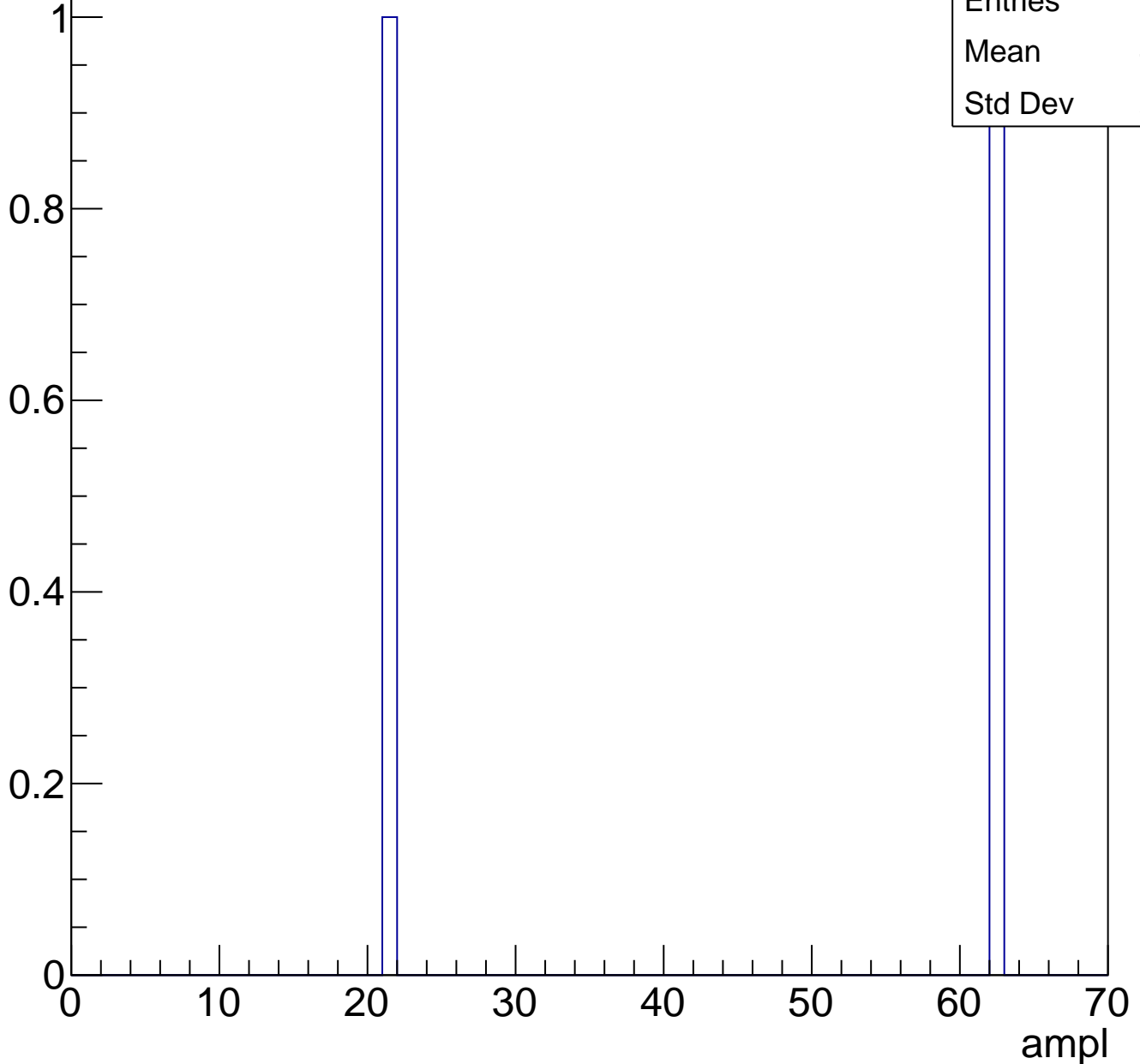
0 10 20 30 40 50 60 70

ampl

B0L101S, U20-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch126, adc0

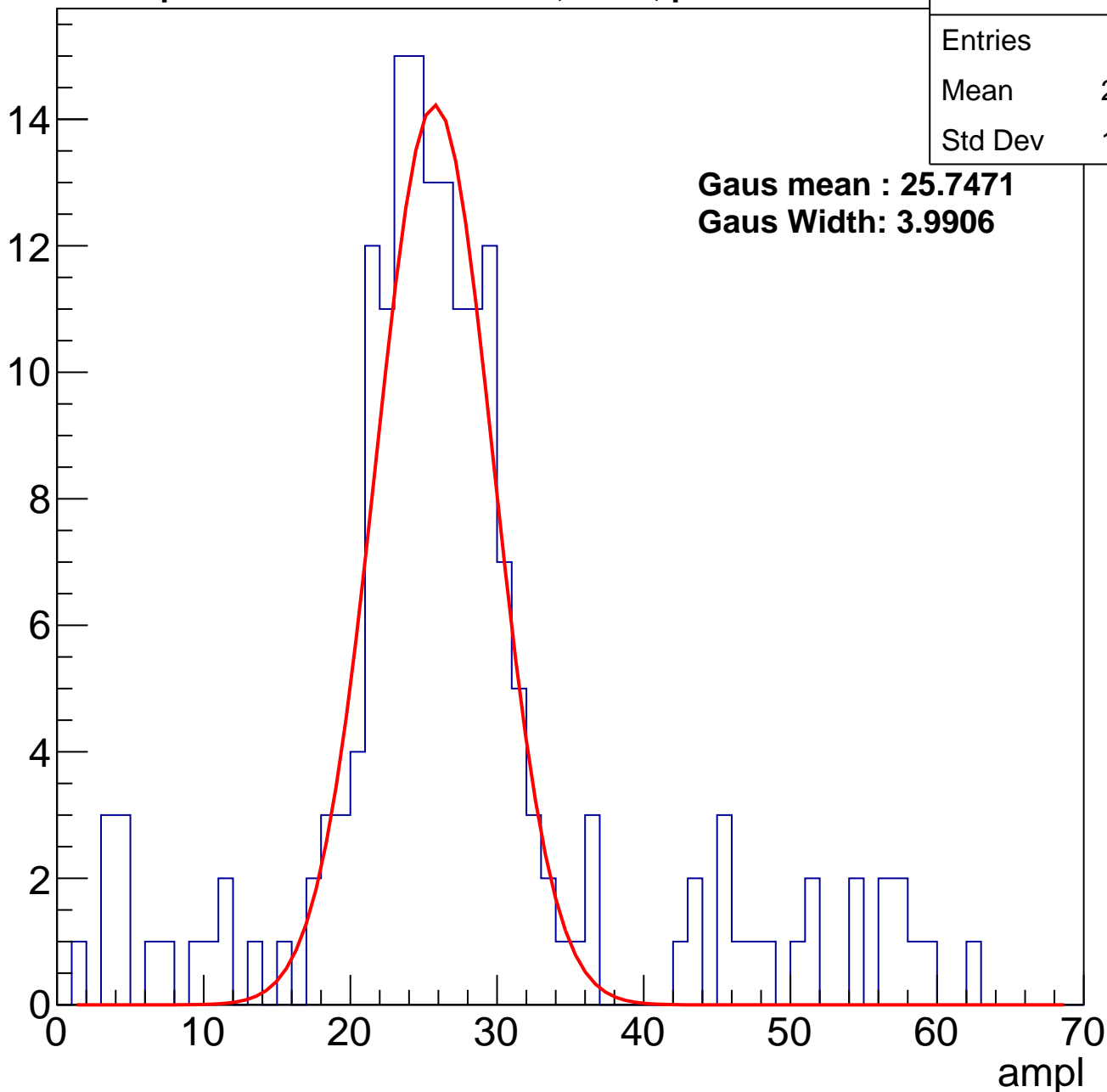
calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	26.87
Std Dev	10.89

Gaus mean : 25.7471

Gaus Width: 3.9906

Entry



B0L101S, U20-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	33.55
Std Dev	5.252

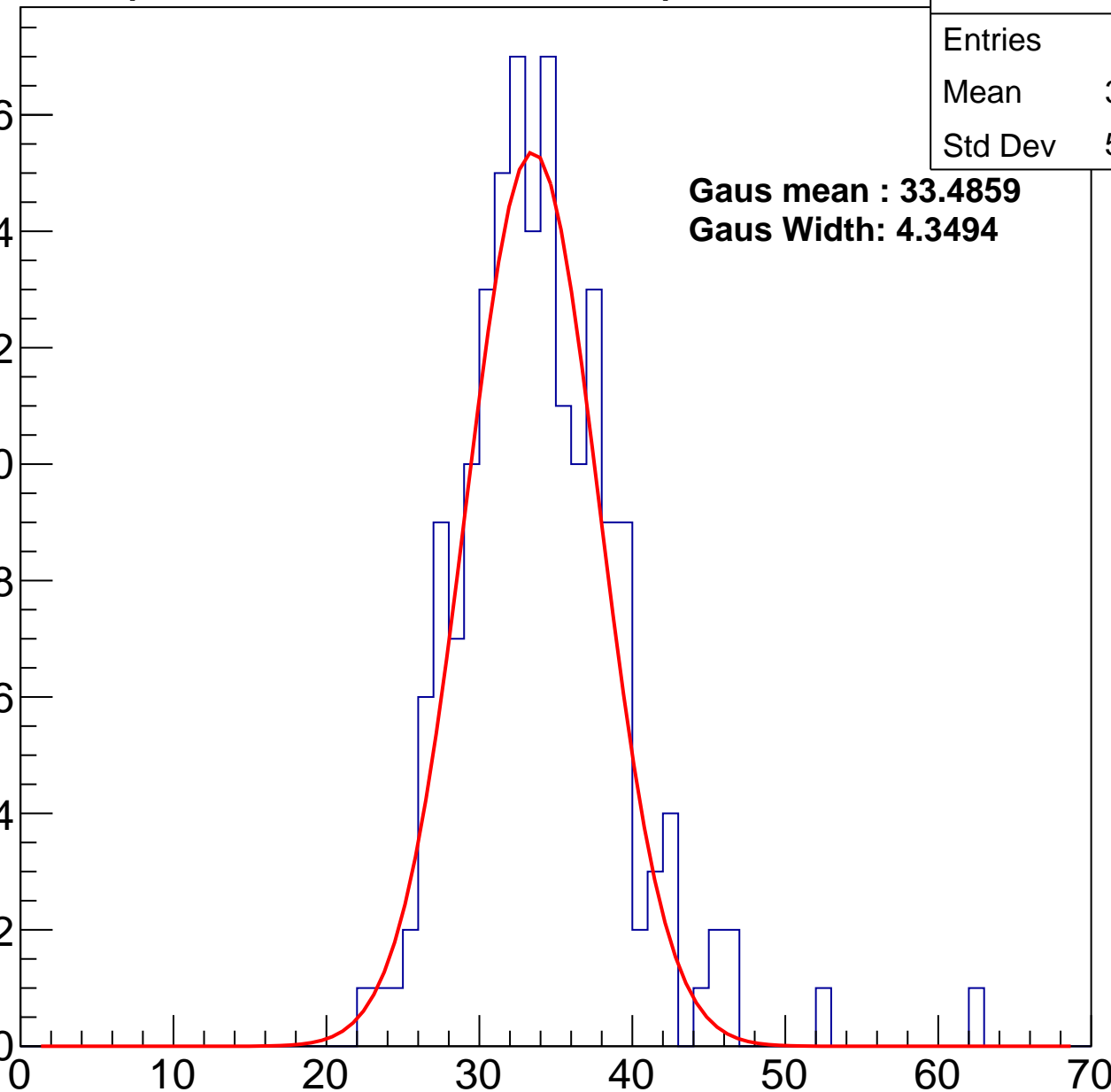
Gaus mean : 33.4859

Gaus Width: 4.3494

Entry

16
14
12
10
8
6
4
2
0

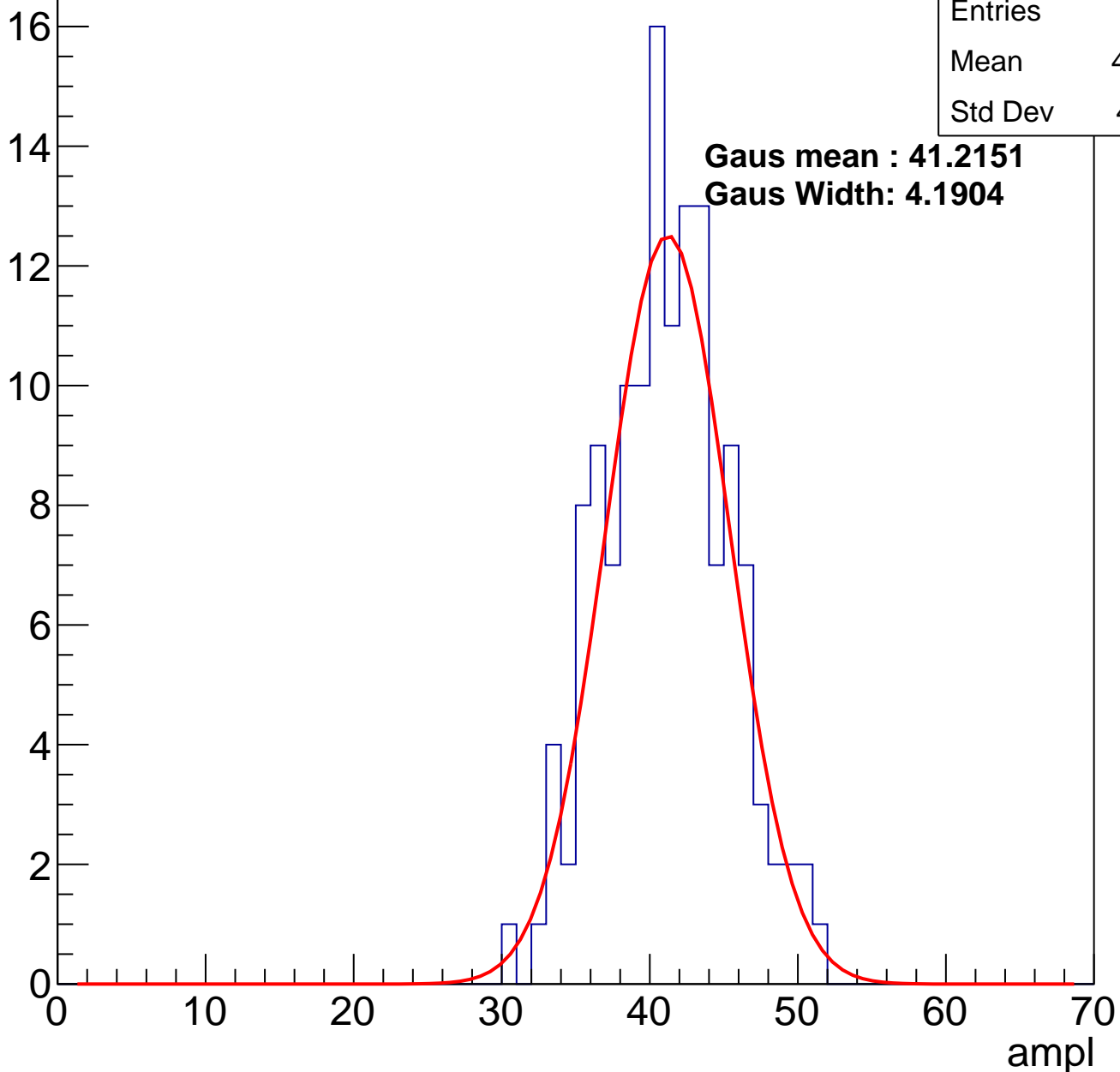
ampl



B0L101S, U20-ch126, adc2

calib_packv5_042523_0143.root, FC#1, port C1

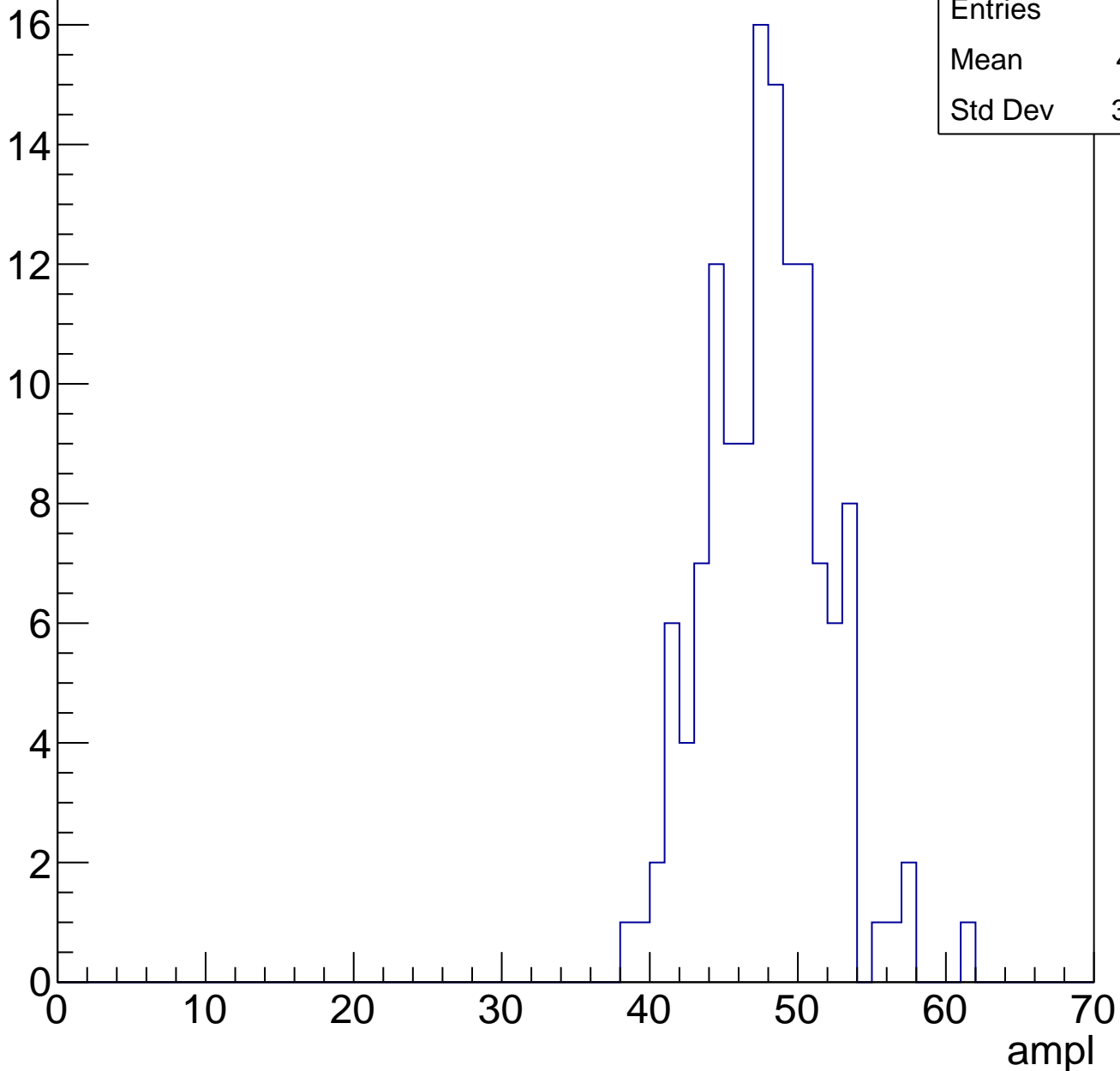
Entry



B0L101S, U20-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

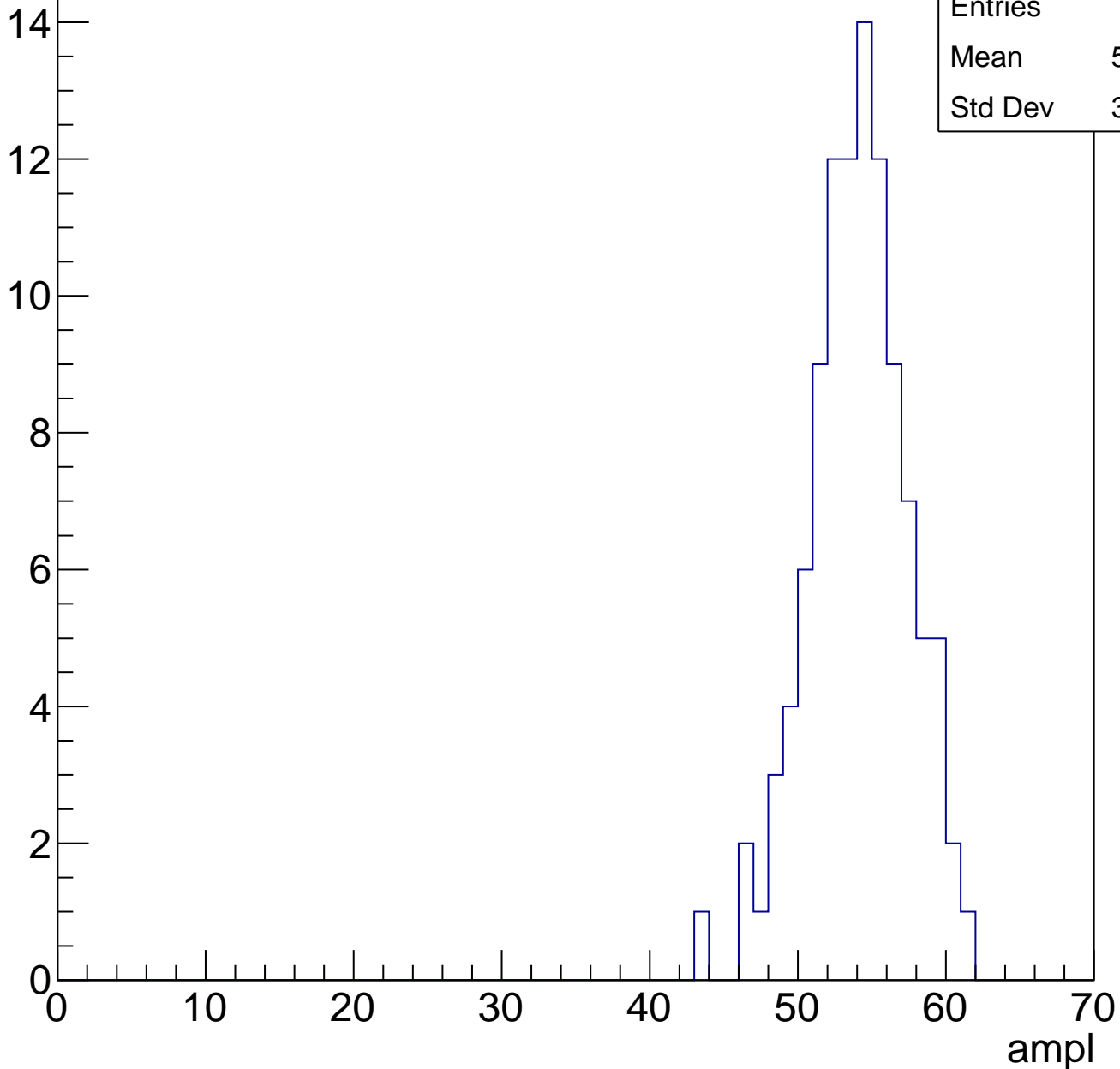


B0L101S, U20-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	105
Mean	53.58
Std Dev	3.326



B0L101S, U20-ch126, adc5

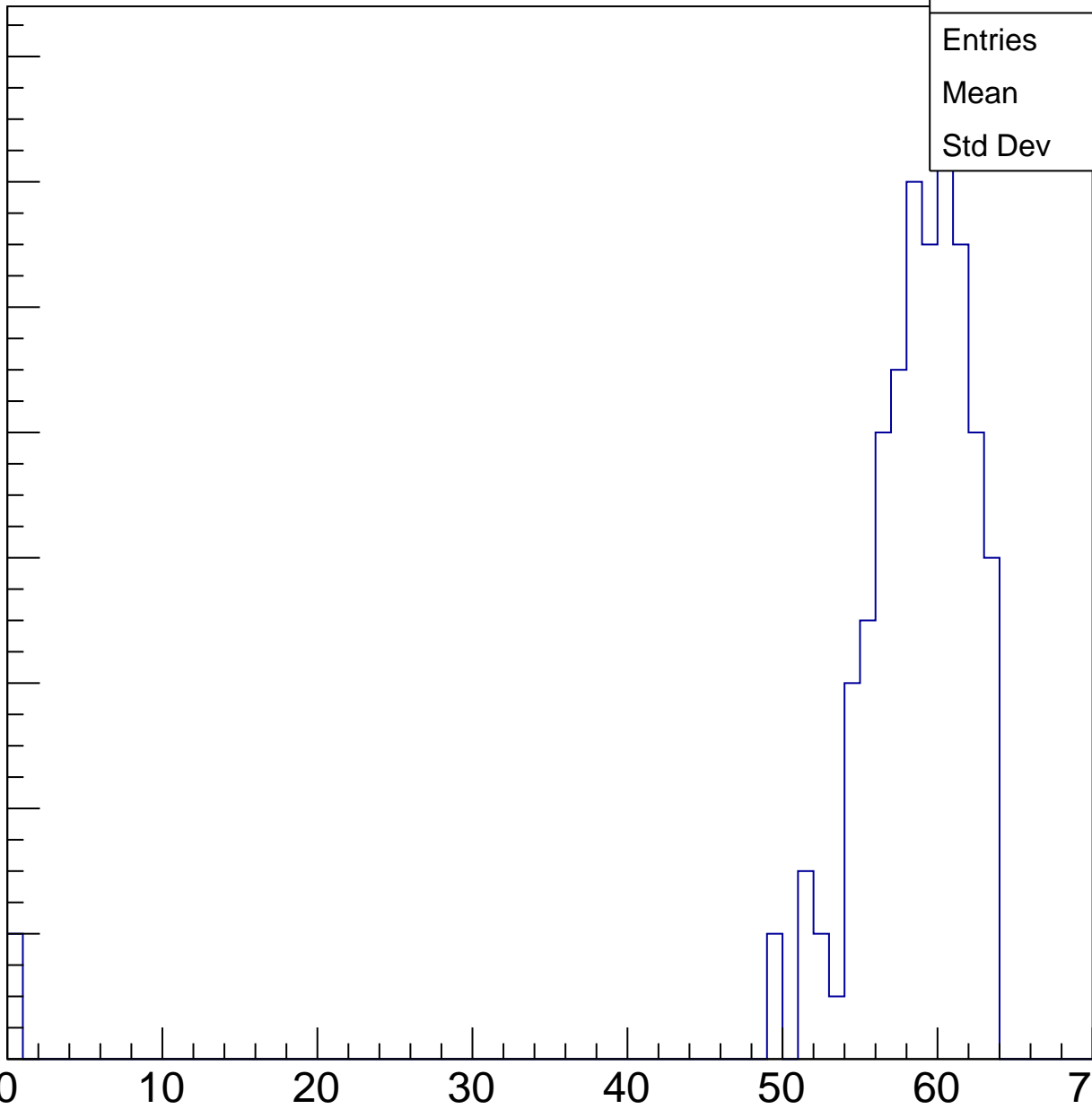
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	57.29
Std Dev	8.15

ampl

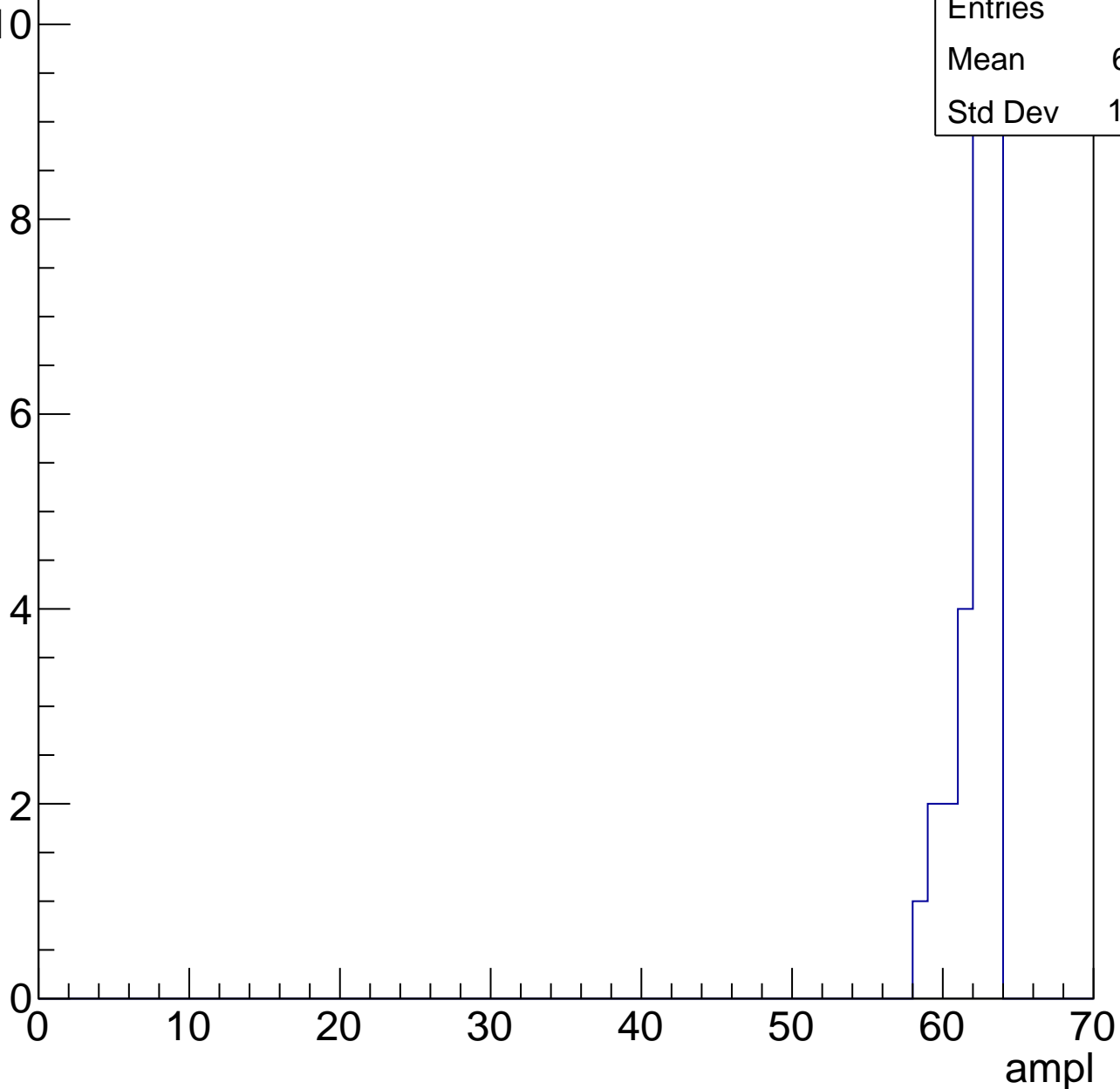


B0L101S, U20-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

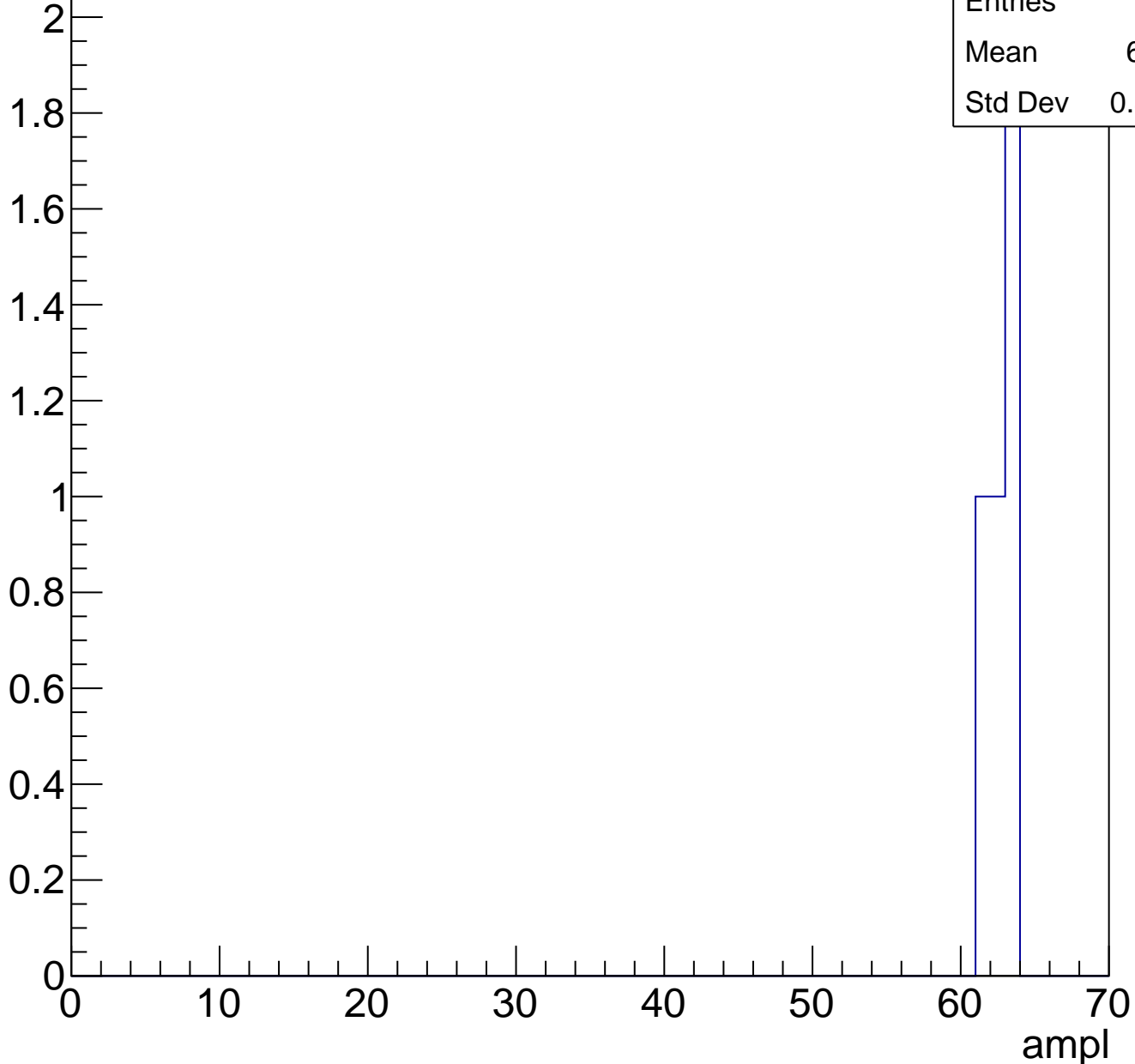
Entries	28
Mean	61.71
Std Dev	1.385



B0L101S, U20-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U20-ch127, adc0

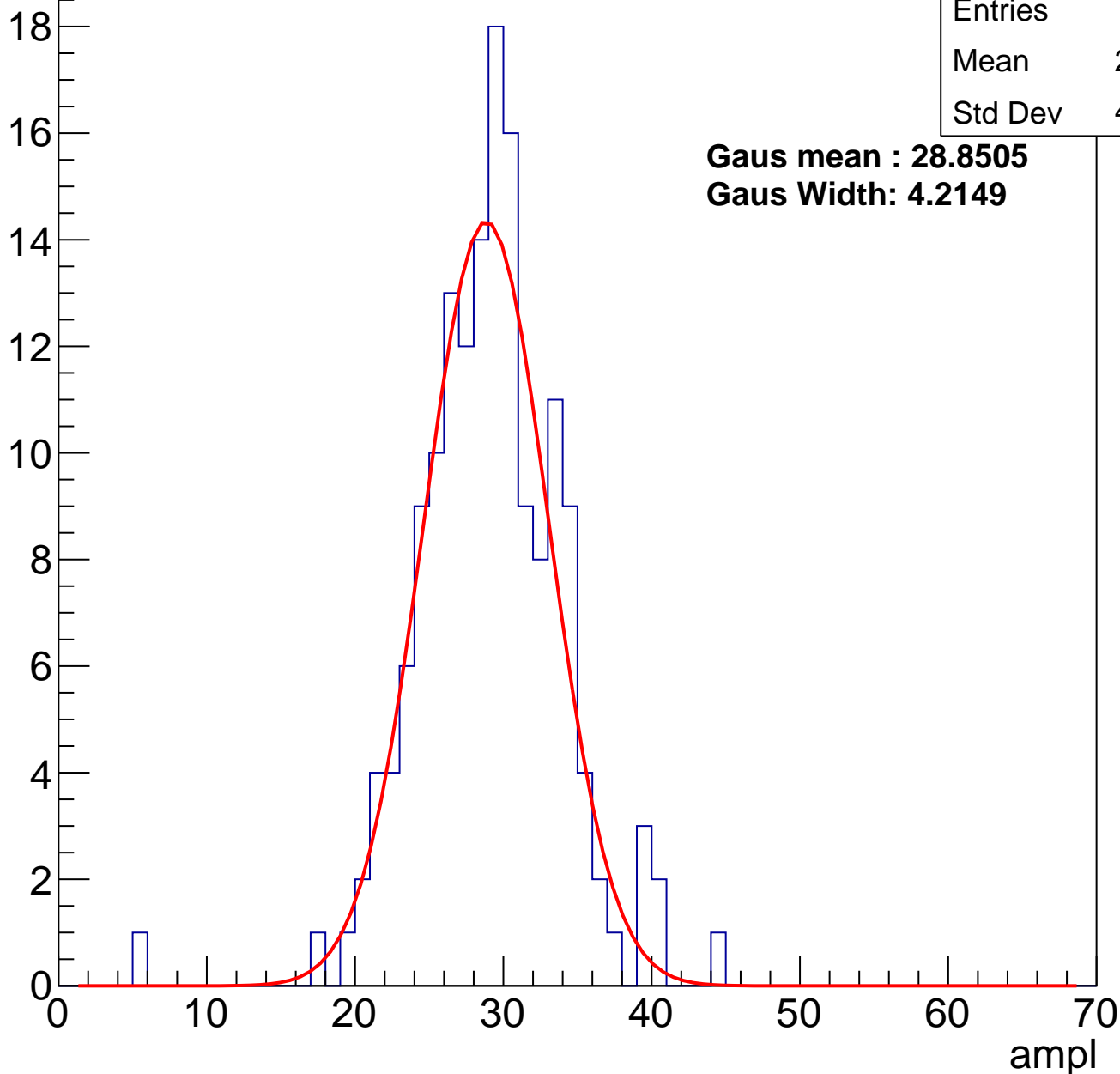
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	28.61
Std Dev	4.851

Gaus mean : 28.8505

Gaus Width: 4.2149

Entry



B0L101S, U20-ch127, adc1

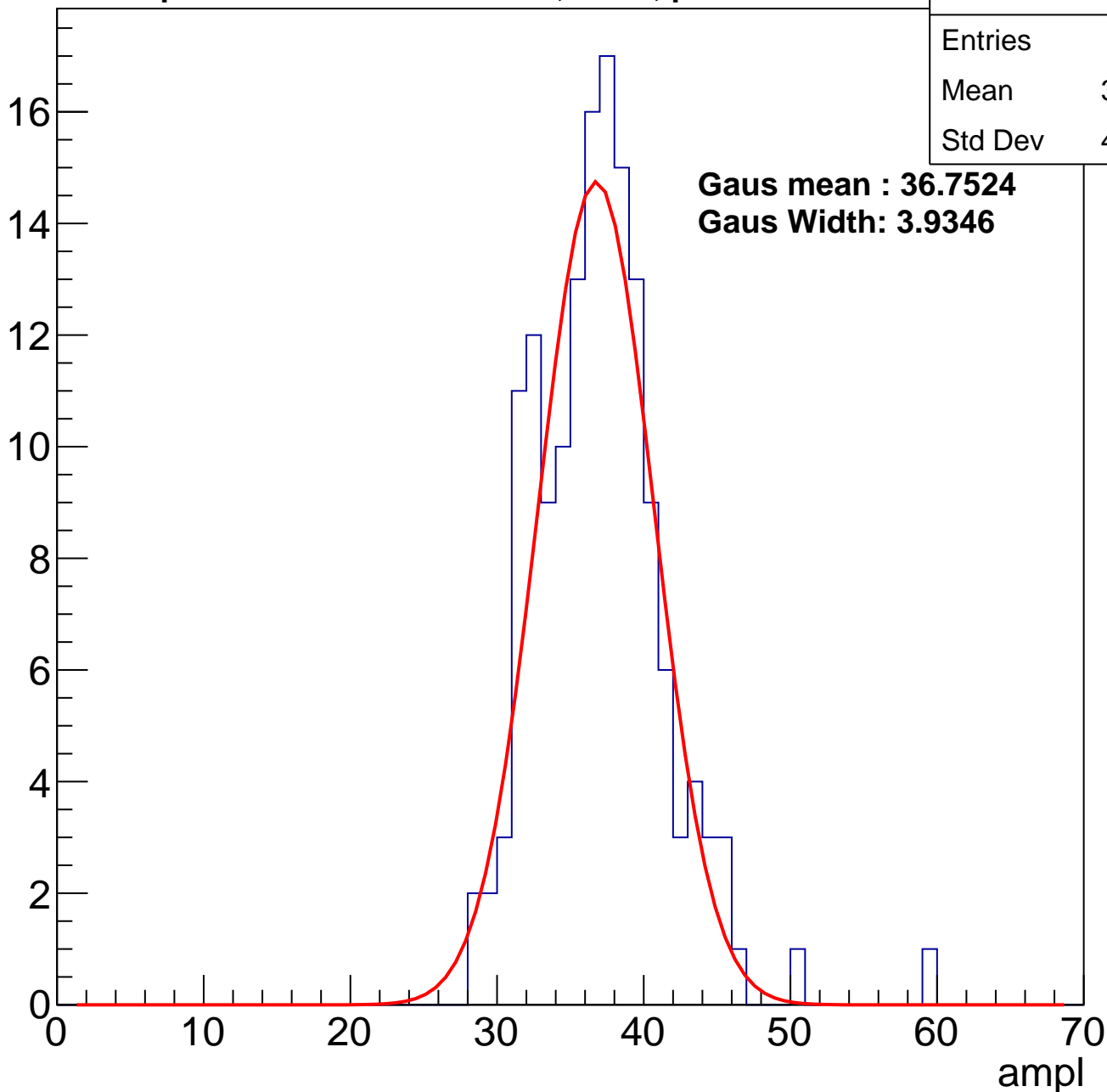
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.55
Std Dev	4.369

Gaus mean : 36.7524

Gaus Width: 3.9346

Entry



B0L101S, U20-ch127, adc2

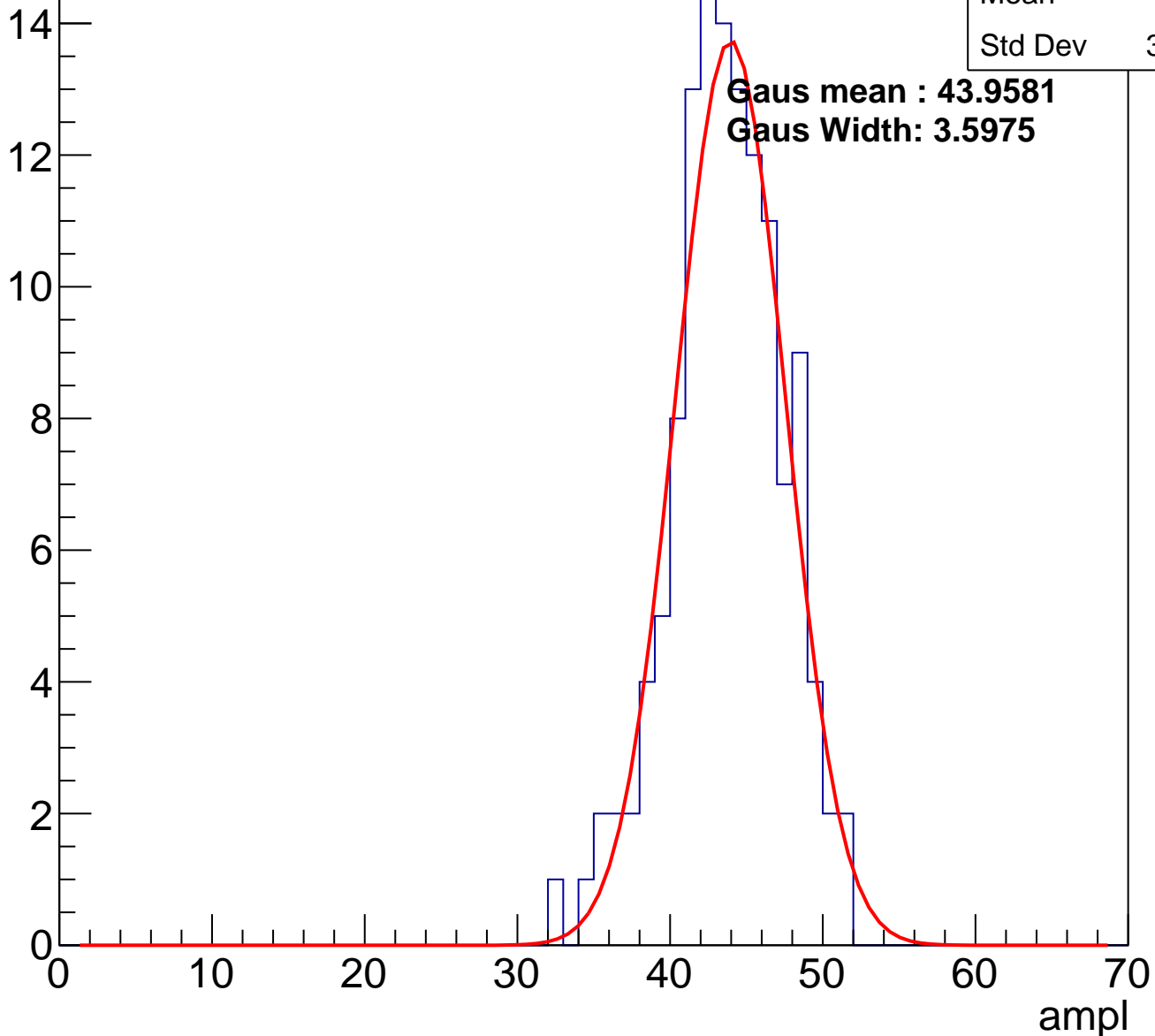
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	43.24
Std Dev	3.636

Gaus mean : 43.9581

Gaus Width: 3.5975

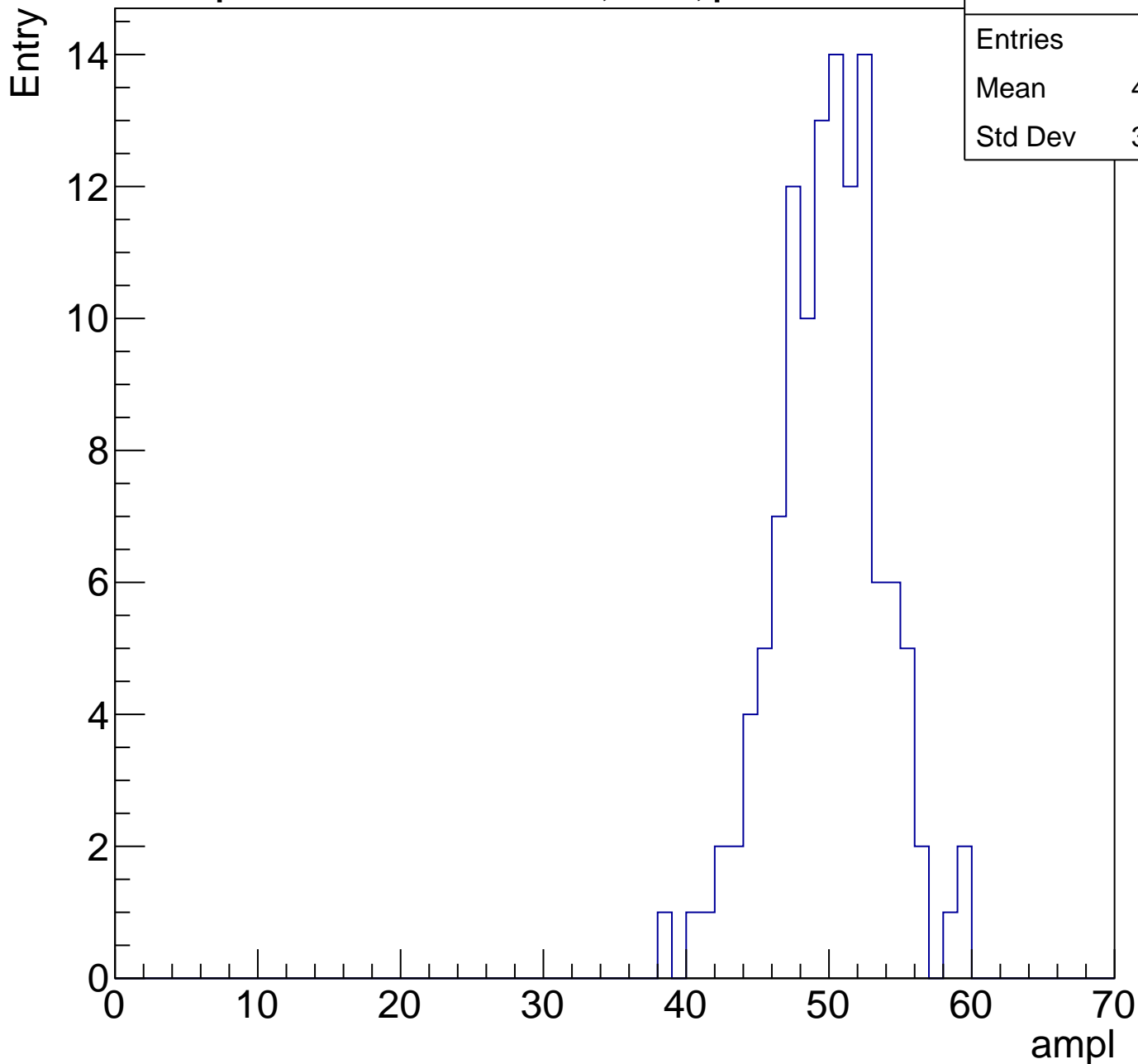
Entry



B0L101S, U20-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	49.48
Std Dev	3.766



B0L101S, U20-ch127, adc4

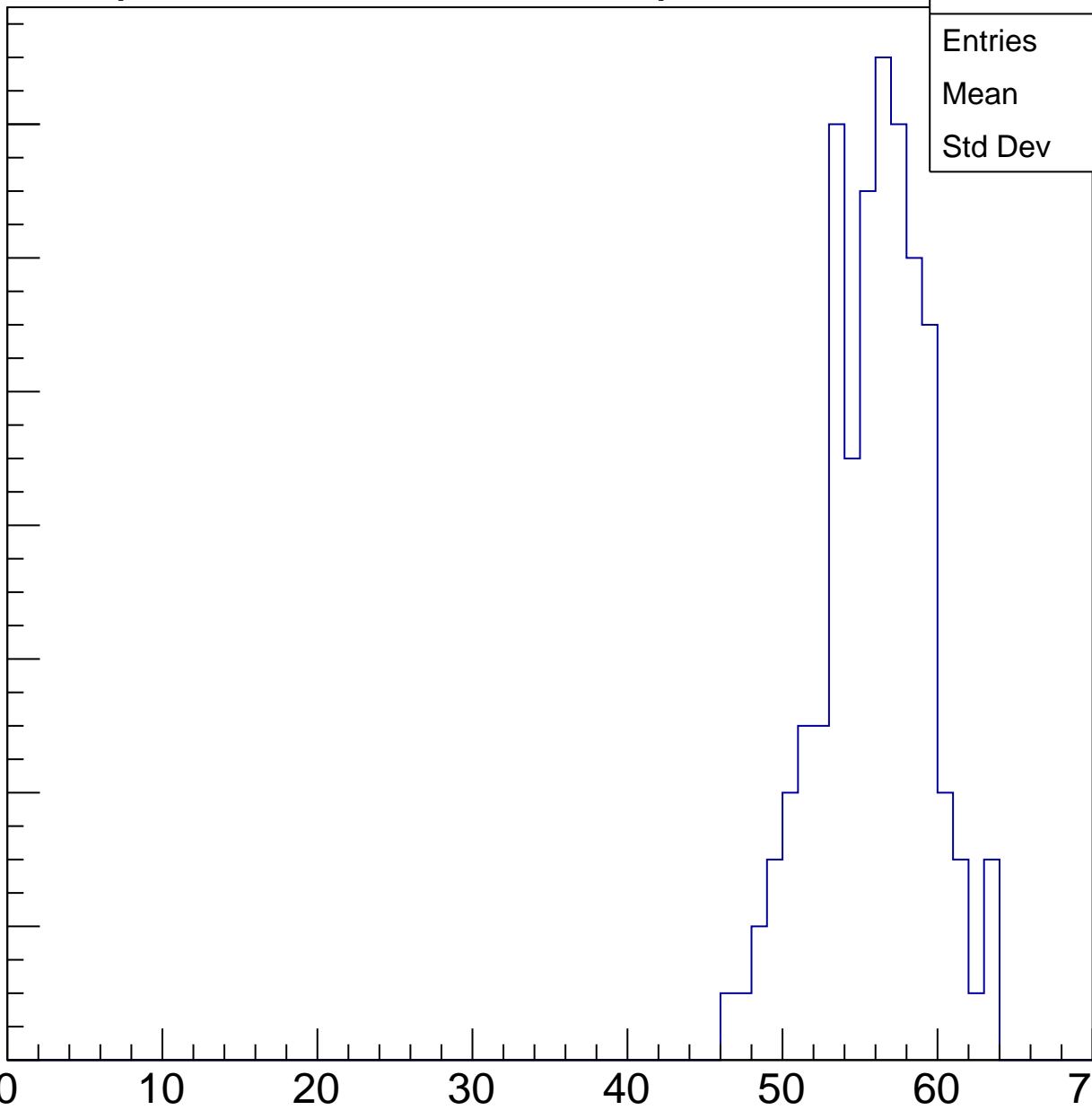
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.42
Std Dev	3.444

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U20-ch127, adc5

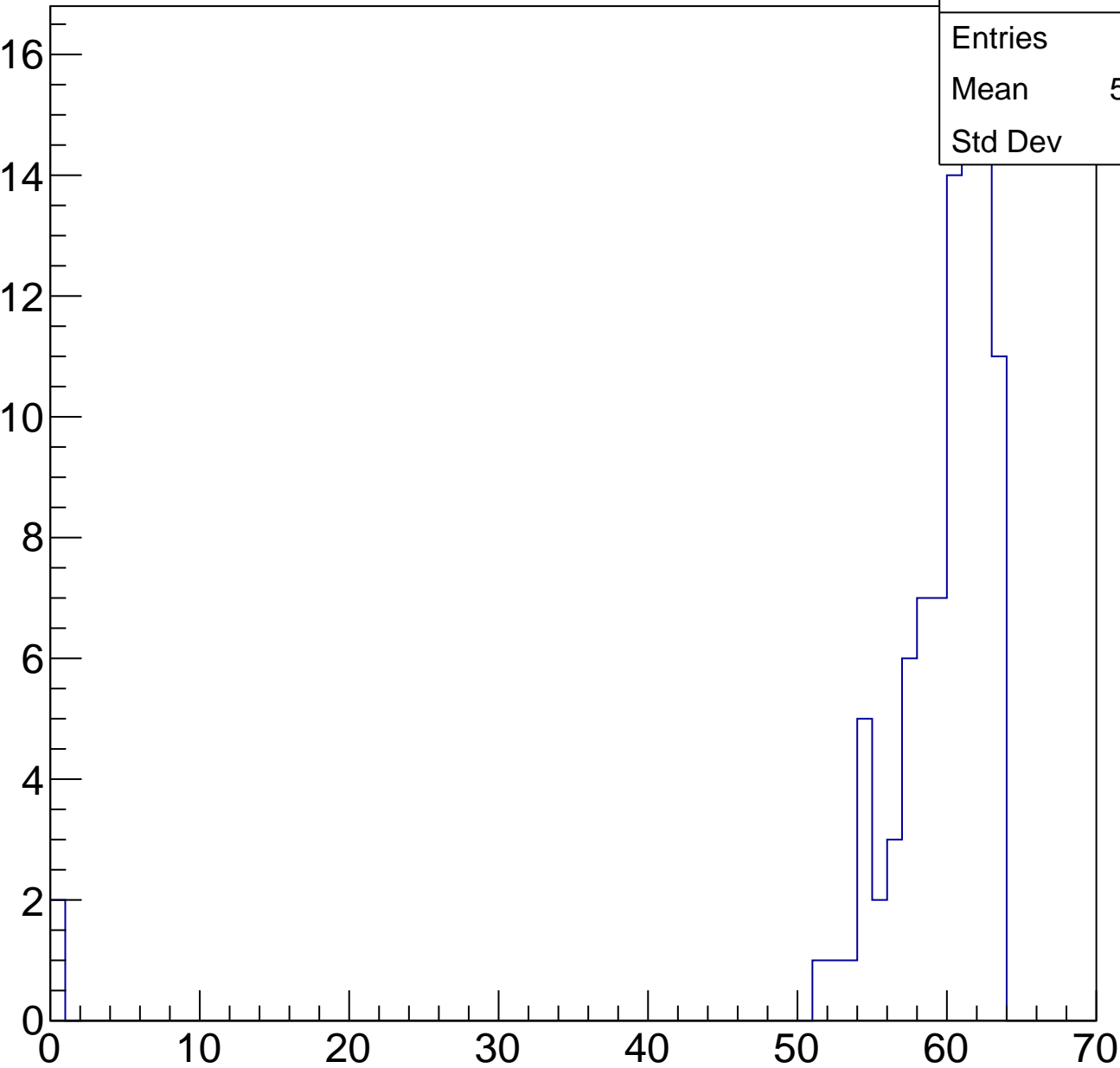
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.29
Std Dev	9.18

ampl

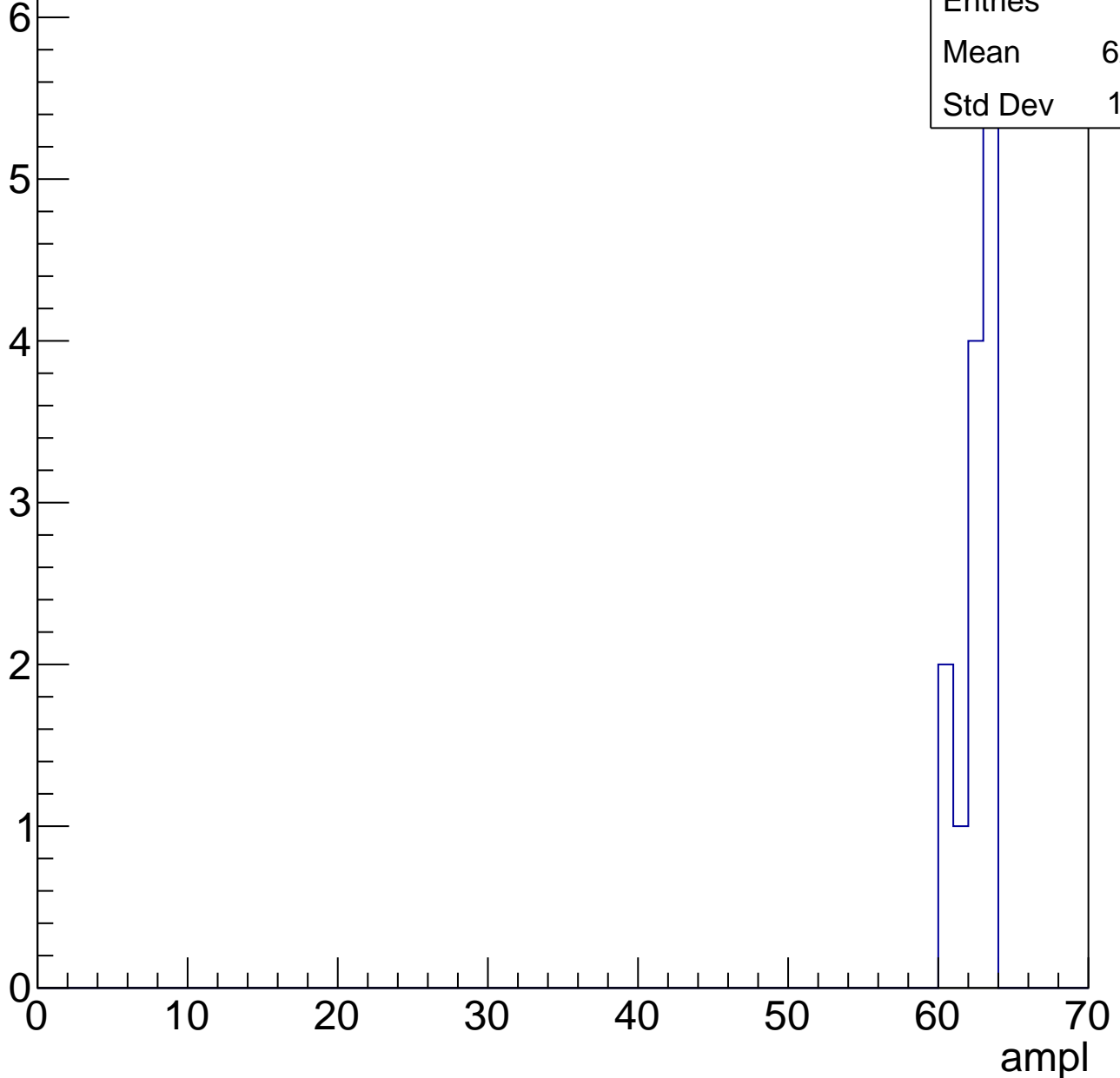


B0L101S, U20-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	62.08
Std Dev	1.071



B0L101S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0