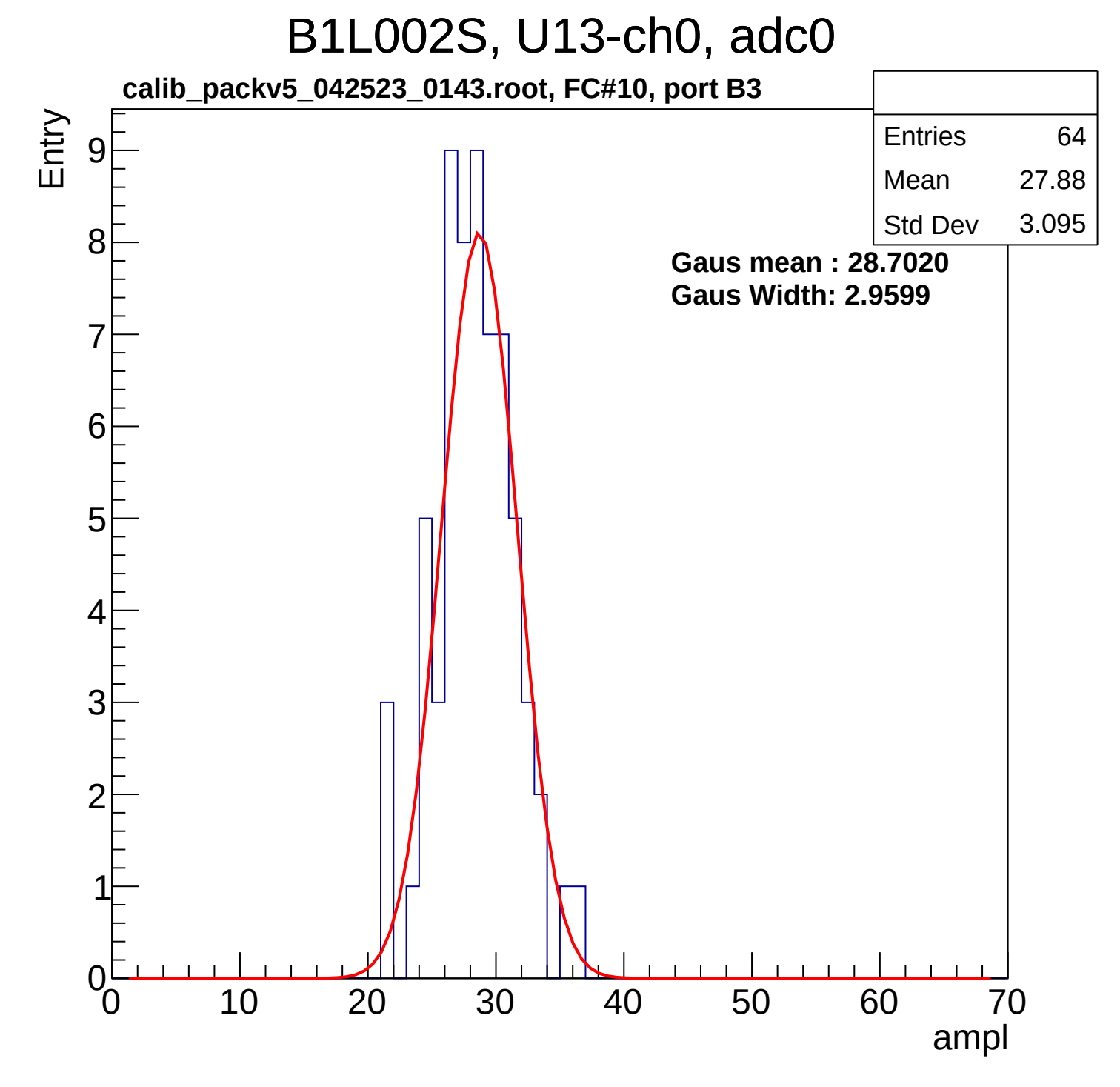
B1L002S, U13-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.88
Std Dev 3.095
Gaus mean : 28.7020
Gaus Width: 2.9599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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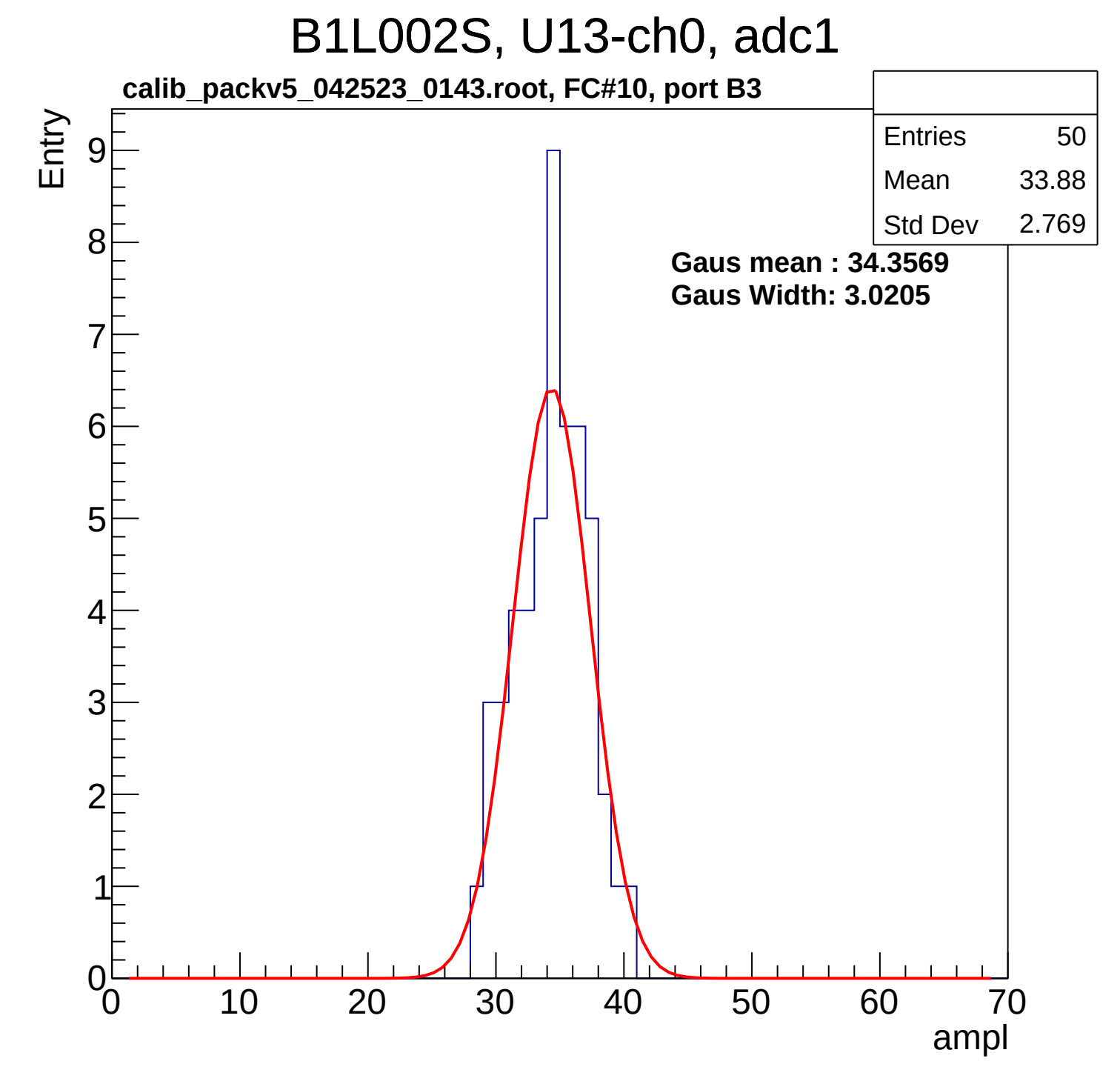
B1L002S, U13-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 33.88
Std Dev 2.769
Gaus mean : 34.3569
Gaus Width: 3.0205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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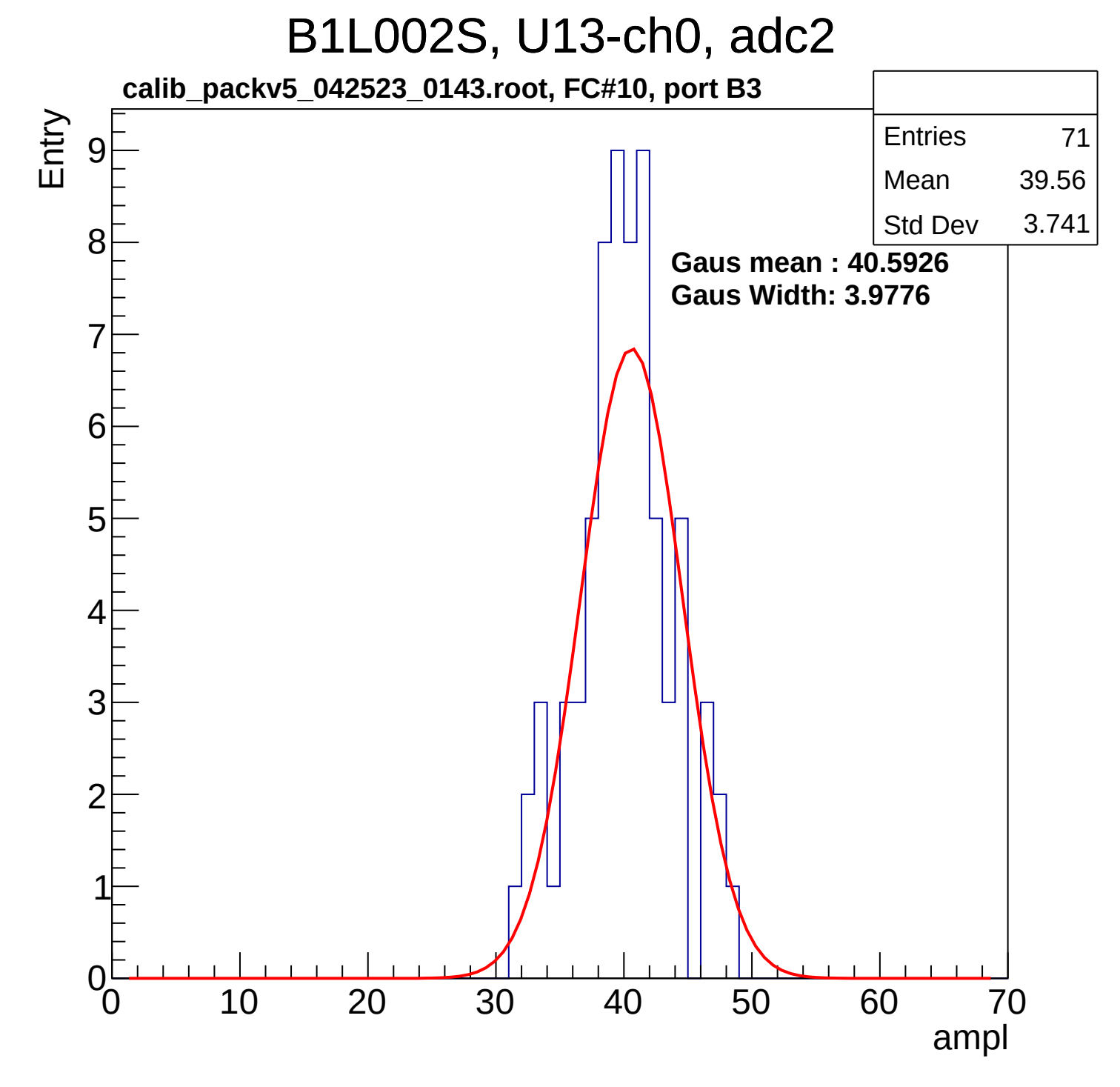
B1L002S, U13-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 39.56
Std Dev 3.741
Gaus mean : 40.5926
Gaus Width: 3.9776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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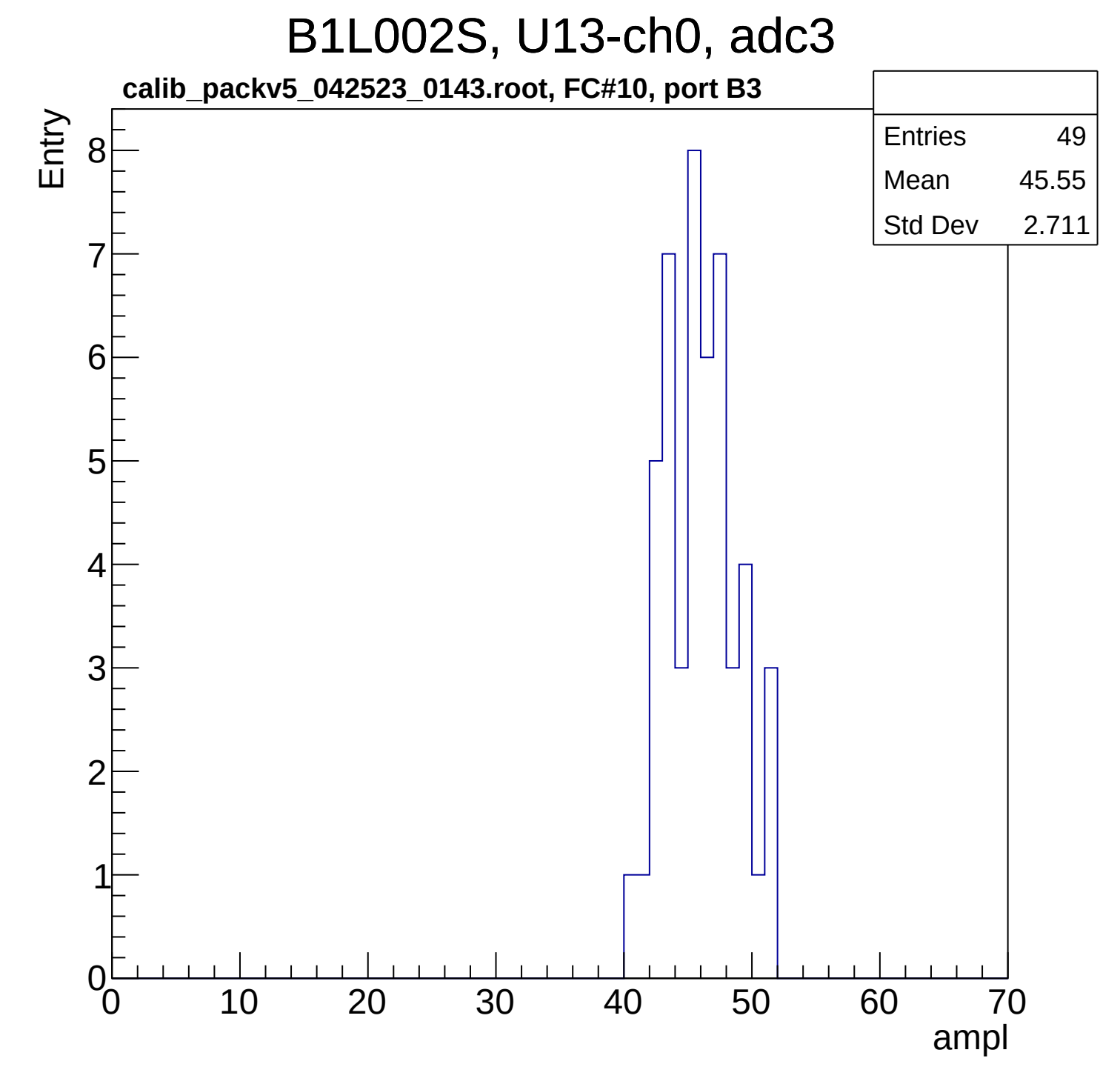
B1L002S, U13-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 45.55
Std Dev 2.711
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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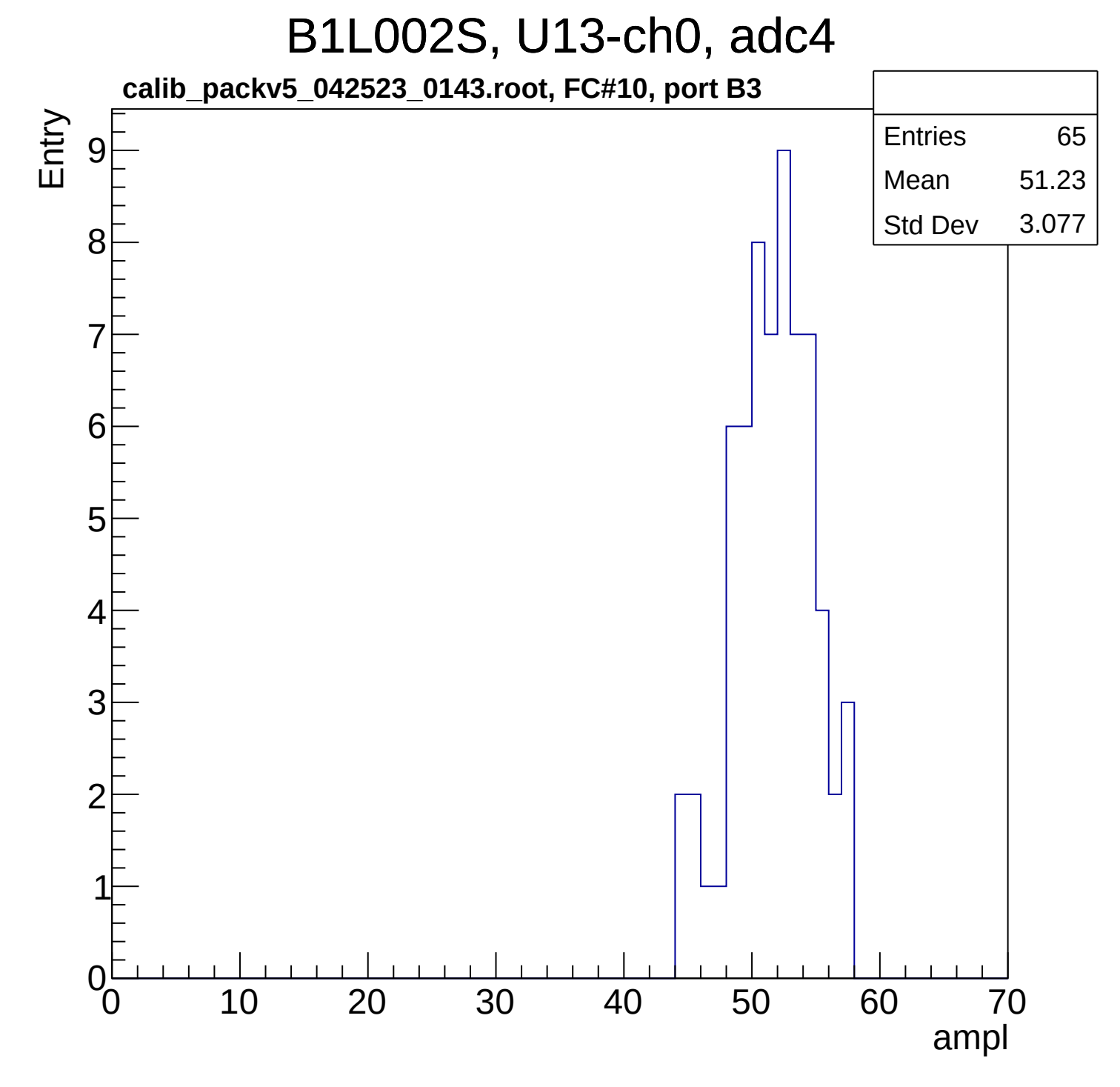
B1L002S, U13-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 51.23
Std Dev 3.077
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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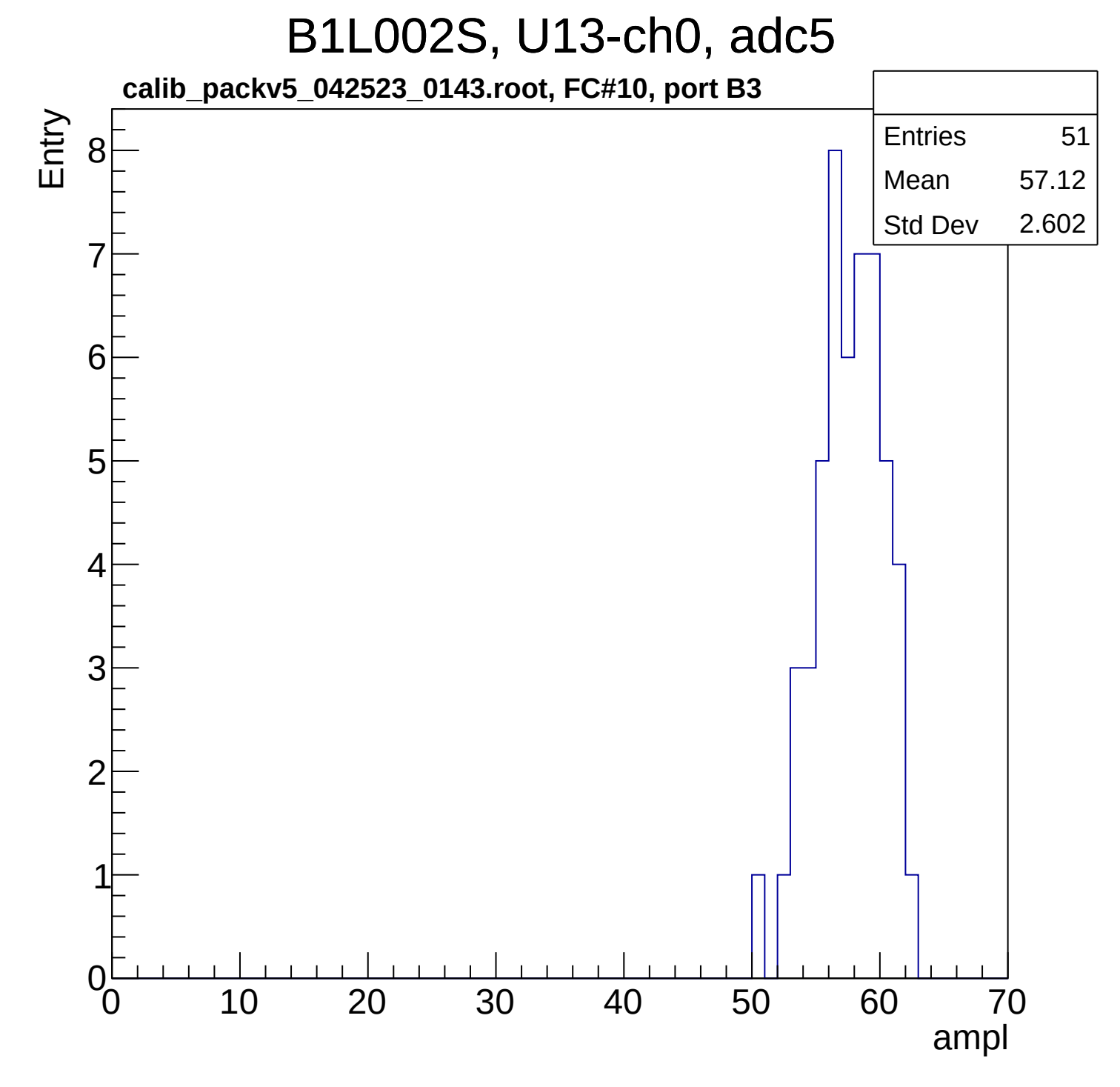
B1L002S, U13-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.12
Std Dev 2.602
Entry
ampl
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2
3
4
5
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7
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0
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60
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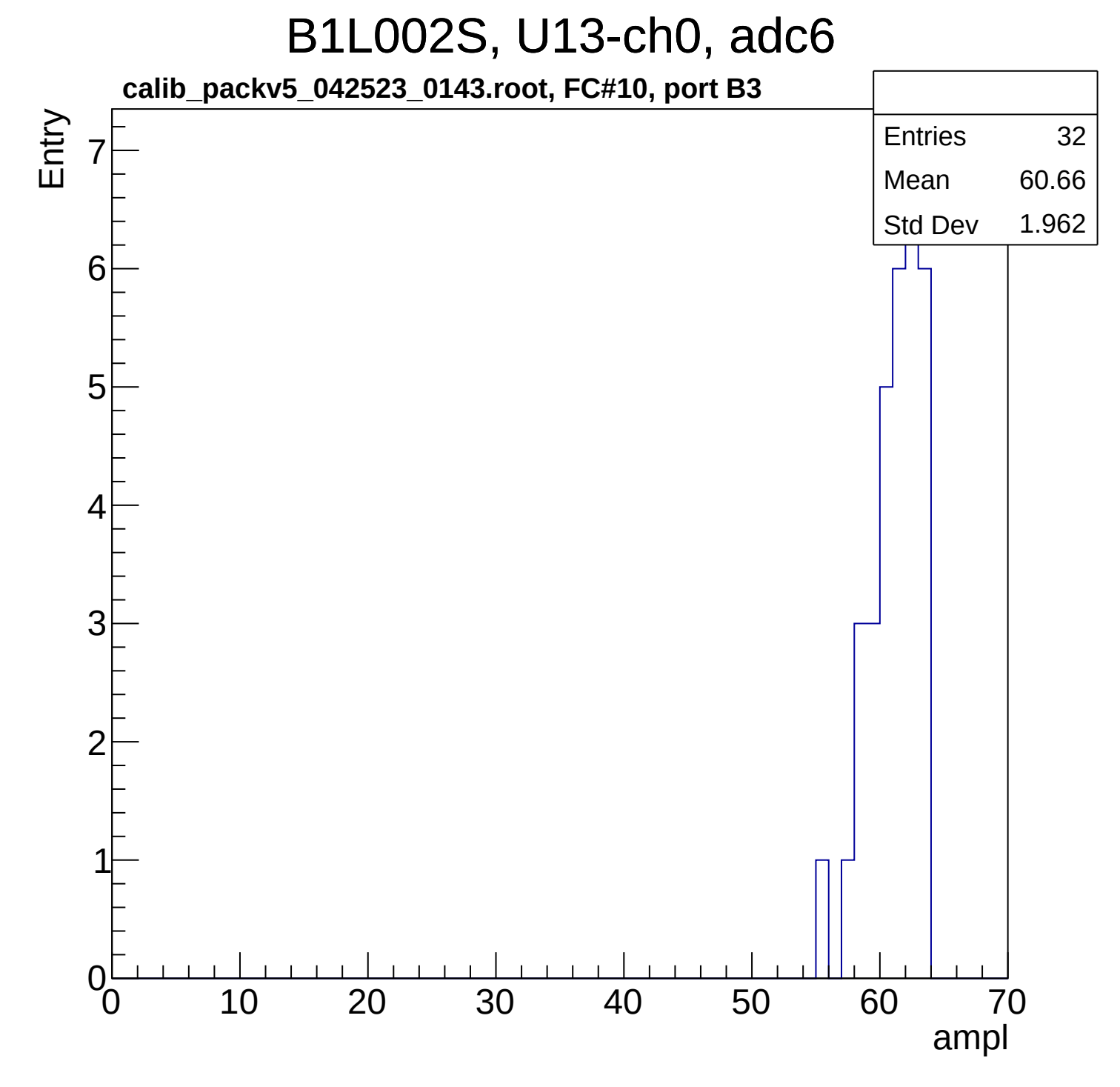
B1L002S, U13-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 60.66
Std Dev 1.962
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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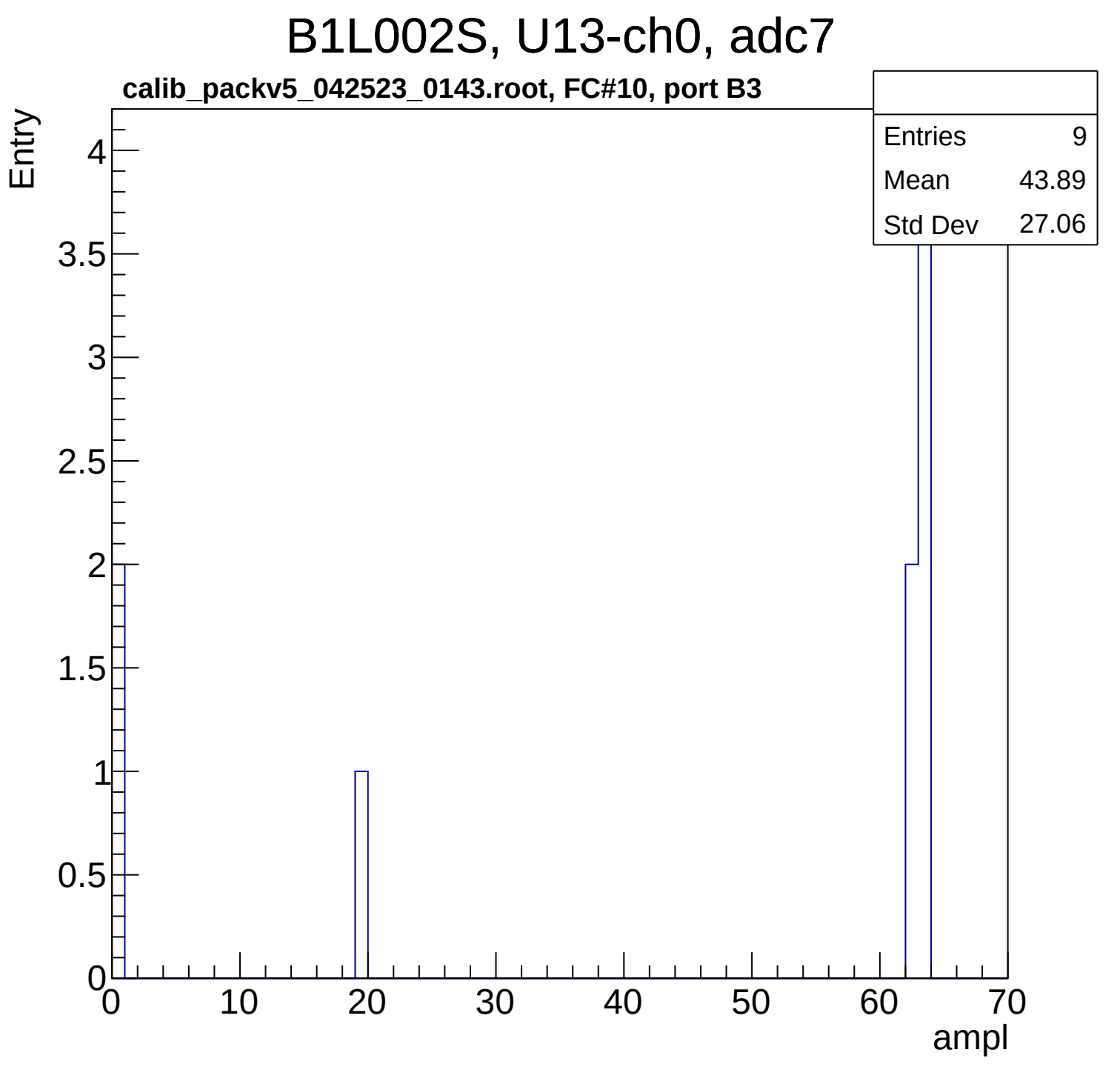
B1L002S, U13-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 43.89
Std Dev 27.06
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
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60
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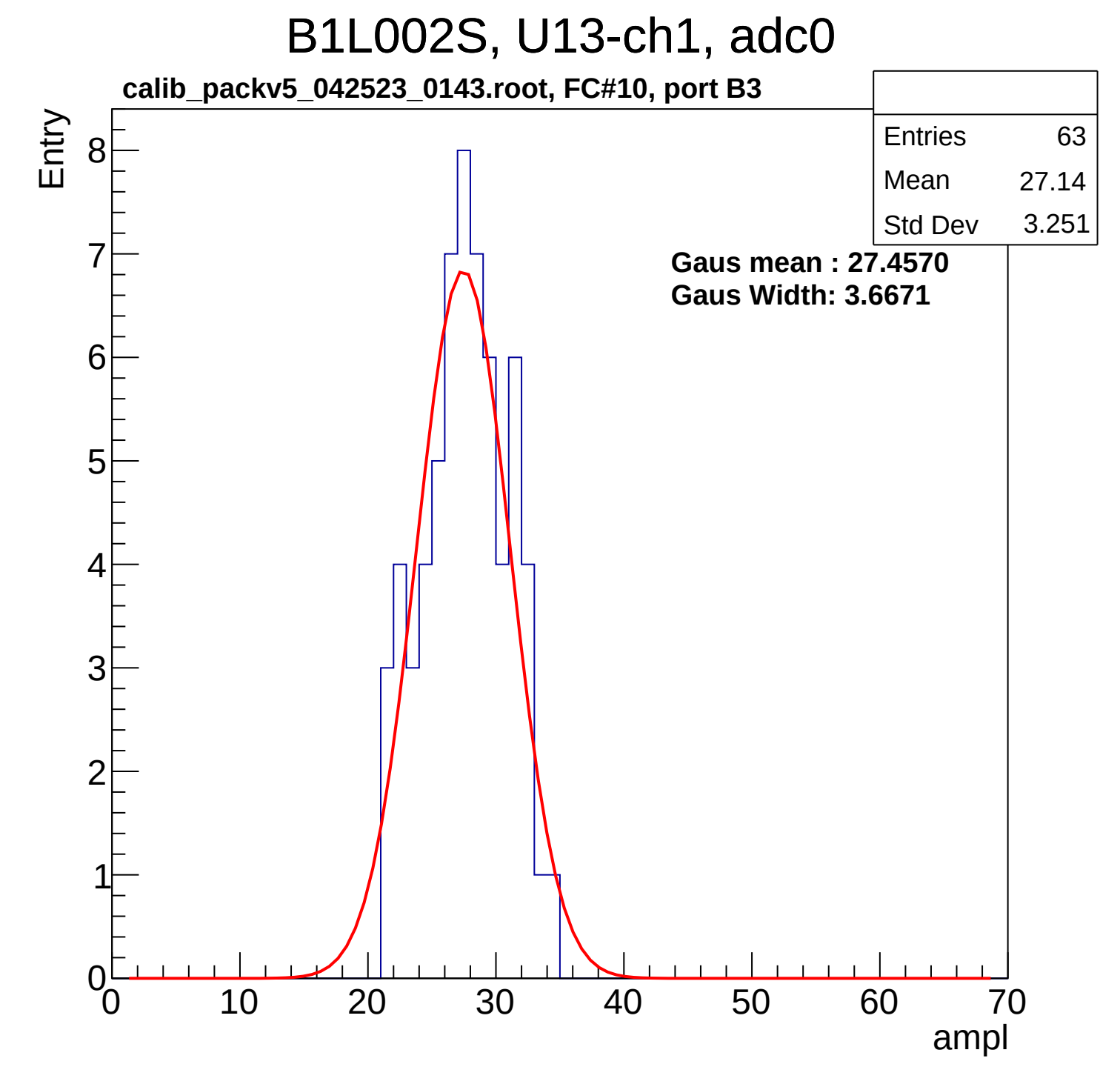
B1L002S, U13-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.14
Std Dev 3.251
Gaus mean : 27.4570
Gaus Width: 3.6671
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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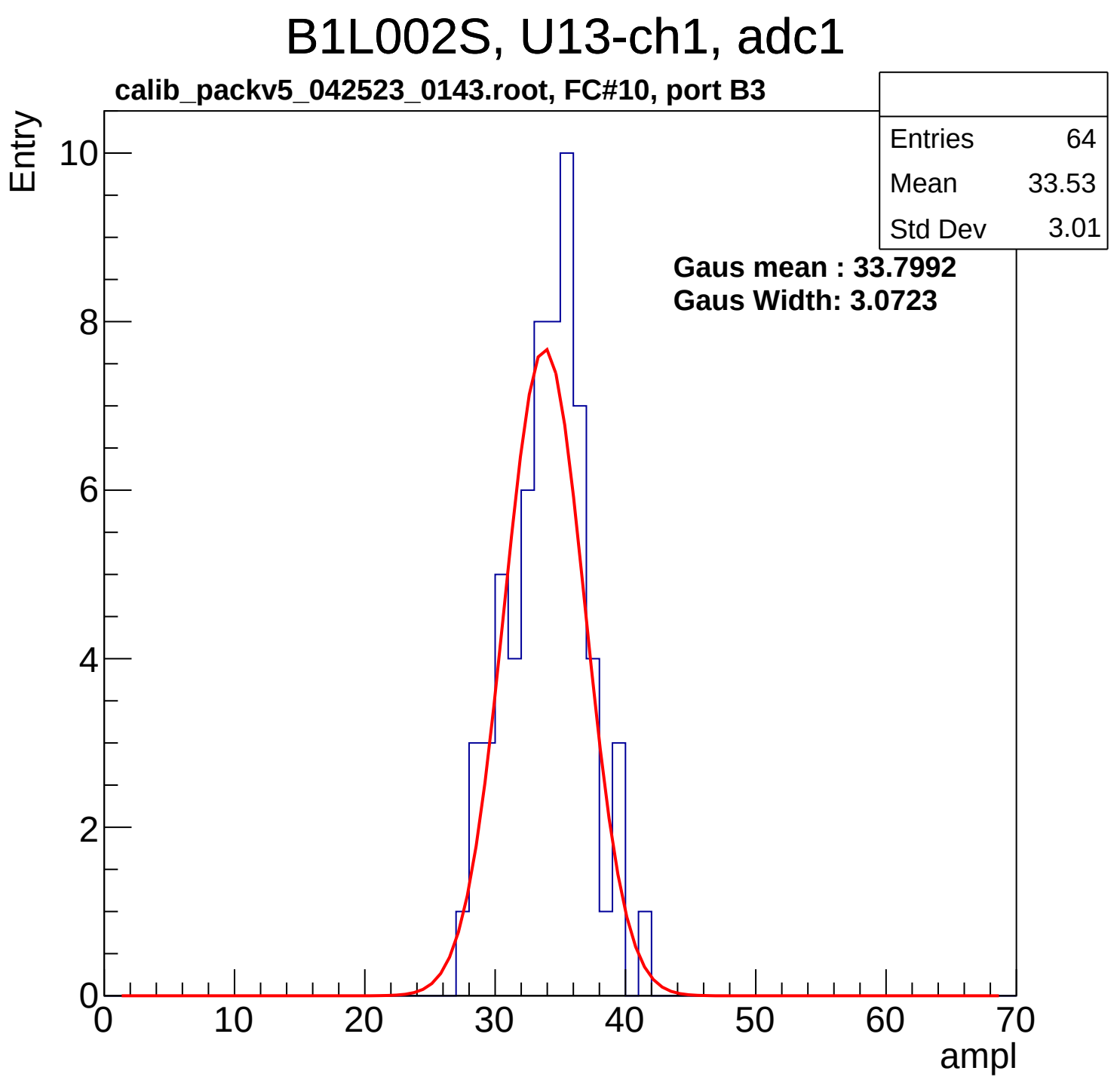
B1L002S, U13-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 33.53
Std Dev 3.01
Gaus mean : 33.7992
Gaus Width: 3.0723
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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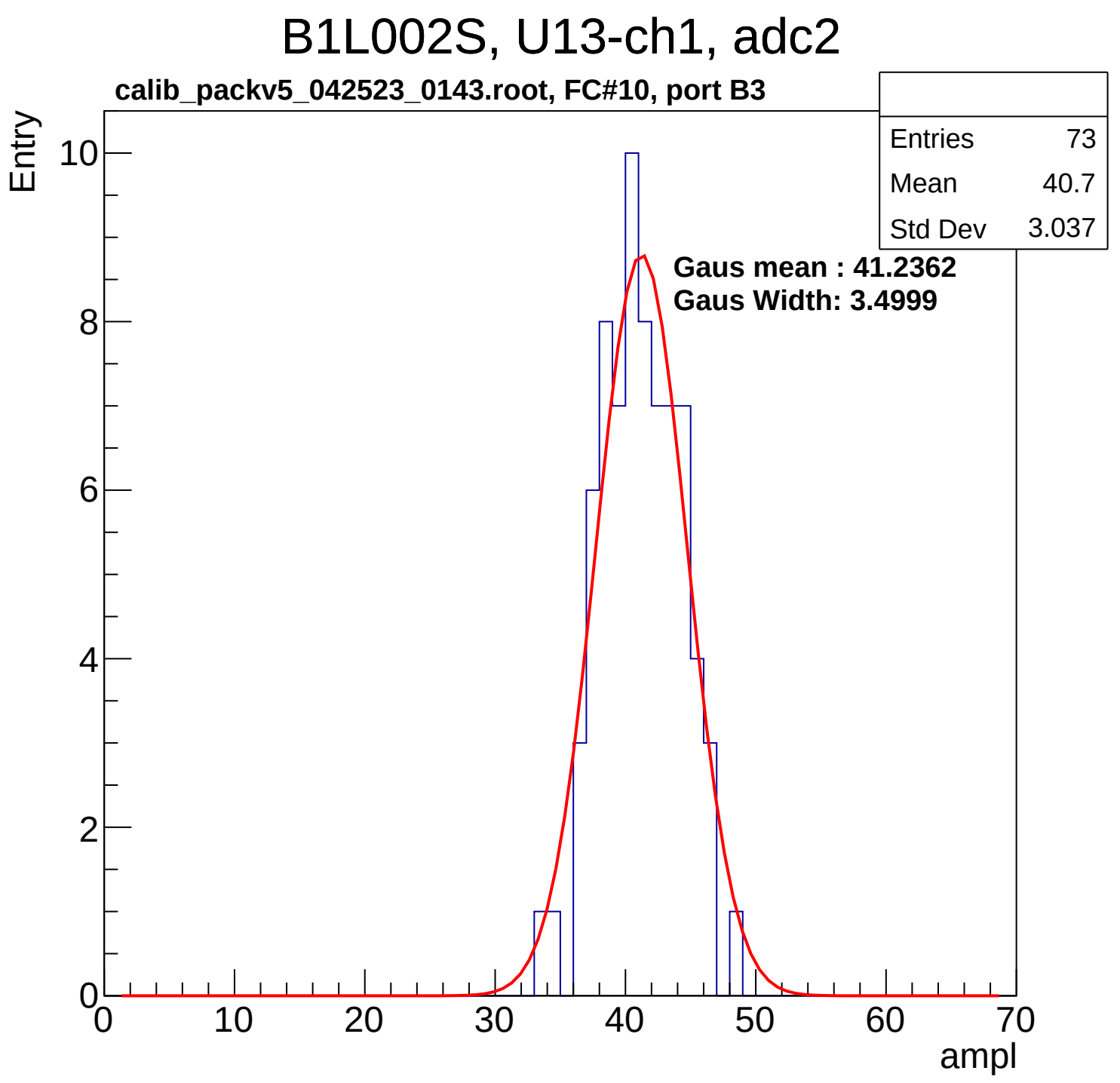
B1L002S, U13-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.7
Std Dev 3.037
Gaus mean : 41.2362
Gaus Width: 3.4999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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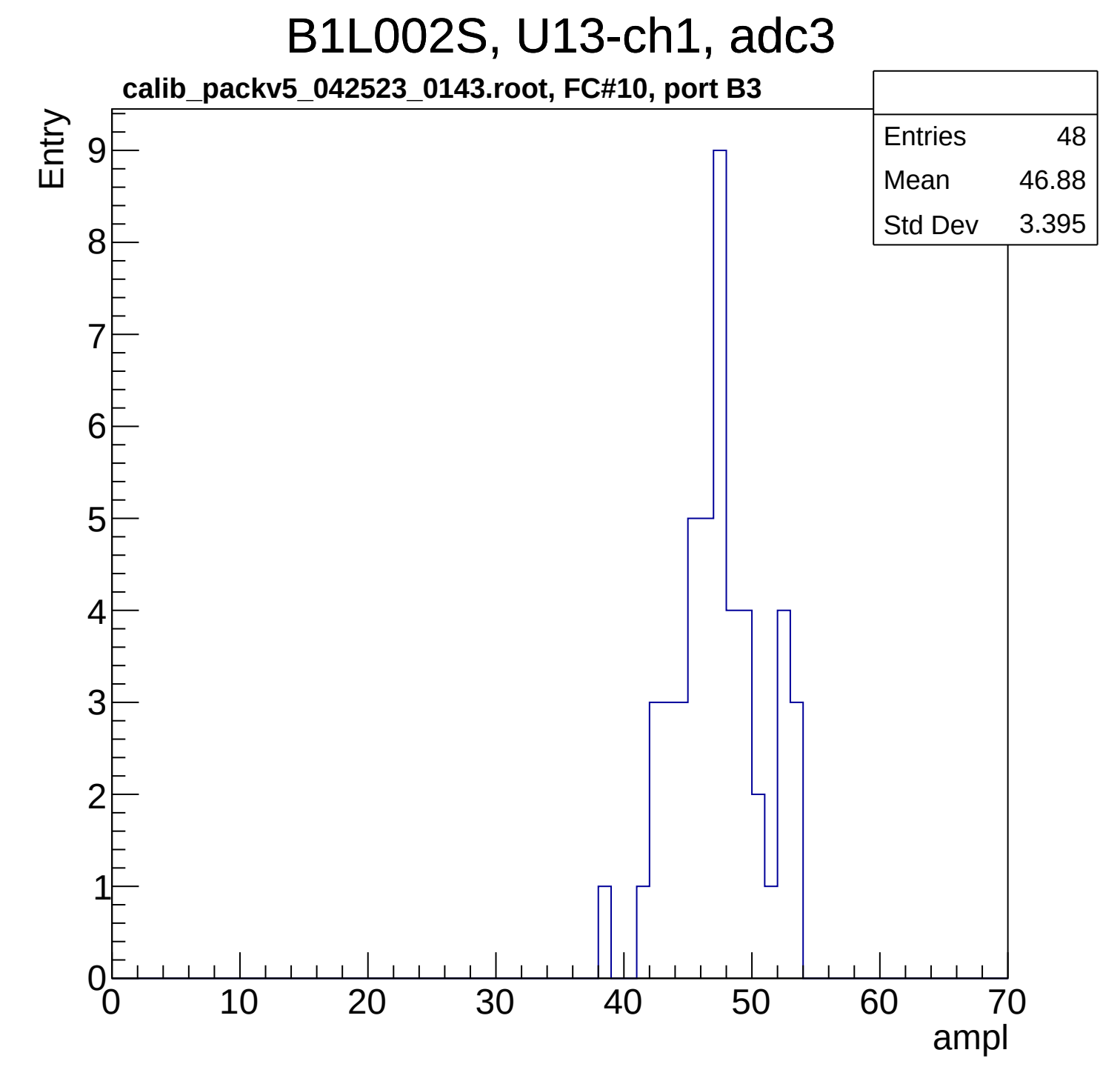
B1L002S, U13-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 46.88
Std Dev 3.395
Entry
ampl
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2
3
4
5
6
7
8
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0
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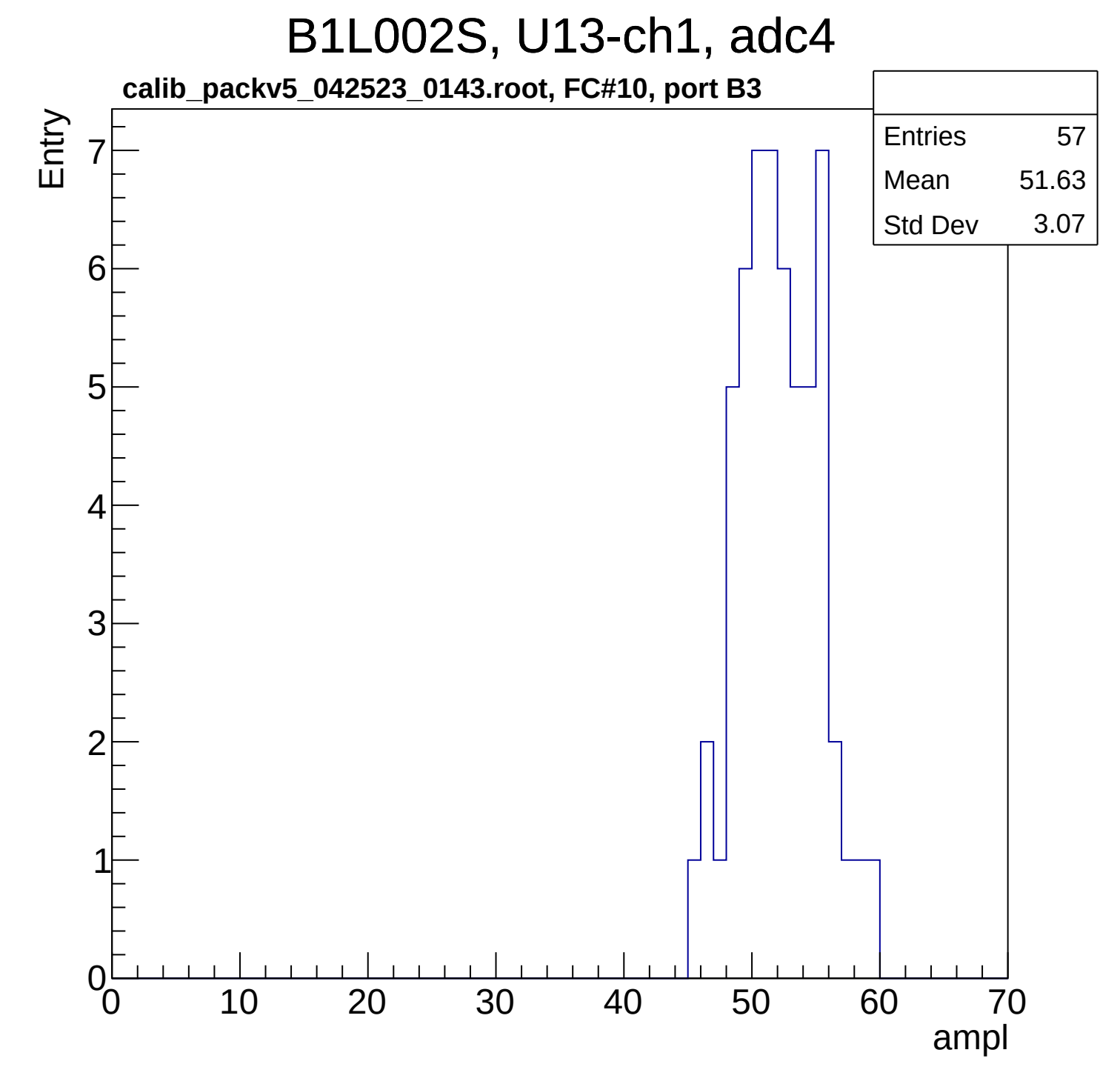
B1L002S, U13-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.63
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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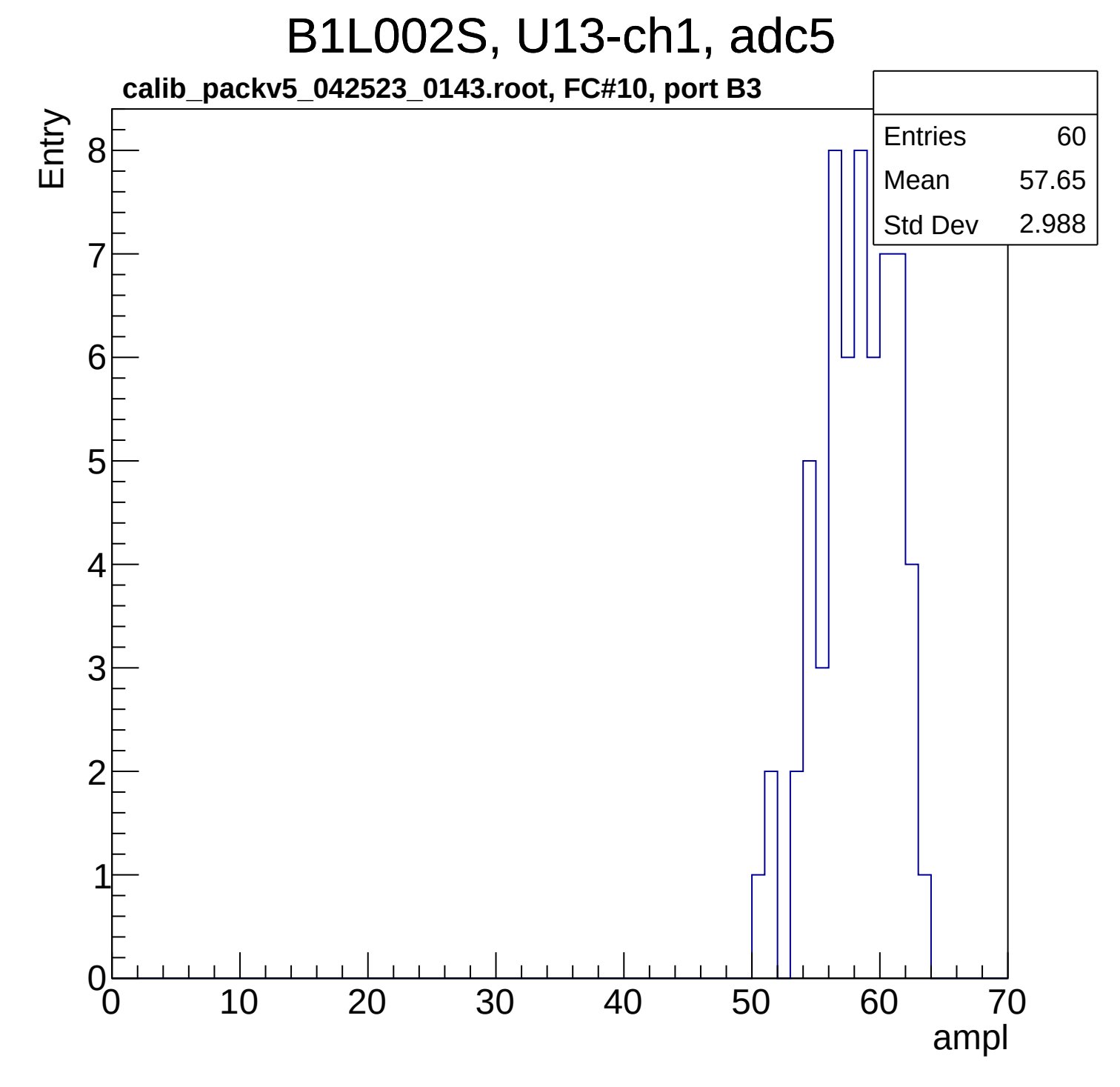
B1L002S, U13-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.65
Std Dev 2.988
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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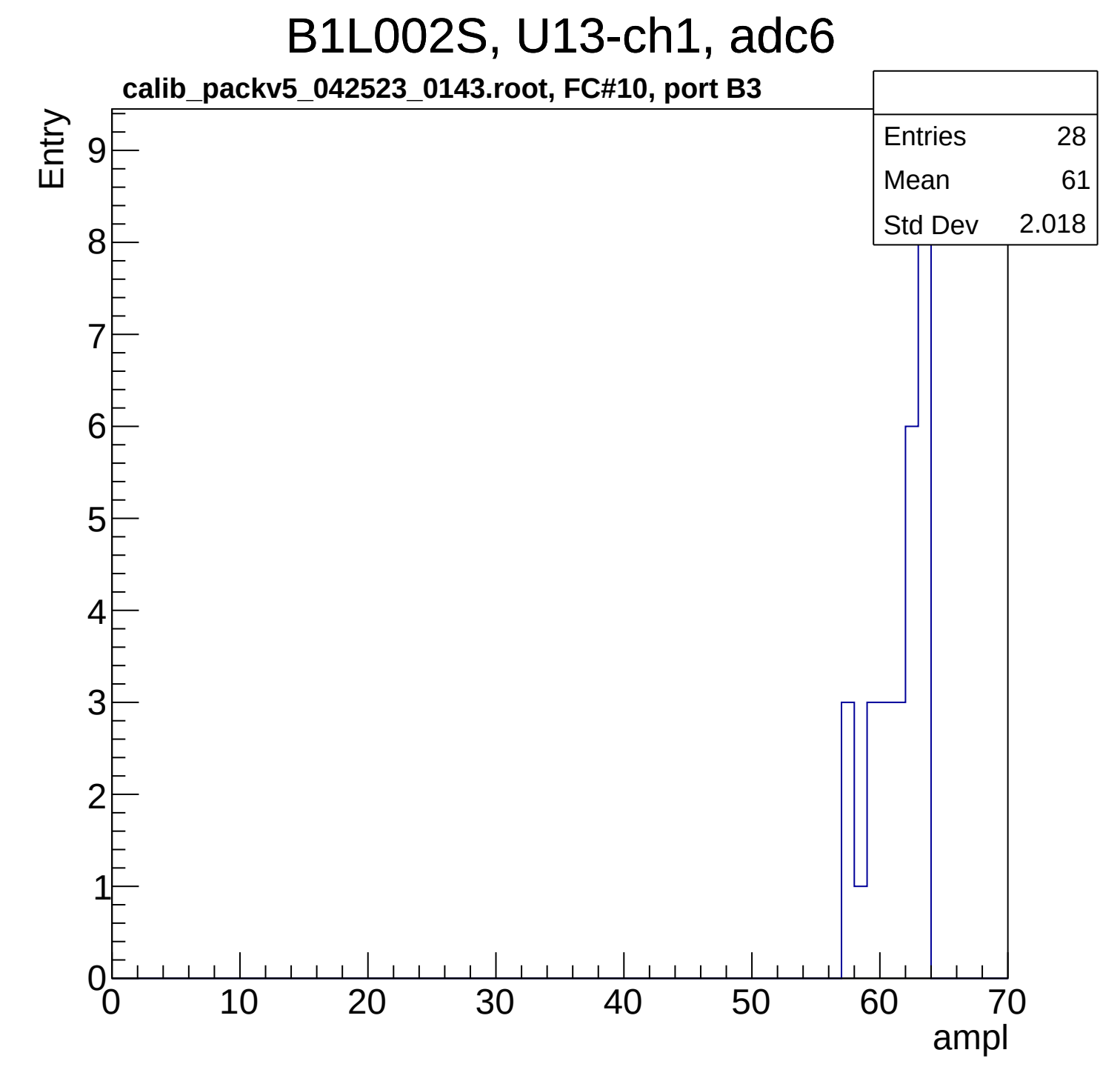
B1L002S, U13-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61
Std Dev 2.018
Entry
ampl
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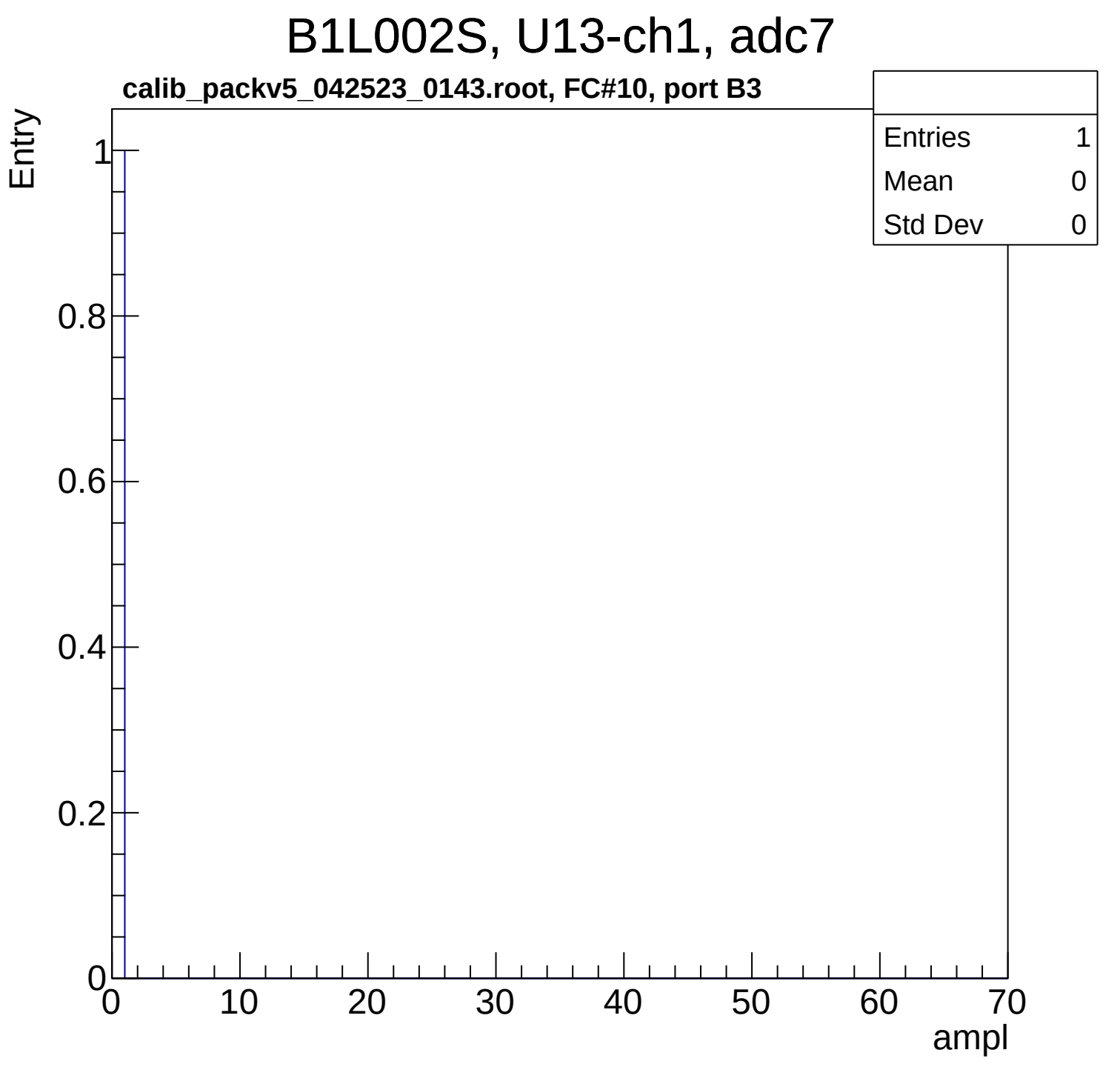
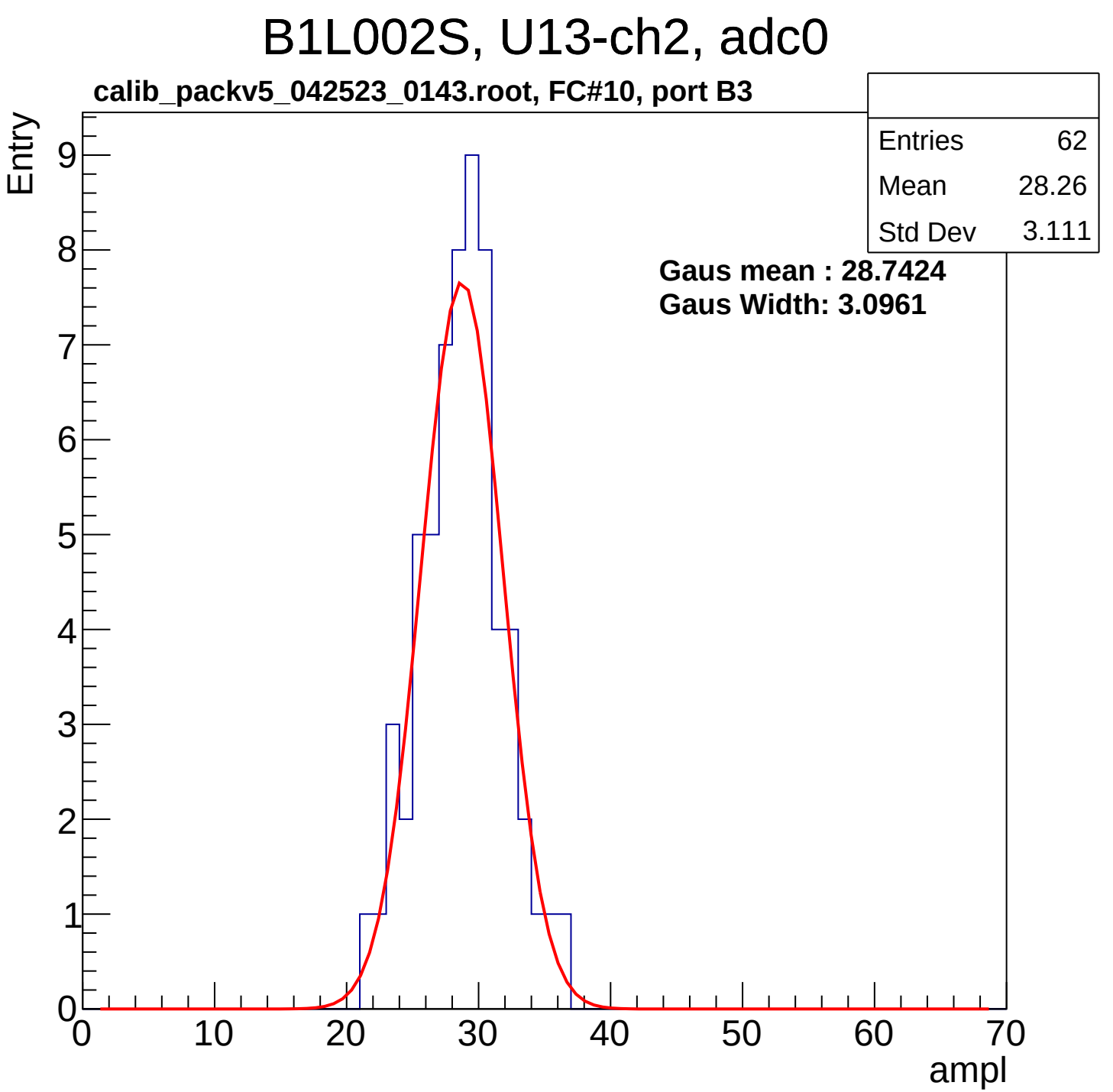
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.26
Std Dev 3.111
Gaus mean : 28.7424
Gaus Width: 3.0961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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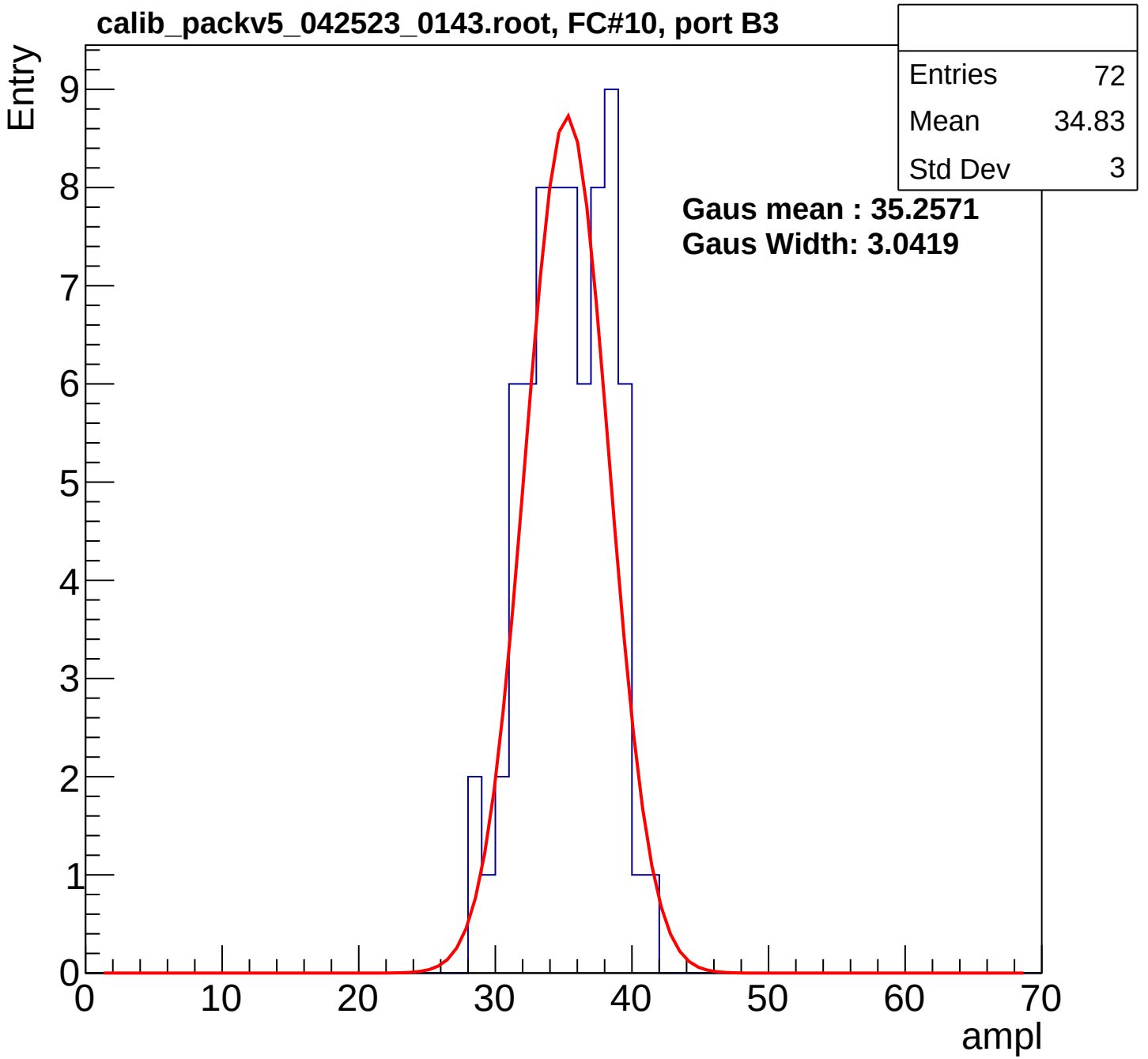
B1L002S, U13-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.83
Std Dev 3
Gaus mean : 35.2571
Gaus Width: 3.0419
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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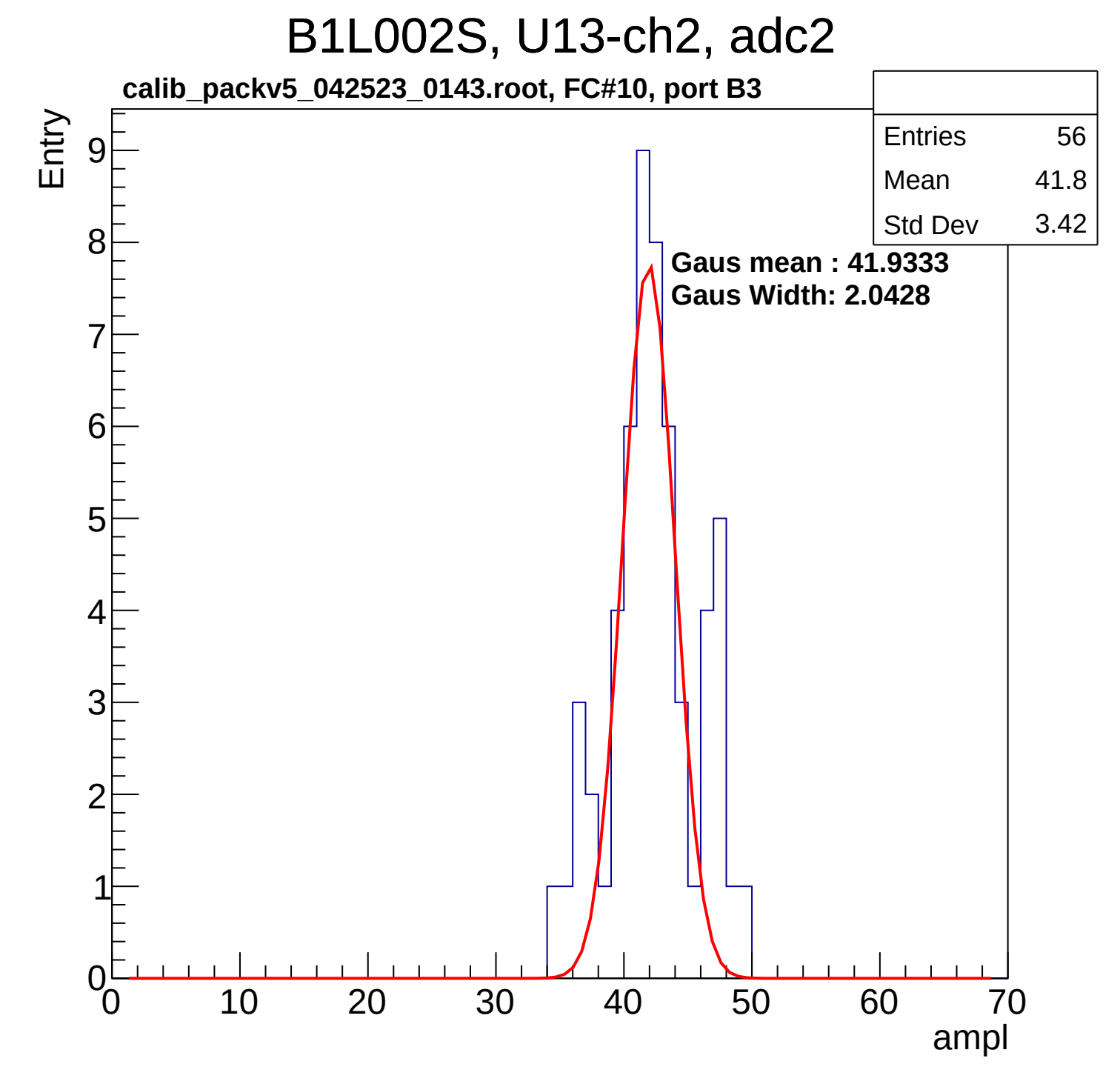
B1L002S, U13-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.8
Std Dev 3.42
Gaus mean : 41.9333
Gaus Width: 2.0428
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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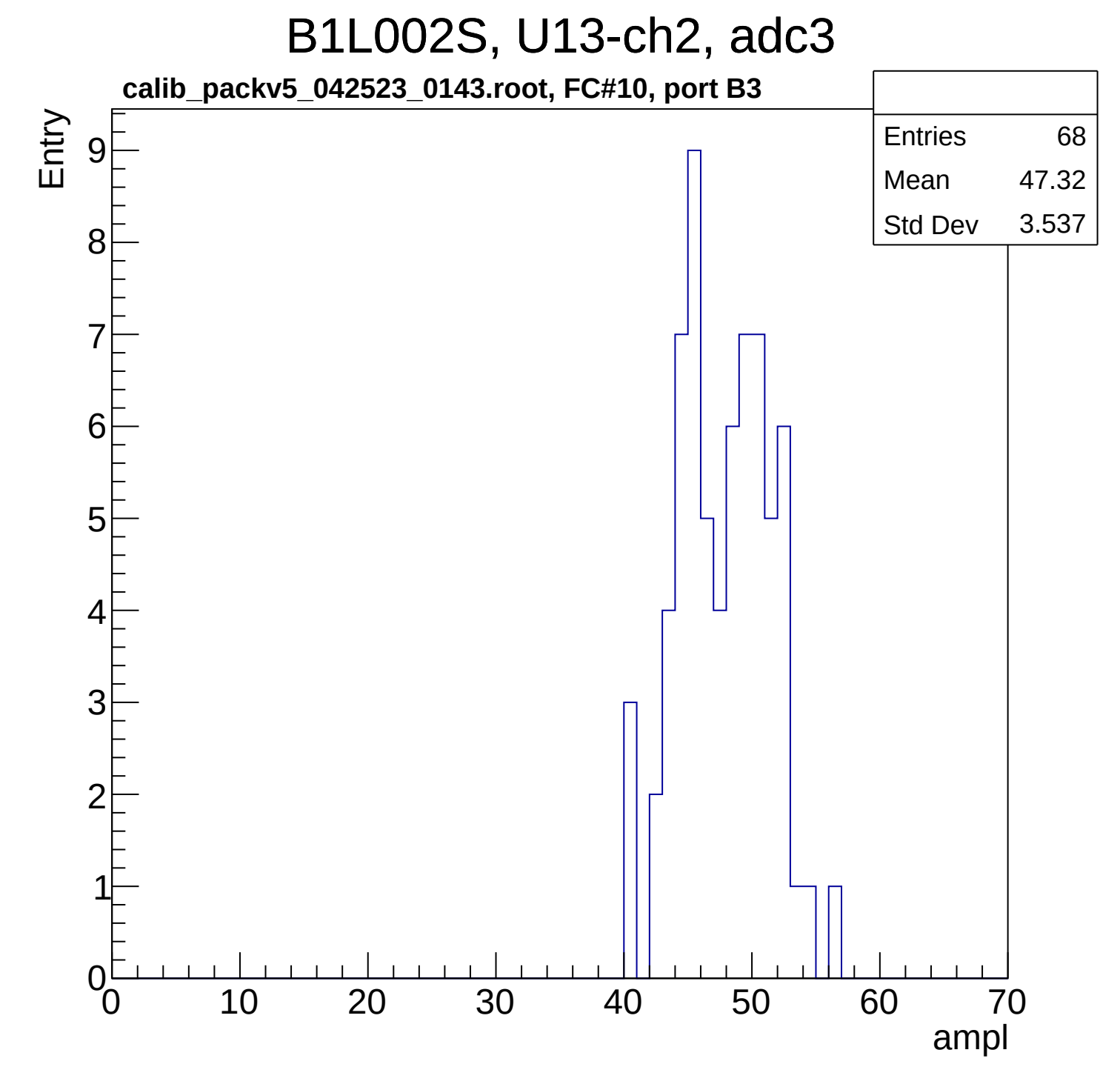
B1L002S, U13-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.32
Std Dev 3.537
Entry
ampl
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3
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5
6
7
8
9
0
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60
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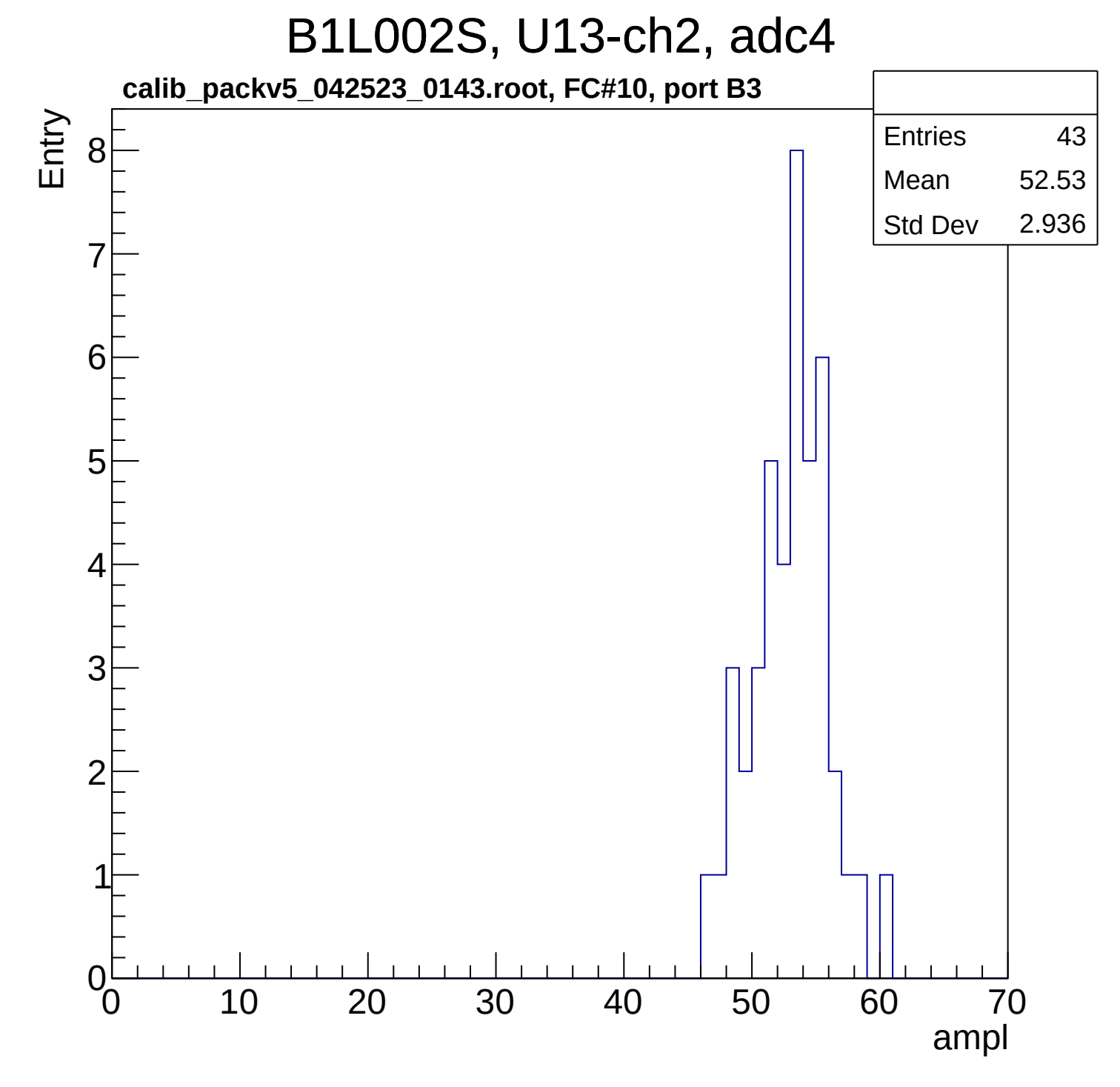
B1L002S, U13-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 52.53
Std Dev 2.936
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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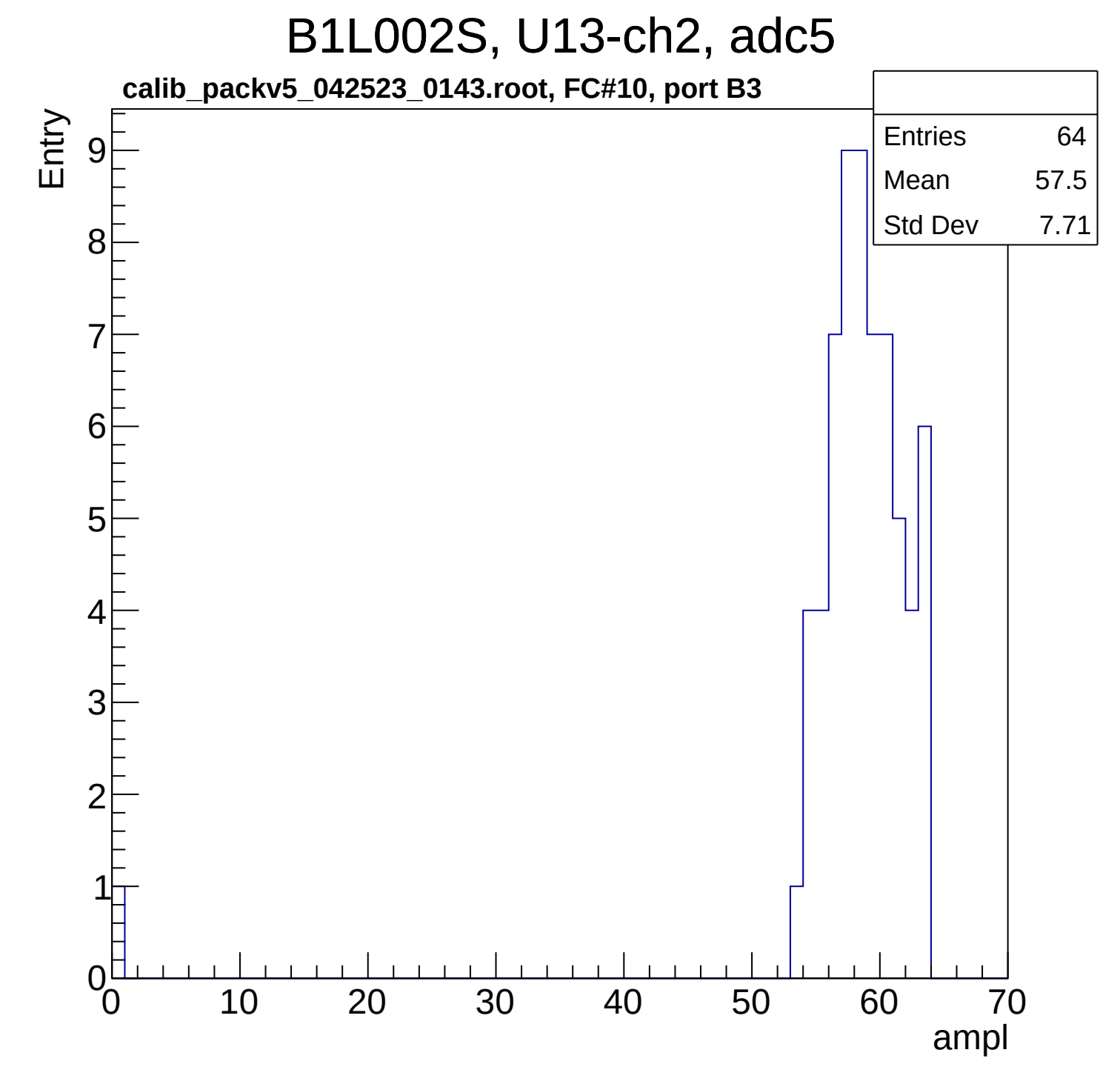
B1L002S, U13-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.5
Std Dev 7.71
Entry
ampl
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2
3
4
5
6
7
8
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0
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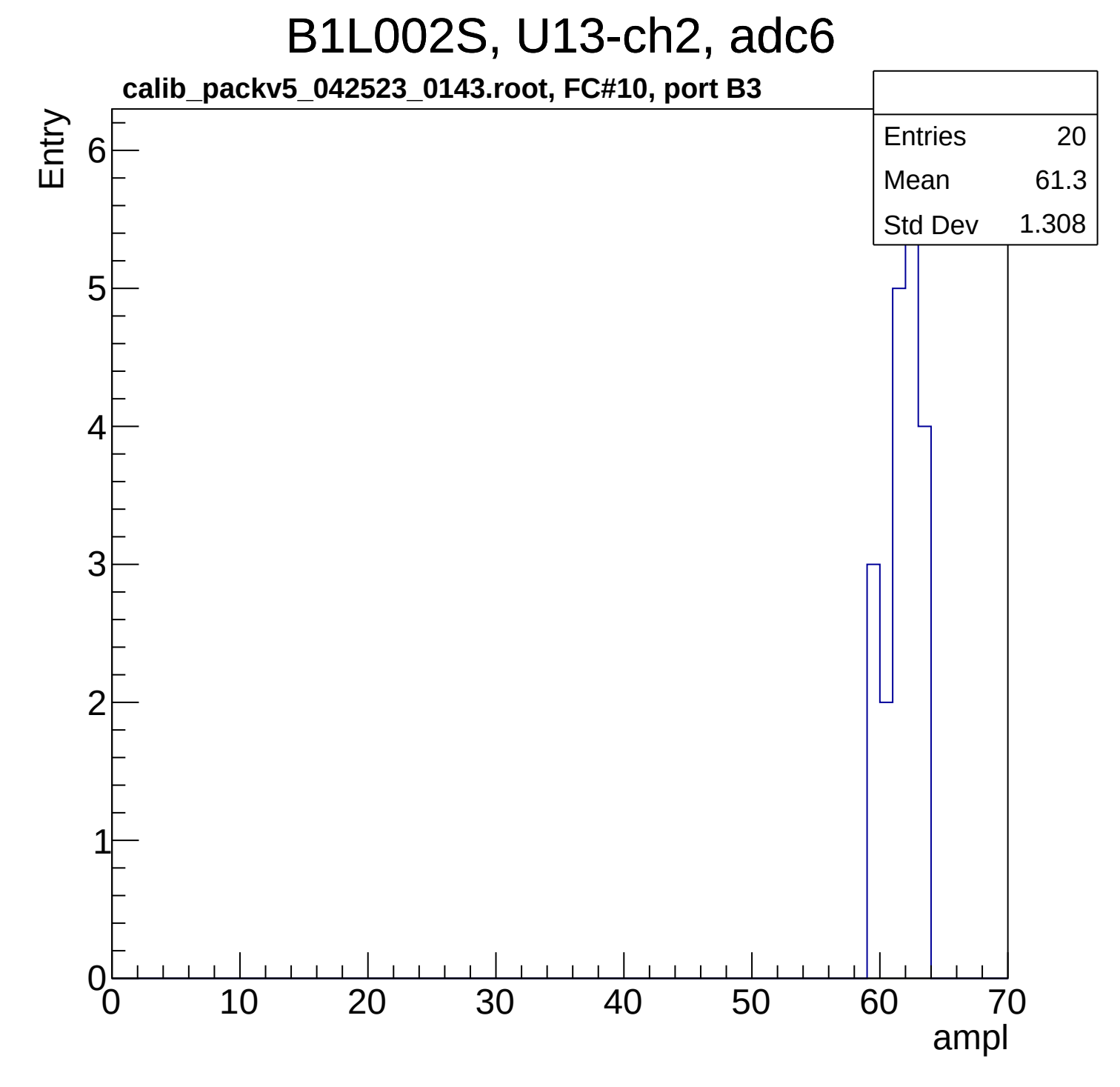
B1L002S, U13-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.3
Std Dev 1.308
Entry
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1
2
3
4
5
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0
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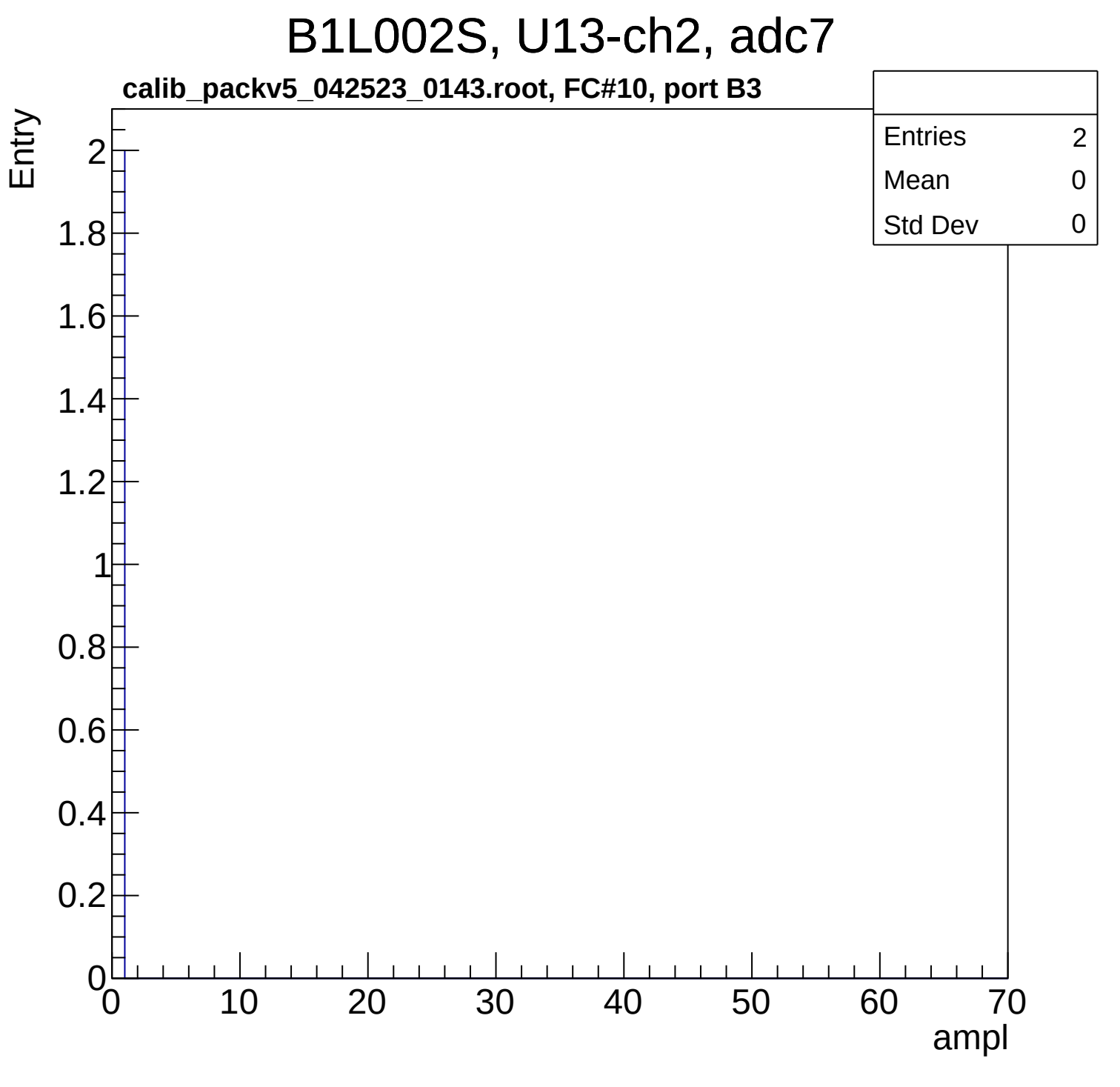
20
30
40
50
60
70
ampl


B1L002S, U13-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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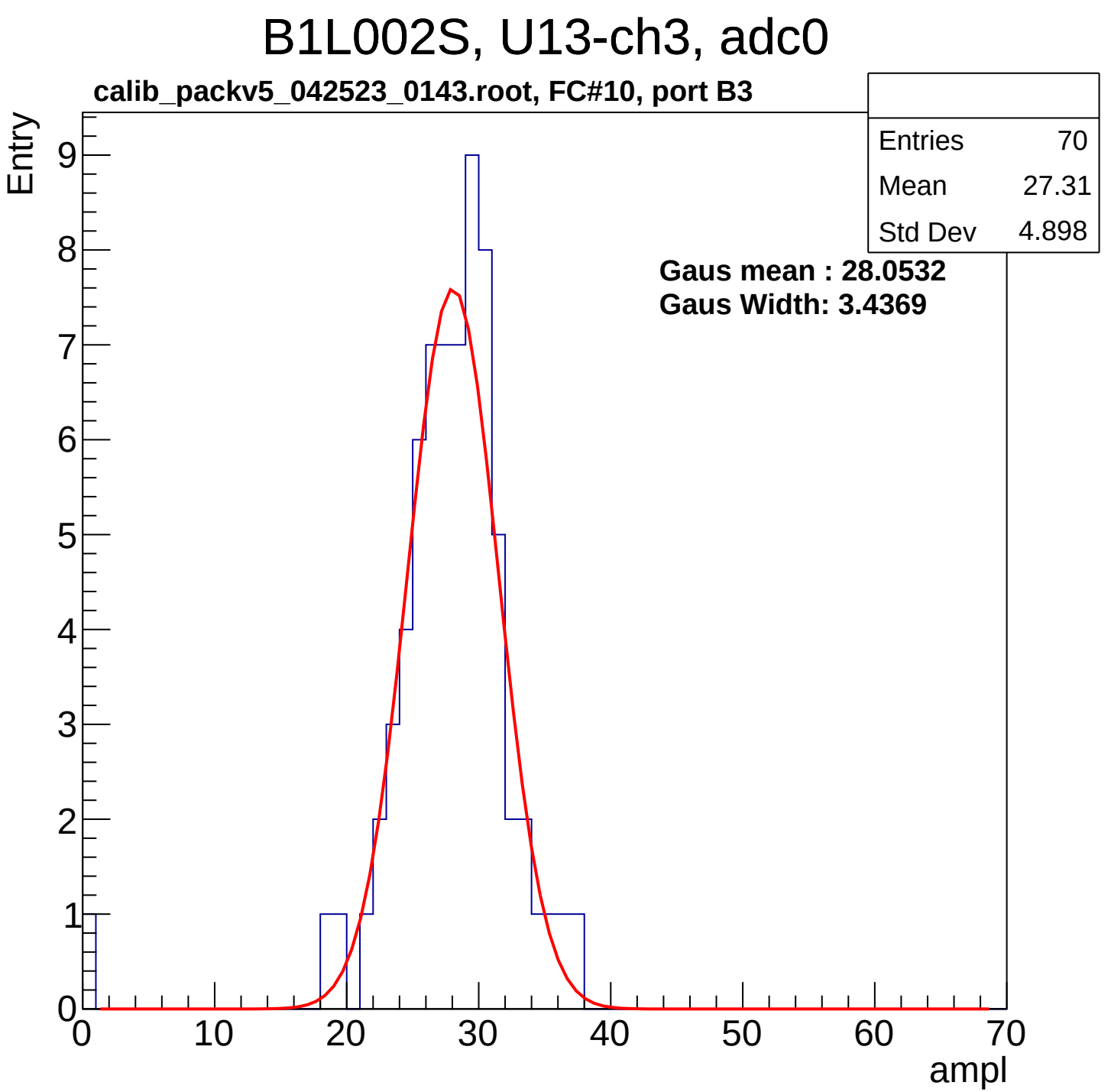
40
50
60
70


B1L002S, U13-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.31
Std Dev 4.898
Gaus mean : 28.0532
Gaus Width: 3.4369
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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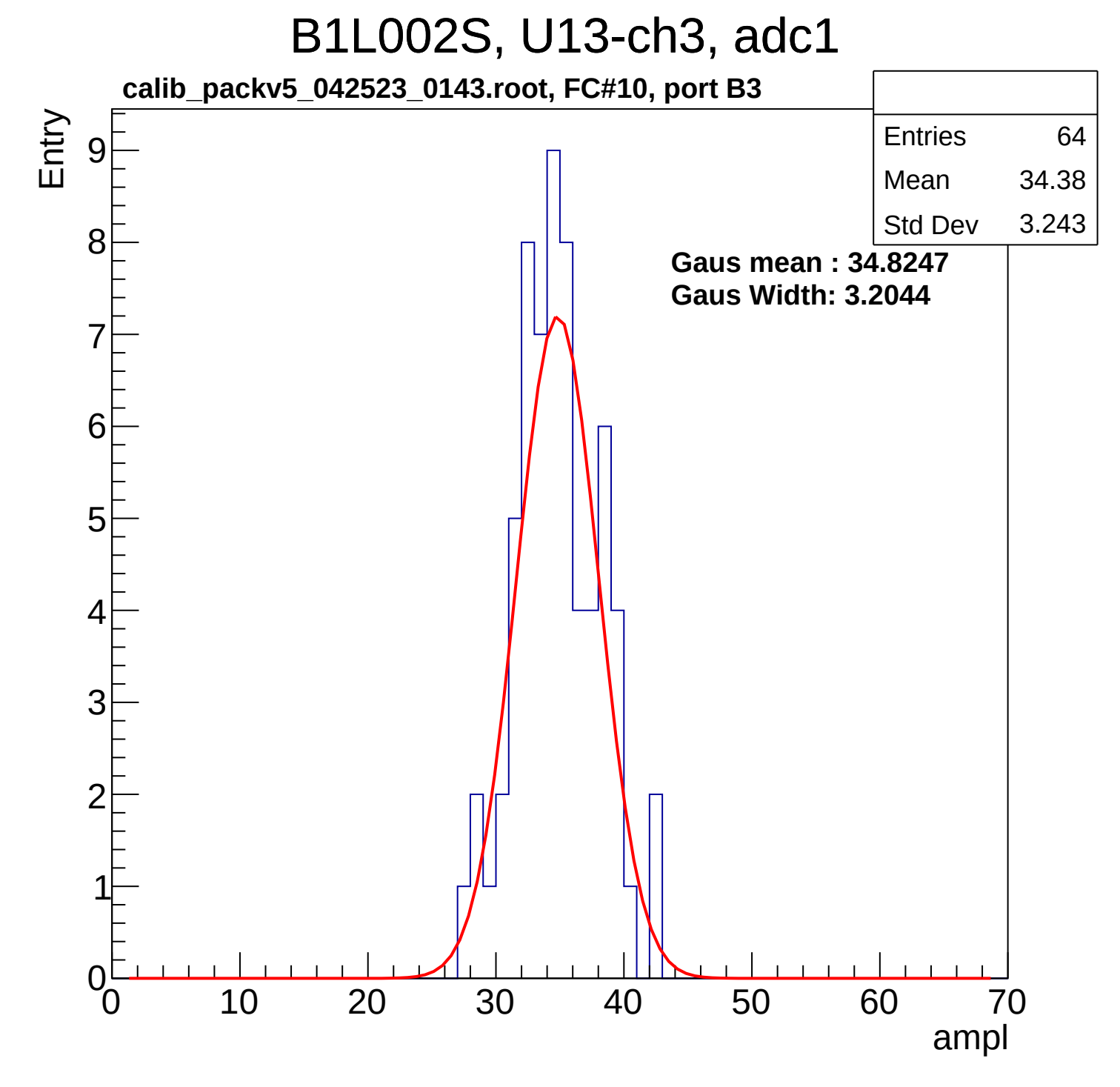
B1L002S, U13-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.38
Std Dev 3.243
Gaus mean : 34.8247
Gaus Width: 3.2044
Entry
ampl
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10
20
30
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50
60
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2
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8
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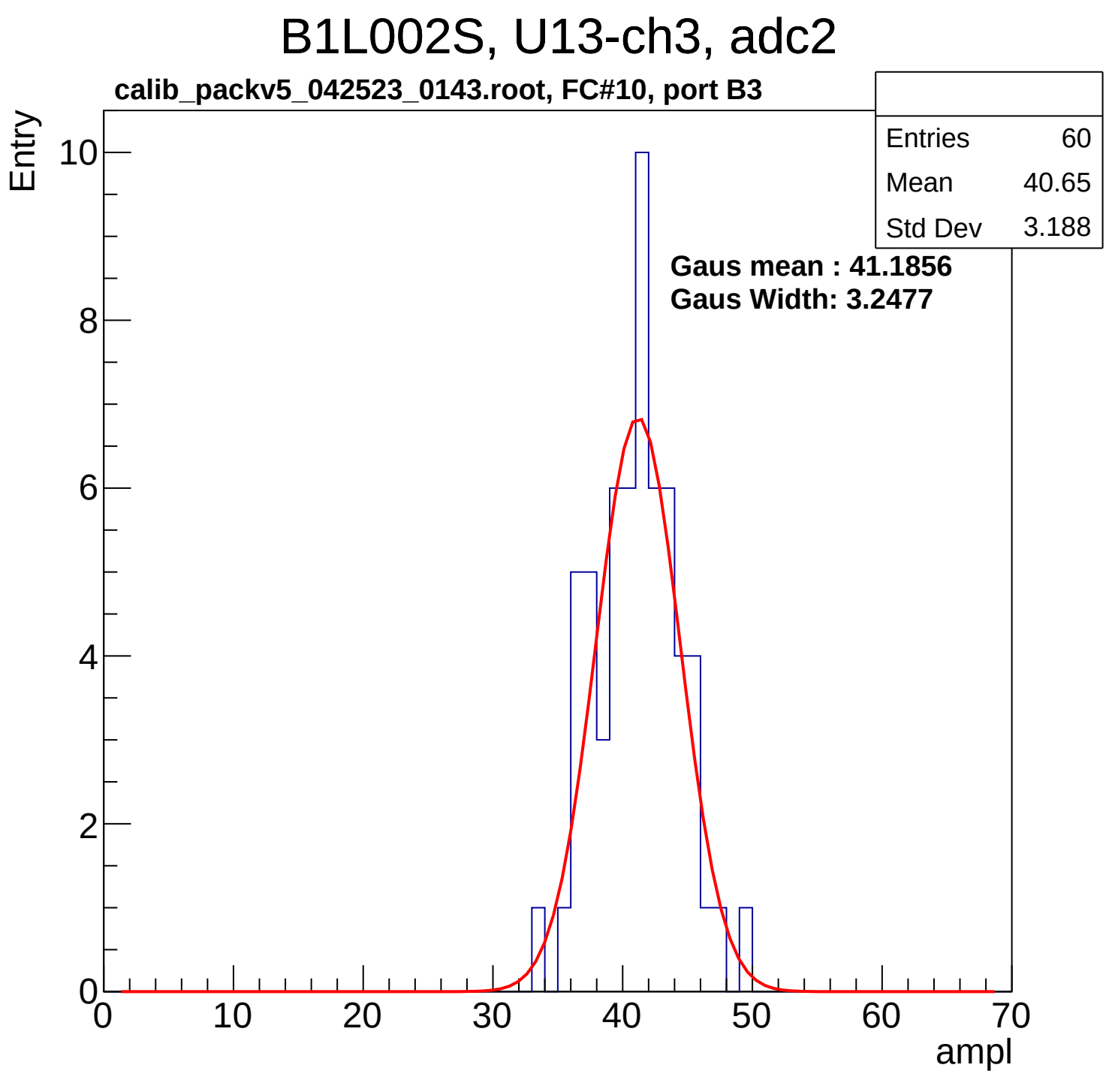
B1L002S, U13-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.65
Std Dev 3.188
Gaus mean : 41.1856
Gaus Width: 3.2477
Entry
ampl
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4
6
8
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0
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40
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60
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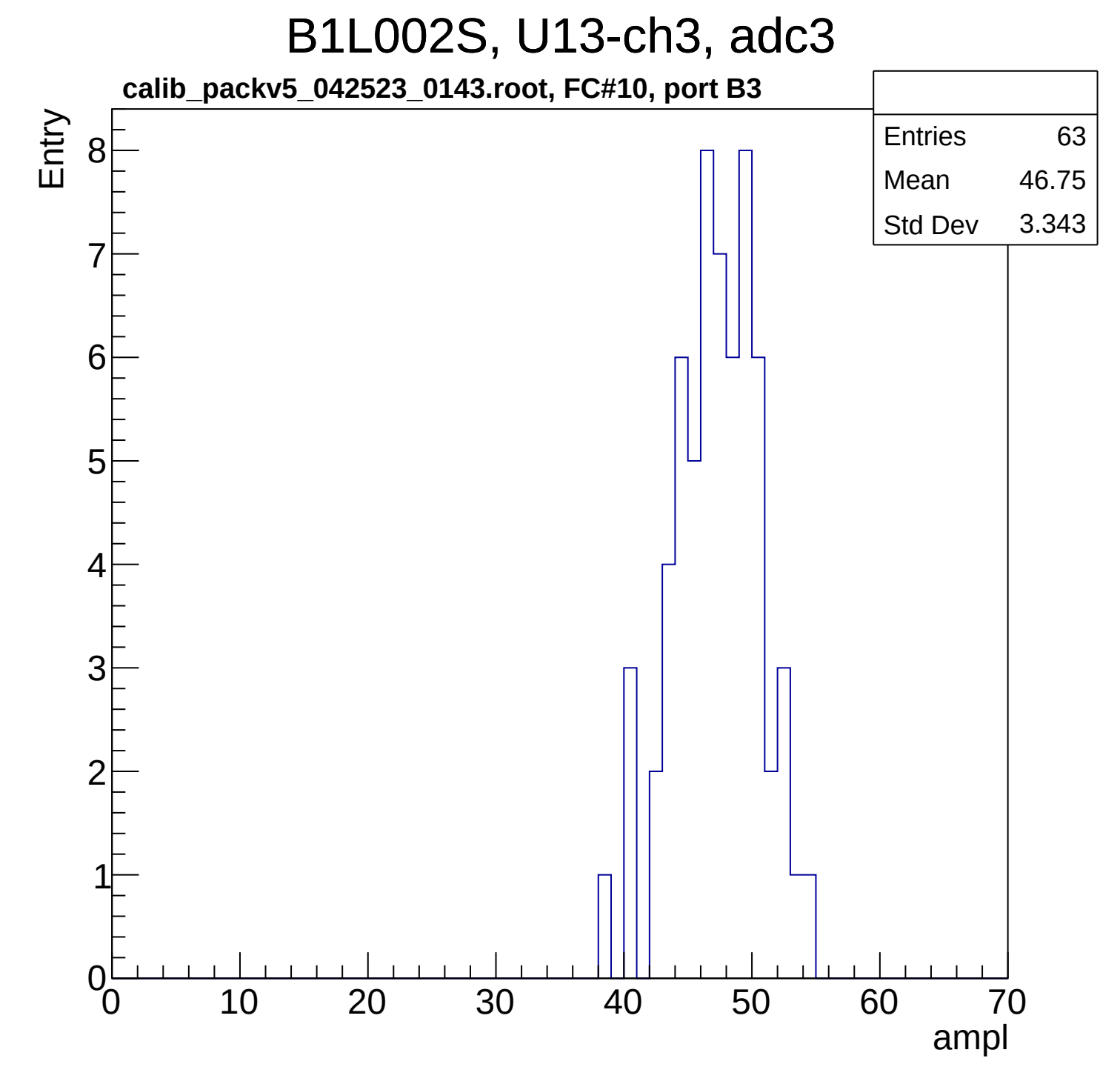
B1L002S, U13-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.75
Std Dev 3.343
Entry
ampl
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2
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4
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60
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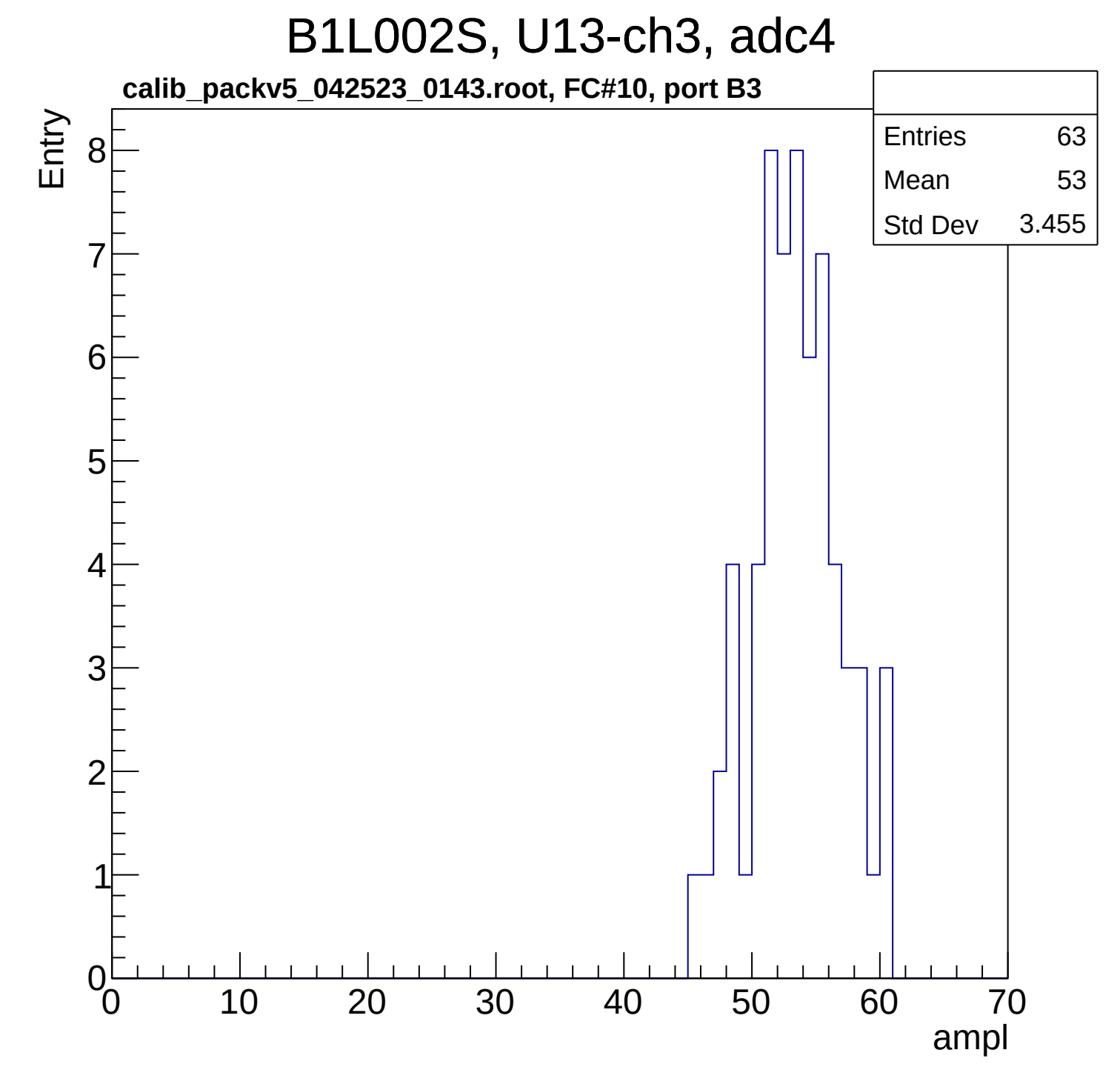
B1L002S, U13-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53
Std Dev 3.455
Entry
ampl
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4
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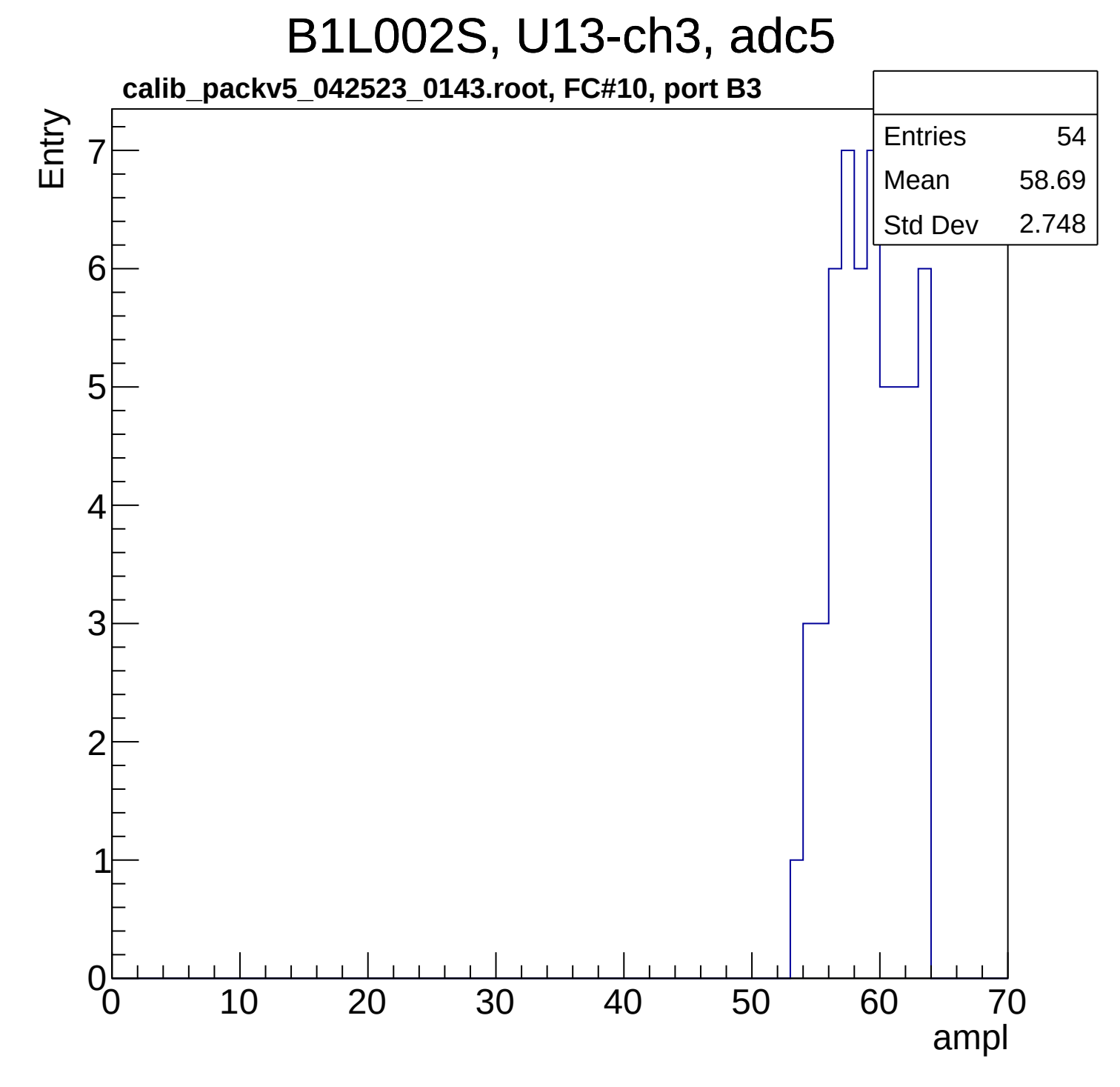
B1L002S, U13-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.69
Std Dev 2.748
Entry
ampl
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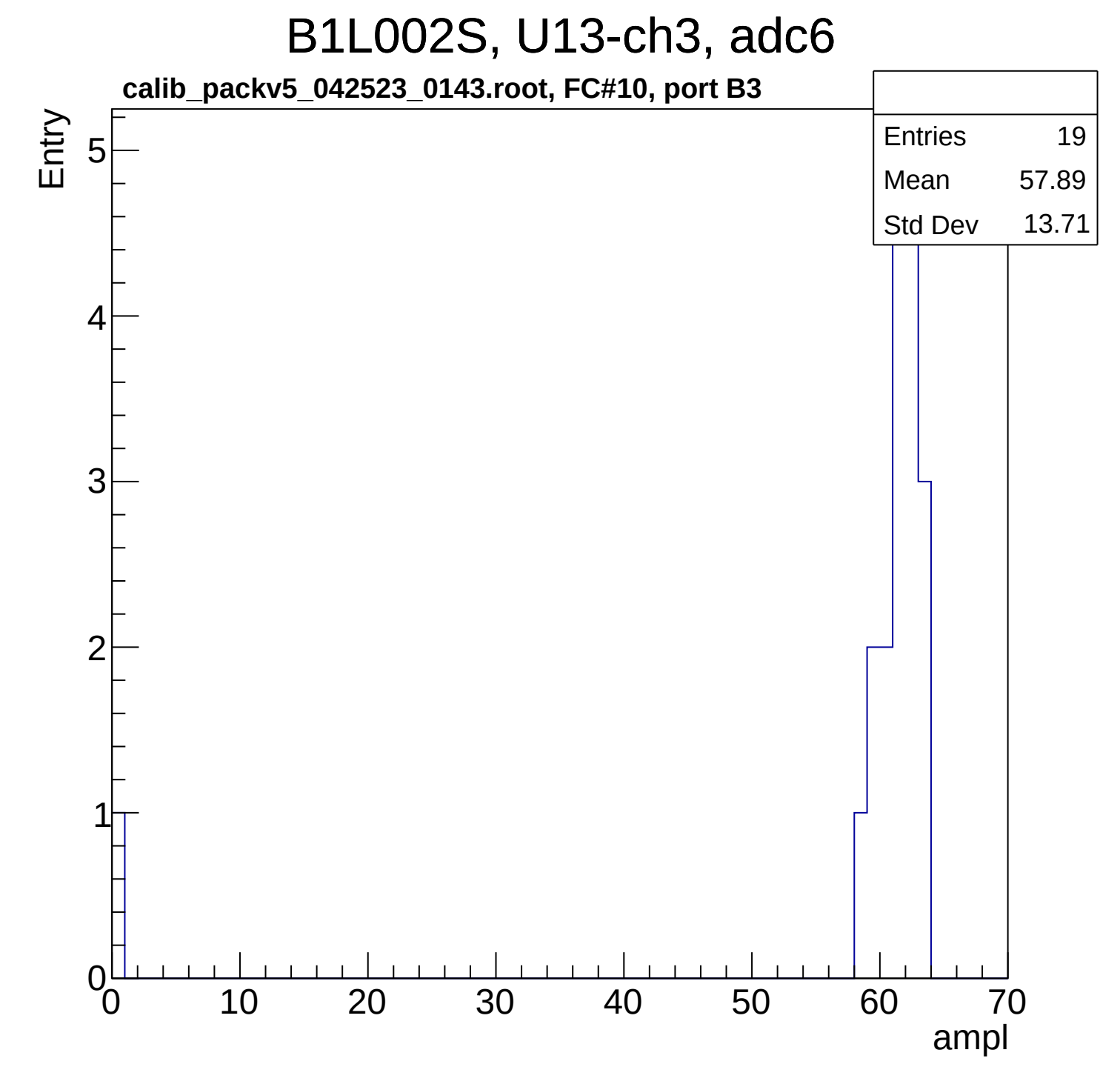
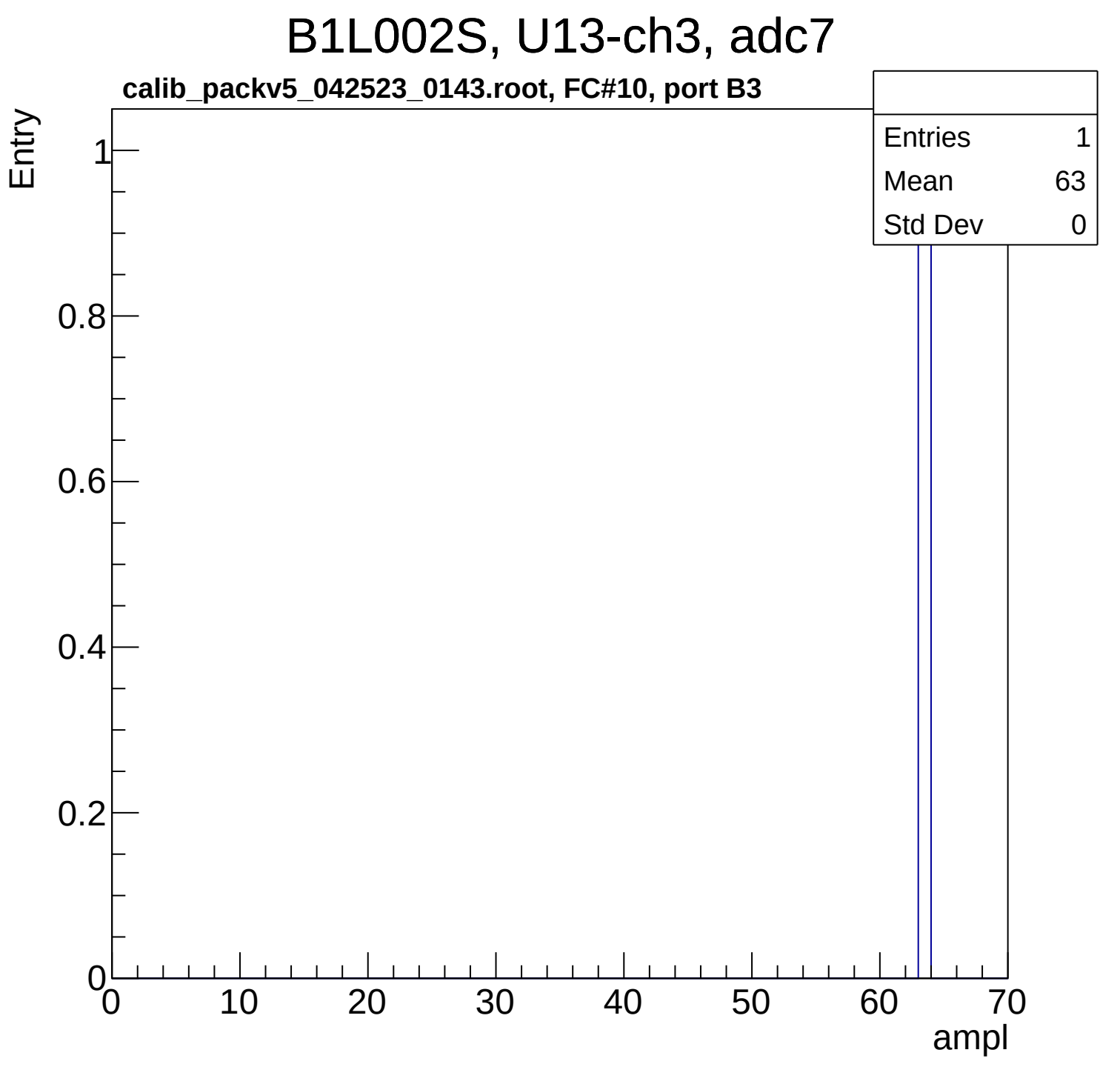

B1L002S, U13-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.89
Std Dev 13.71
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
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50
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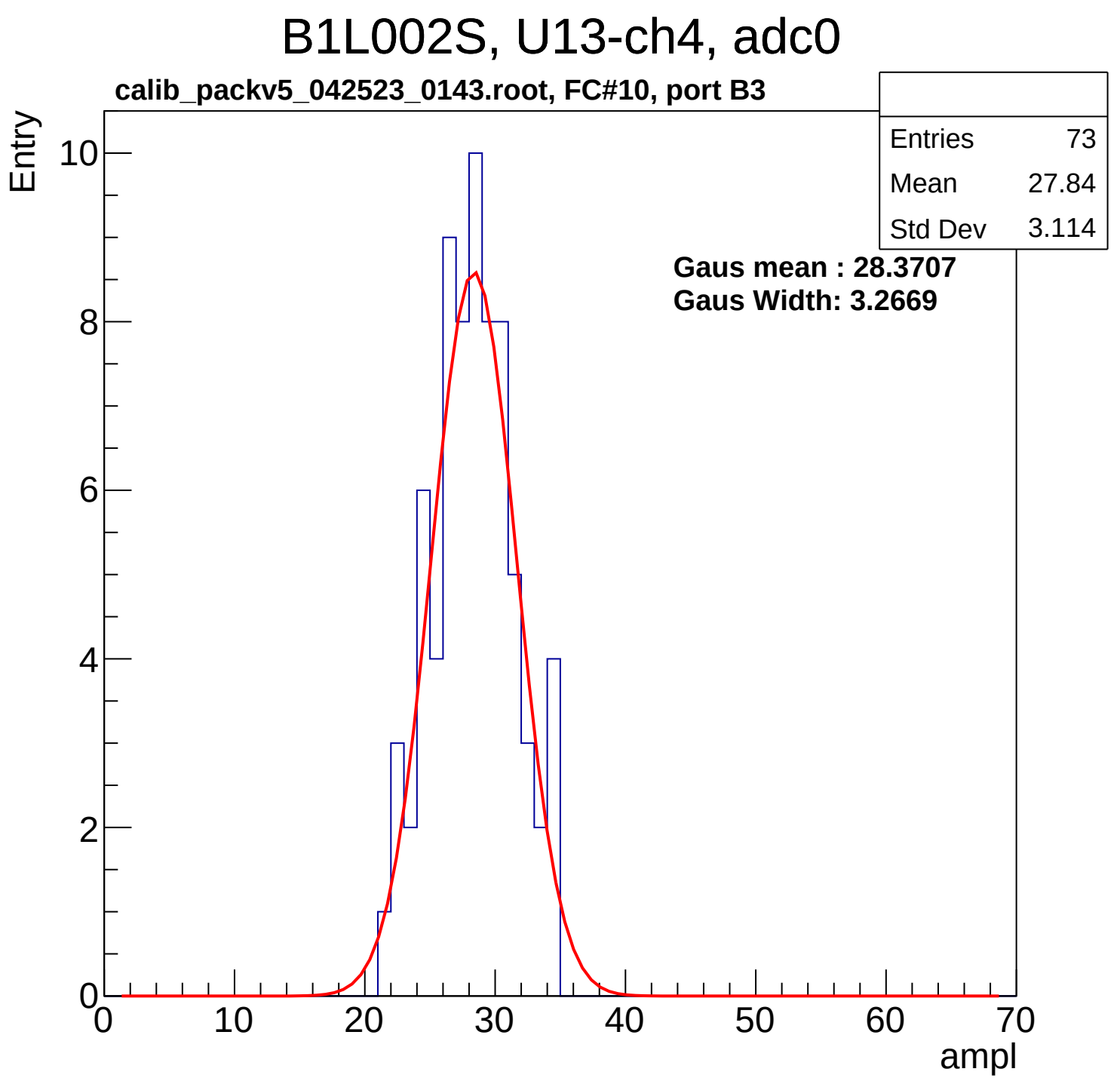
70
ampl


B1L002S, U13-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.84
Std Dev 3.114
Gaus mean : 28.3707
Gaus Width: 3.2669
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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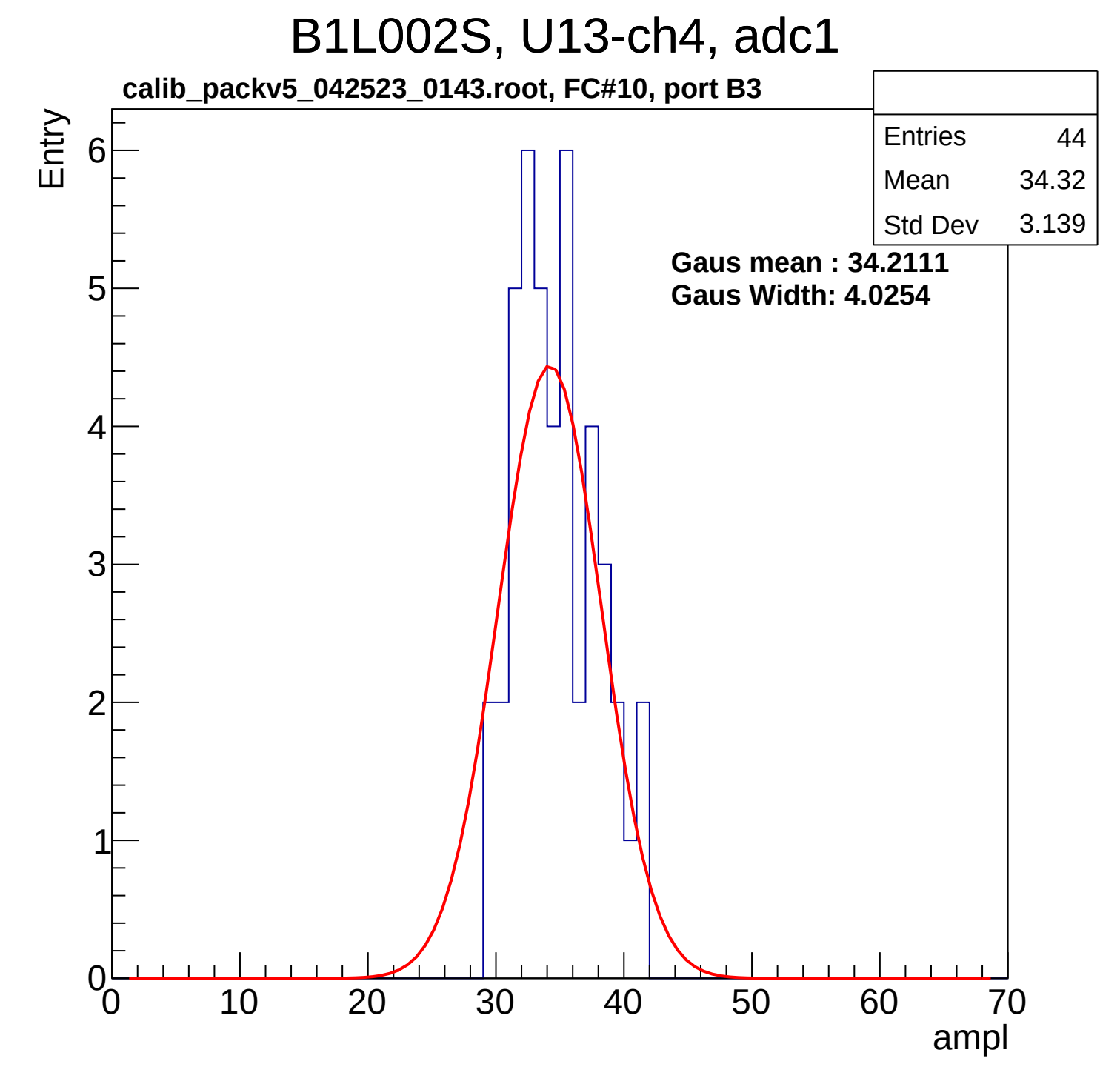
B1L002S, U13-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 34.32
Std Dev 3.139
Gaus mean : 34.2111
Gaus Width: 4.0254
Entry
ampl
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1
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3
4
5
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0
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40
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60
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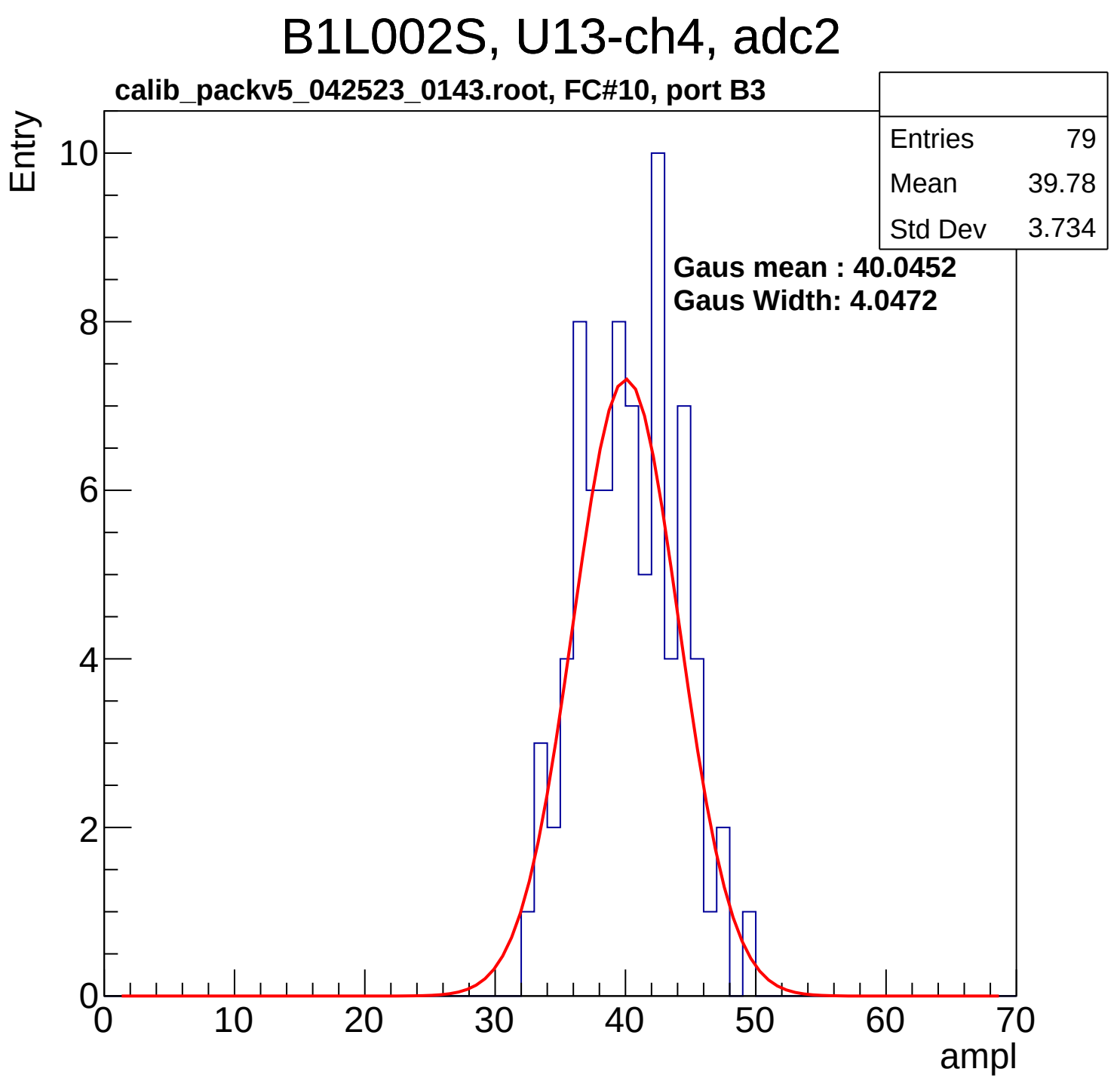
B1L002S, U13-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.78
Std Dev 3.734
Gaus mean : 40.0452
Gaus Width: 4.0472
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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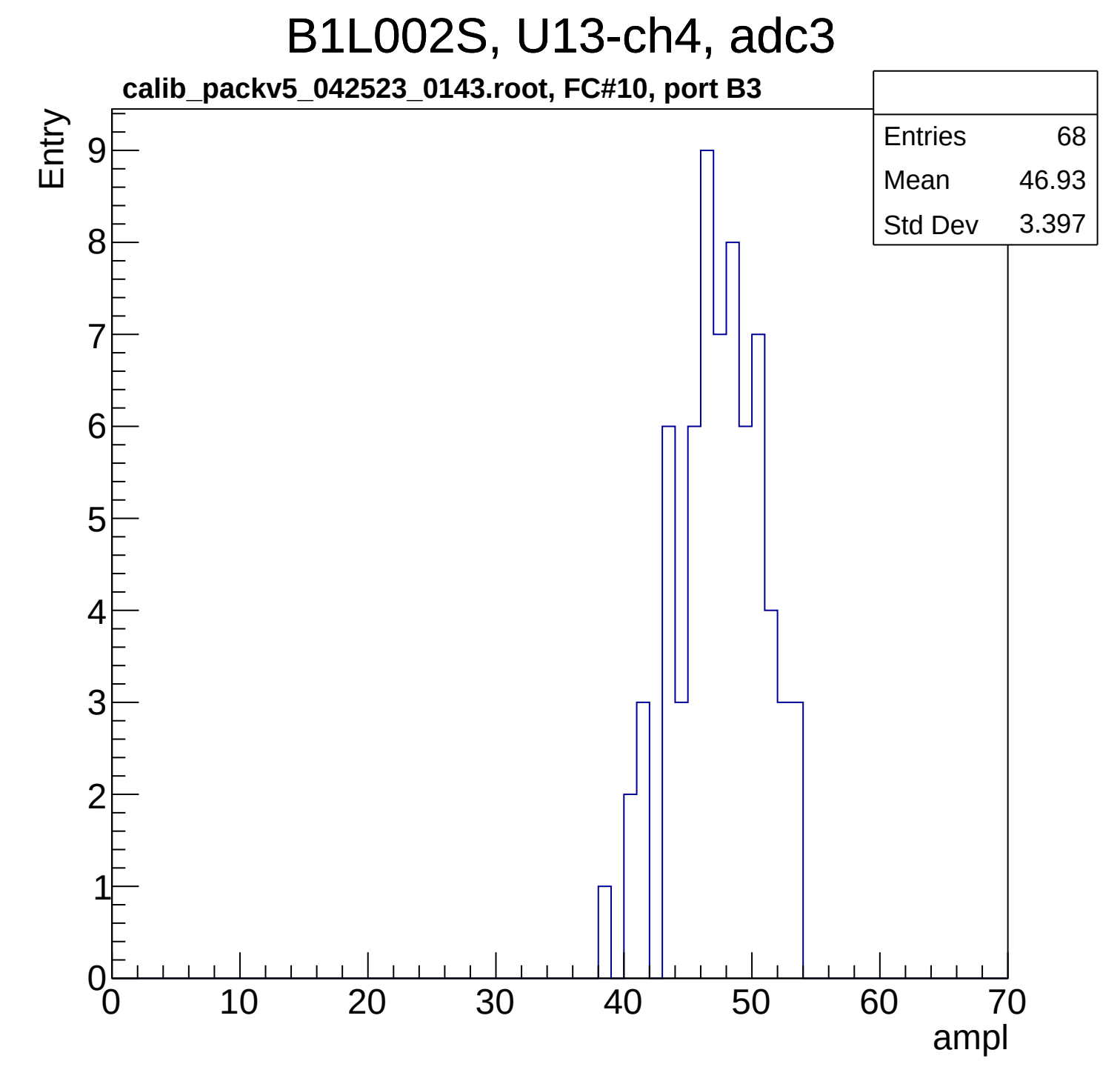
B1L002S, U13-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.93
Std Dev 3.397
Entry
ampl
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2
3
4
5
6
7
8
9
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60
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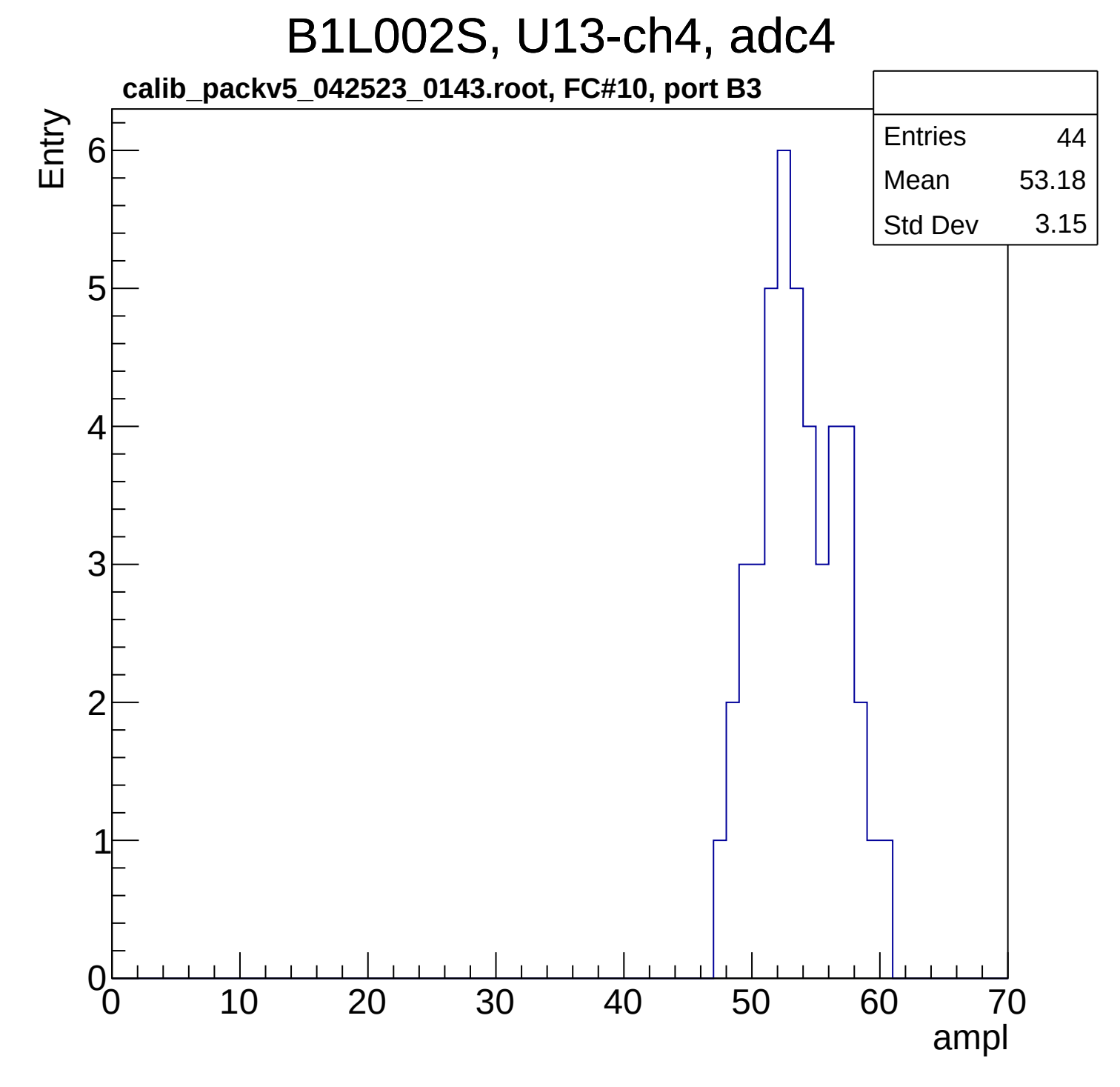
B1L002S, U13-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.18
Std Dev 3.15
Entry
0
1
2
3
4
5
6
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10
20
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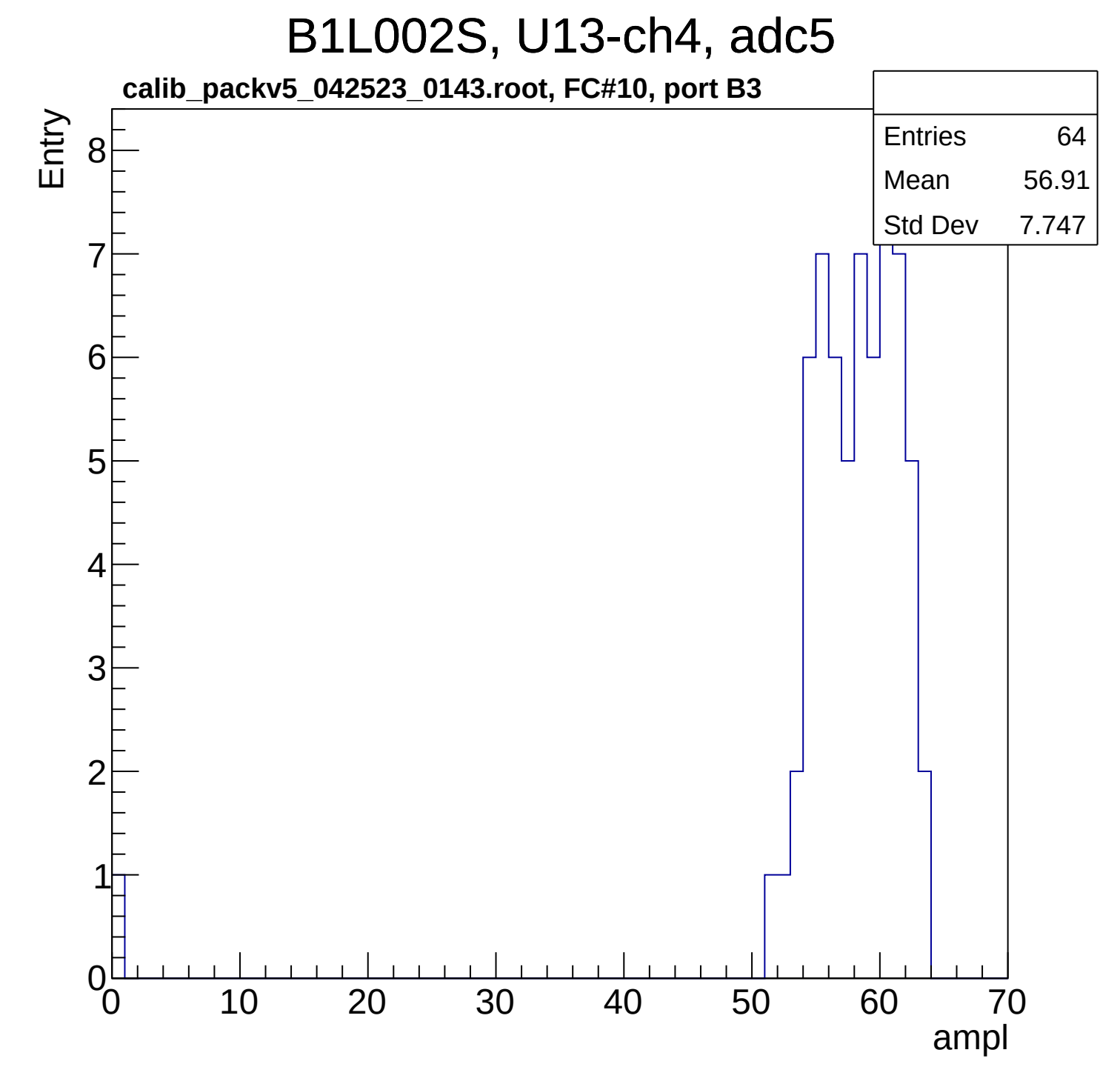
50
60
70
ampl


B1L002S, U13-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 56.91
Std Dev 7.747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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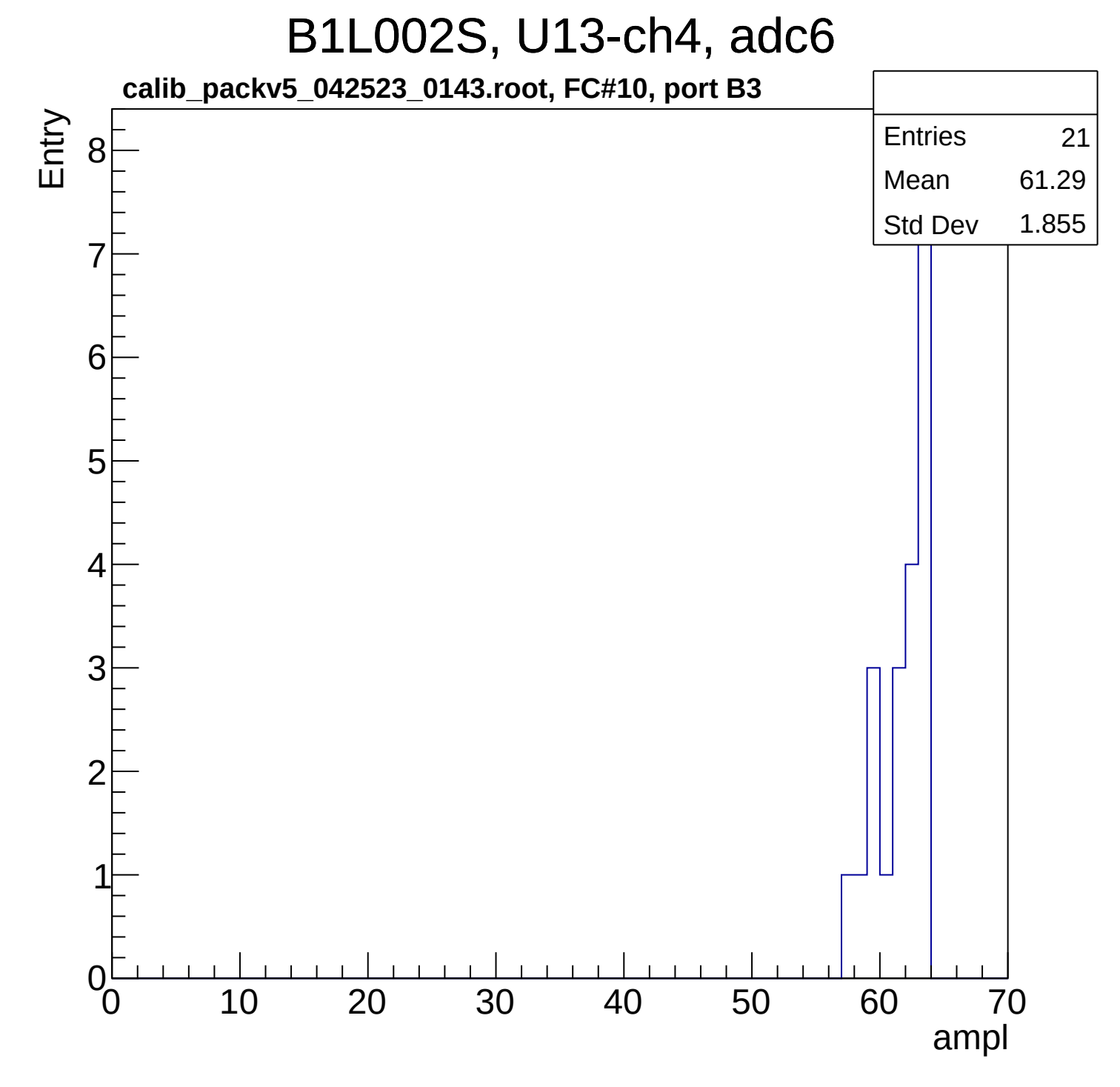
B1L002S, U13-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.29
Std Dev 1.855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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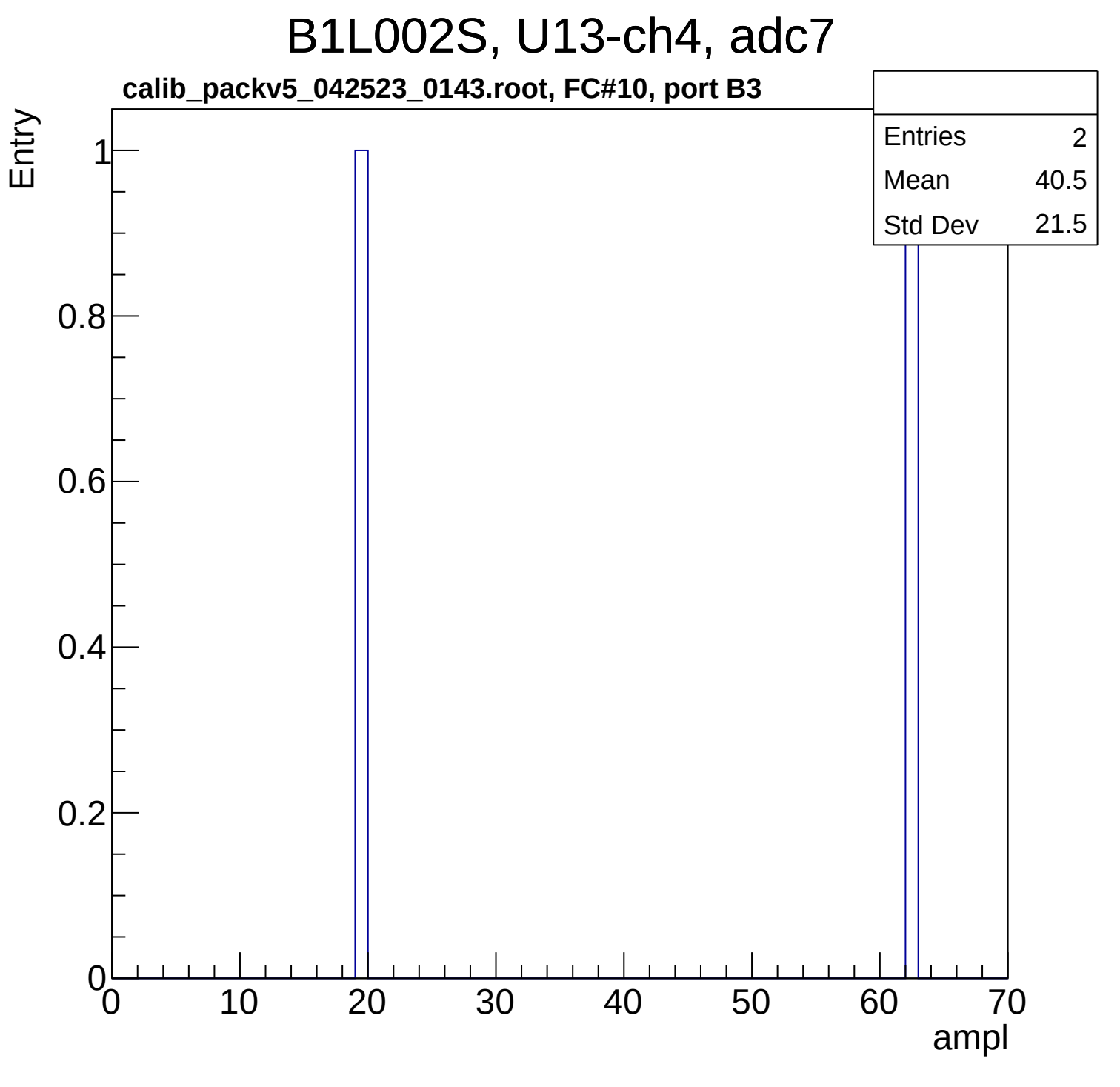
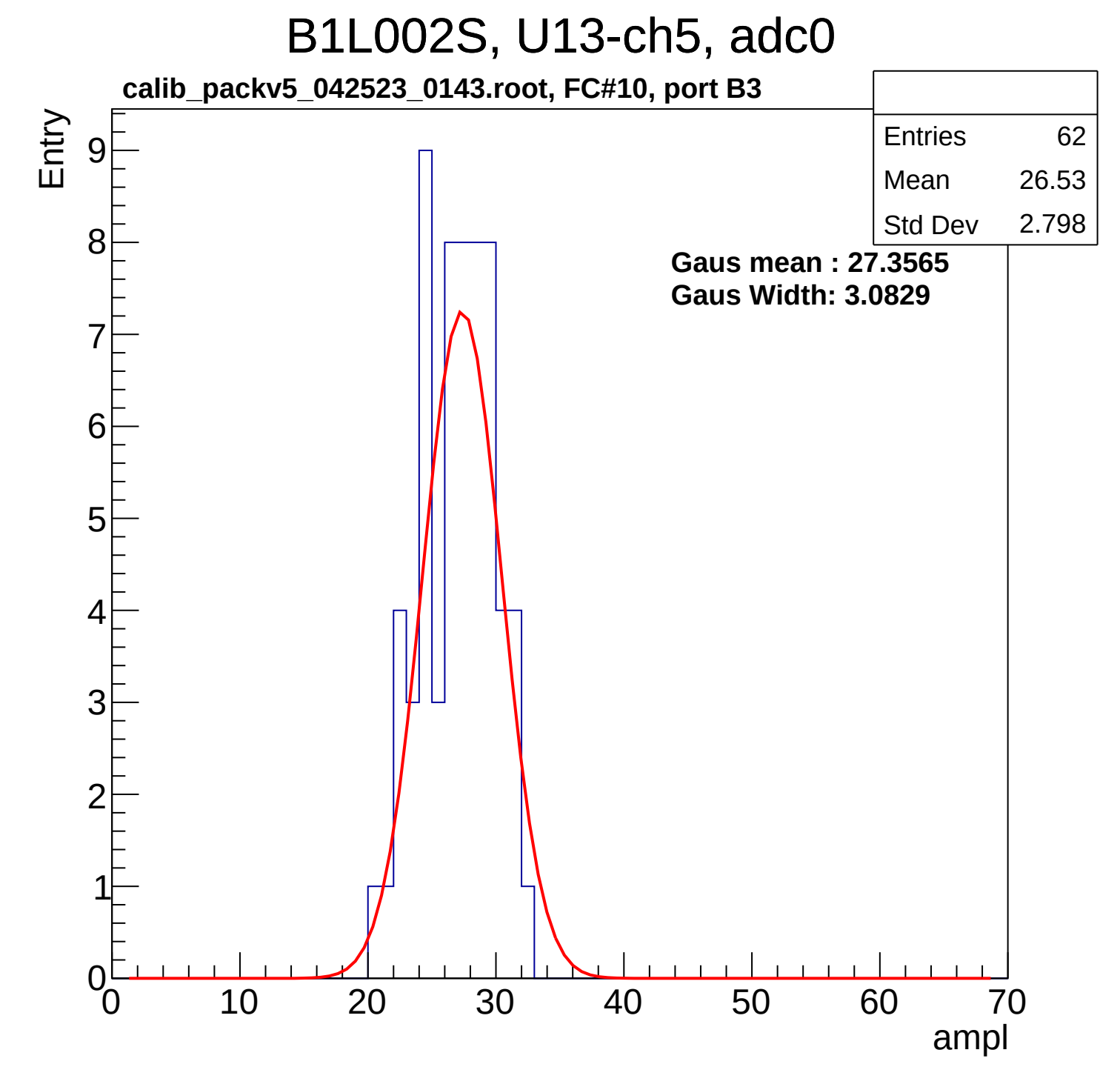

B1L002S, U13-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 40.5
Std Dev 21.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U13-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.53
Std Dev 2.798
Gaus mean : 27.3565
Gaus Width: 3.0829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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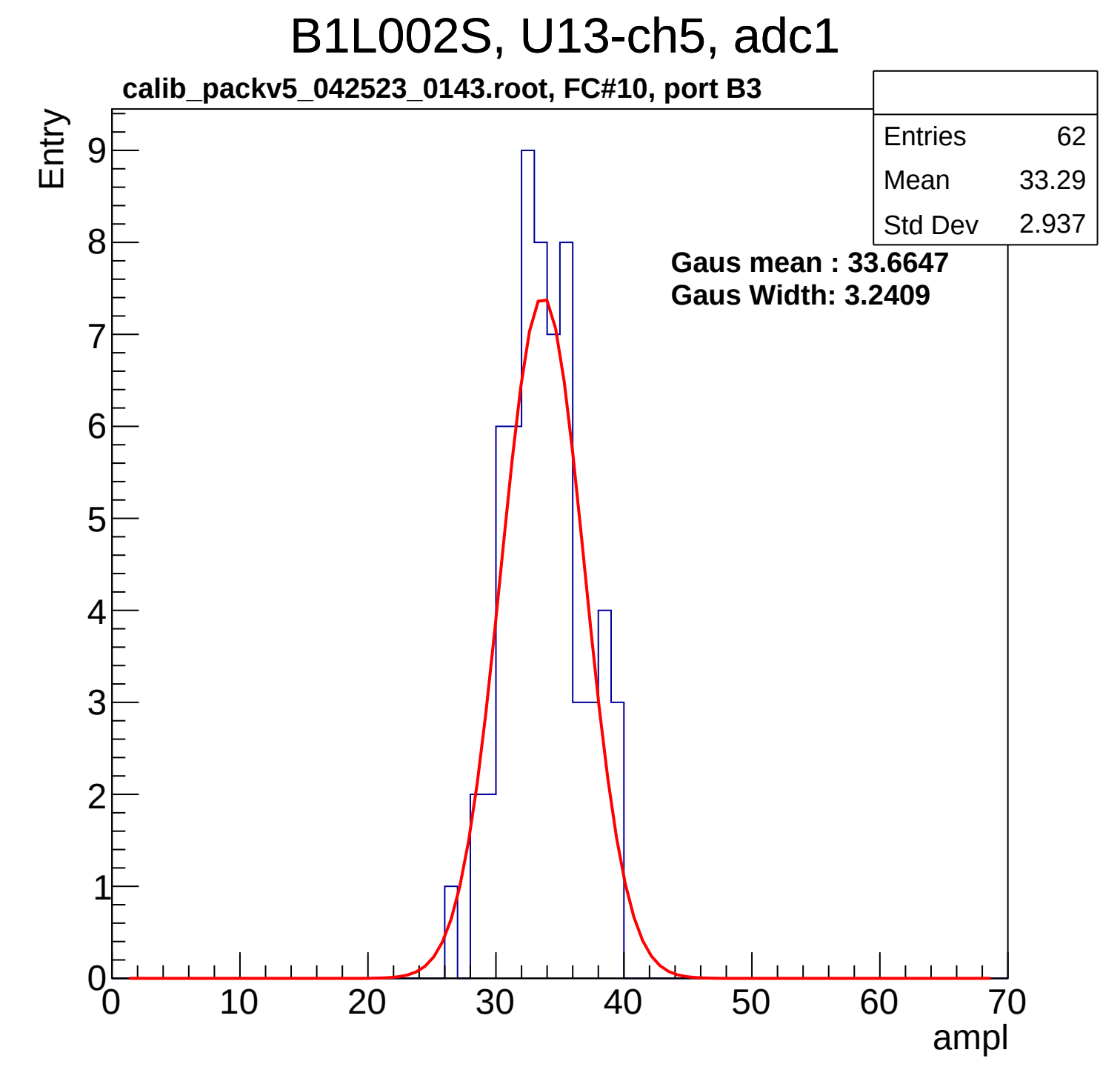
B1L002S, U13-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.29
Std Dev 2.937
Gaus mean : 33.6647
Gaus Width: 3.2409
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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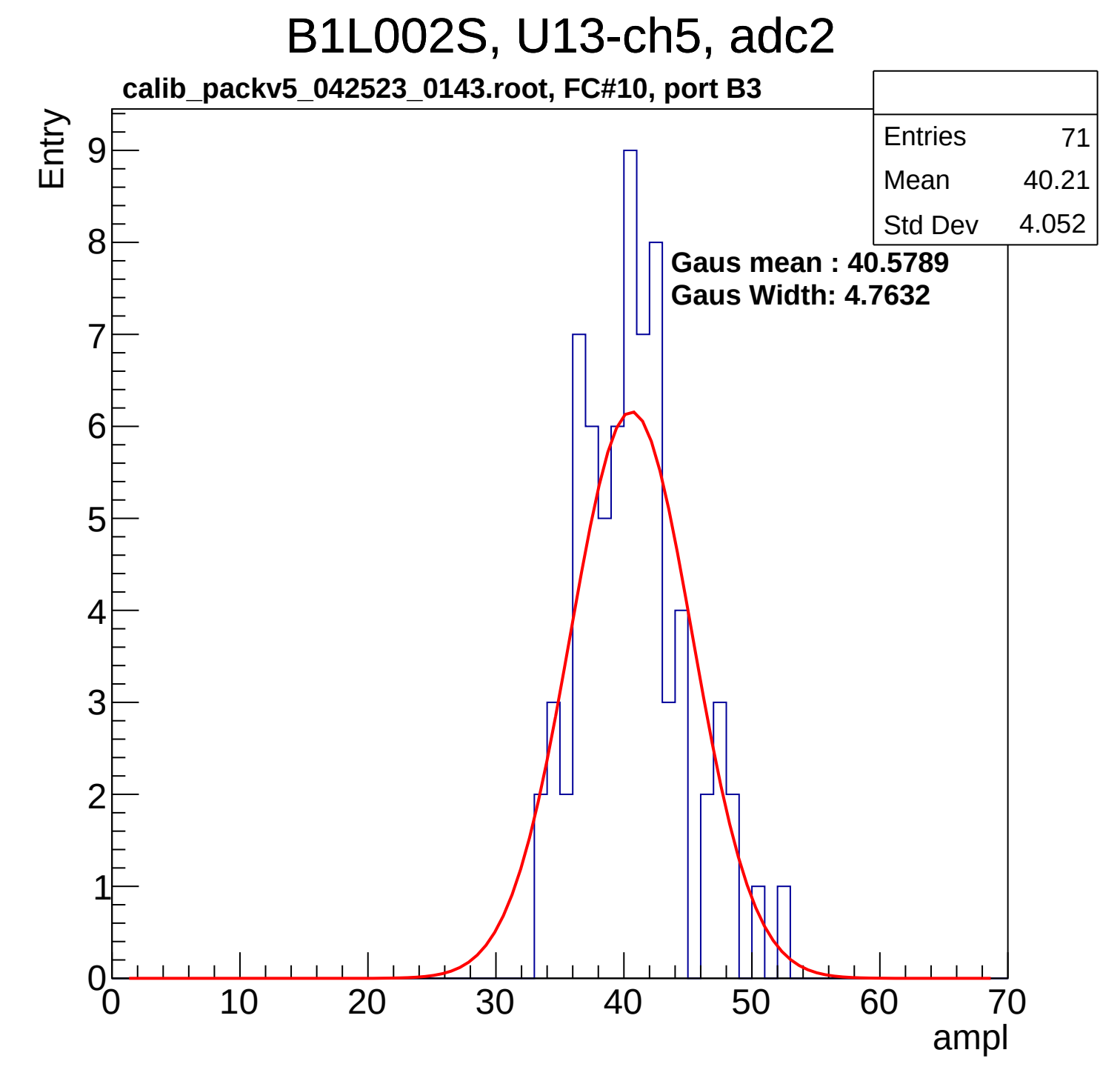
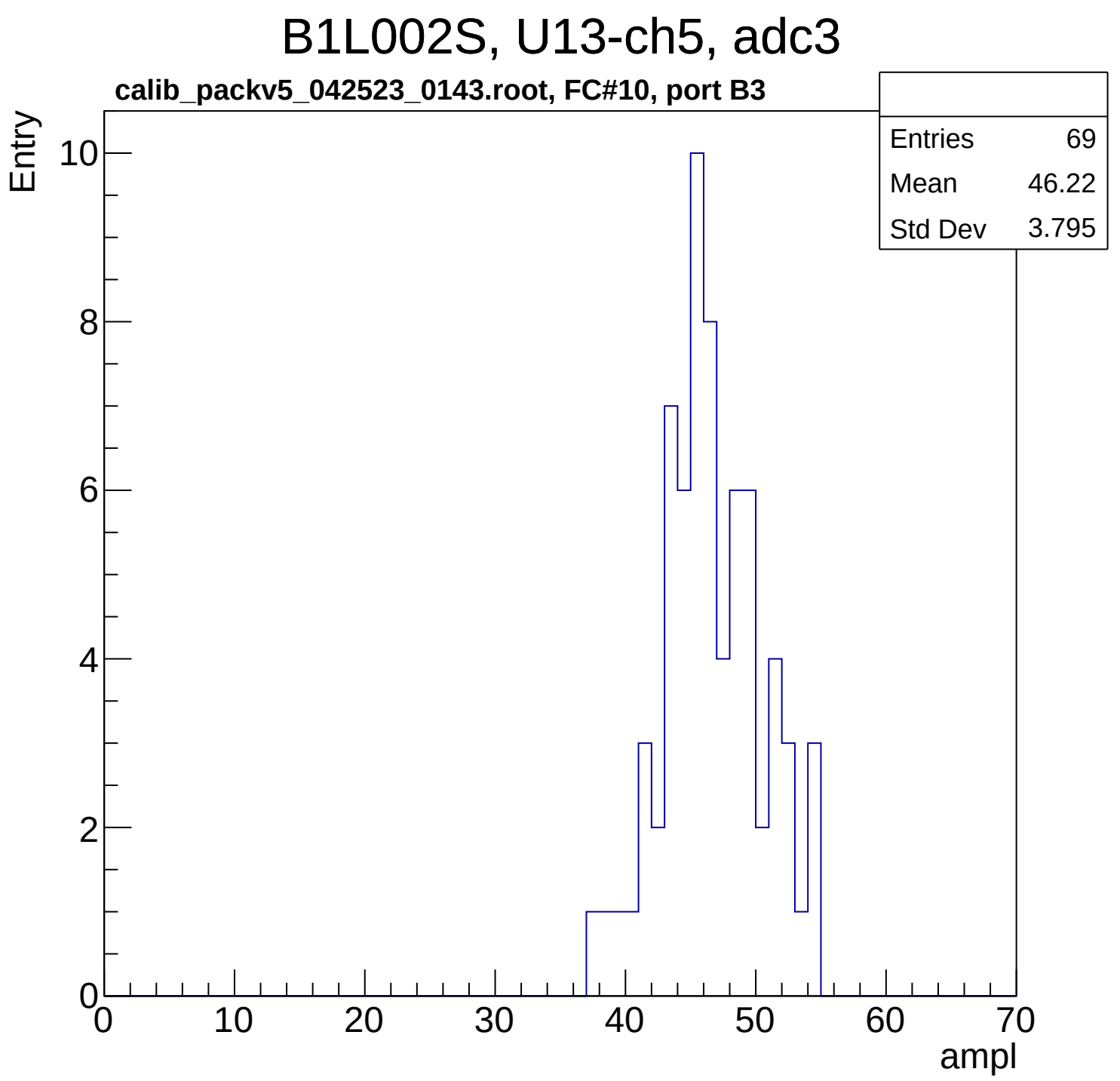
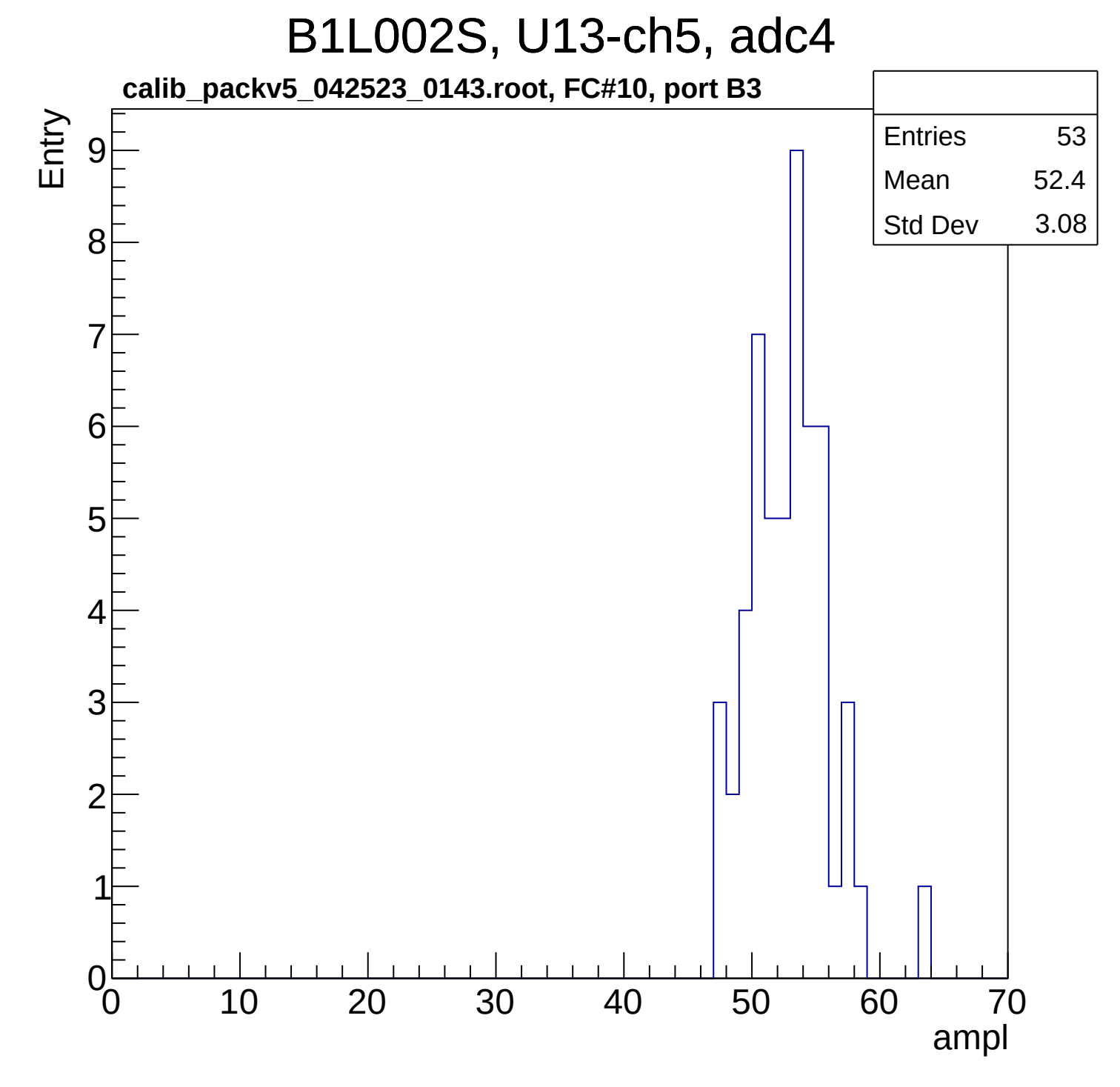
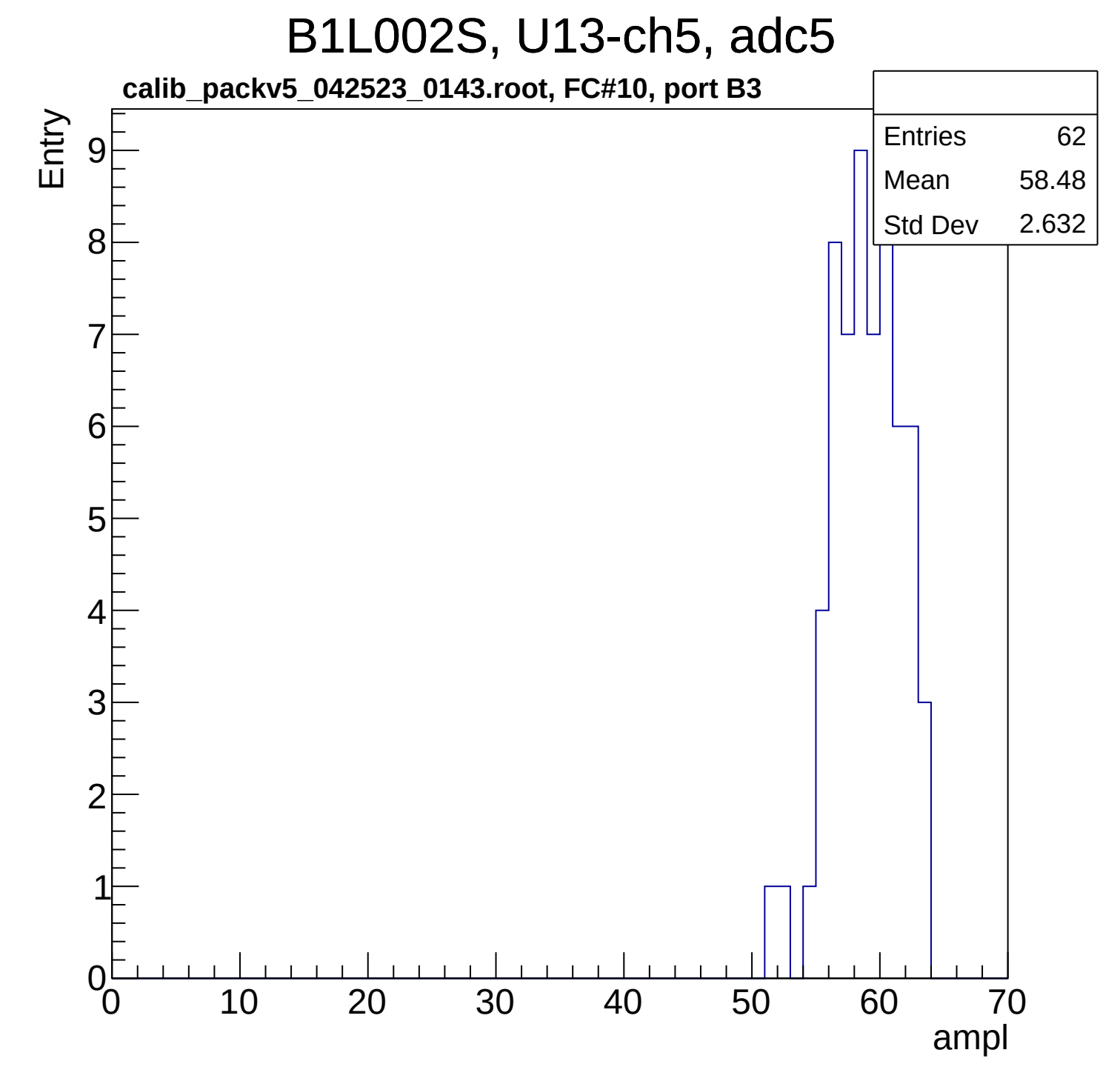
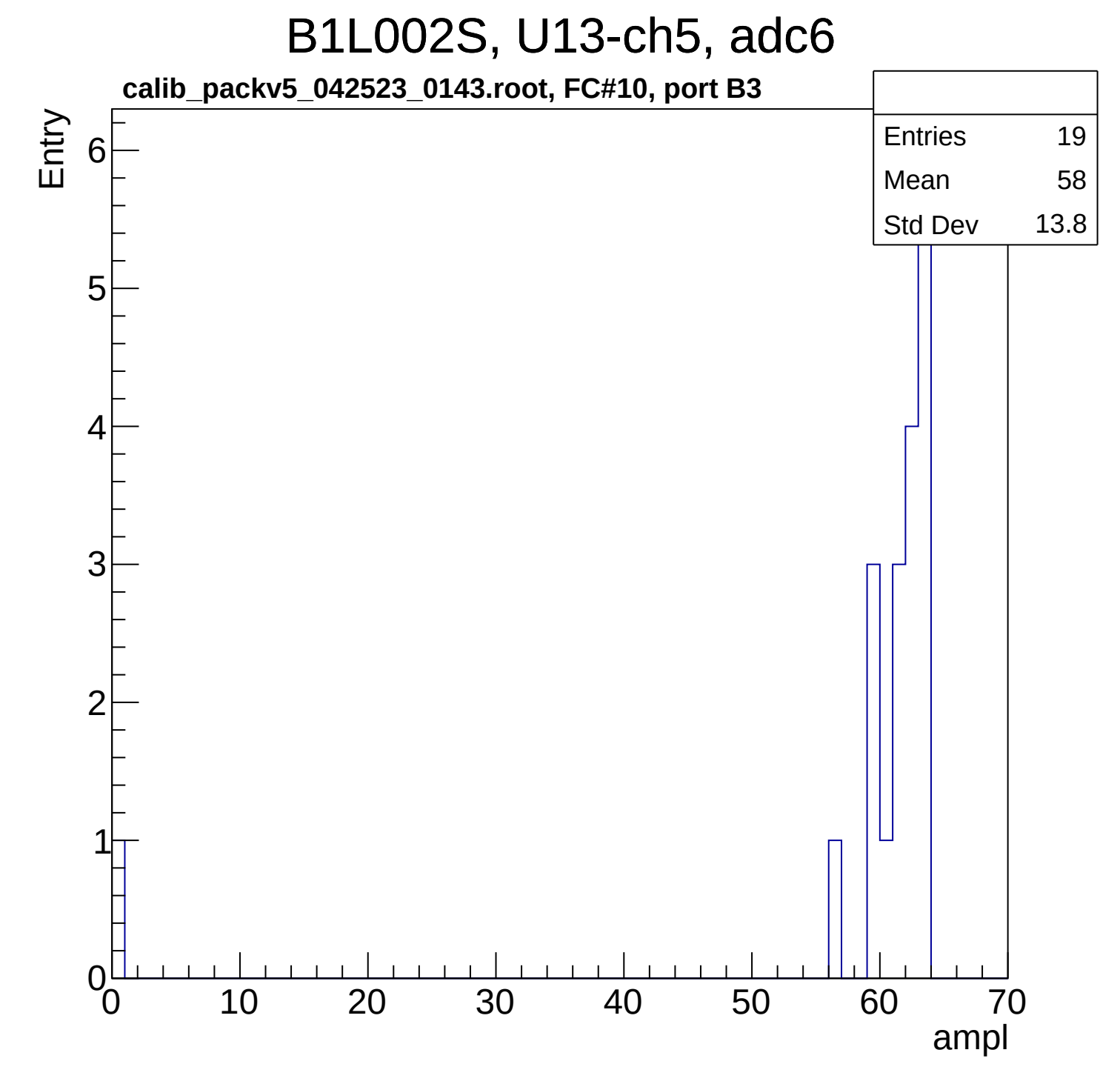
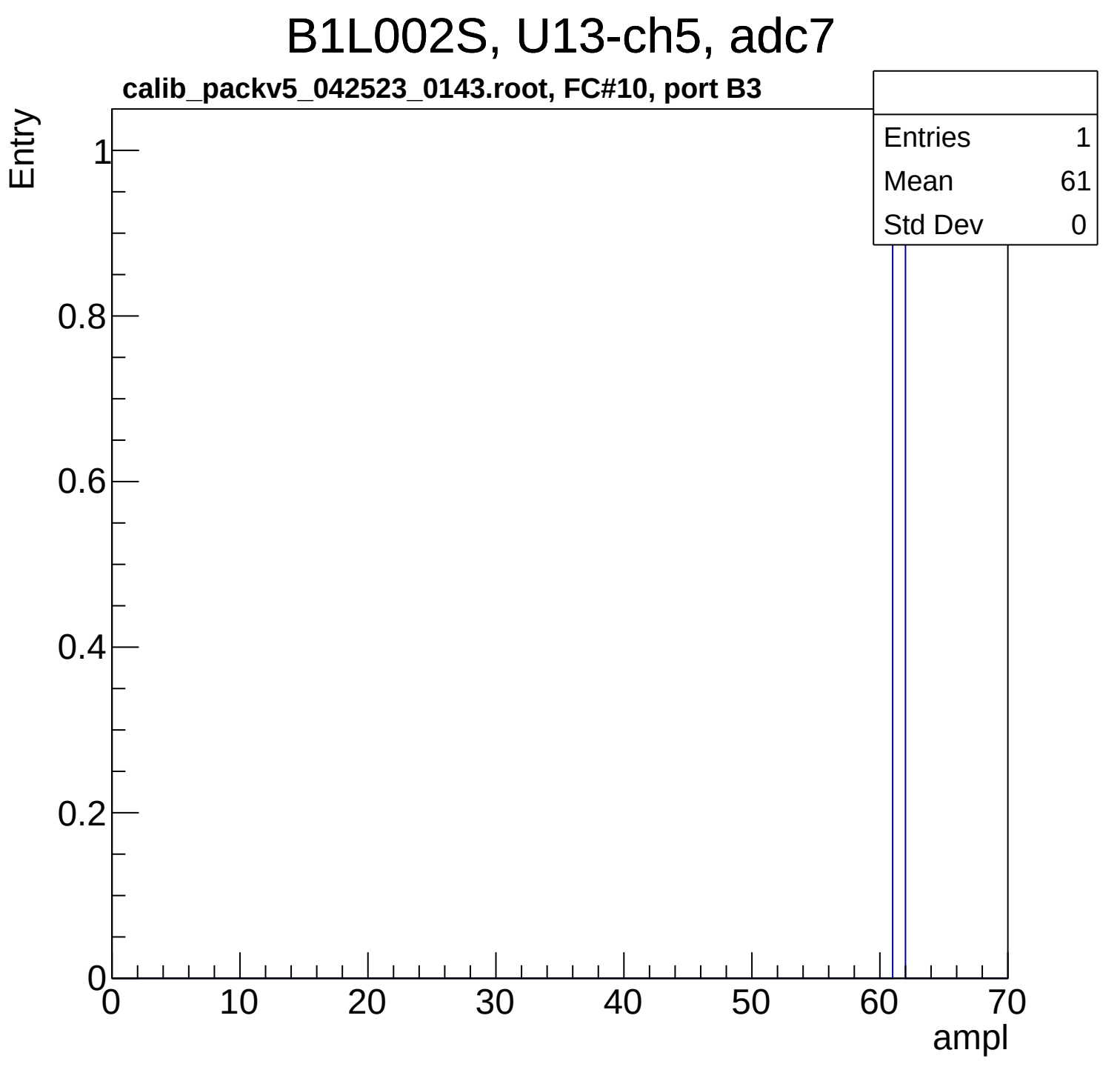

B1L002S, U13-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.21
Std Dev 4.052
Gaus mean : 40.5789
Gaus Width: 4.7632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

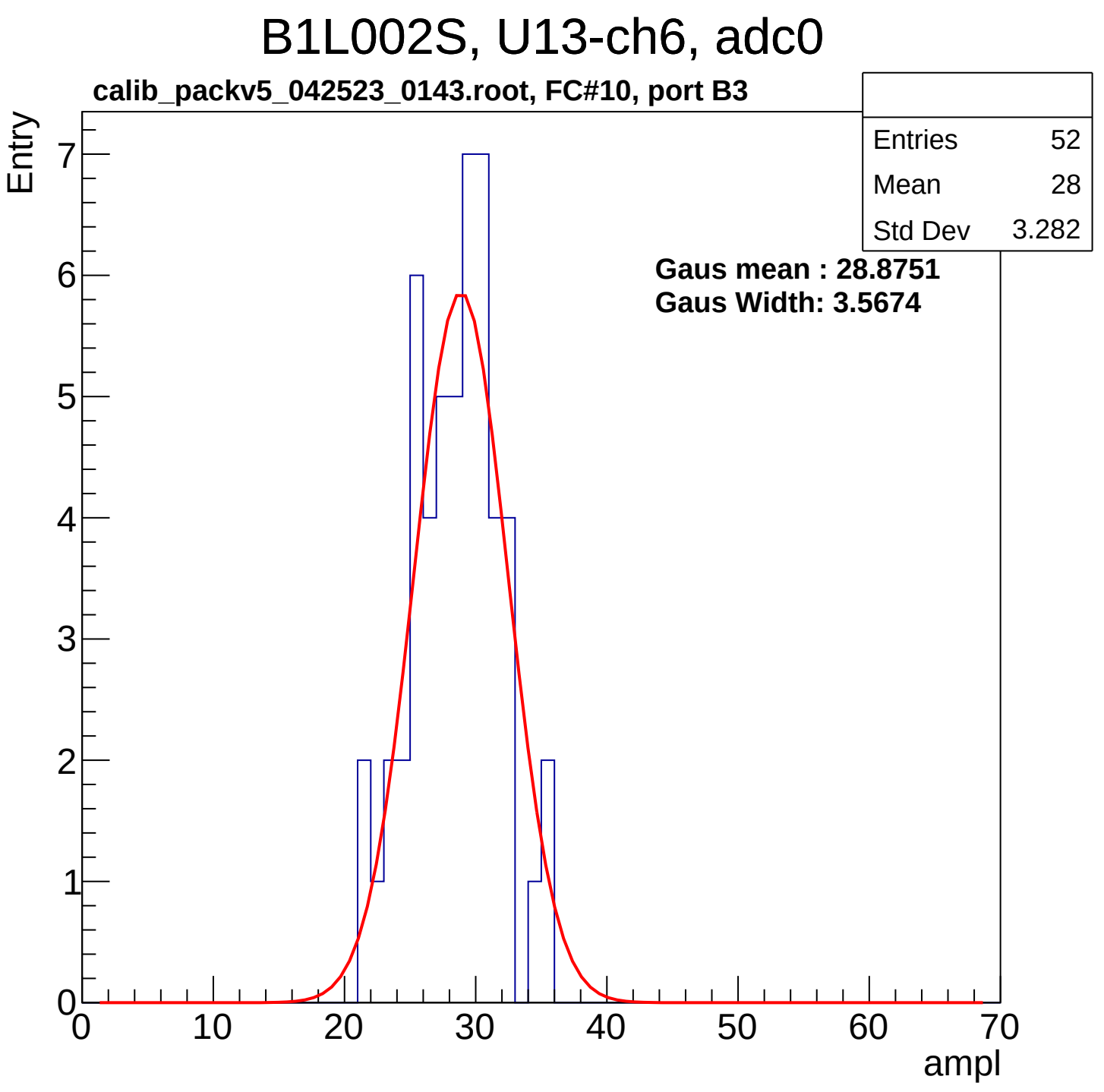

B1L002S, U13-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.22
Std Dev 3.795
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

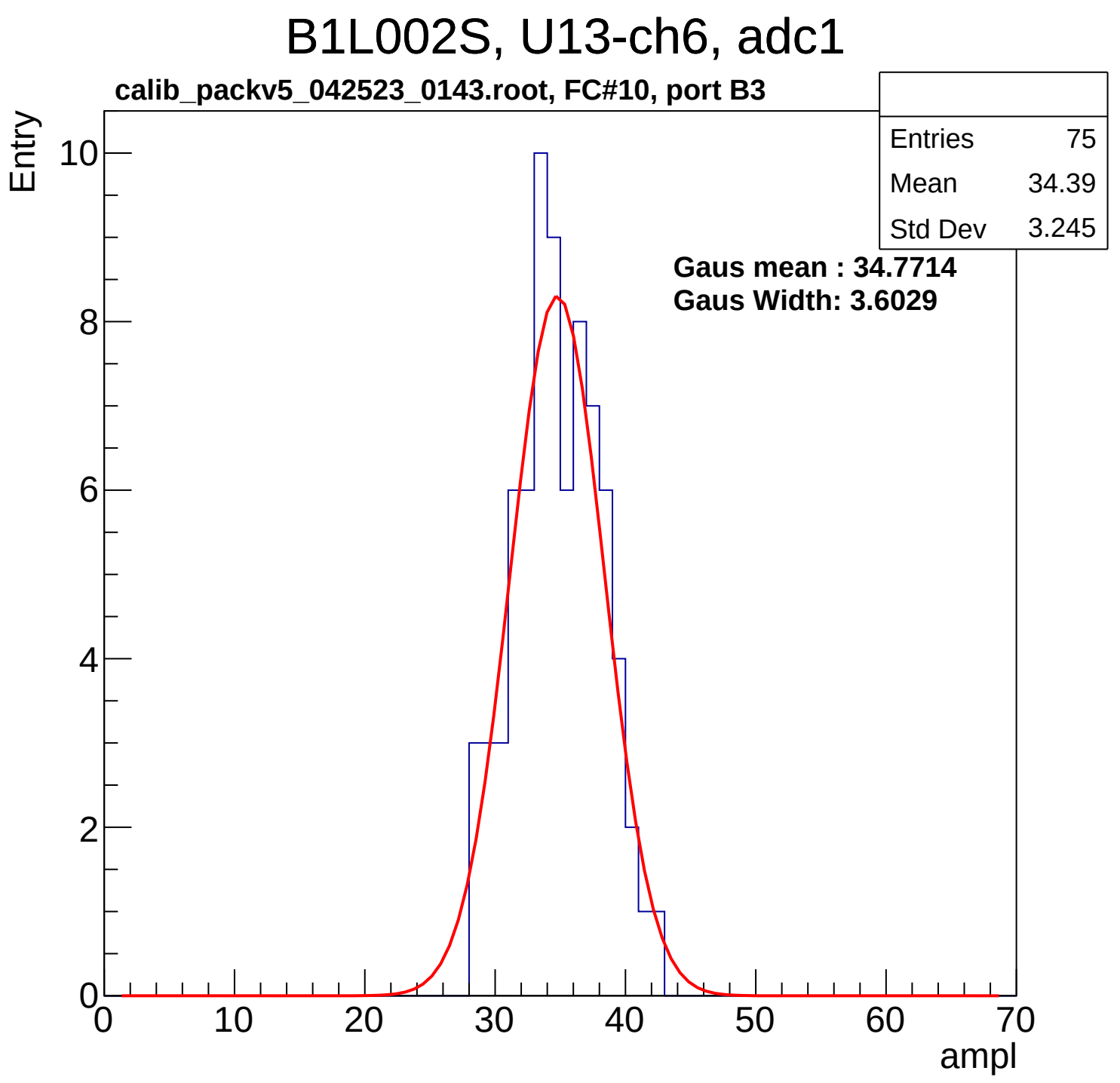

B1L002S, U13-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.4
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.48
Std Dev 2.632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58
Std Dev 13.8
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28
Std Dev 3.282
Gaus mean : 28.8751
Gaus Width: 3.5674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.39
Std Dev 3.245
Gaus mean : 34.7714
Gaus Width: 3.6029
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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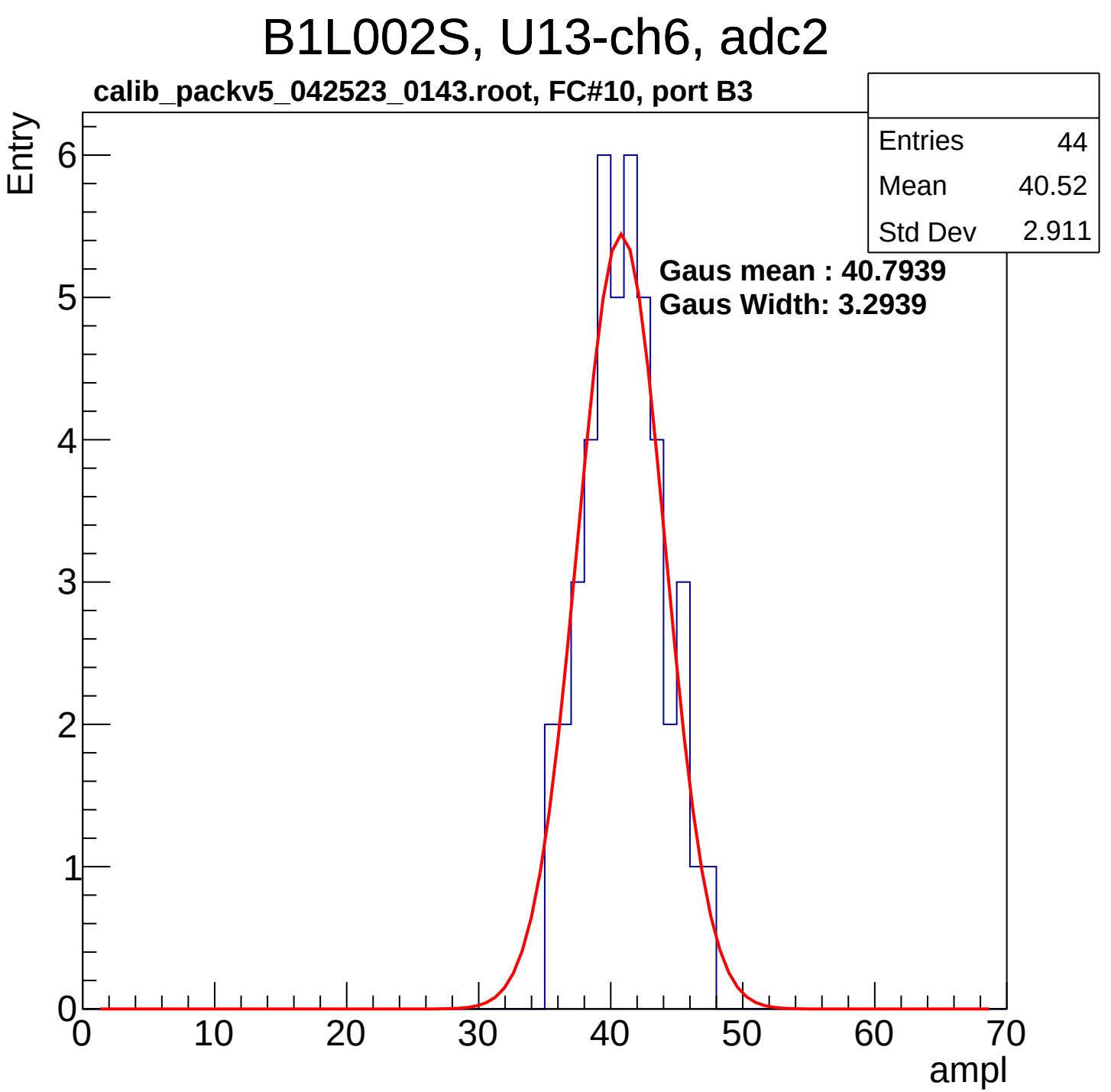
B1L002S, U13-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 40.52
Std Dev 2.911
Gaus mean : 40.7939
Gaus Width: 3.2939
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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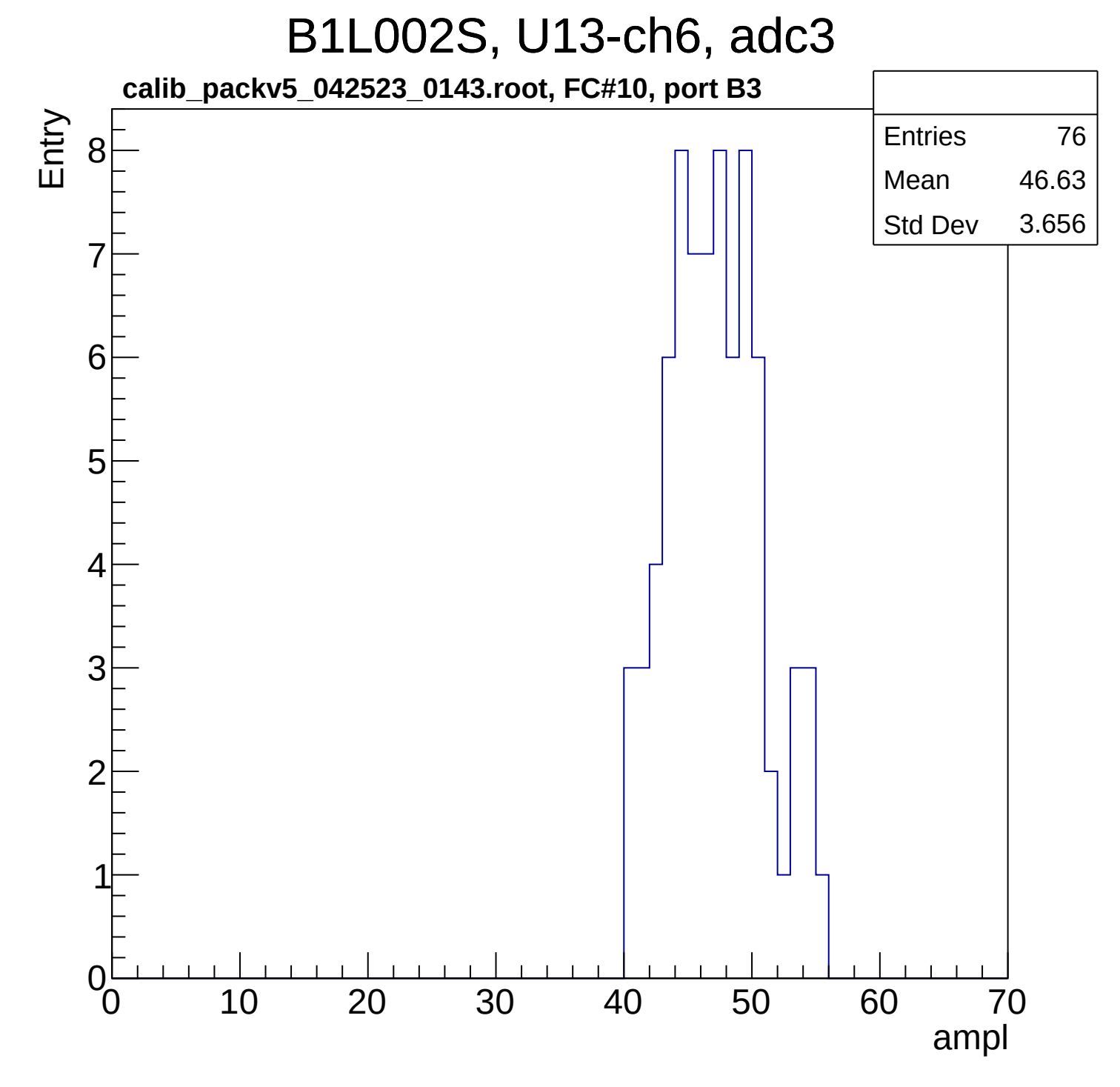
60
70


B1L002S, U13-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.63
Std Dev 3.656
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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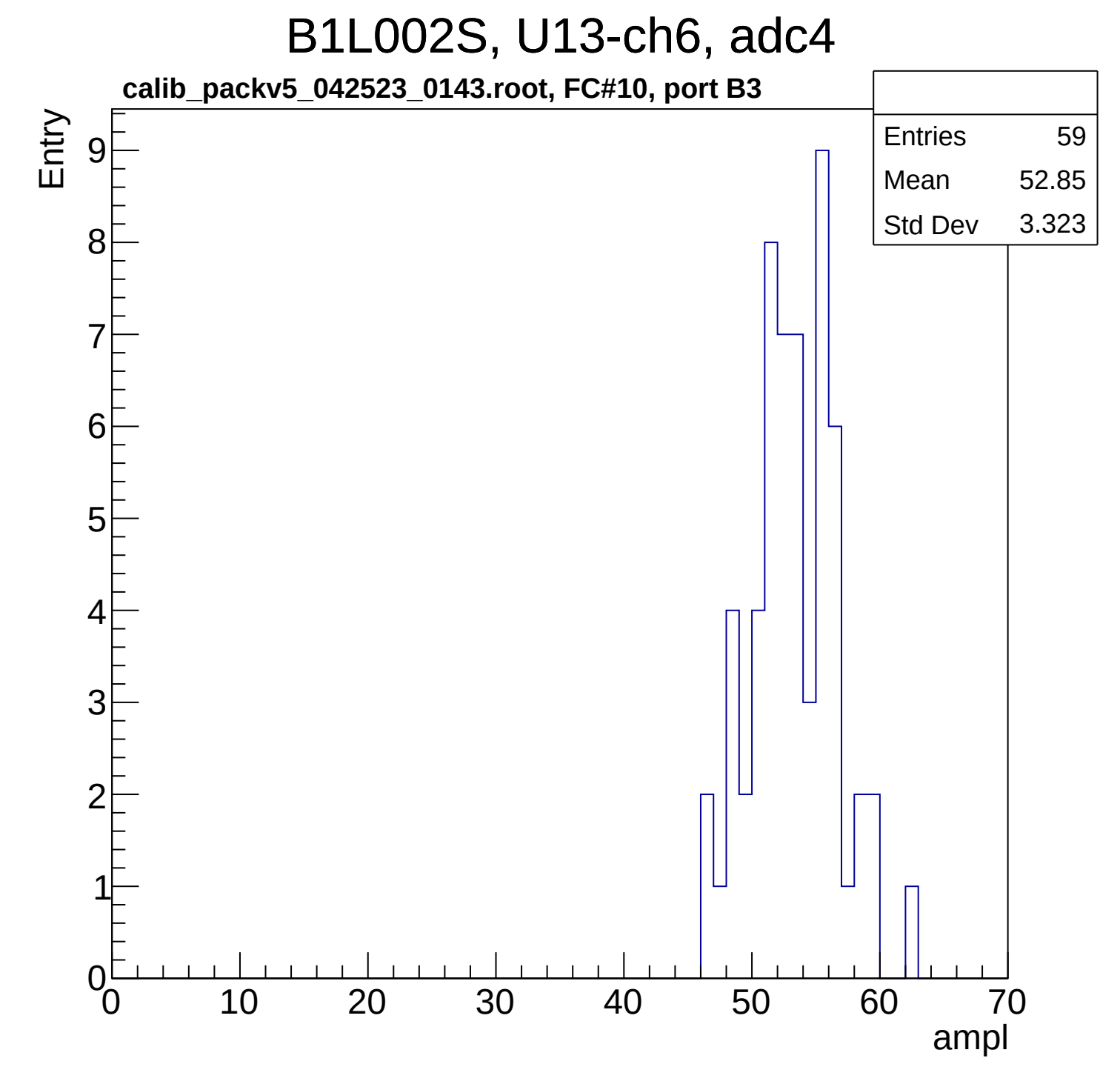
B1L002S, U13-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.85
Std Dev 3.323
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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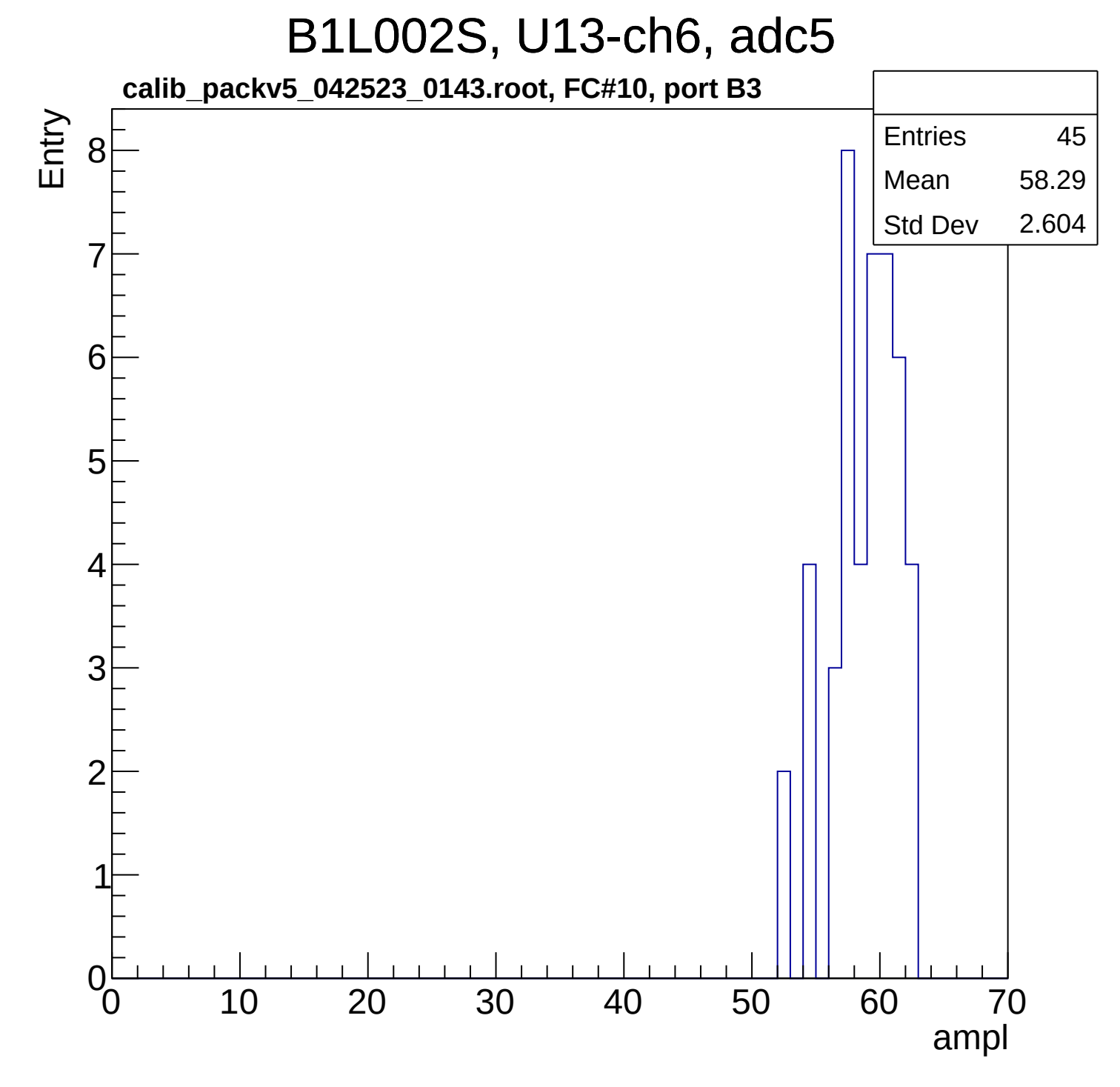
B1L002S, U13-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.29
Std Dev 2.604
Entry
ampl
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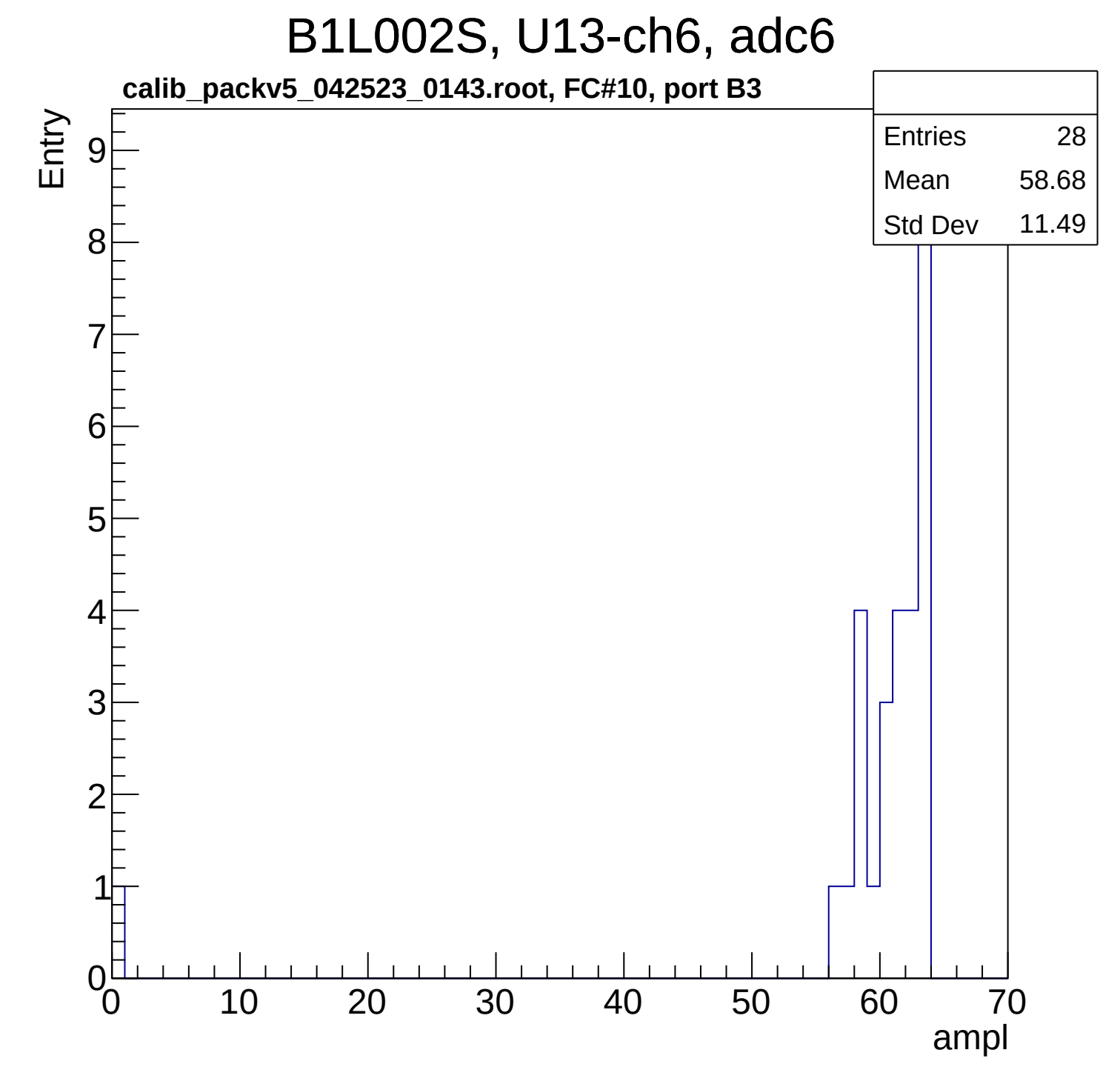
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.68
Std Dev 11.49
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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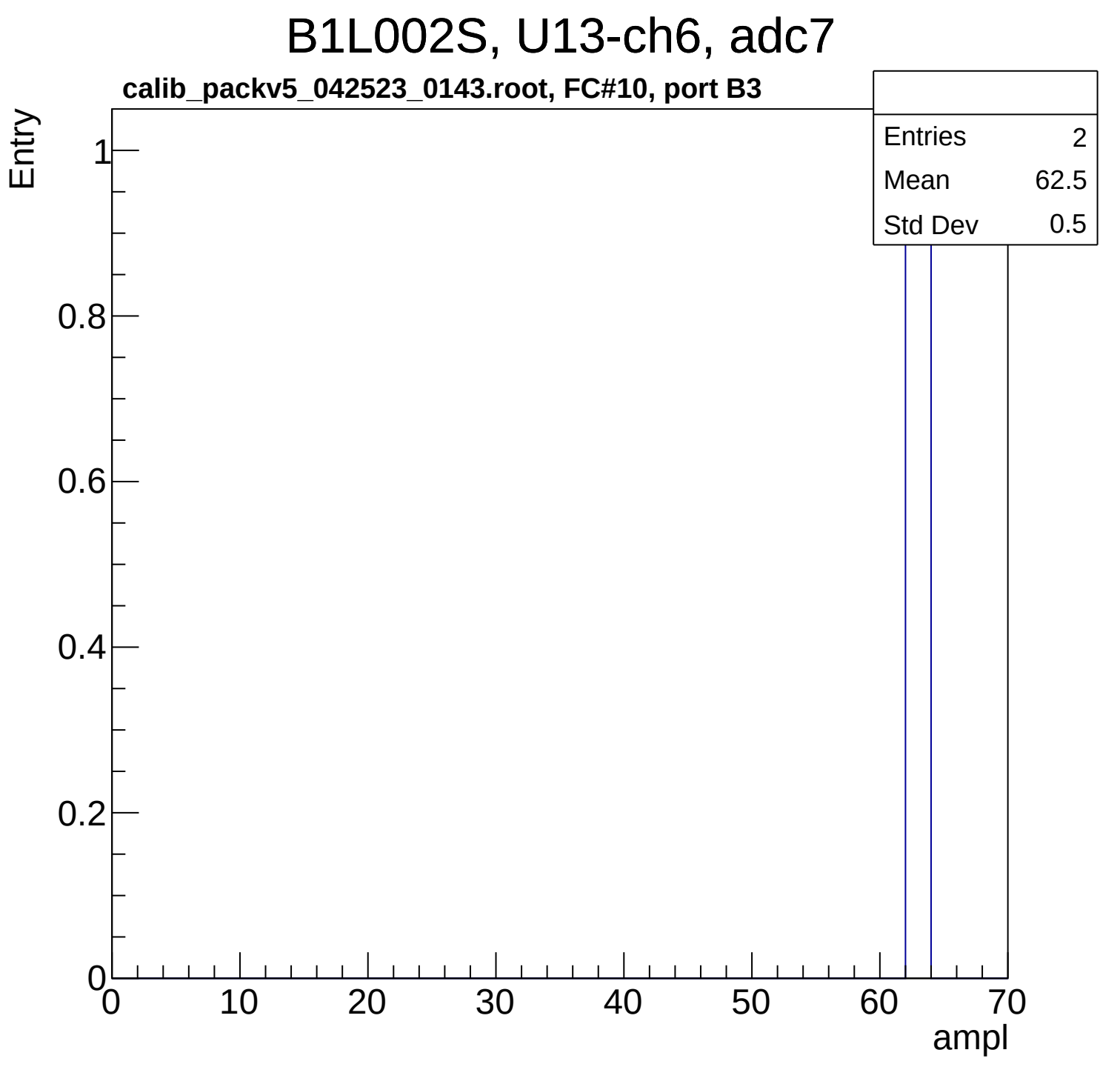
B1L002S, U13-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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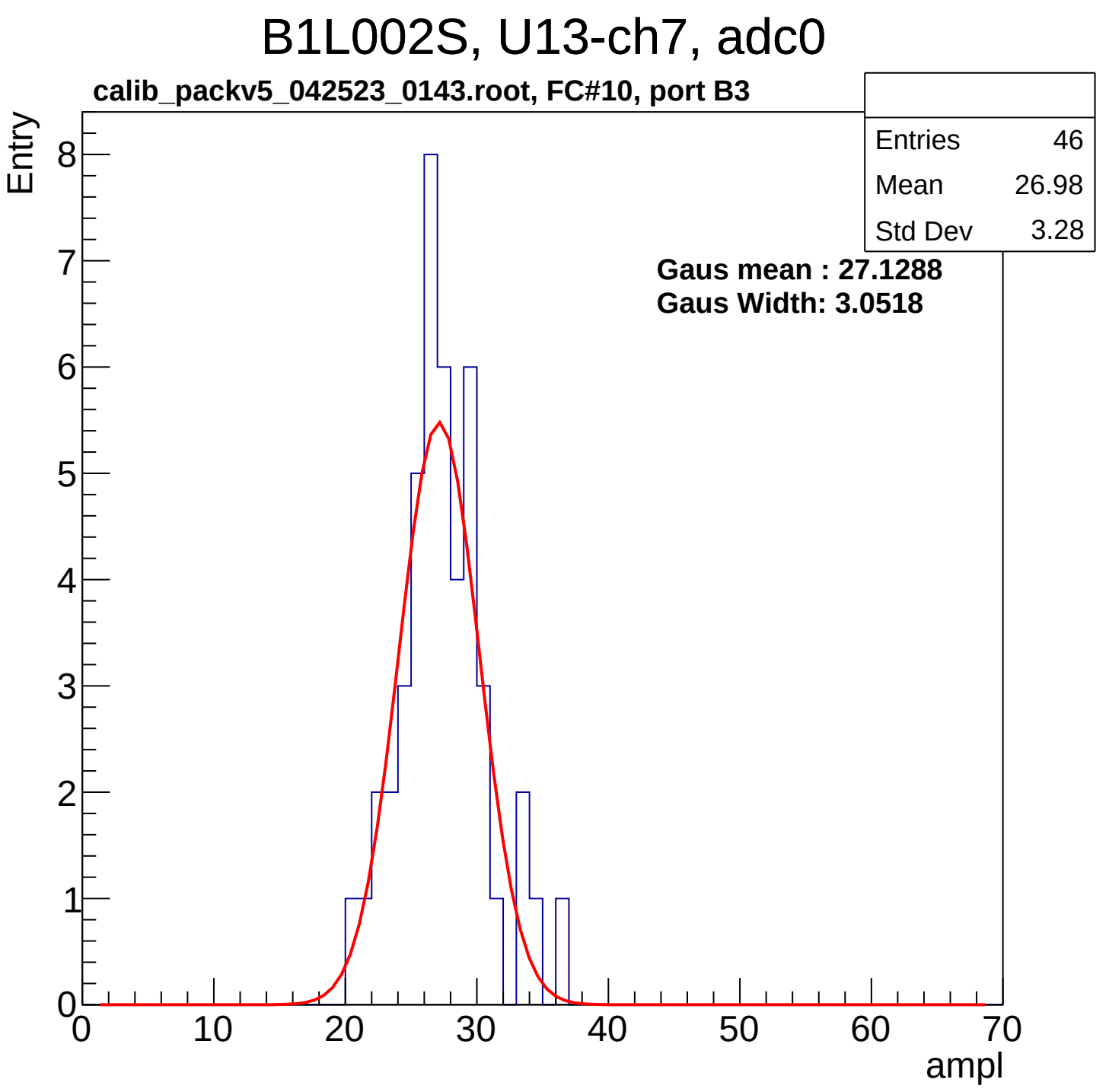
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 26.98
Std Dev 3.28
Gaus mean : 27.1288
Gaus Width: 3.0518
Entry
ampl
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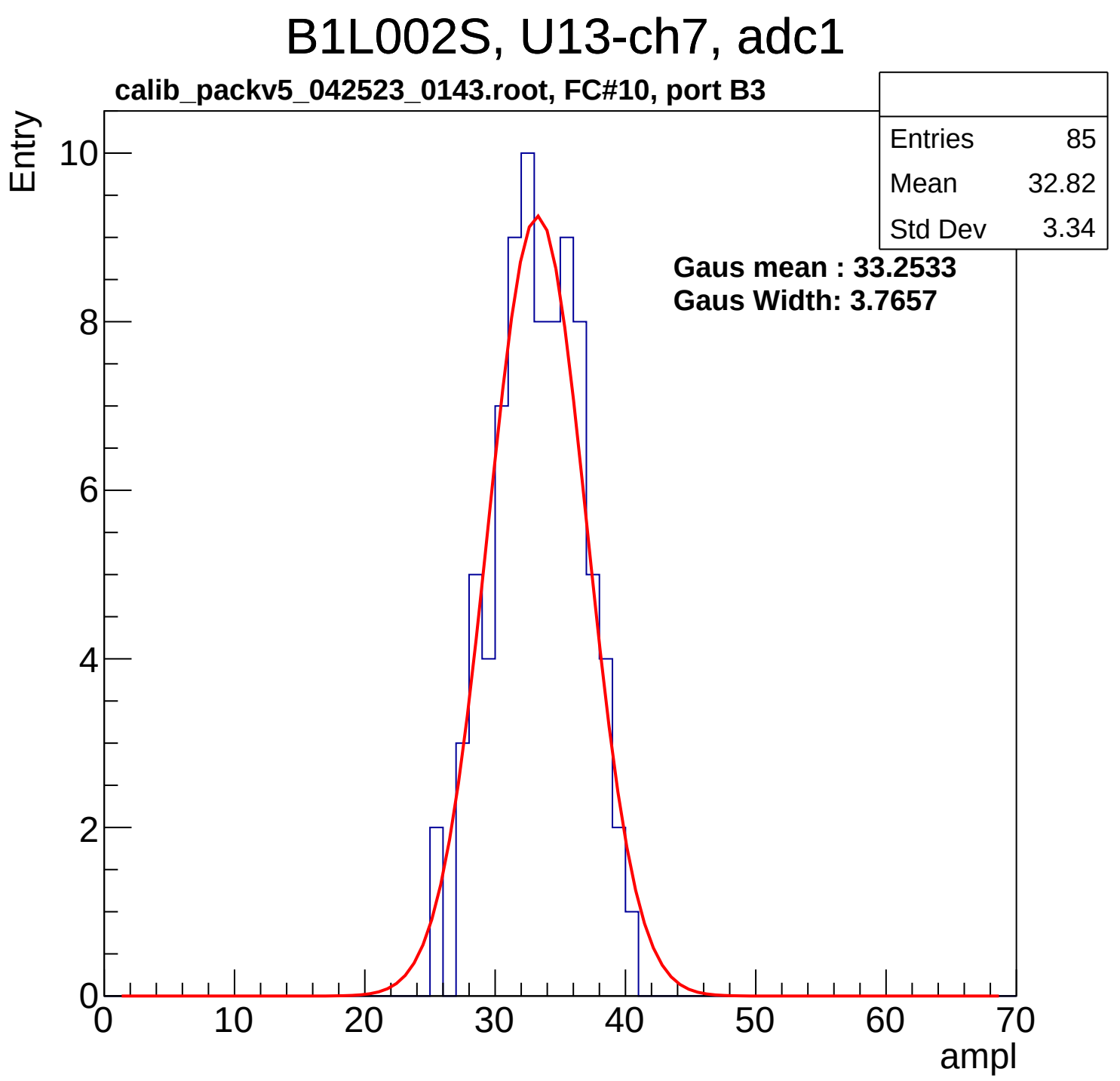
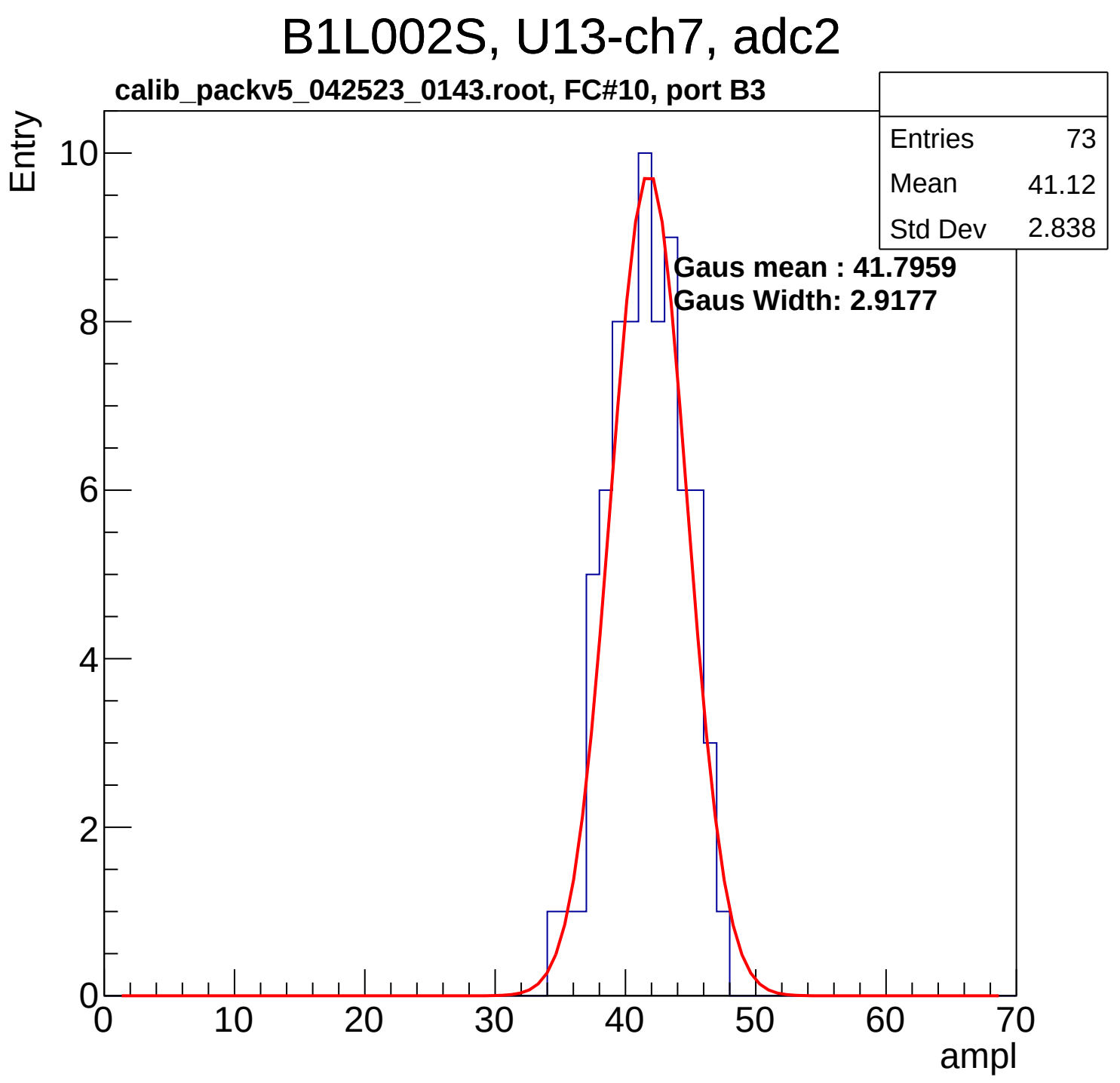
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 32.82
Std Dev 3.34
Gaus mean : 33.2533
Gaus Width: 3.7657
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 41.12
Std Dev 2.838
Gaus mean : 41.7959
Gaus Width: 2.9177
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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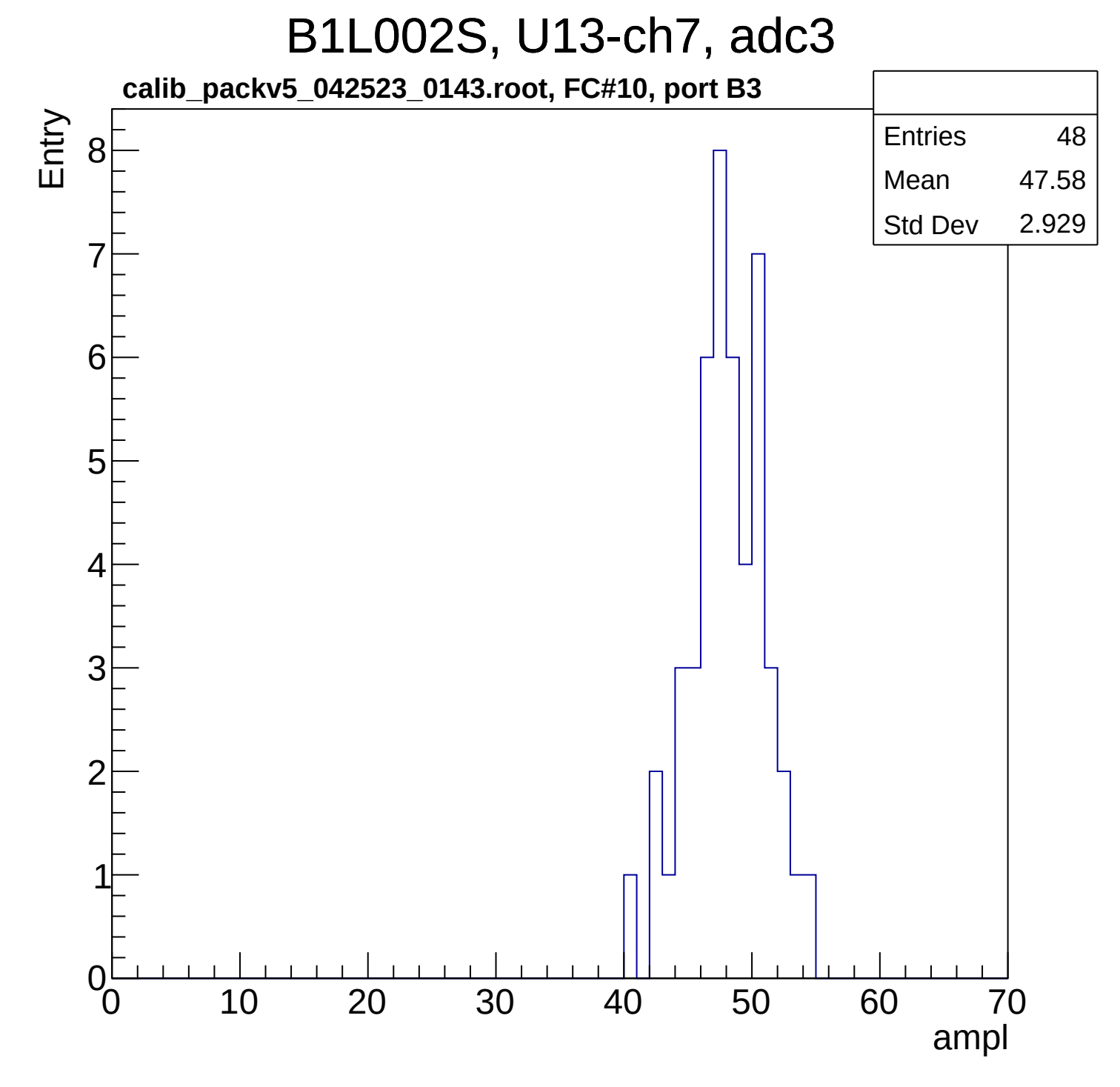
B1L002S, U13-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 47.58
Std Dev 2.929
Entry
ampl
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3
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5
6
7
8
0
10
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40
50
60
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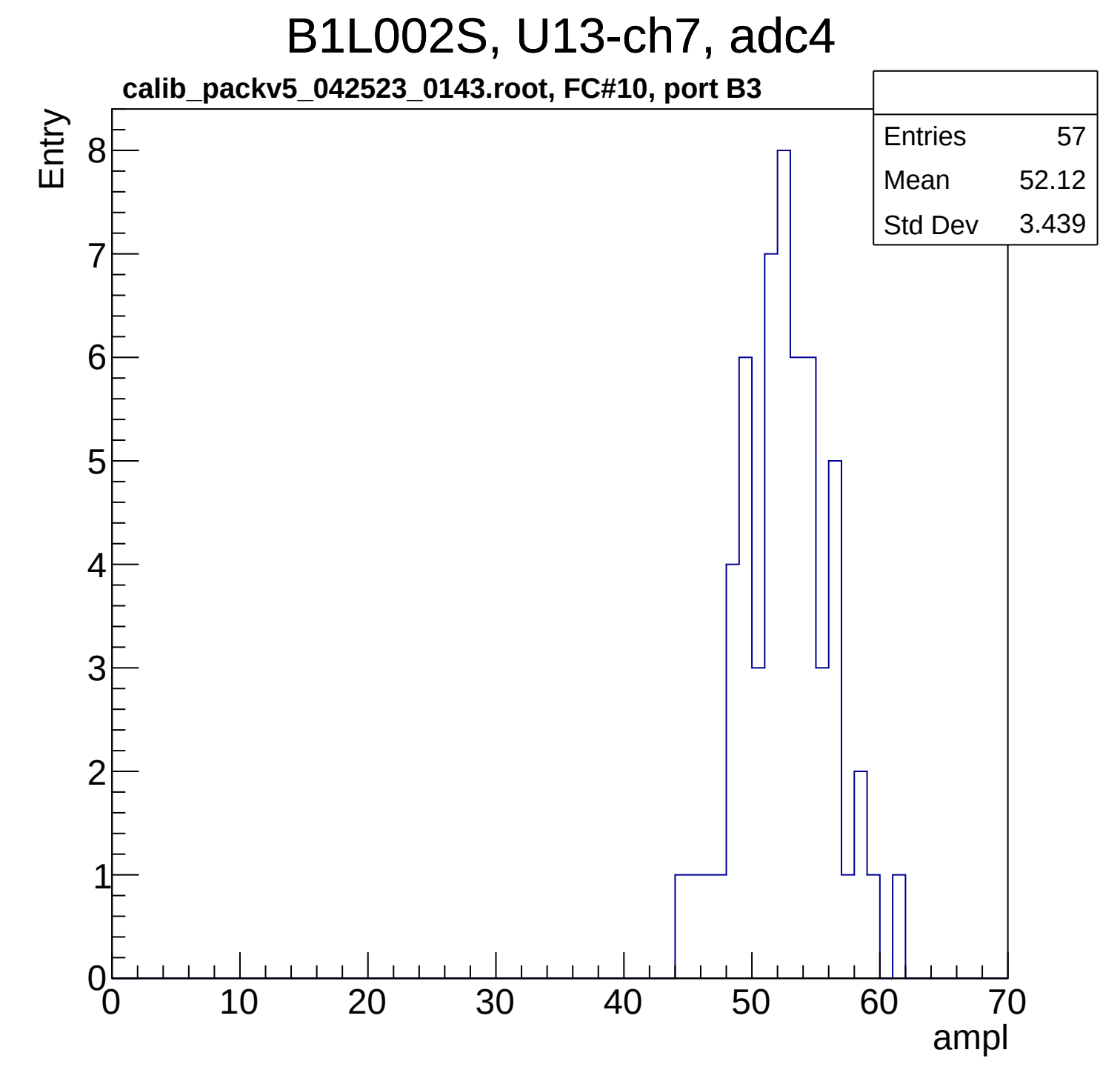
B1L002S, U13-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.12
Std Dev 3.439
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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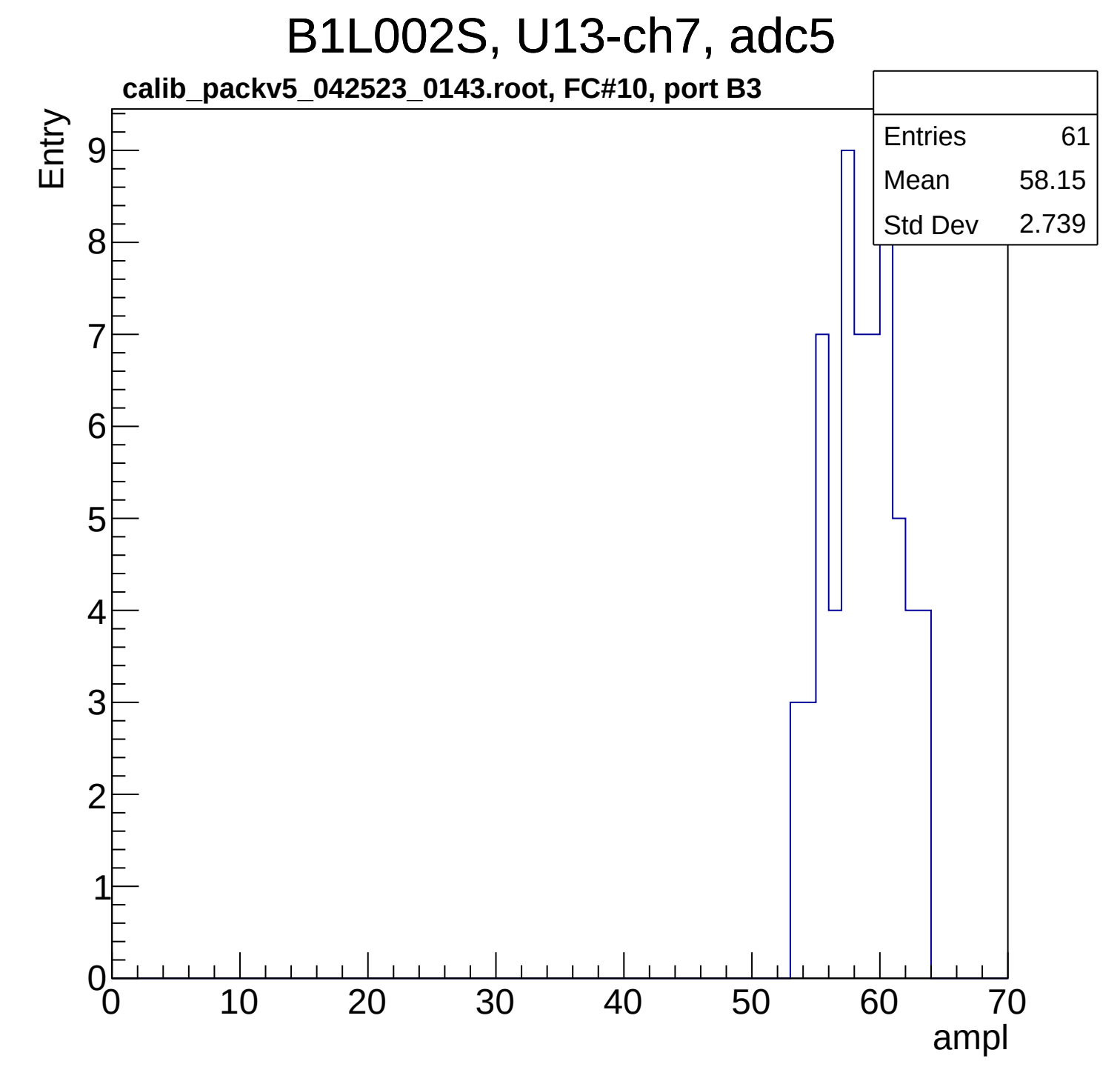
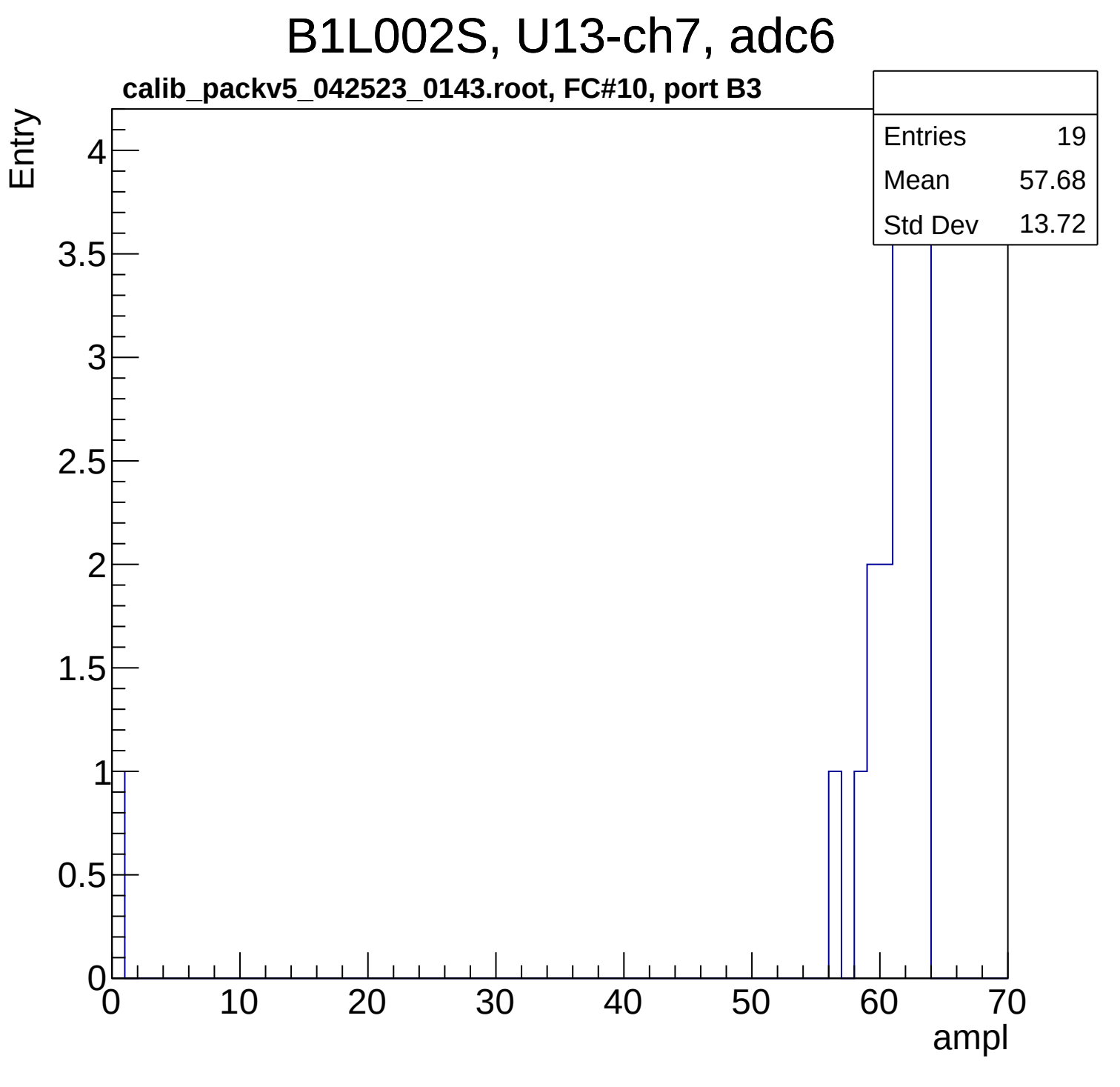

B1L002S, U13-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.15
Std Dev 2.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.68
Std Dev 13.72
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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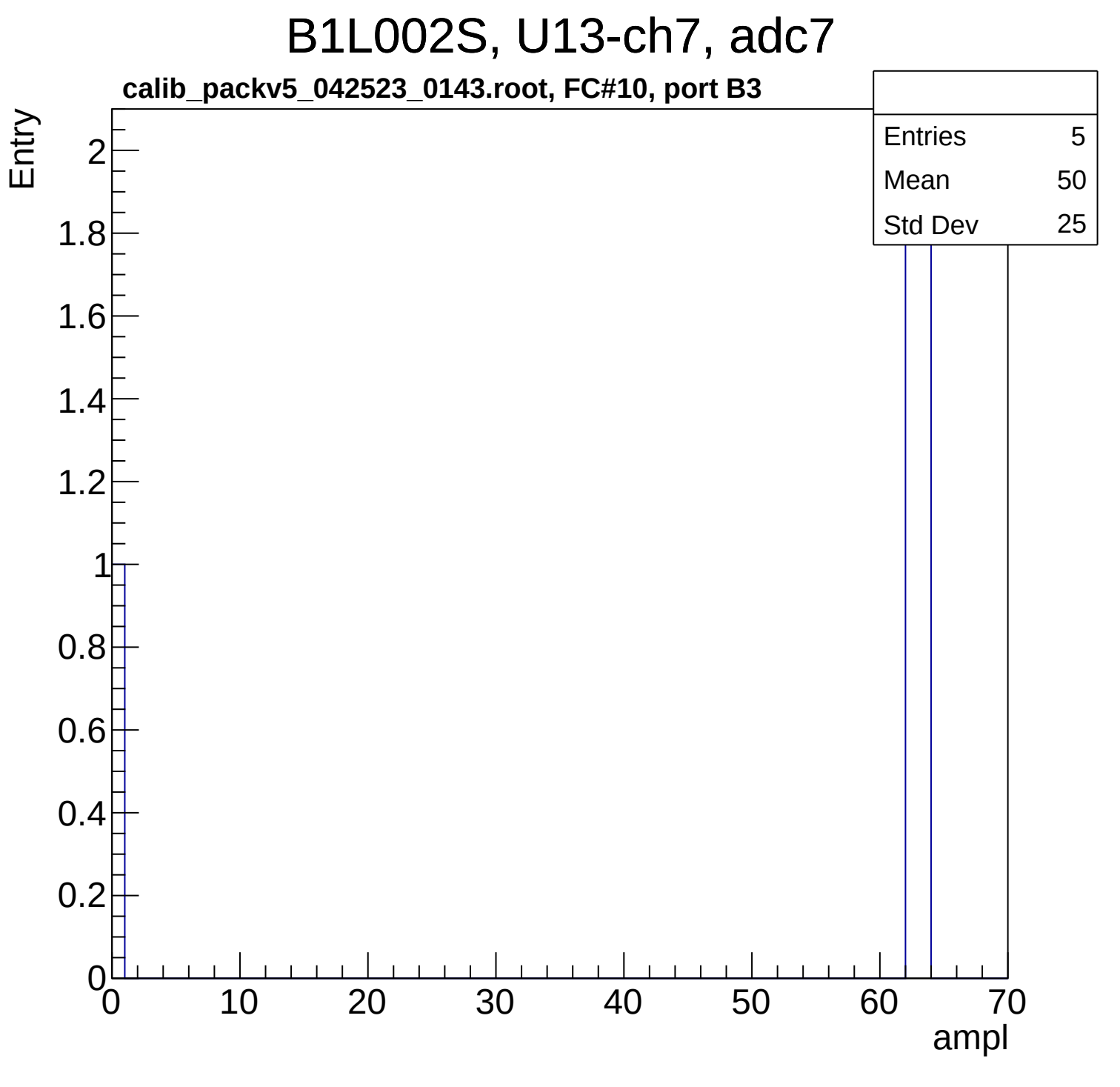
B1L002S, U13-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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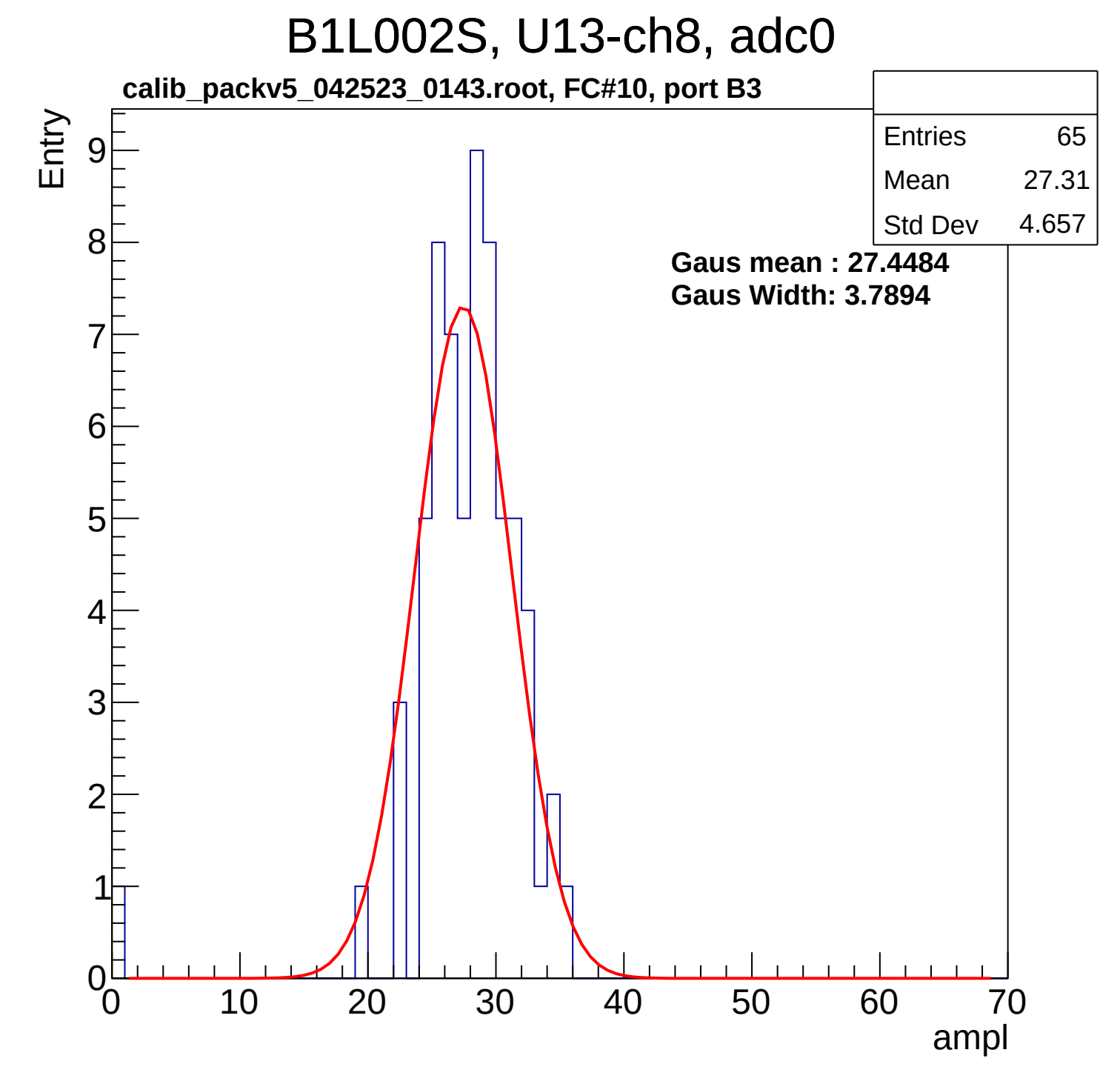
B1L002S, U13-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.31
Std Dev 4.657
Gaus mean : 27.4484
Gaus Width: 3.7894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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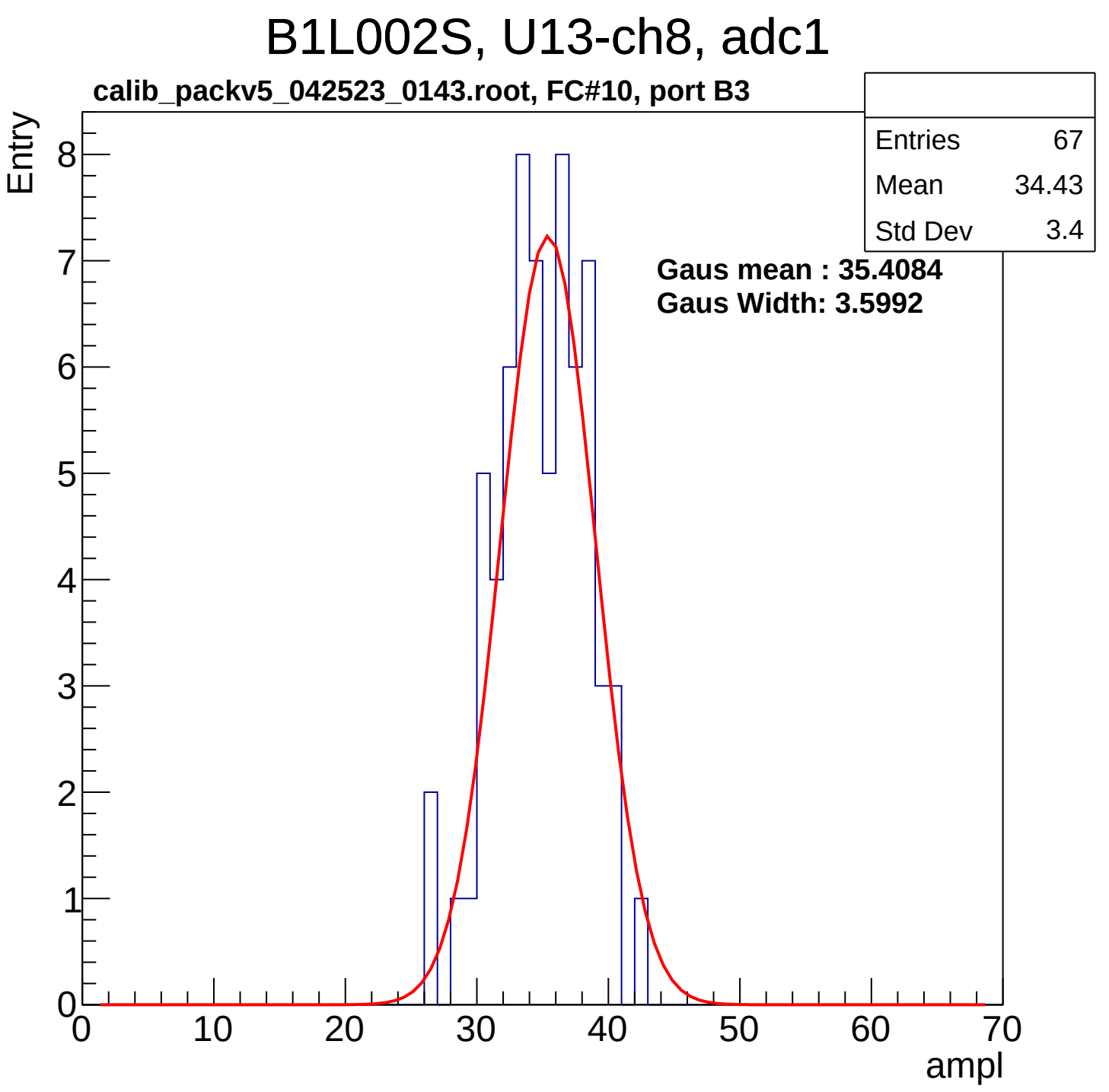
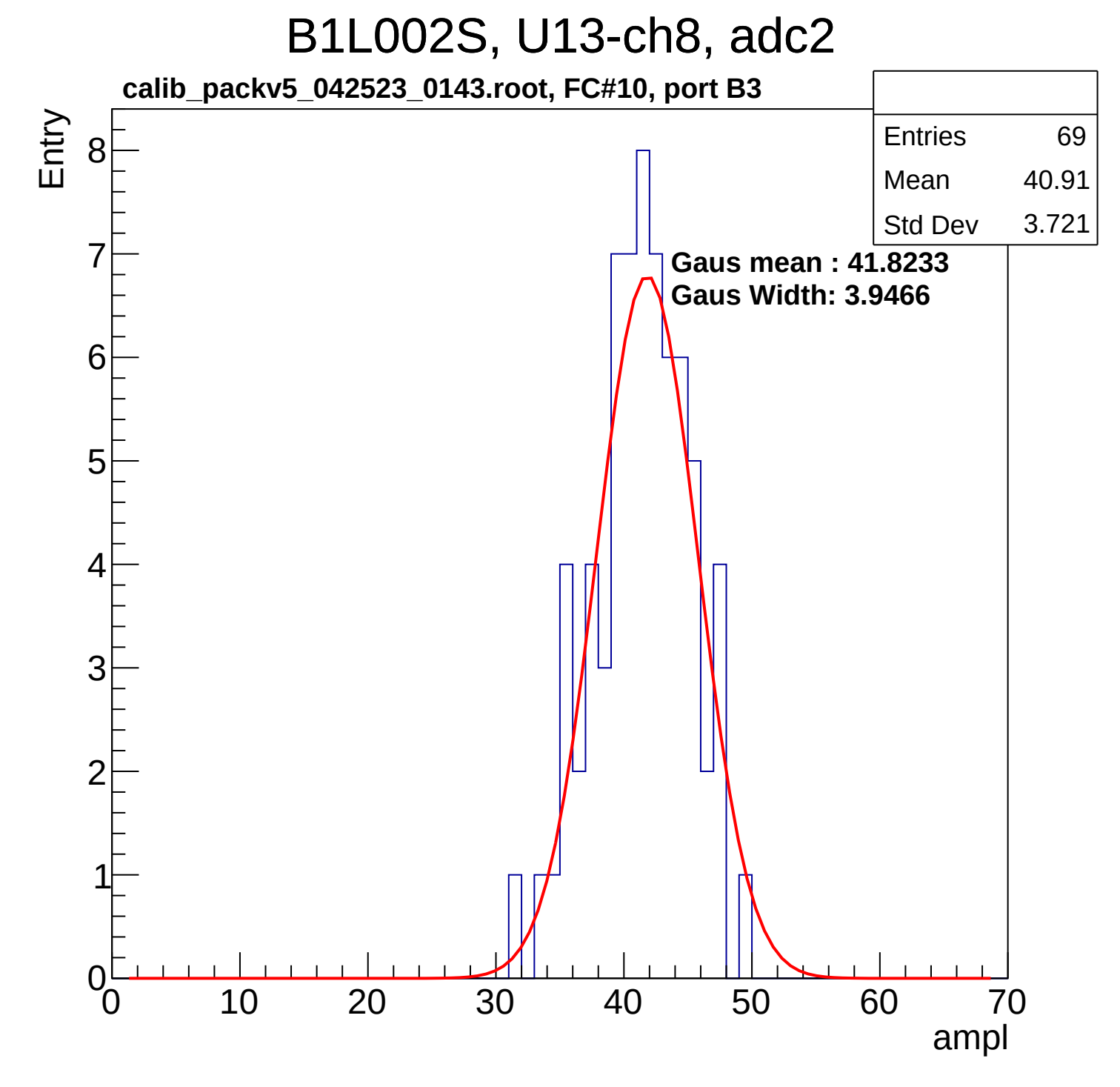

B1L002S, U13-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.43
Std Dev 3.4
Gaus mean : 35.4084
Gaus Width: 3.5992
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.91
Std Dev 3.721
Gaus mean : 41.8233
Gaus Width: 3.9466
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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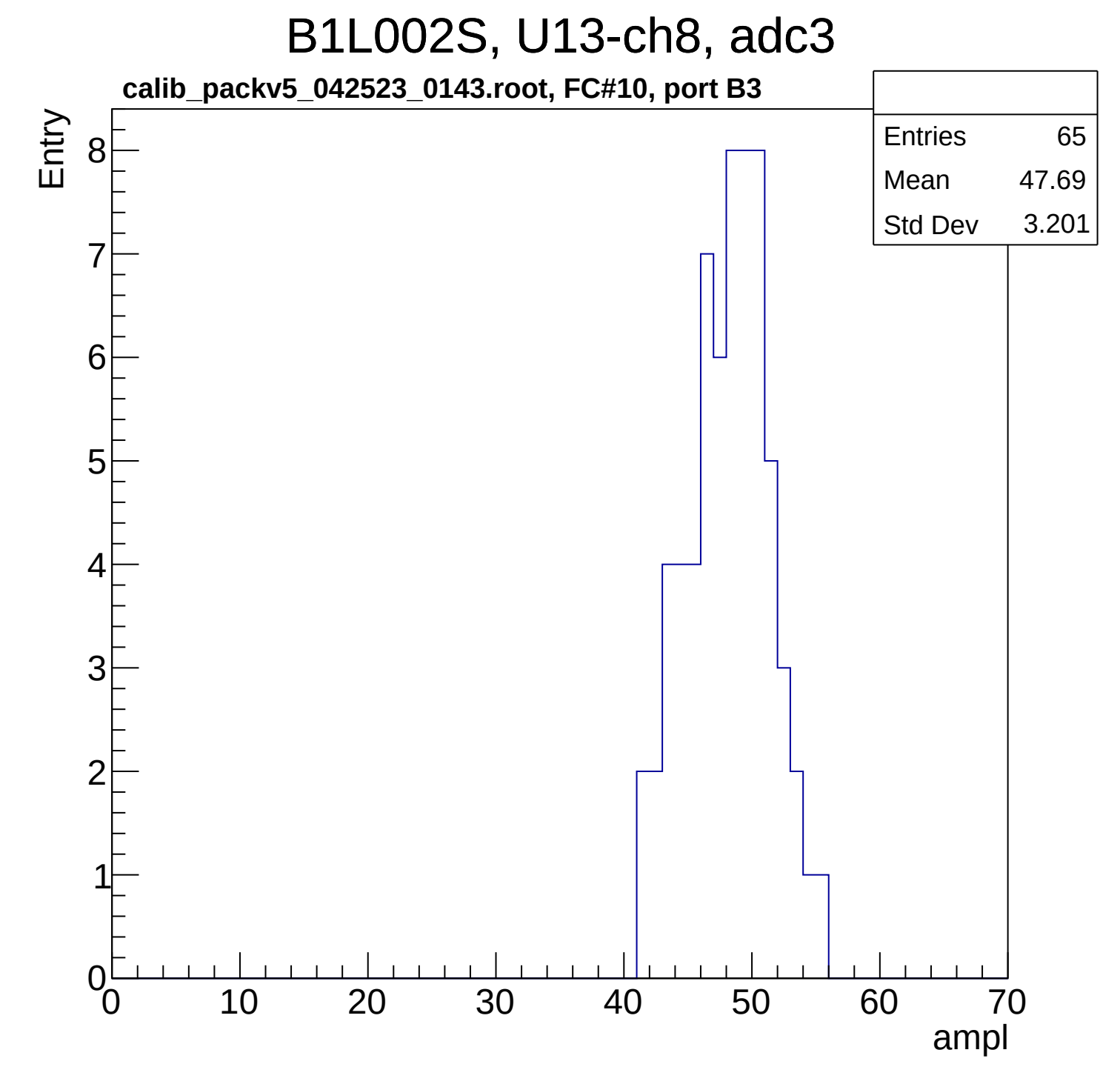
B1L002S, U13-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.69
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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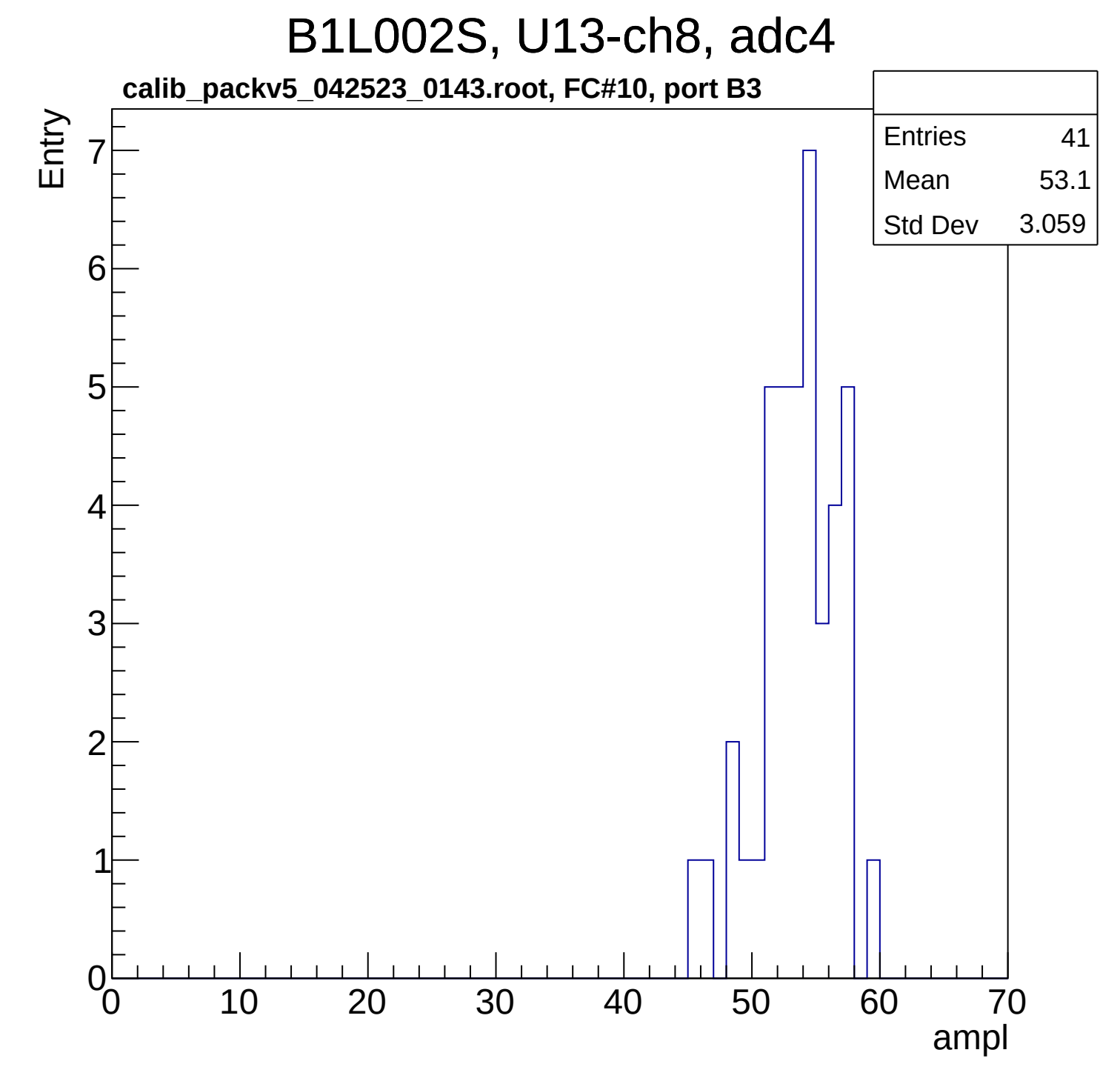
B1L002S, U13-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 53.1
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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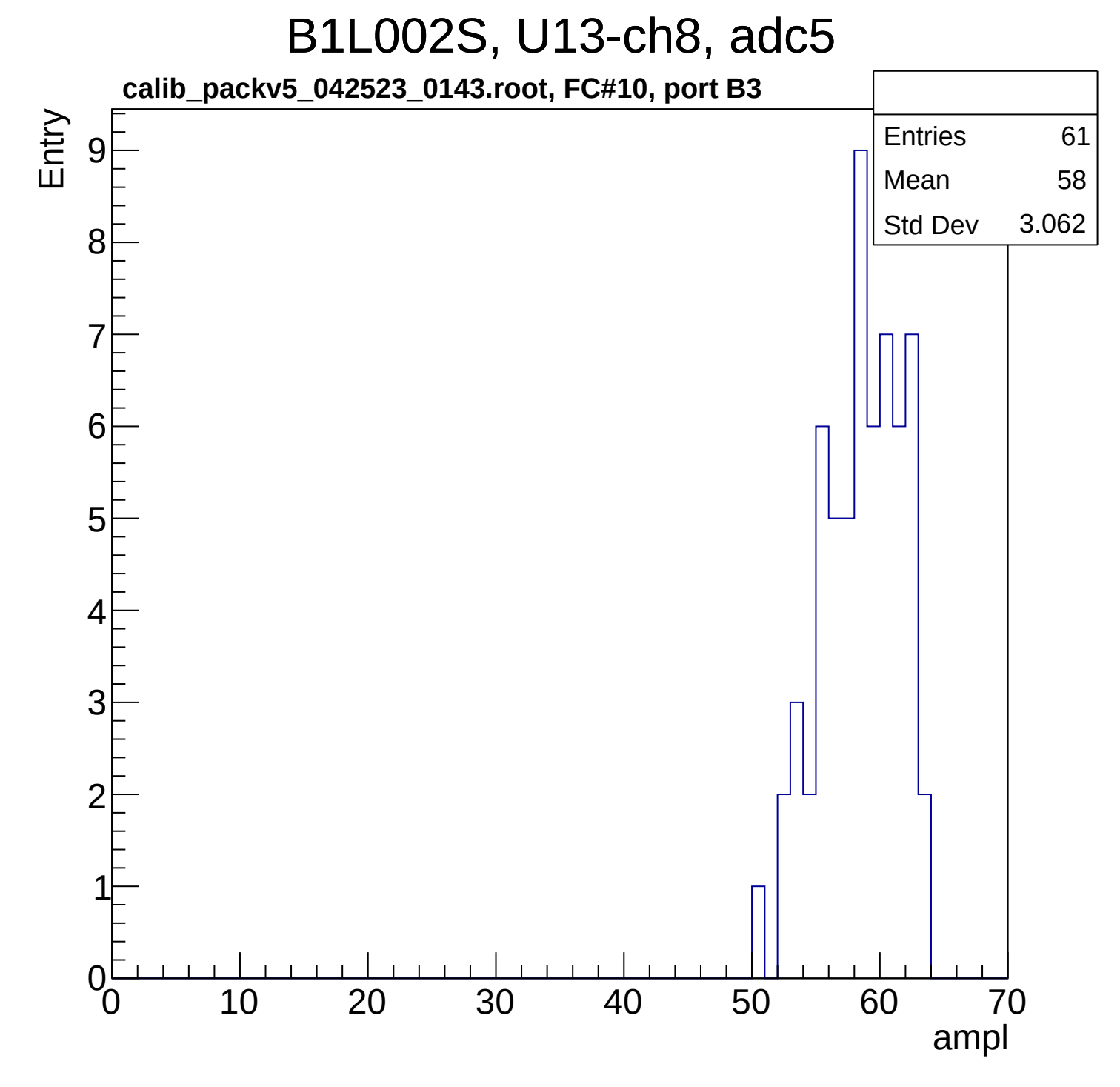
B1L002S, U13-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58
Std Dev 3.062
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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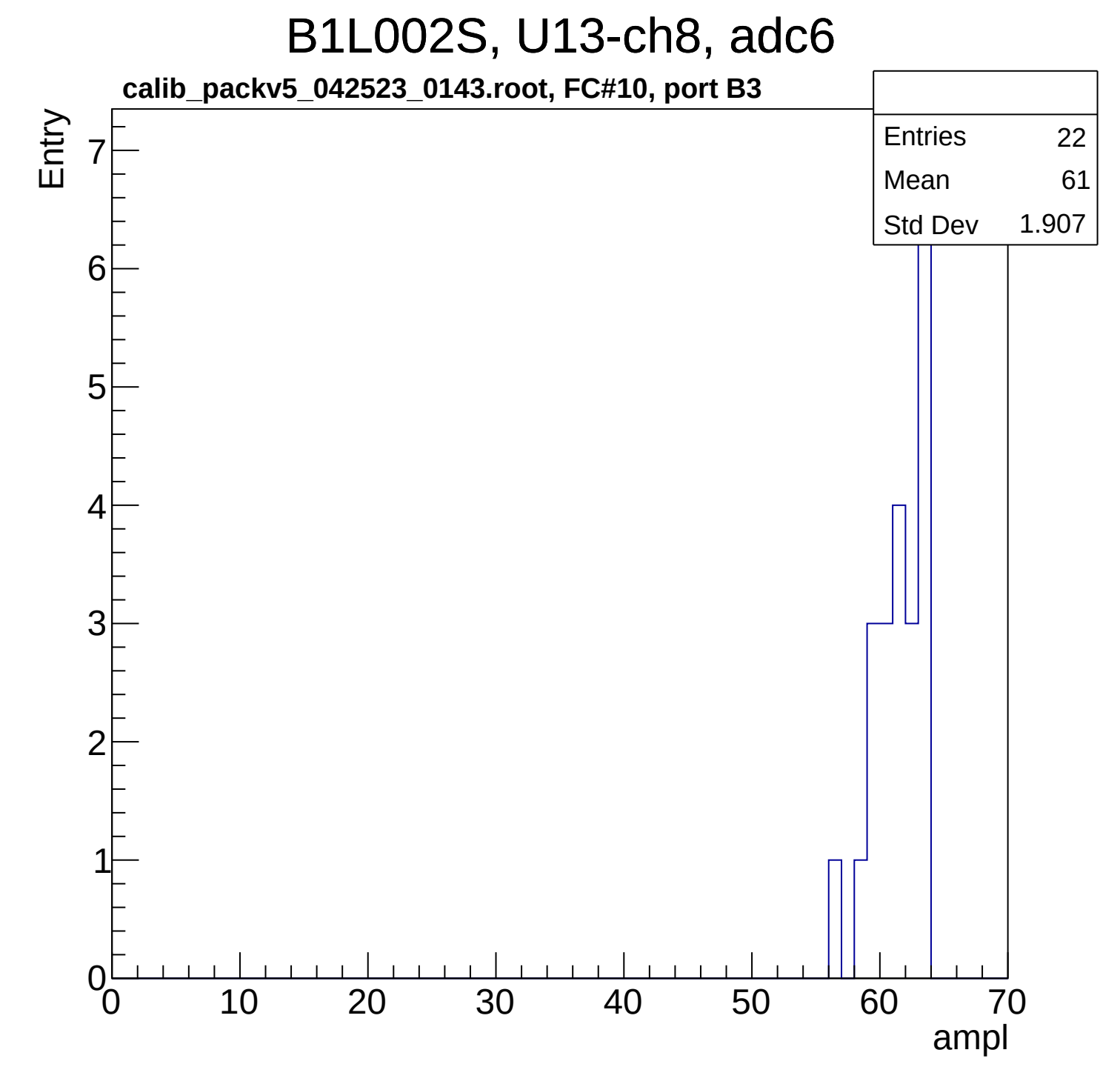
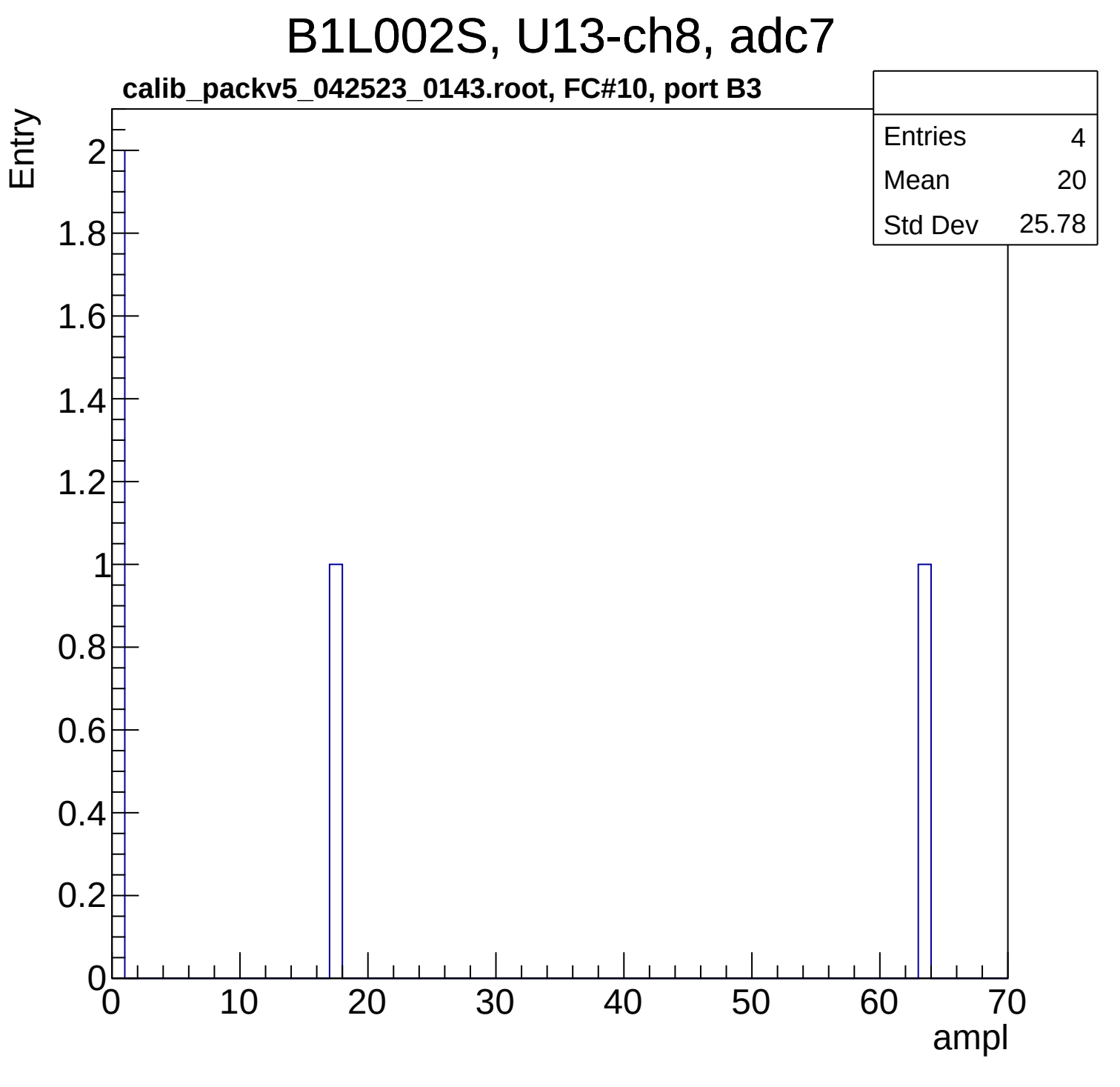

B1L002S, U13-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61
Std Dev 1.907
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20
Std Dev 25.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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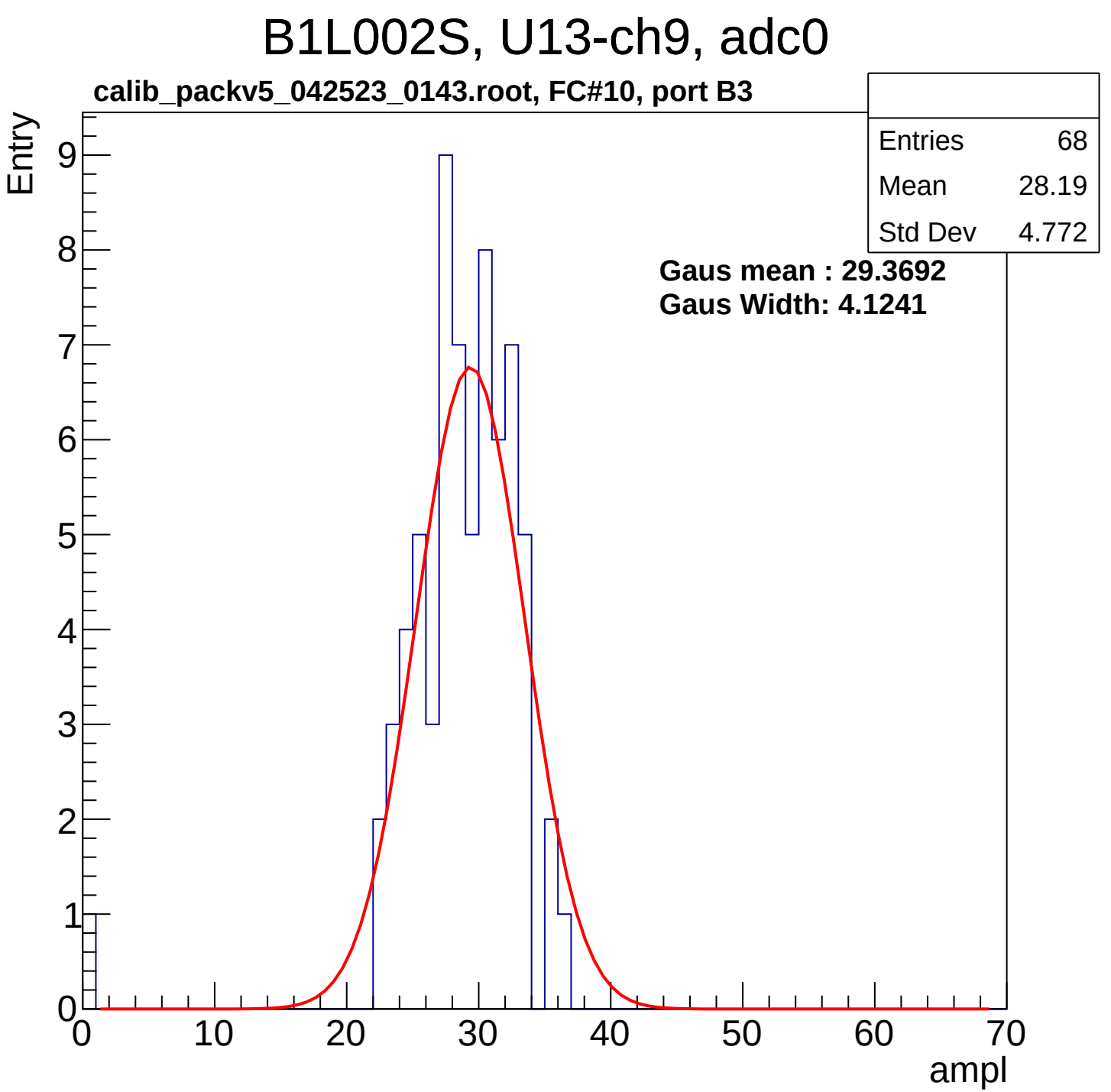
B1L002S, U13-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.19
Std Dev 4.772
Gaus mean : 29.3692
Gaus Width: 4.1241
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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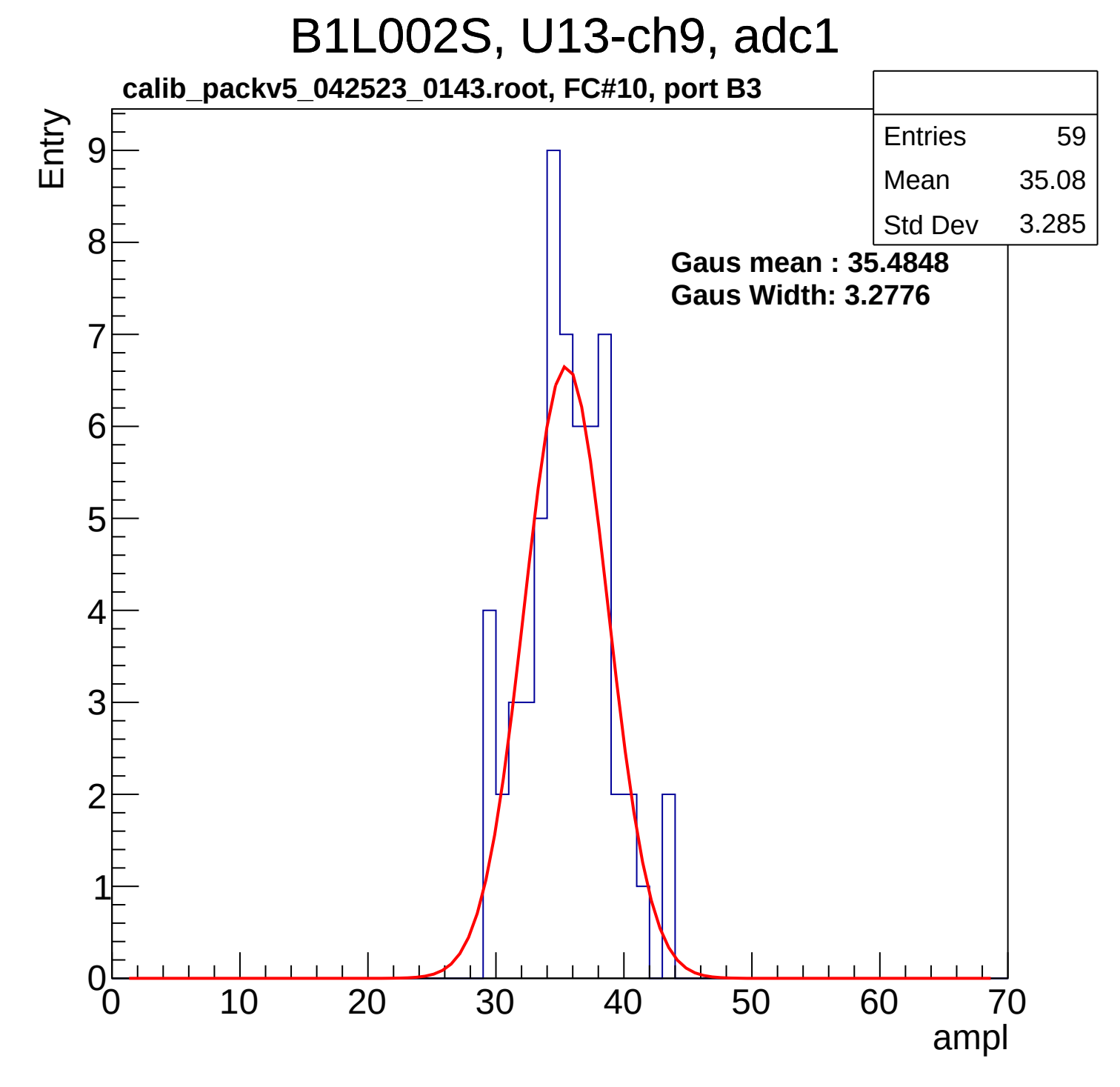
B1L002S, U13-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.08
Std Dev 3.285
Gaus mean : 35.4848
Gaus Width: 3.2776
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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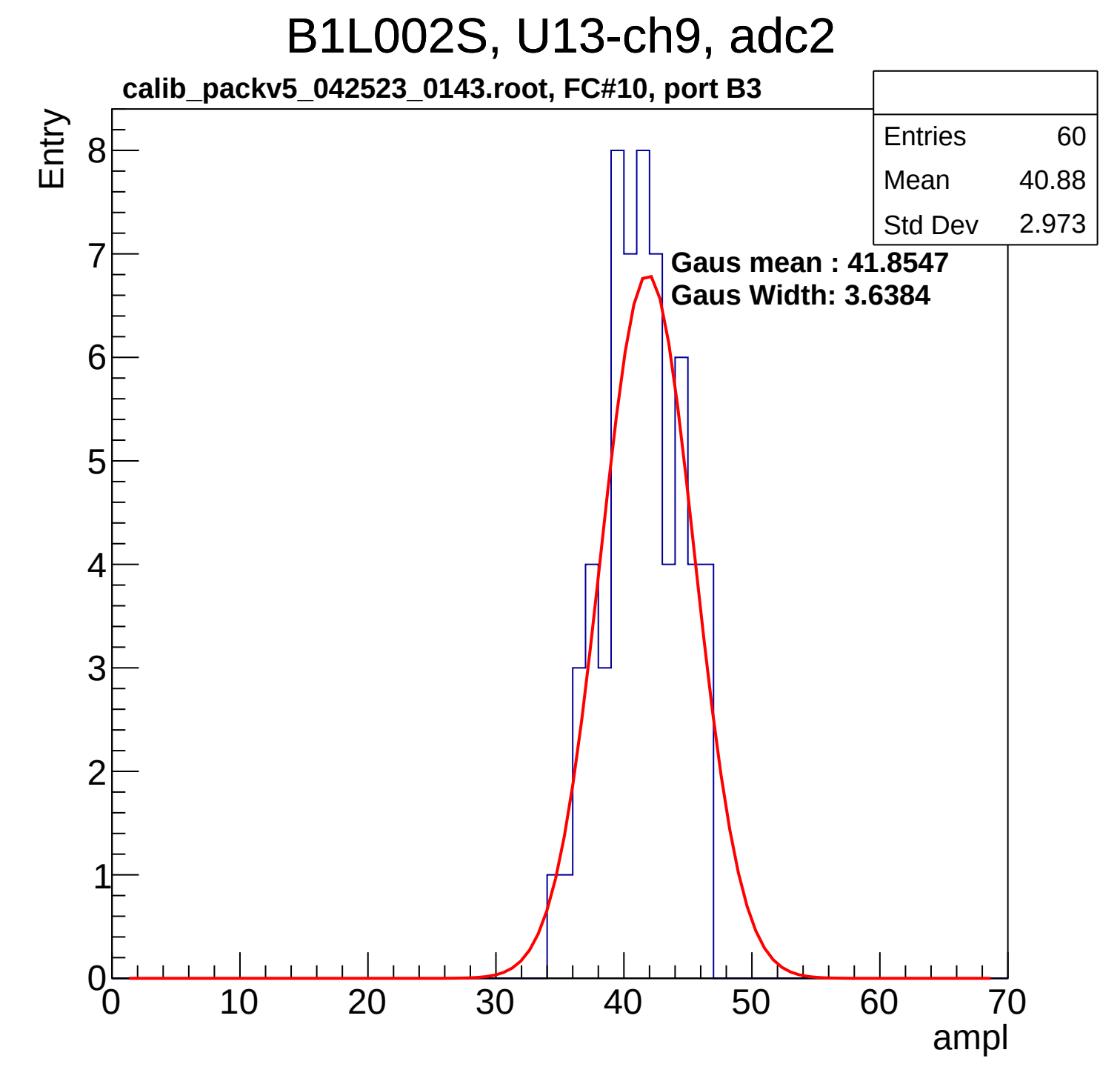
B1L002S, U13-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.88
Std Dev 2.973
Gaus mean : 41.8547
Gaus Width: 3.6384
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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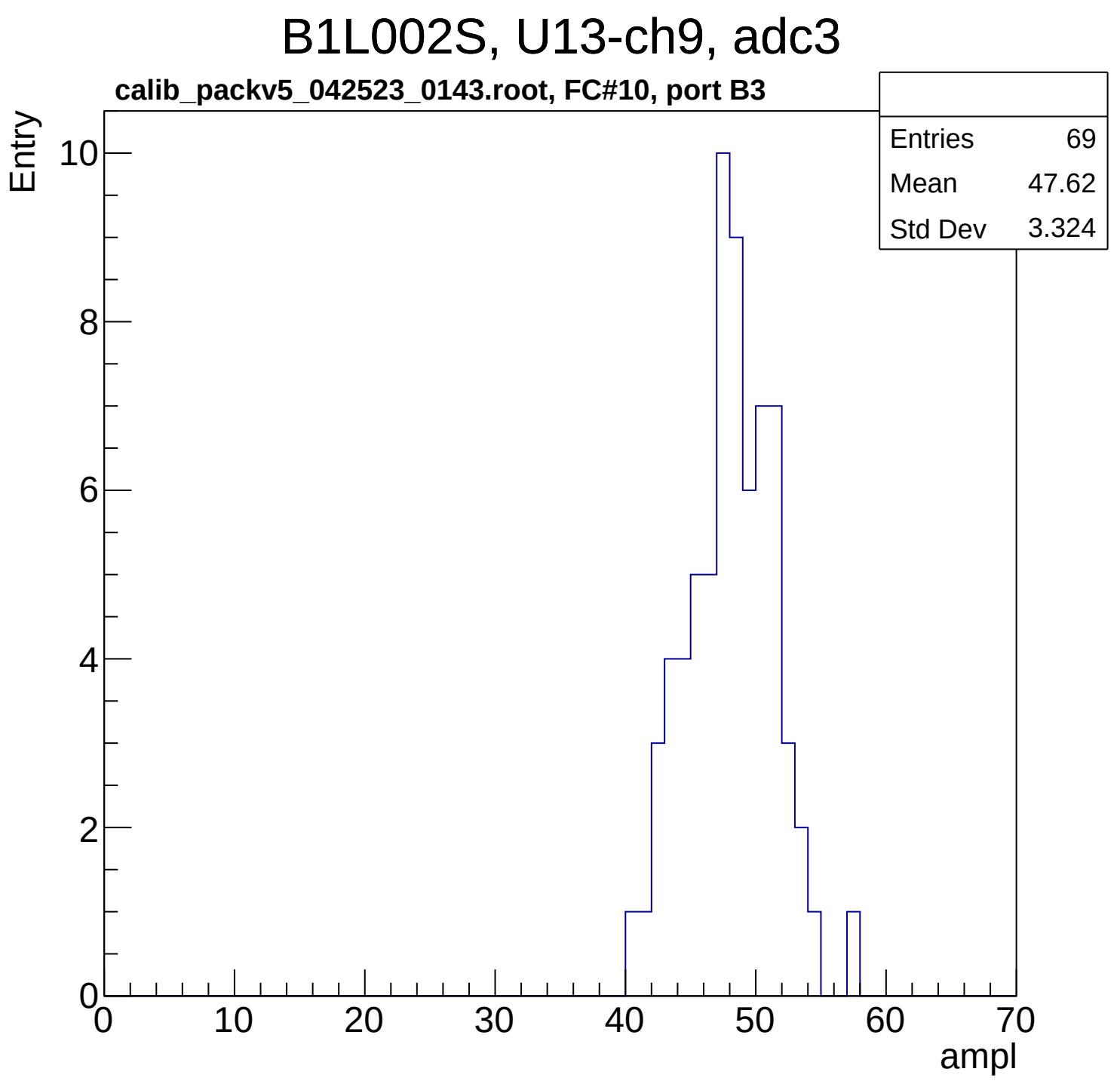
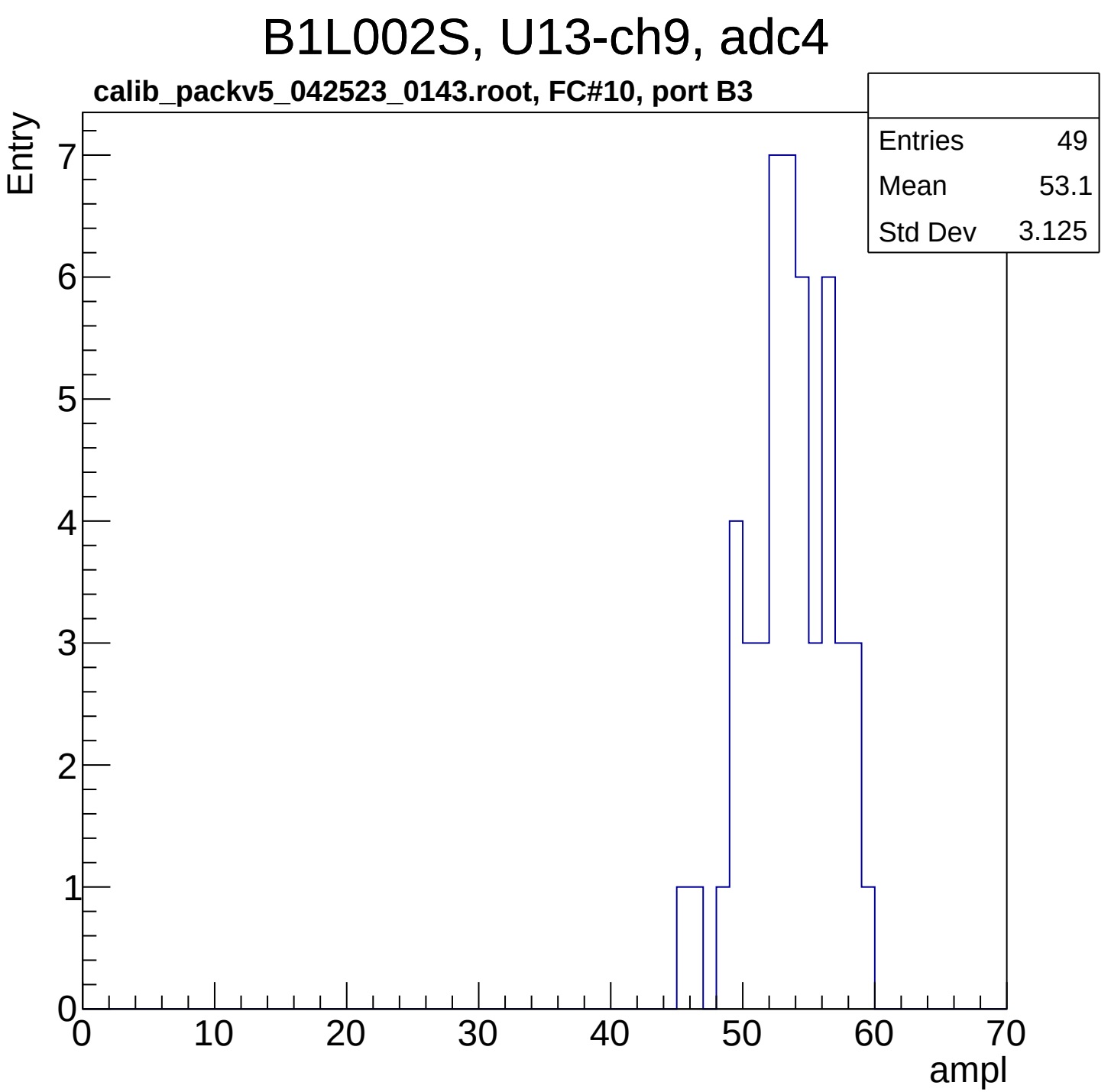

B1L002S, U13-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.62
Std Dev 3.324
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.1
Std Dev 3.125
Entry
ampl
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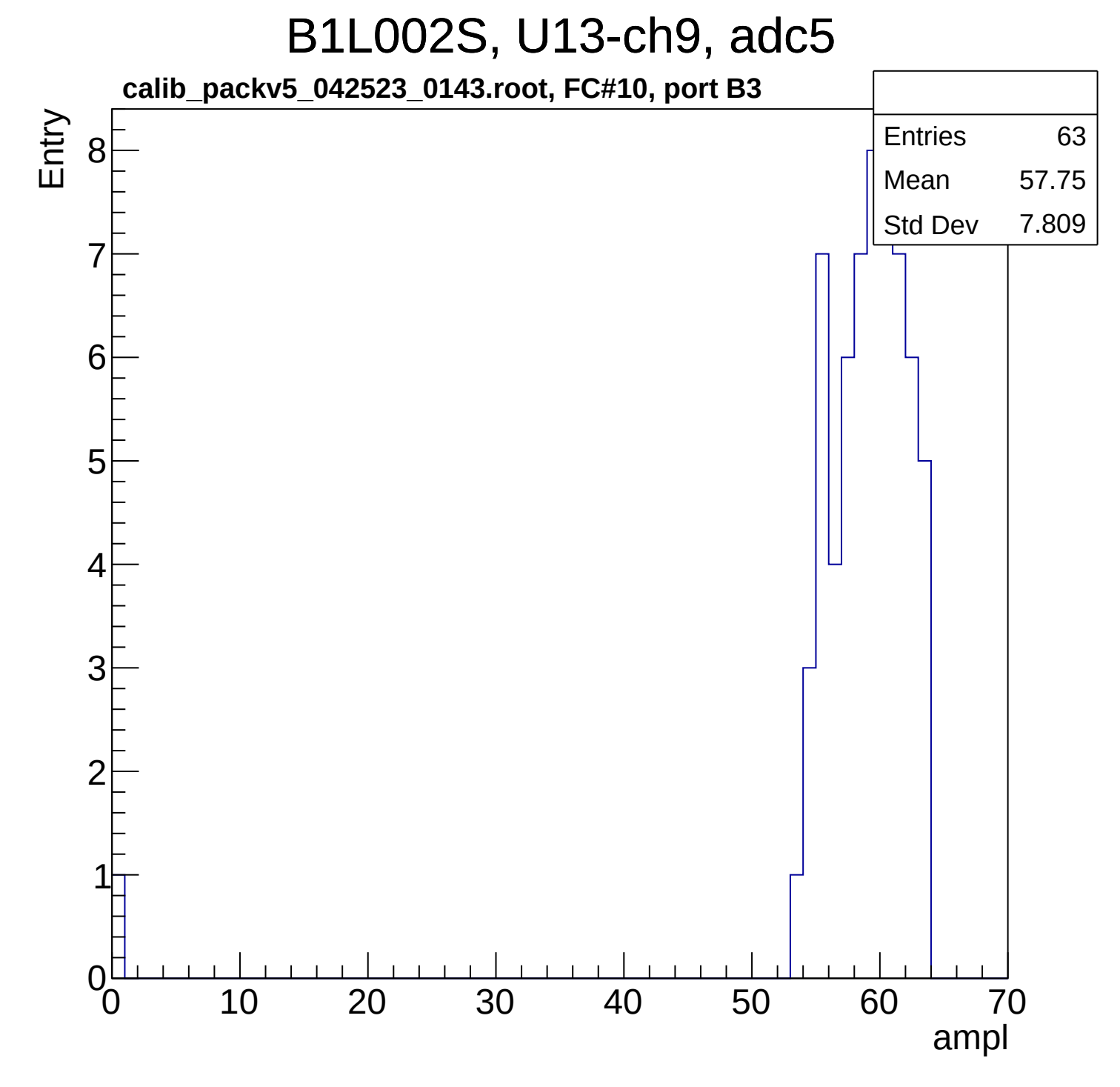
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.75
Std Dev 7.809
Entry
ampl
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3
4
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7
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0
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40
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60
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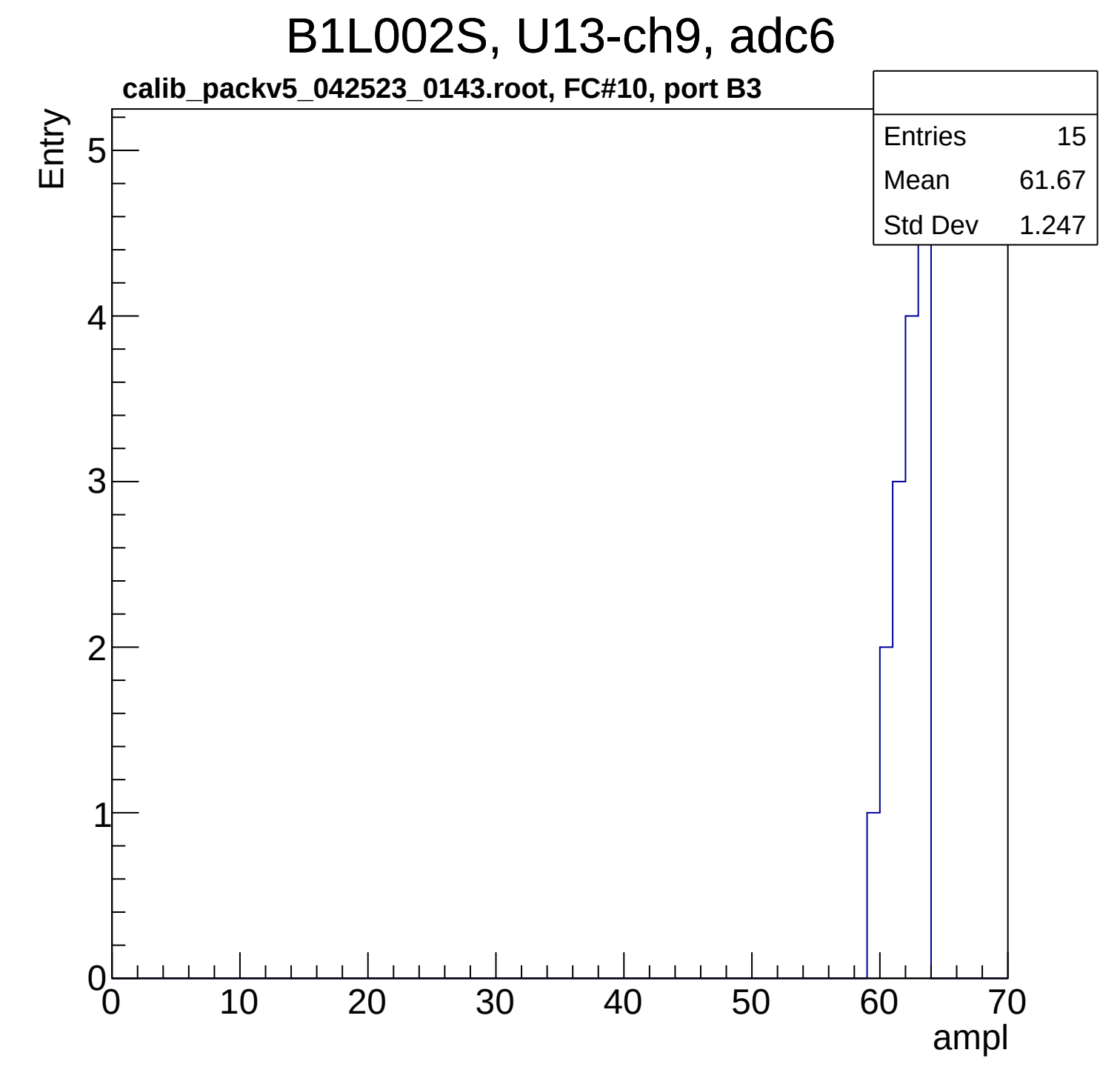
B1L002S, U13-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.67
Std Dev 1.247
Entry
ampl
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1
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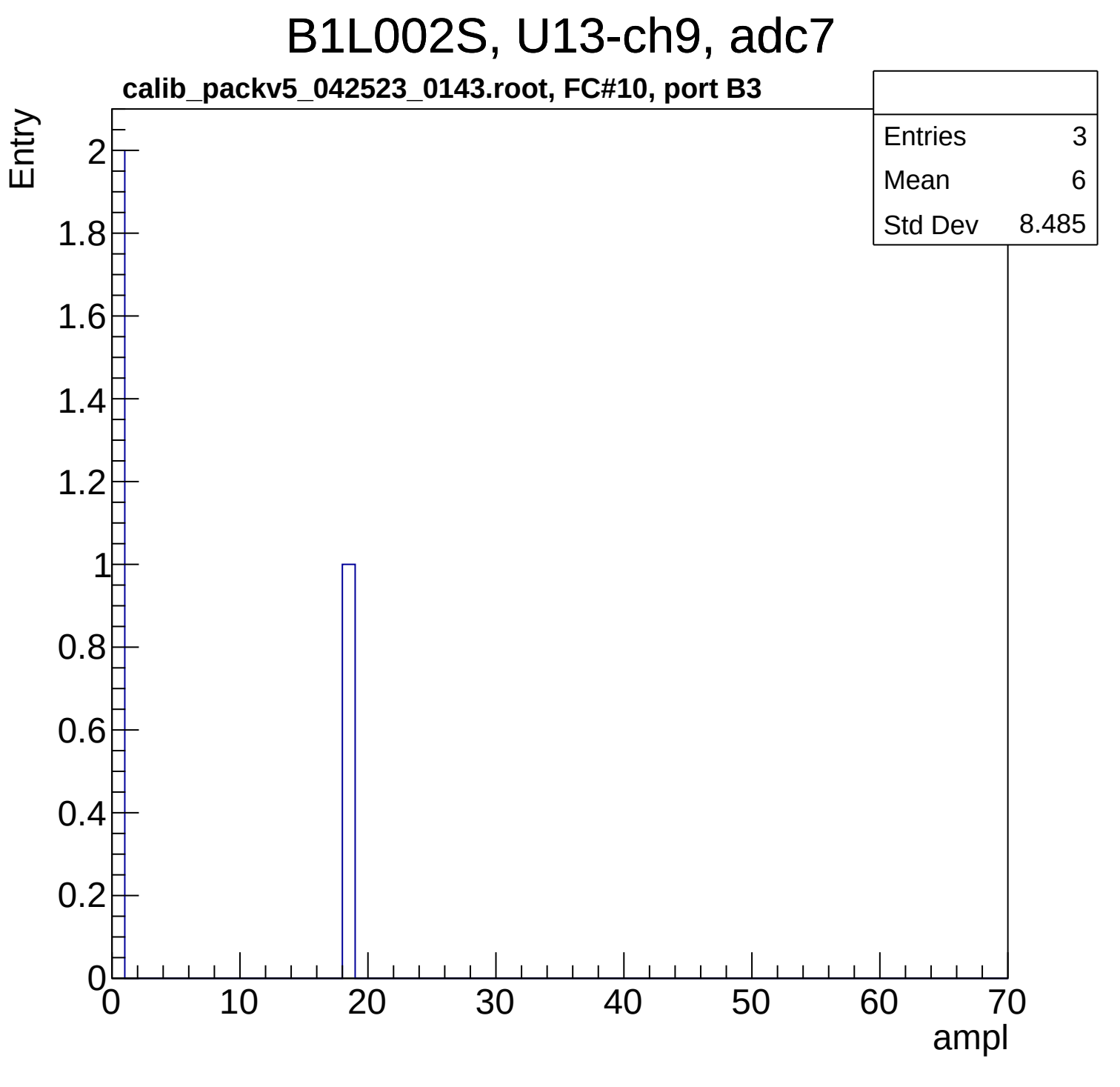
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U13-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6
Std Dev 8.485
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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0
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60
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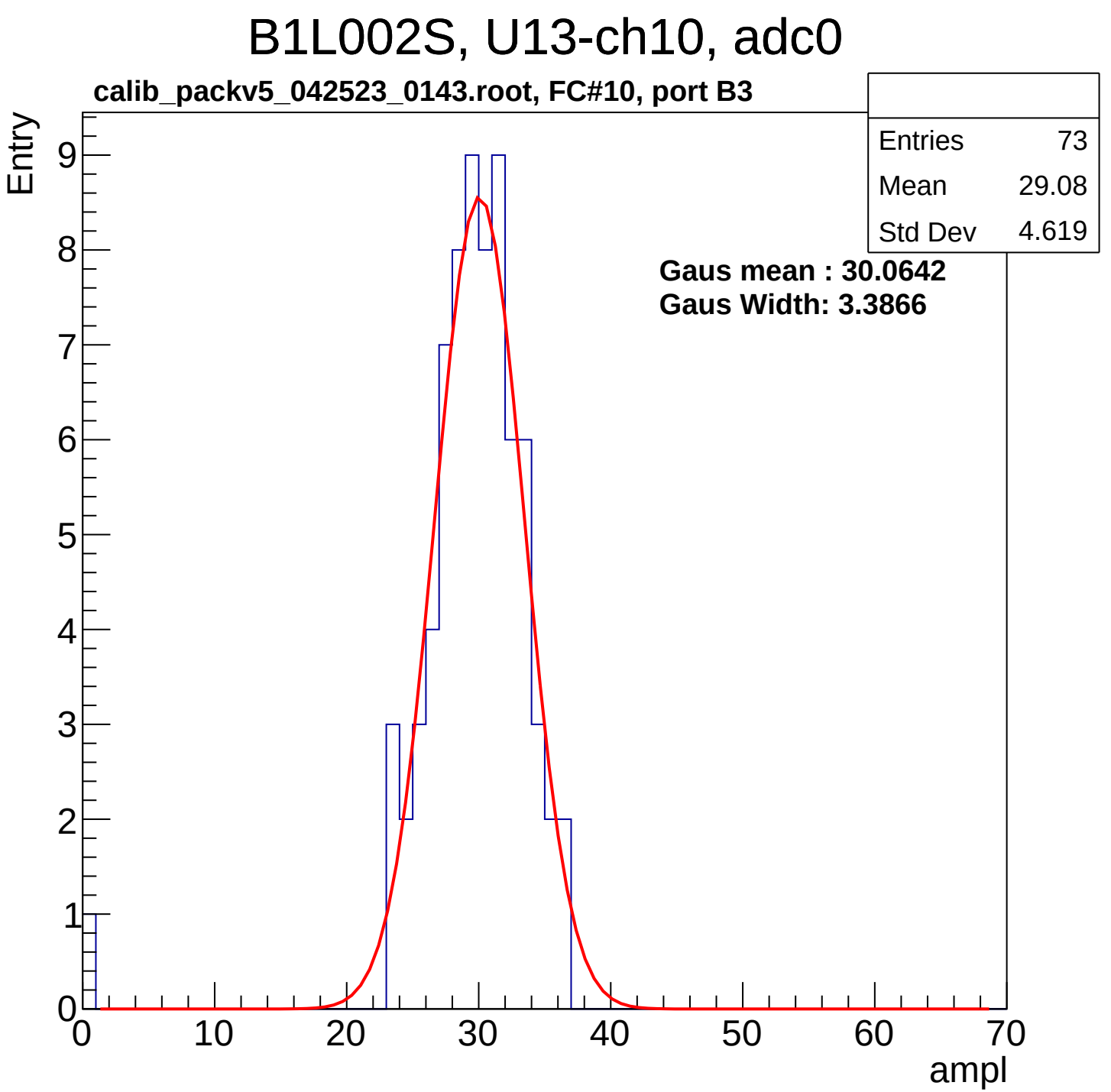
B1L002S, U13-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.08
Std Dev 4.619
Gaus mean : 30.0642
Gaus Width: 3.3866
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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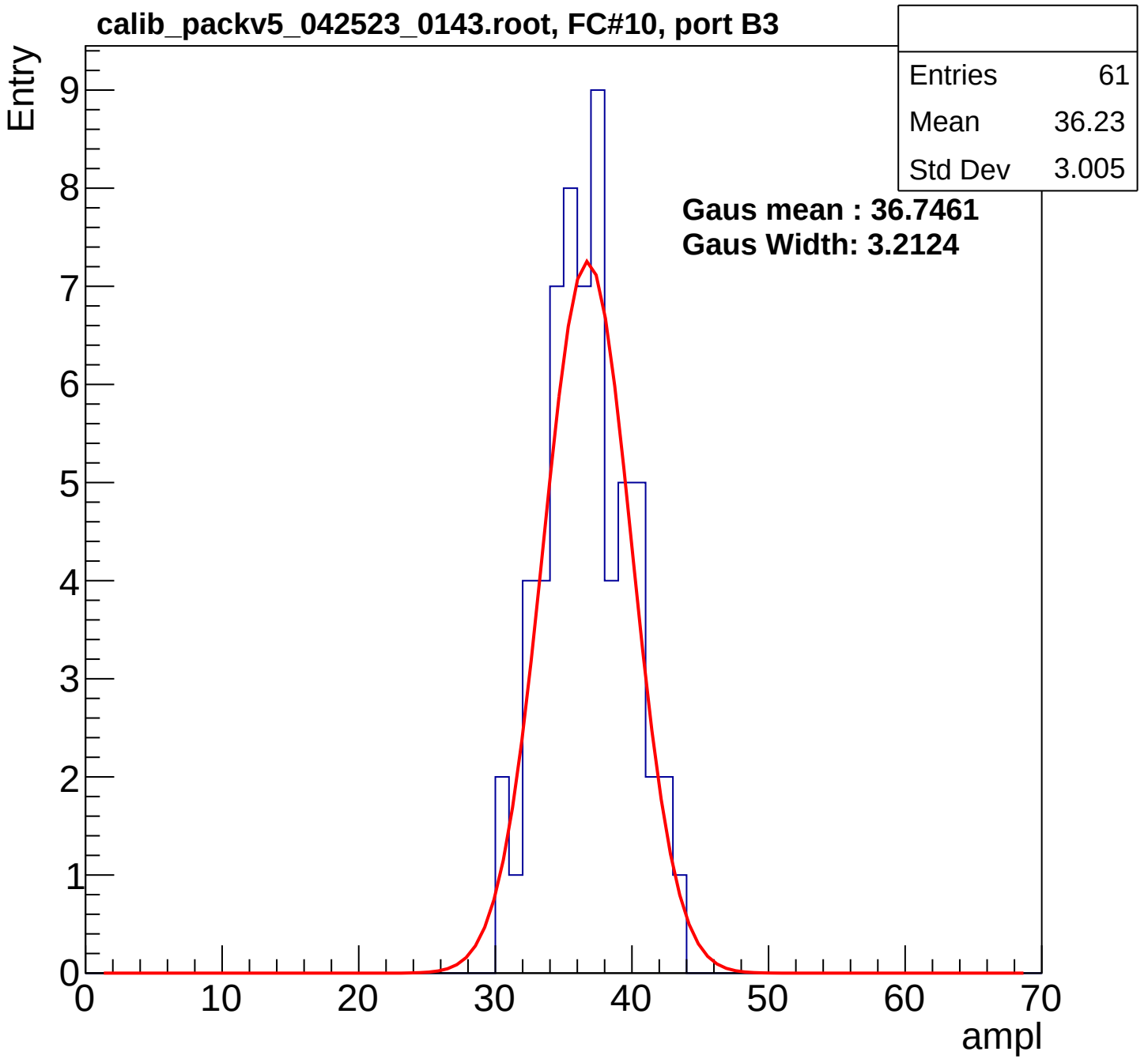
B1L002S, U13-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 36.23
Std Dev 3.005
Gaus mean : 36.7461
Gaus Width: 3.2124
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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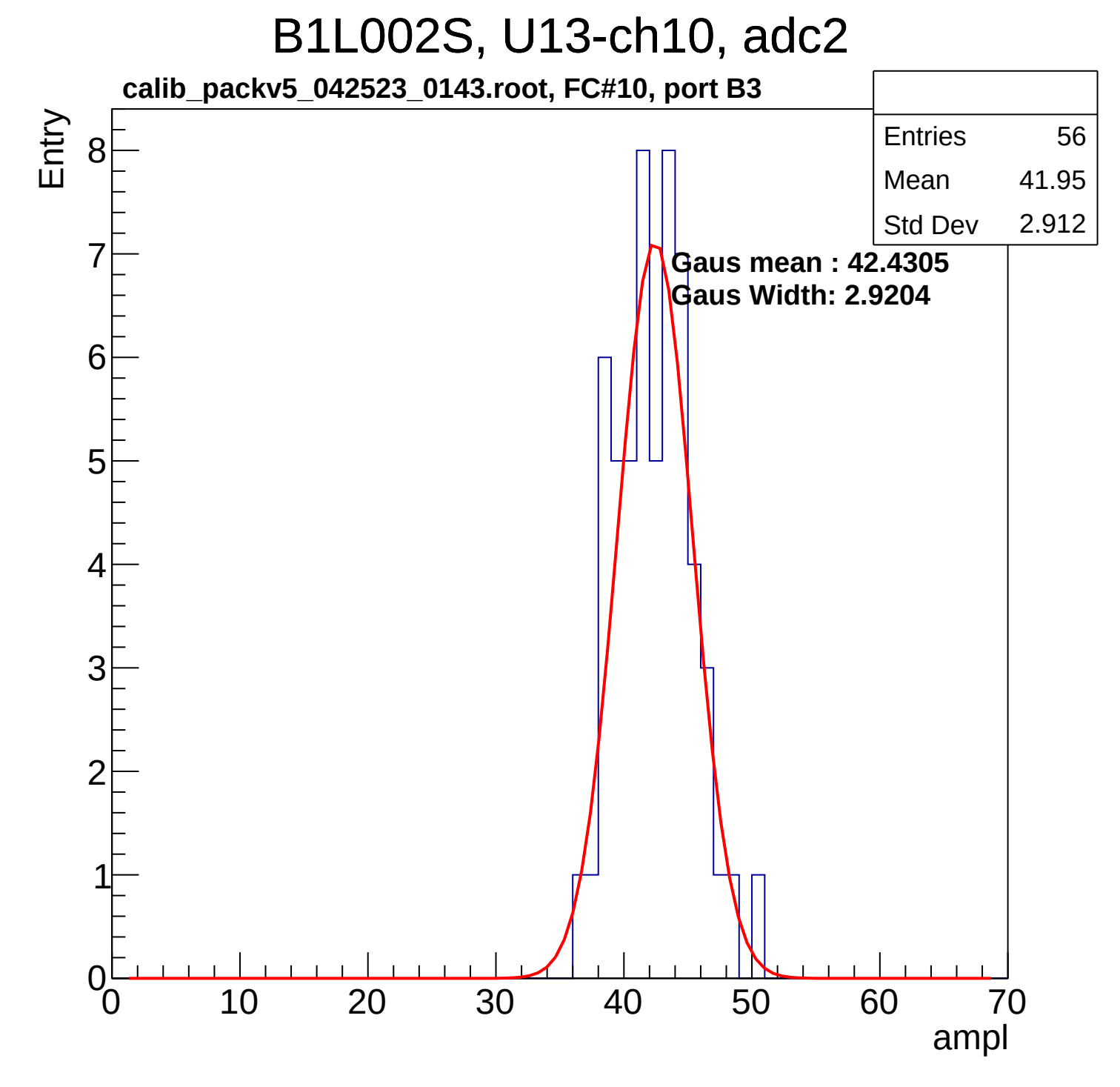
B1L002S, U13-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.95
Std Dev 2.912
Gaus mean : 42.4305
Gaus Width: 2.9204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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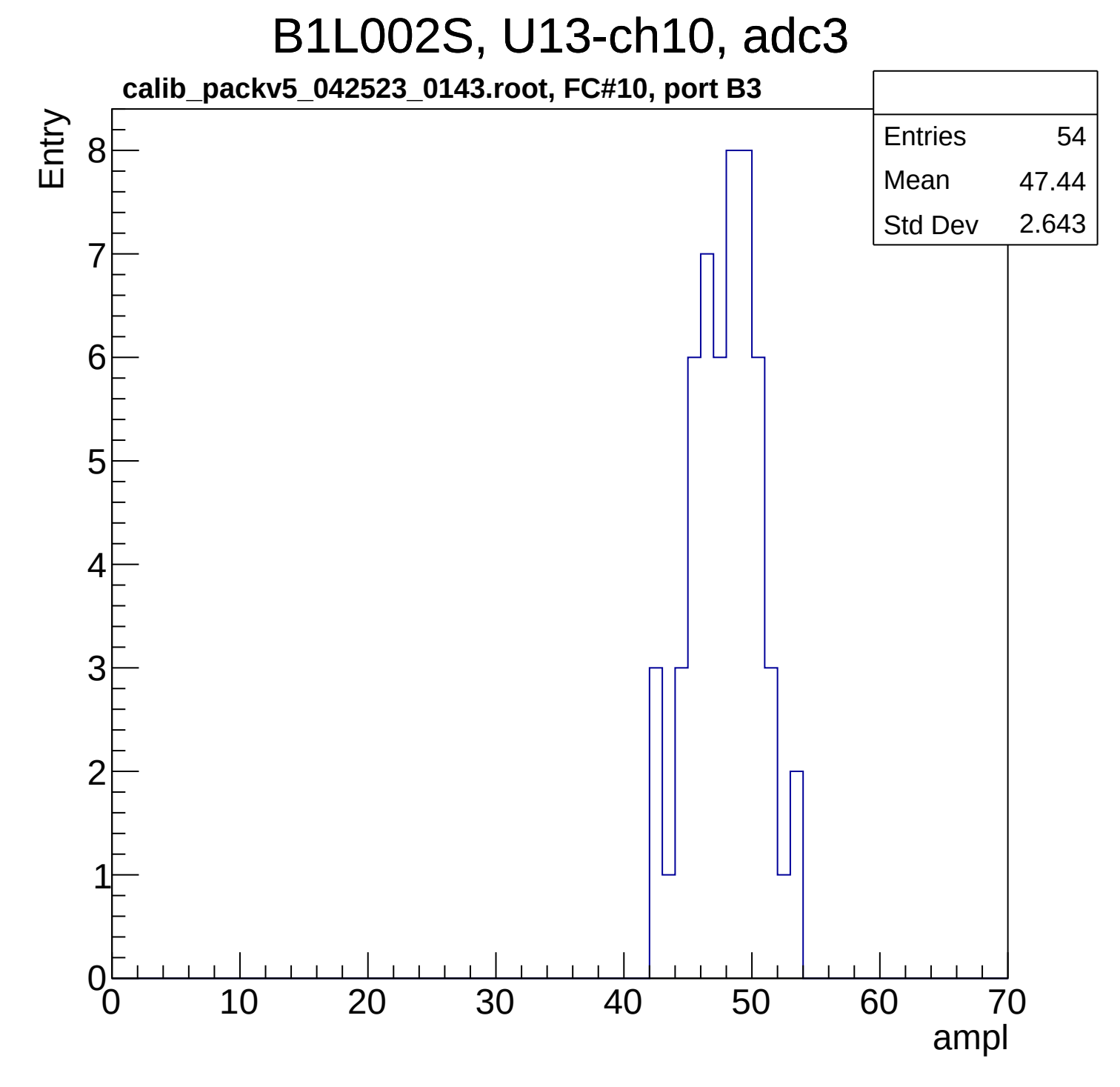
B1L002S, U13-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.44
Std Dev 2.643
Entry
ampl
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7
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0
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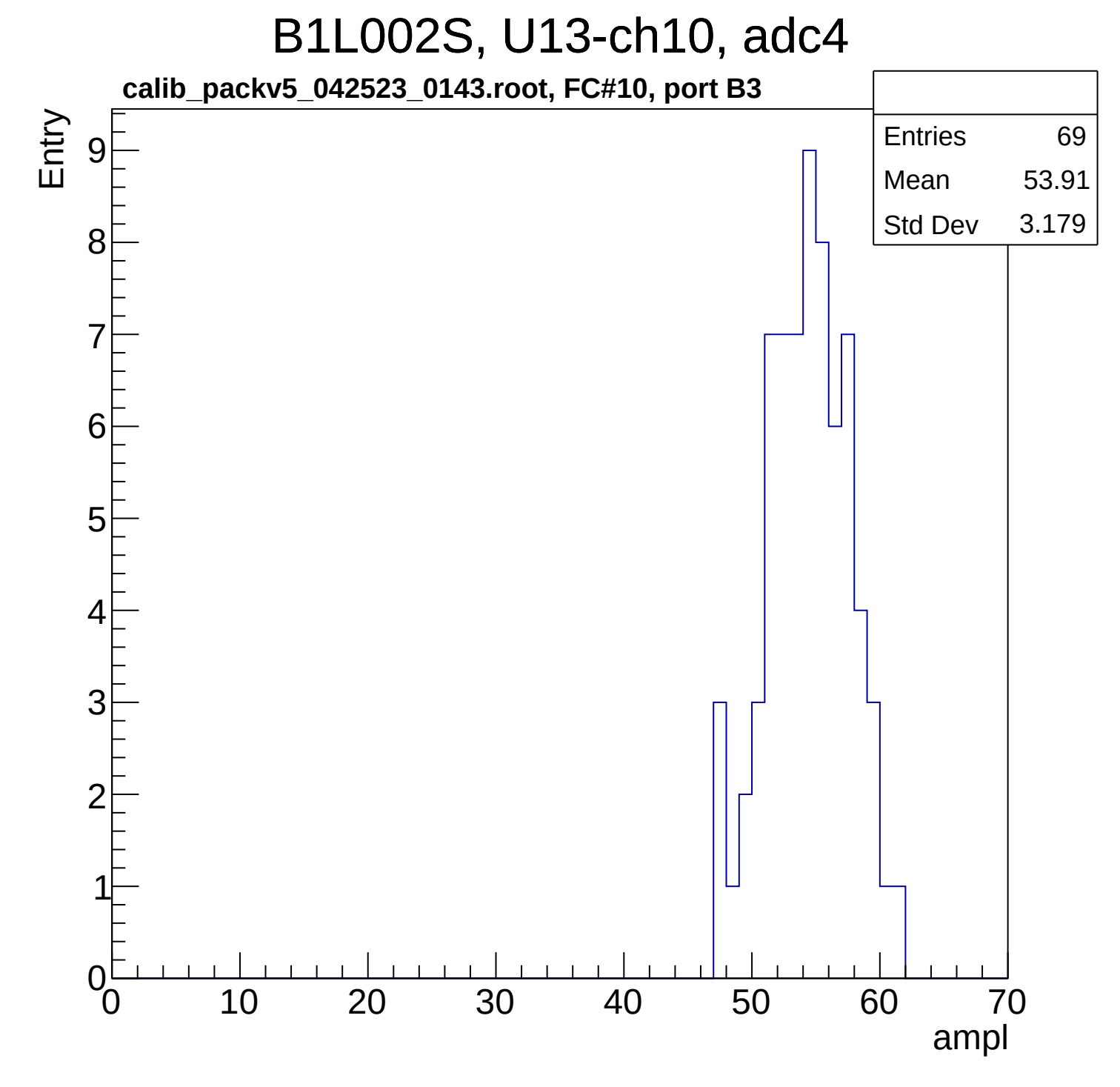
B1L002S, U13-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.91
Std Dev 3.179
Entry
ampl
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3
4
5
6
7
8
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0
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60
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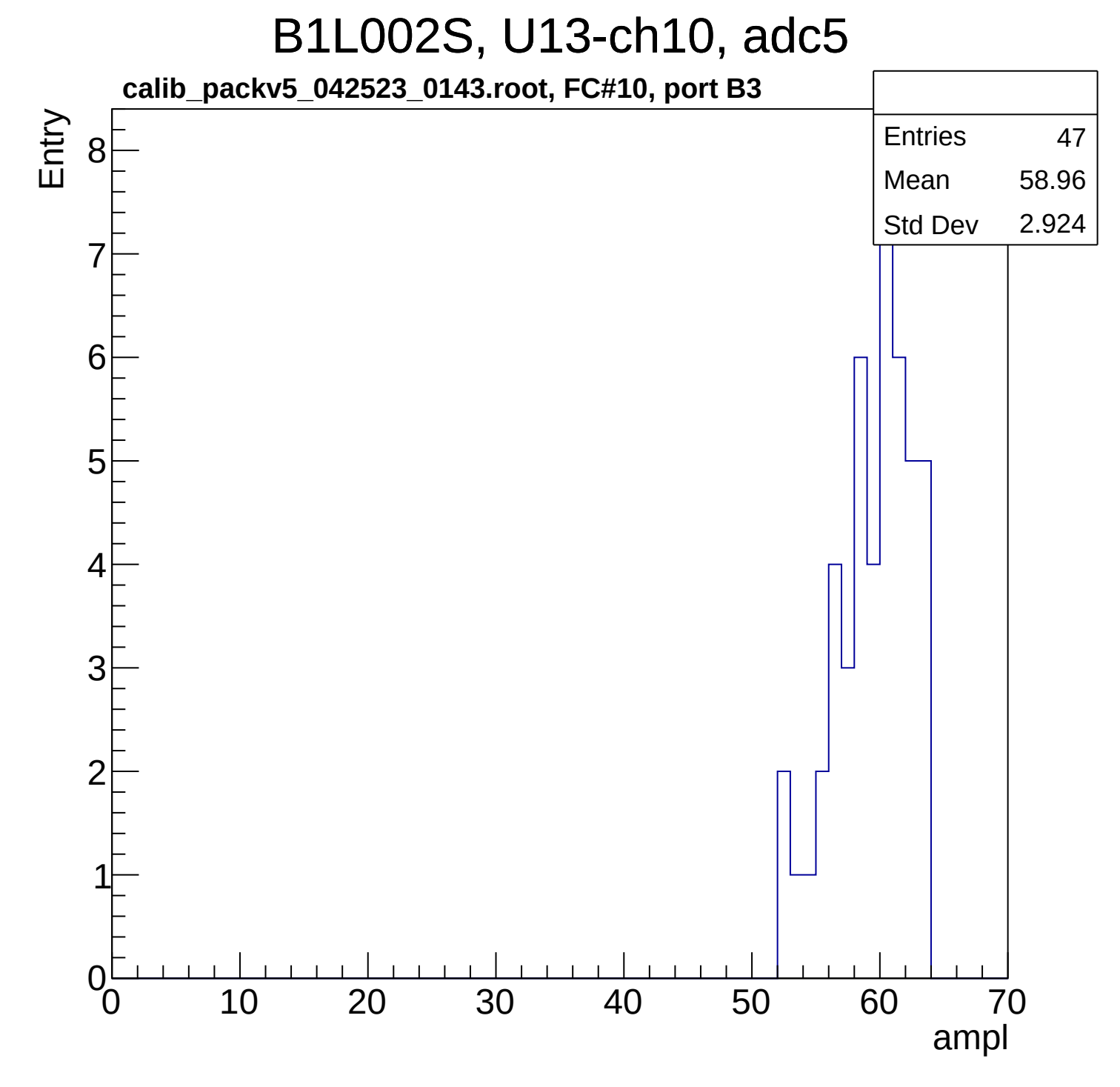
B1L002S, U13-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.96
Std Dev 2.924
Entry
ampl
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2
3
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5
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7
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0
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20
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60
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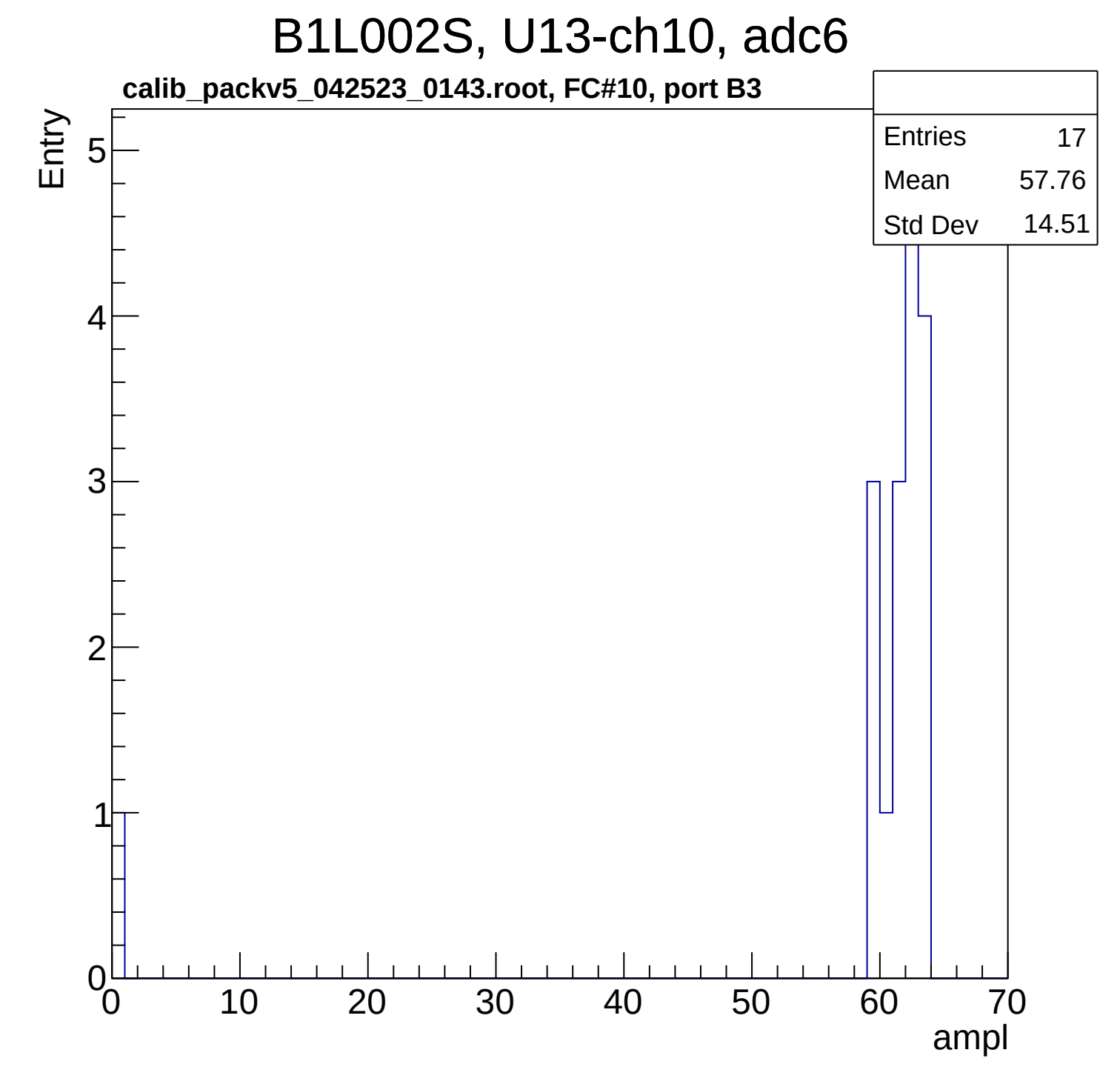
B1L002S, U13-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.76
Std Dev 14.51
Entry
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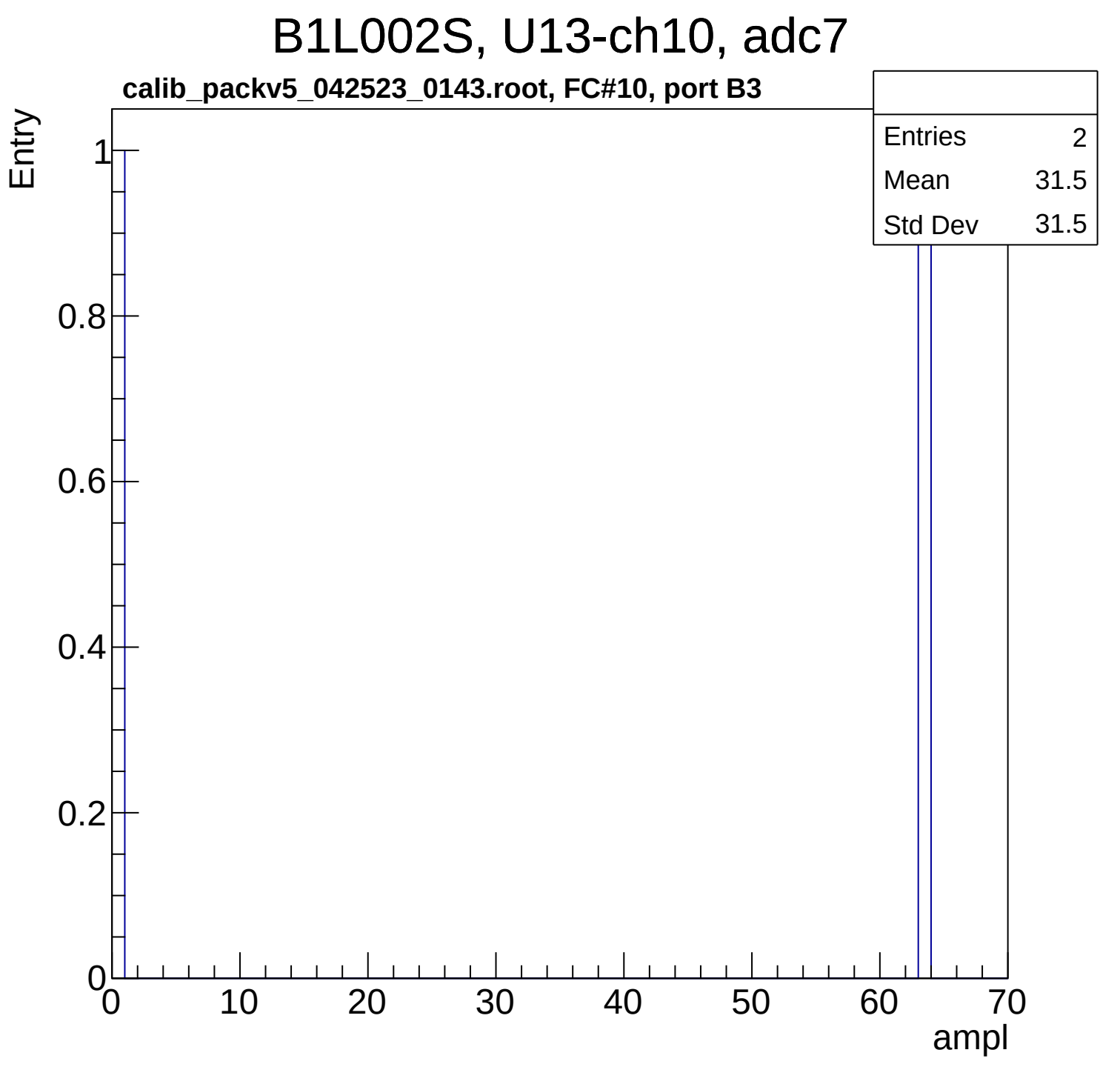
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
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0.2
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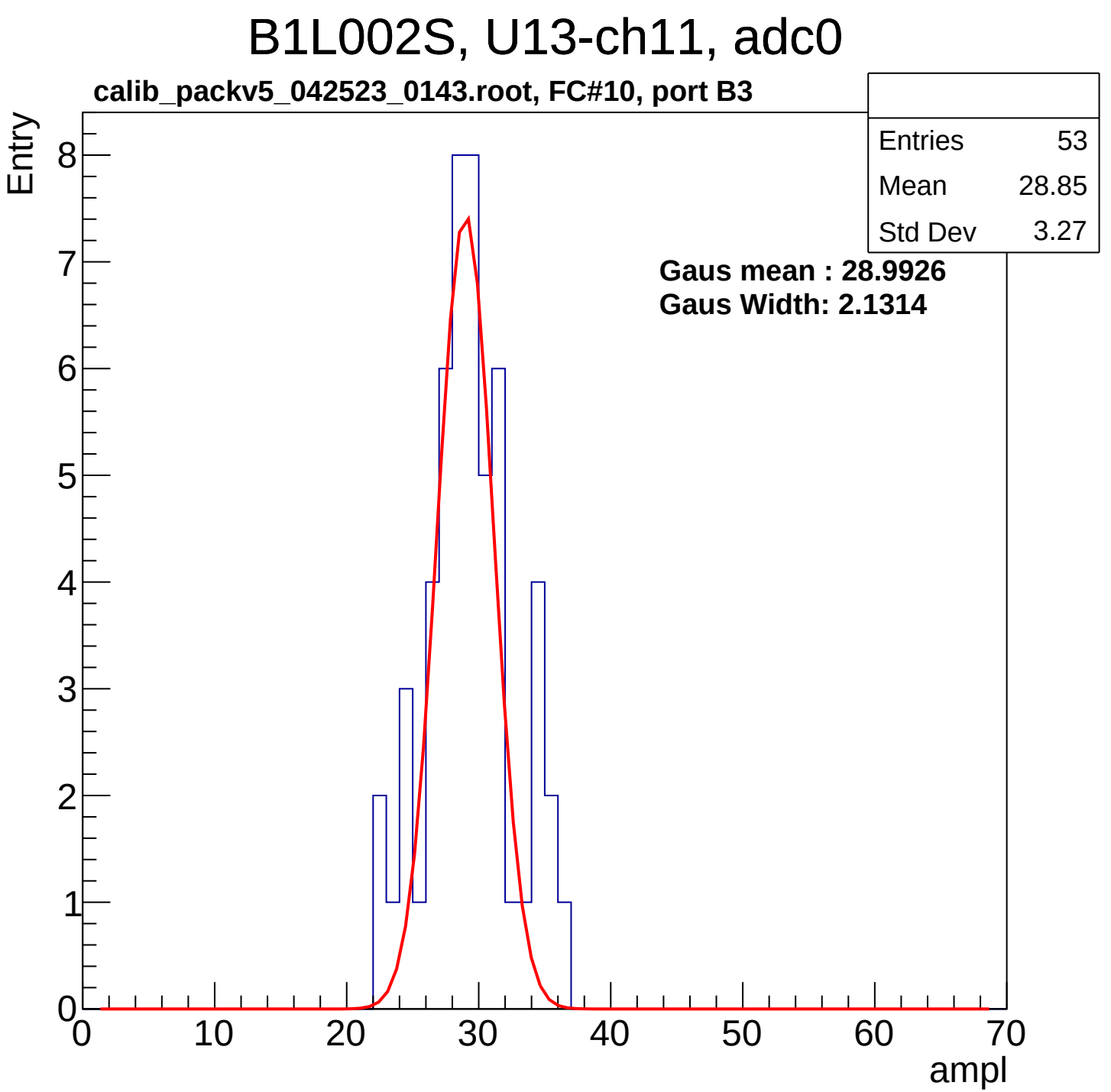
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 28.85
Std Dev 3.27
Gaus mean : 28.9926
Gaus Width: 2.1314
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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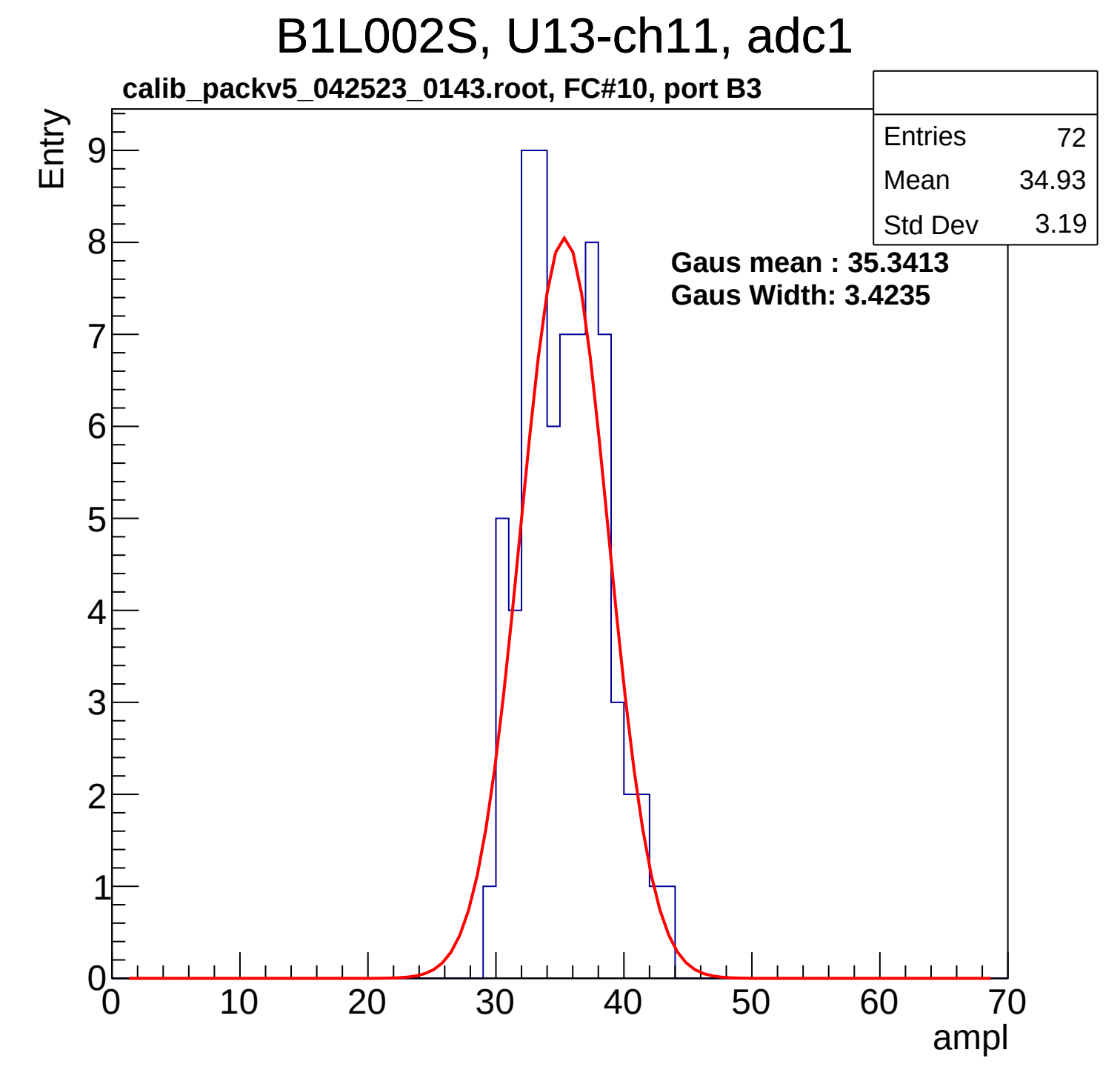
B1L002S, U13-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.93
Std Dev 3.19
Gaus mean : 35.3413
Gaus Width: 3.4235
Entry
ampl
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2
3
4
5
6
7
8
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0
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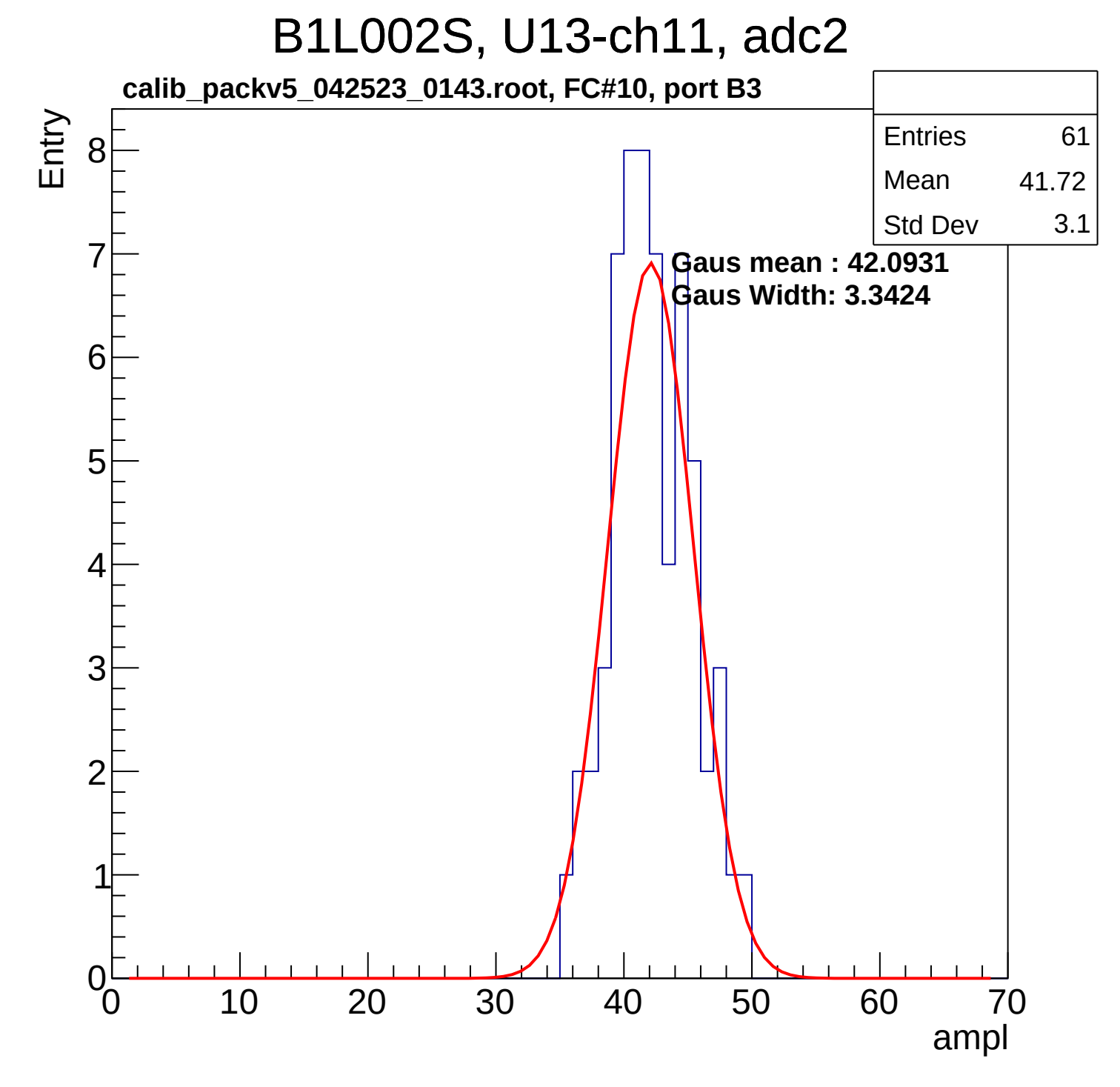
B1L002S, U13-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.72
Std Dev 3.1
Gaus mean : 42.0931
Gaus Width: 3.3424
Entry
ampl
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1
2
3
4
5
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7
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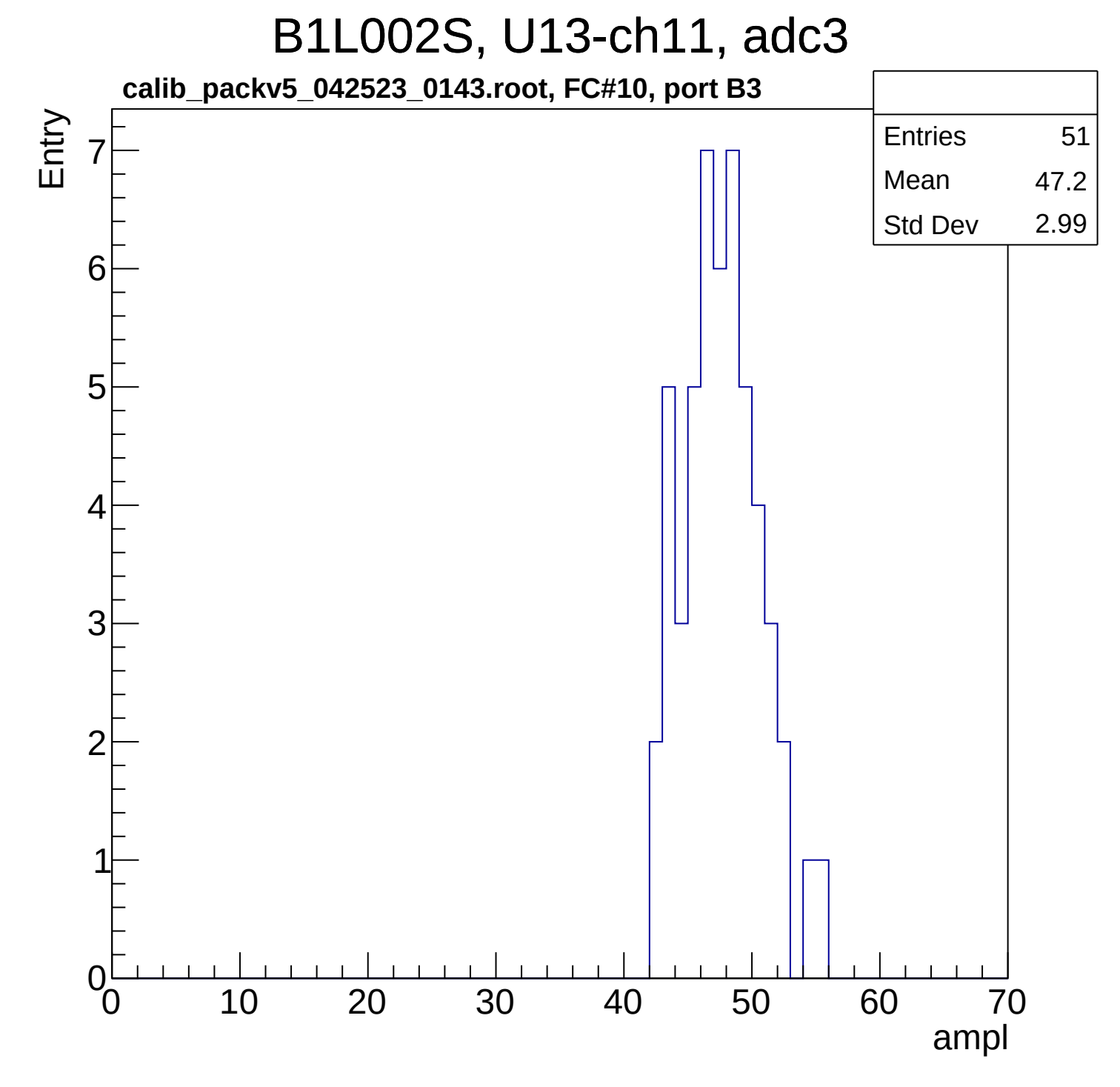
B1L002S, U13-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.2
Std Dev 2.99
Entry
ampl
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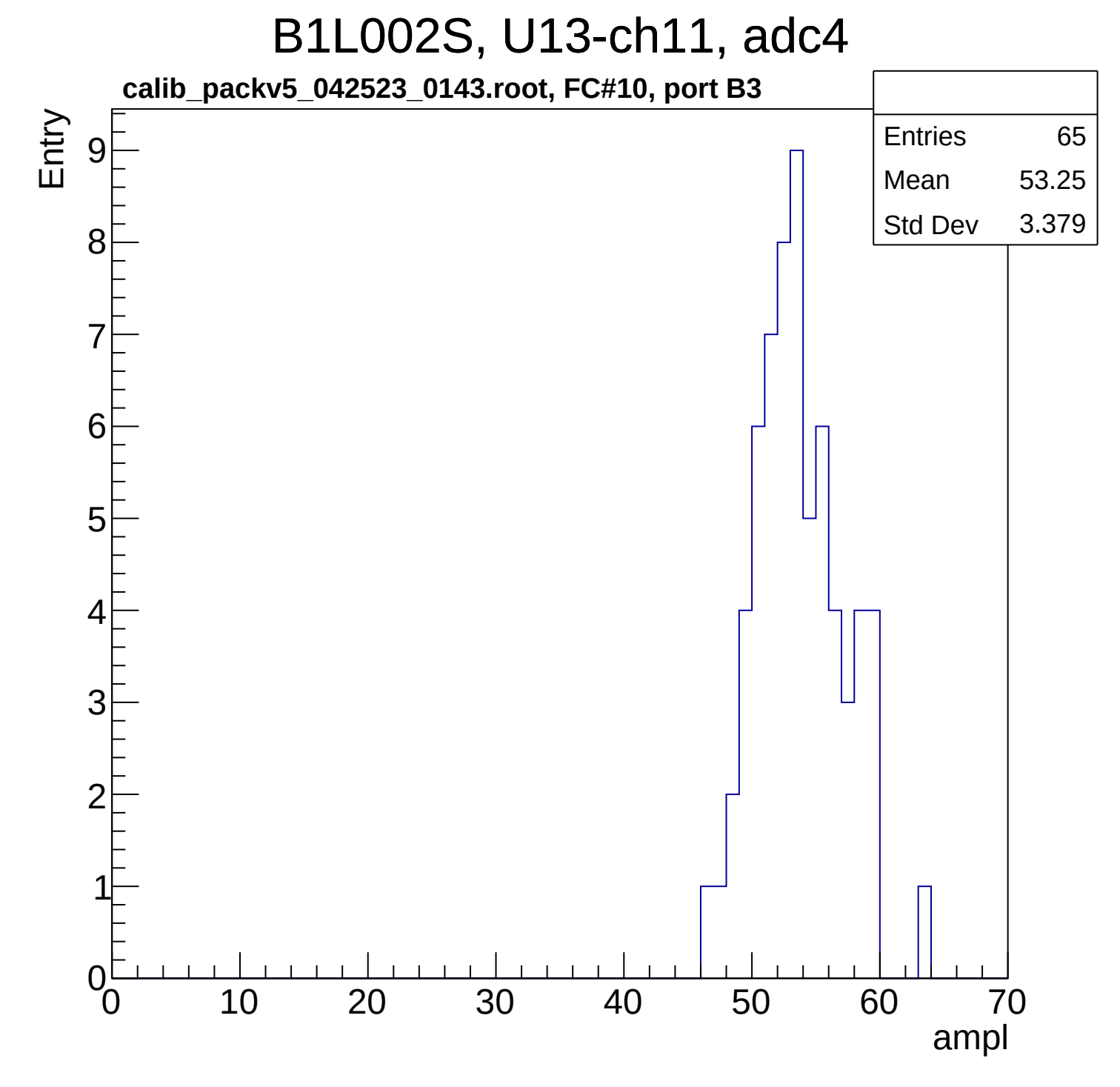
B1L002S, U13-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.25
Std Dev 3.379
Entry
ampl
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60
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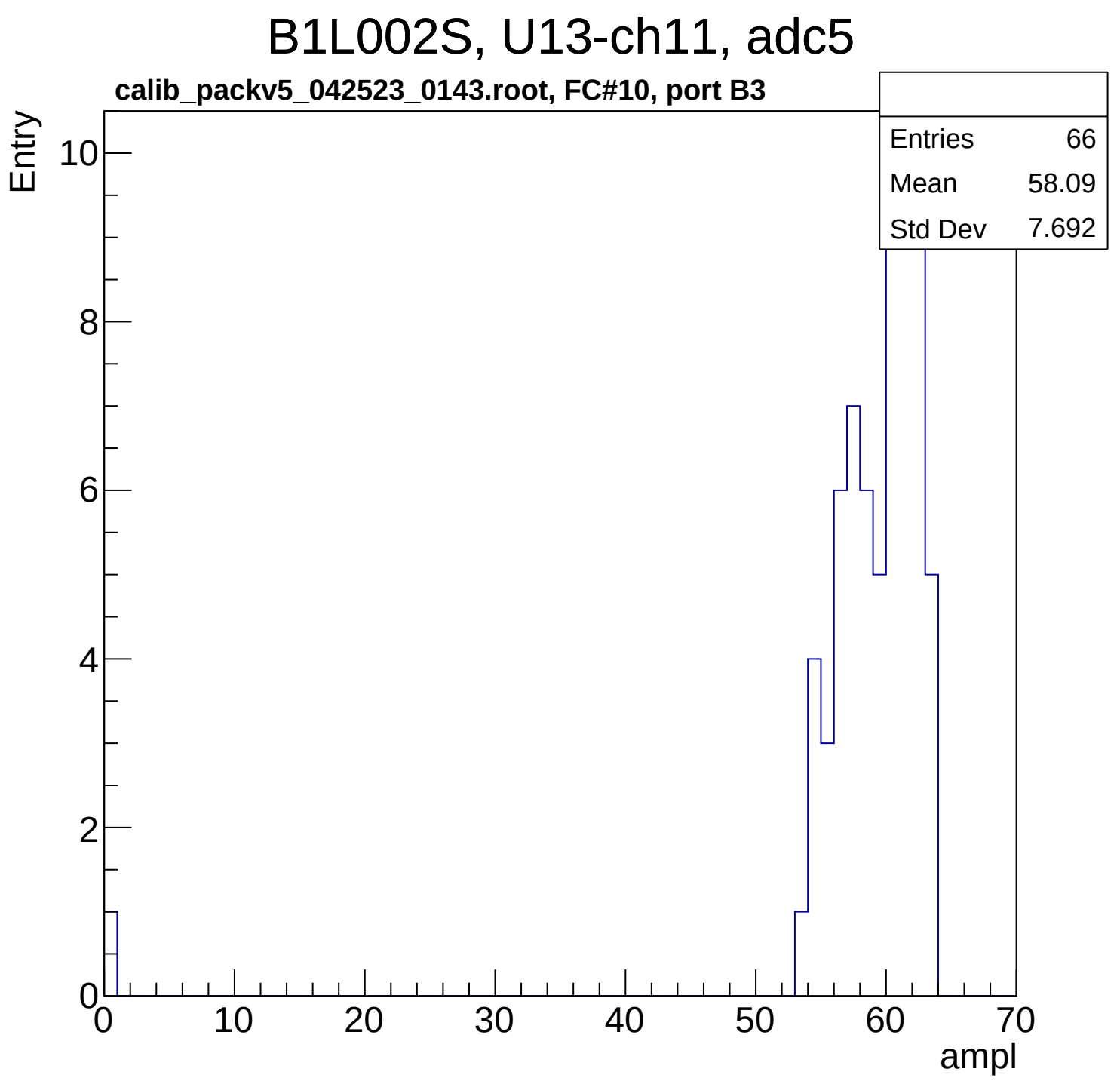
B1L002S, U13-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.09
Std Dev 7.692
Entry
0
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6
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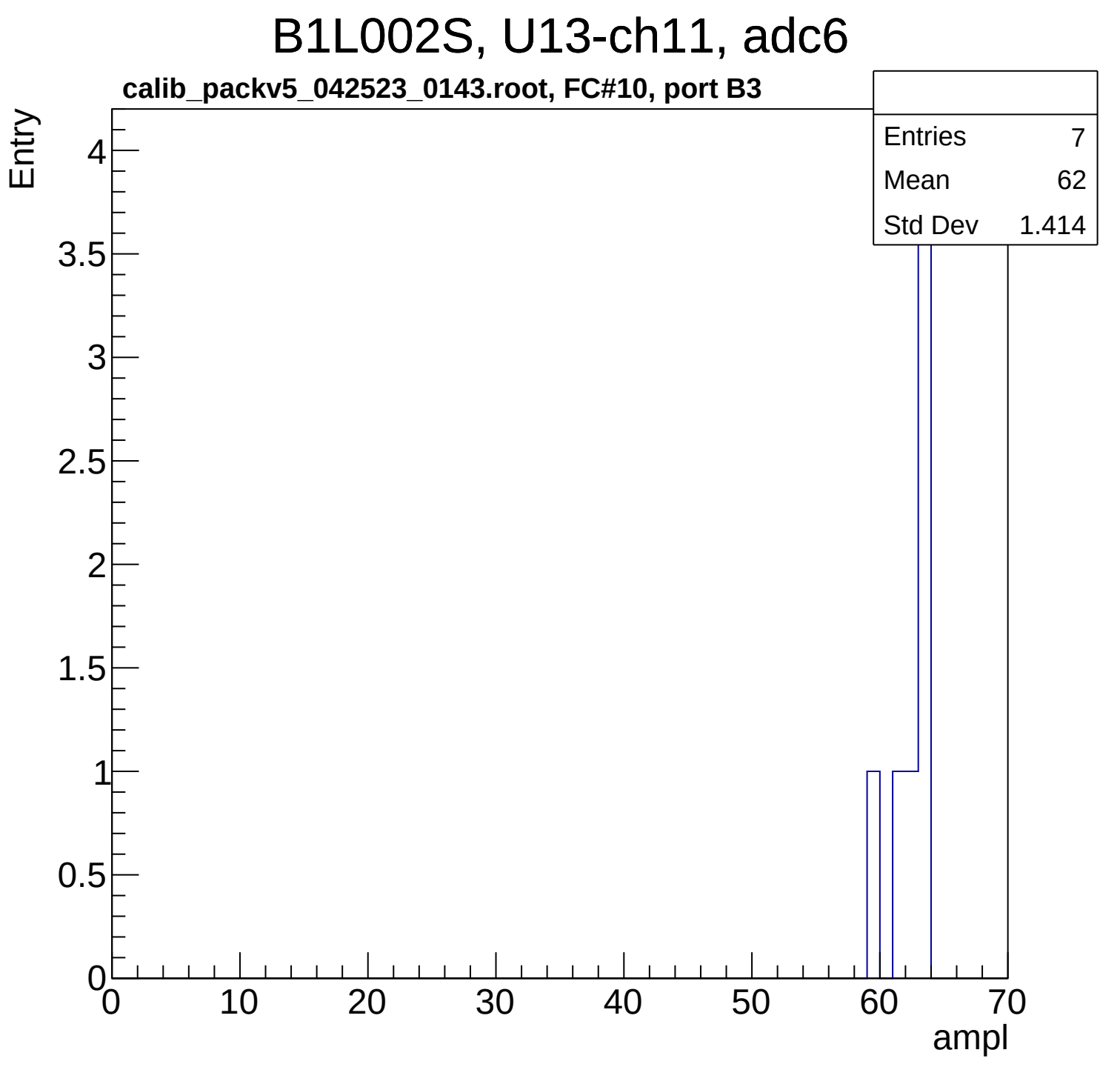
60
70
ampl


B1L002S, U13-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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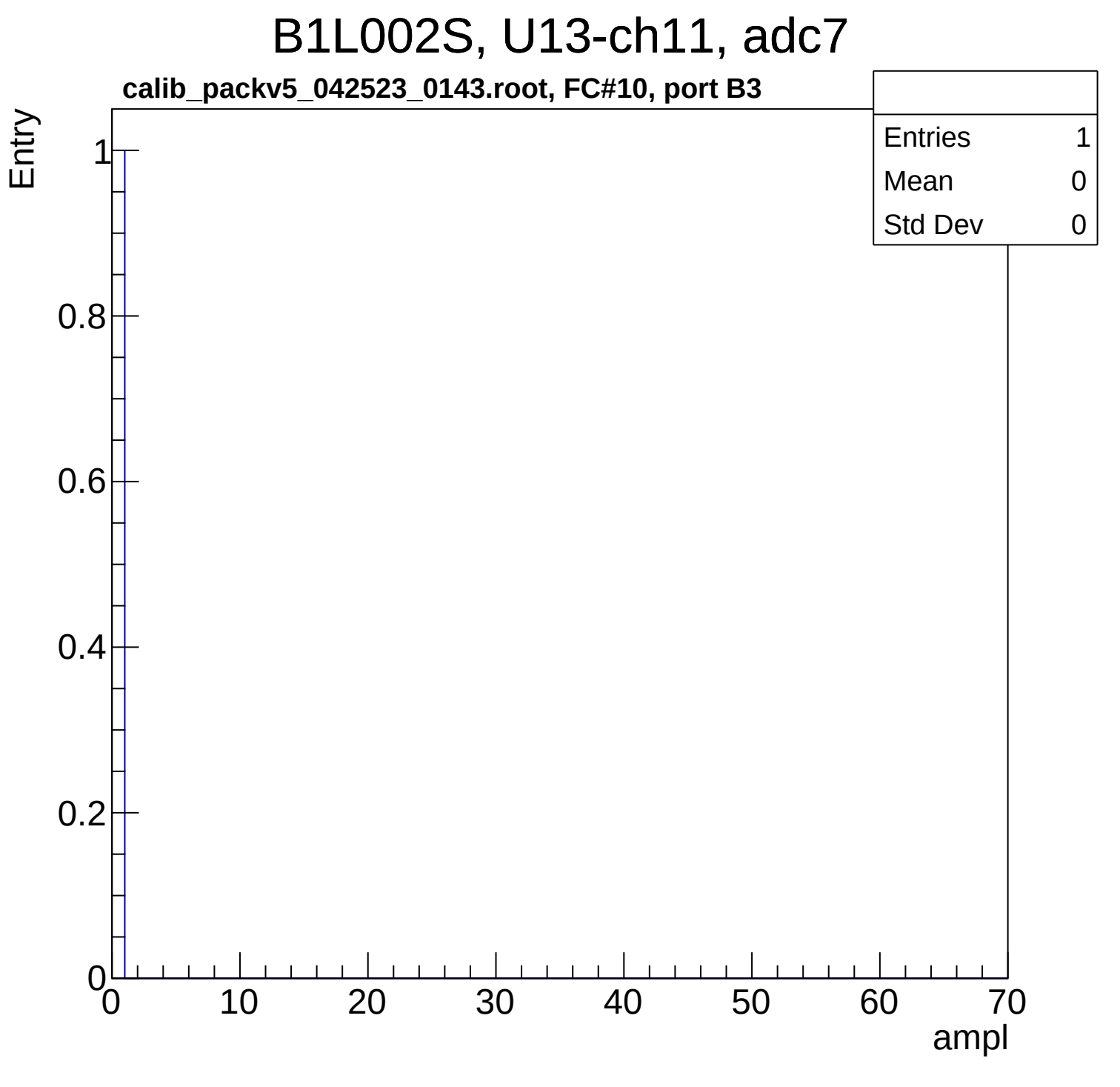
B1L002S, U13-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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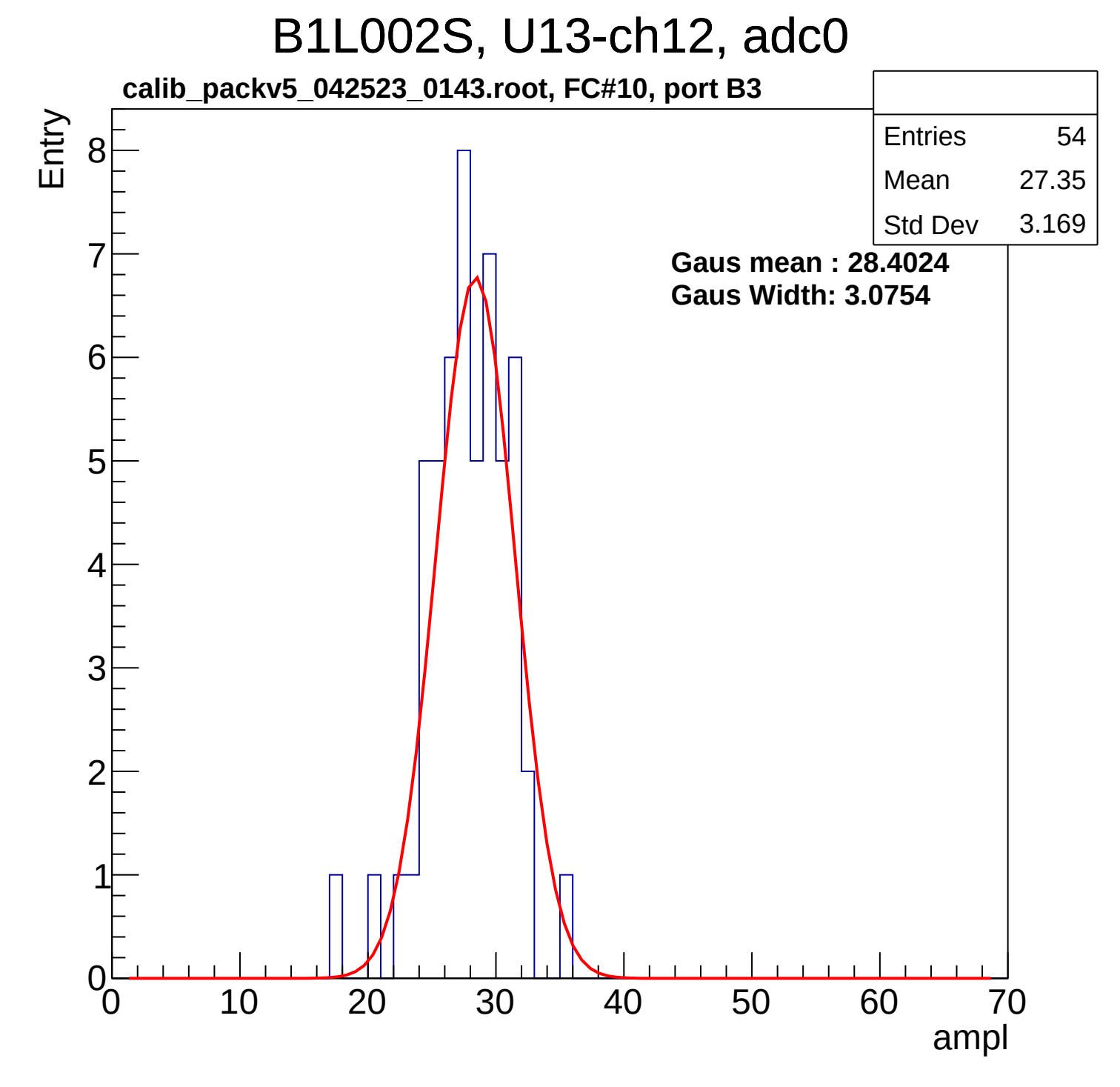
40
50
60
70
ampl


B1L002S, U13-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.35
Std Dev 3.169
Gaus mean : 28.4024
Gaus Width: 3.0754
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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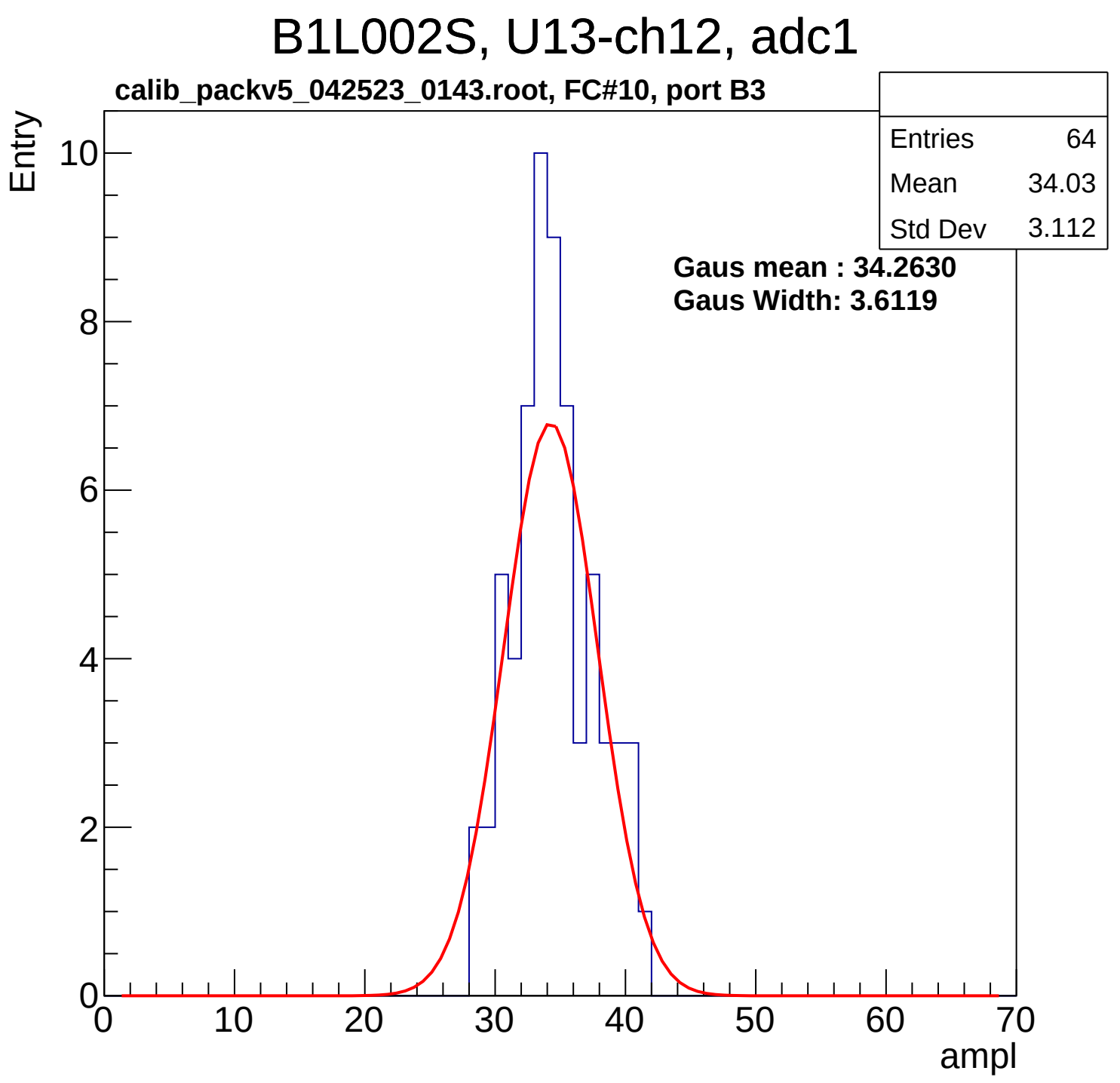
B1L002S, U13-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.03
Std Dev 3.112
Gaus mean : 34.2630
Gaus Width: 3.6119
Entry
ampl
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0
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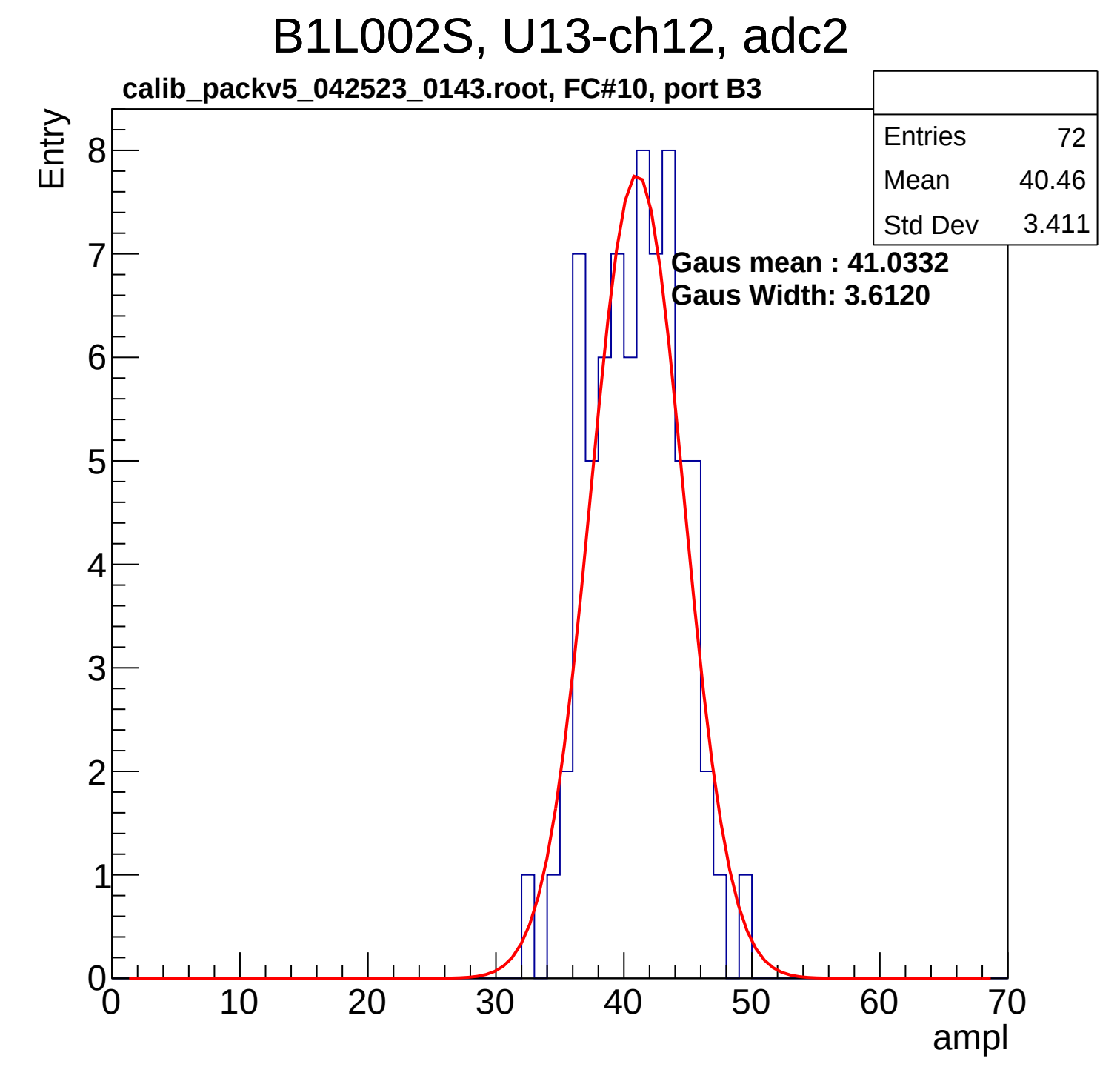
B1L002S, U13-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 40.46
Std Dev 3.411
Gaus mean : 41.0332
Gaus Width: 3.6120
Entry
ampl
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1
2
3
4
5
6
7
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0
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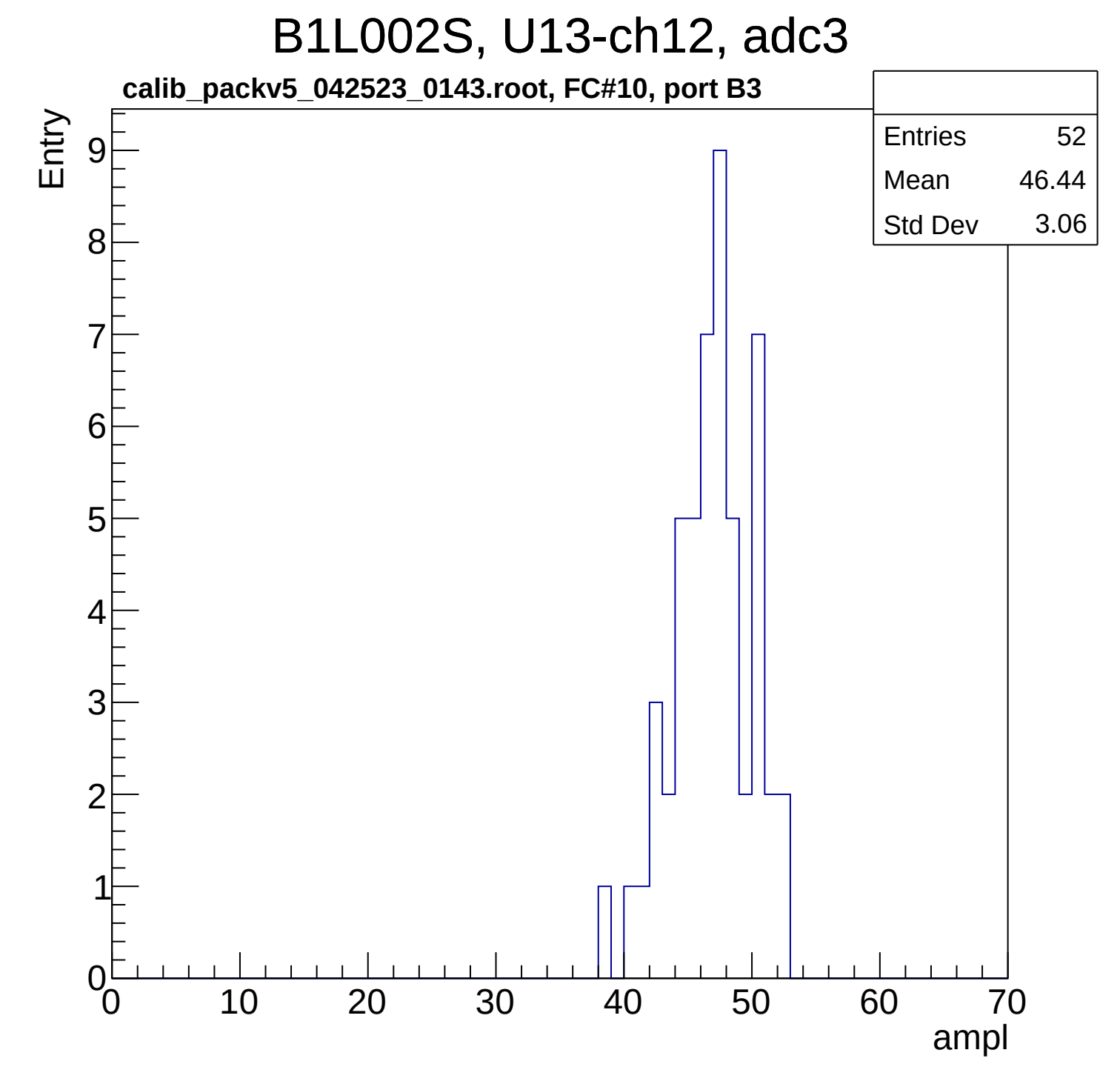
B1L002S, U13-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 46.44
Std Dev 3.06
Entry
ampl
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2
3
4
5
6
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0
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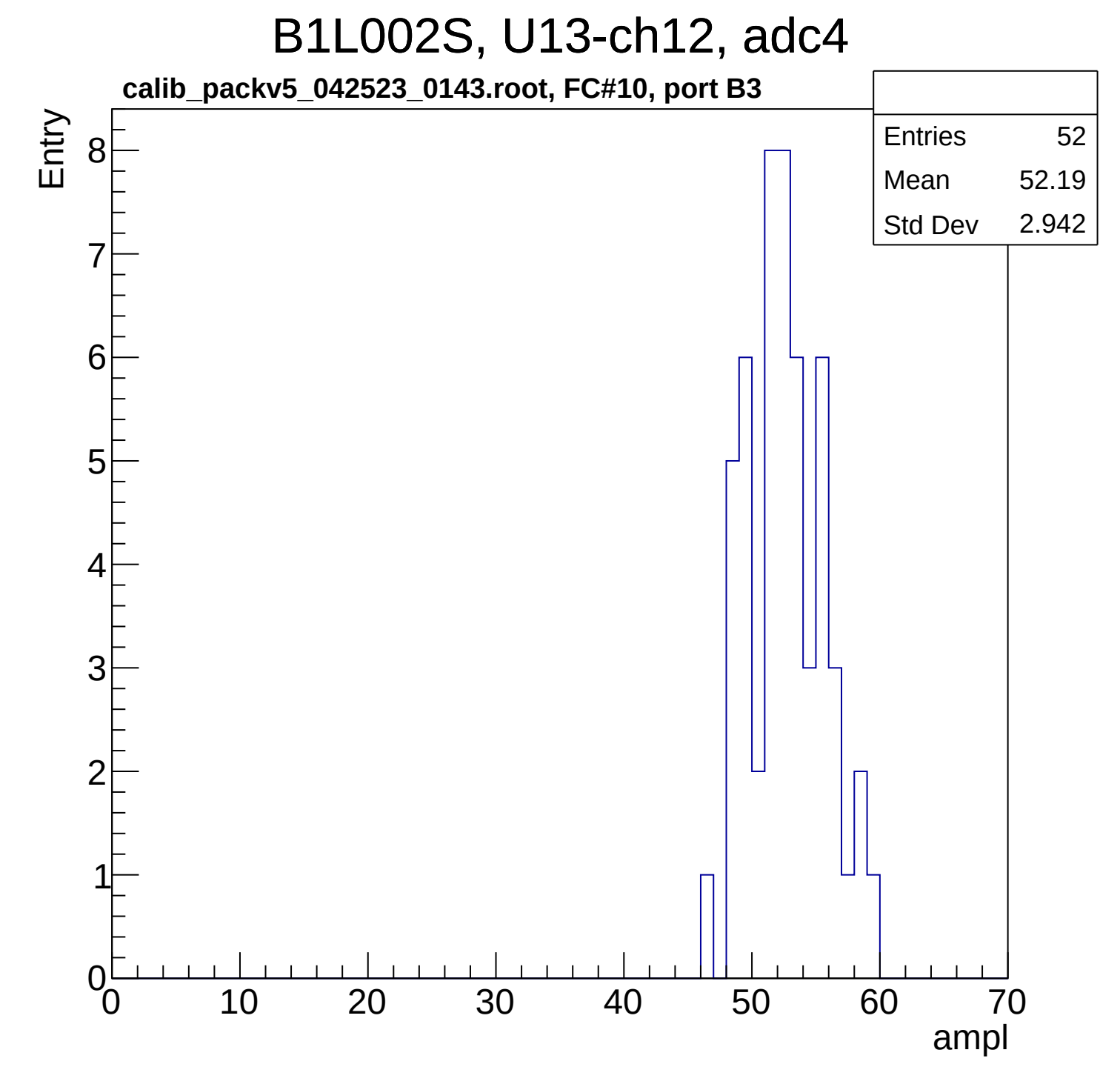
B1L002S, U13-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.19
Std Dev 2.942
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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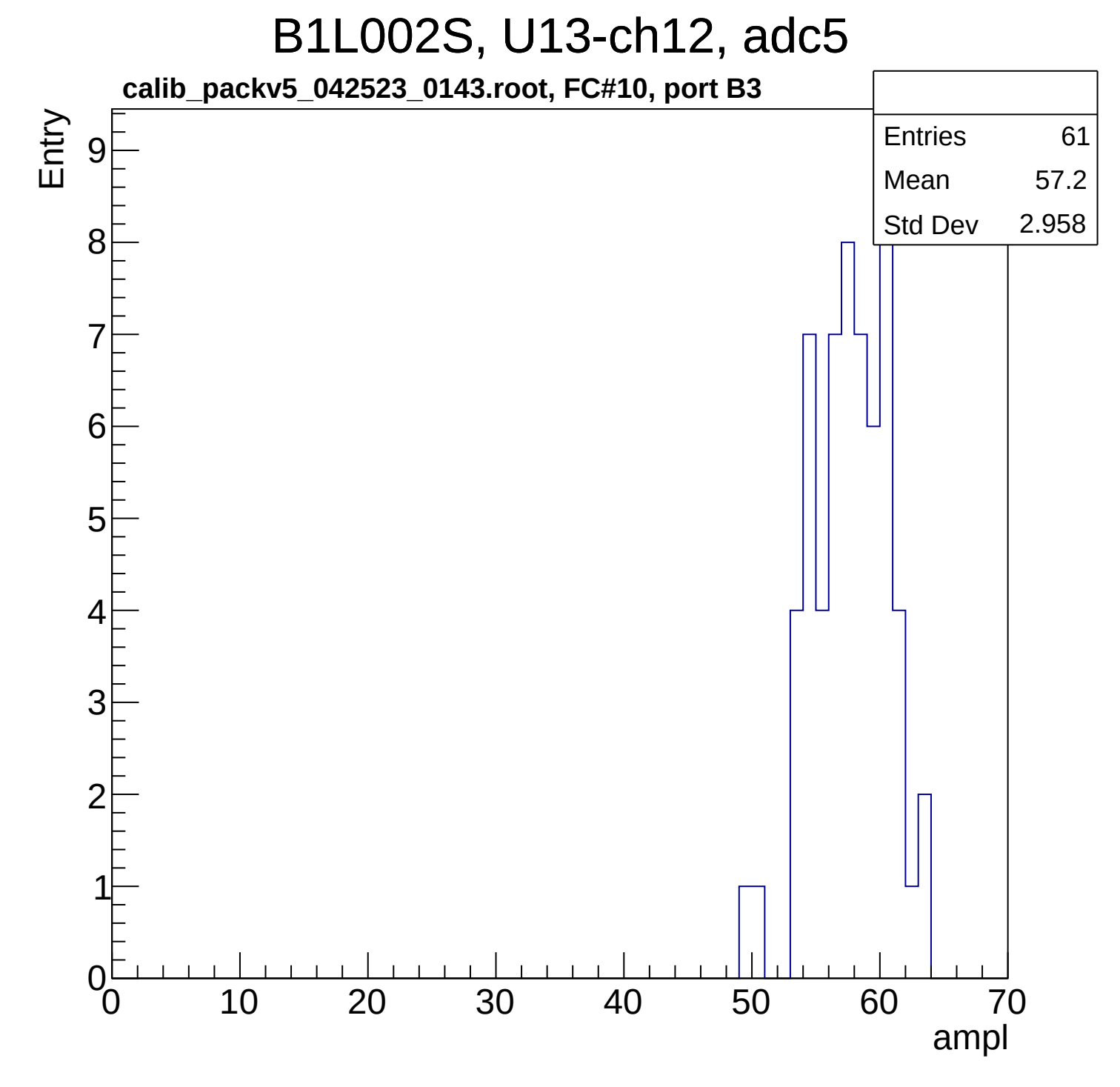
B1L002S, U13-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.2
Std Dev 2.958
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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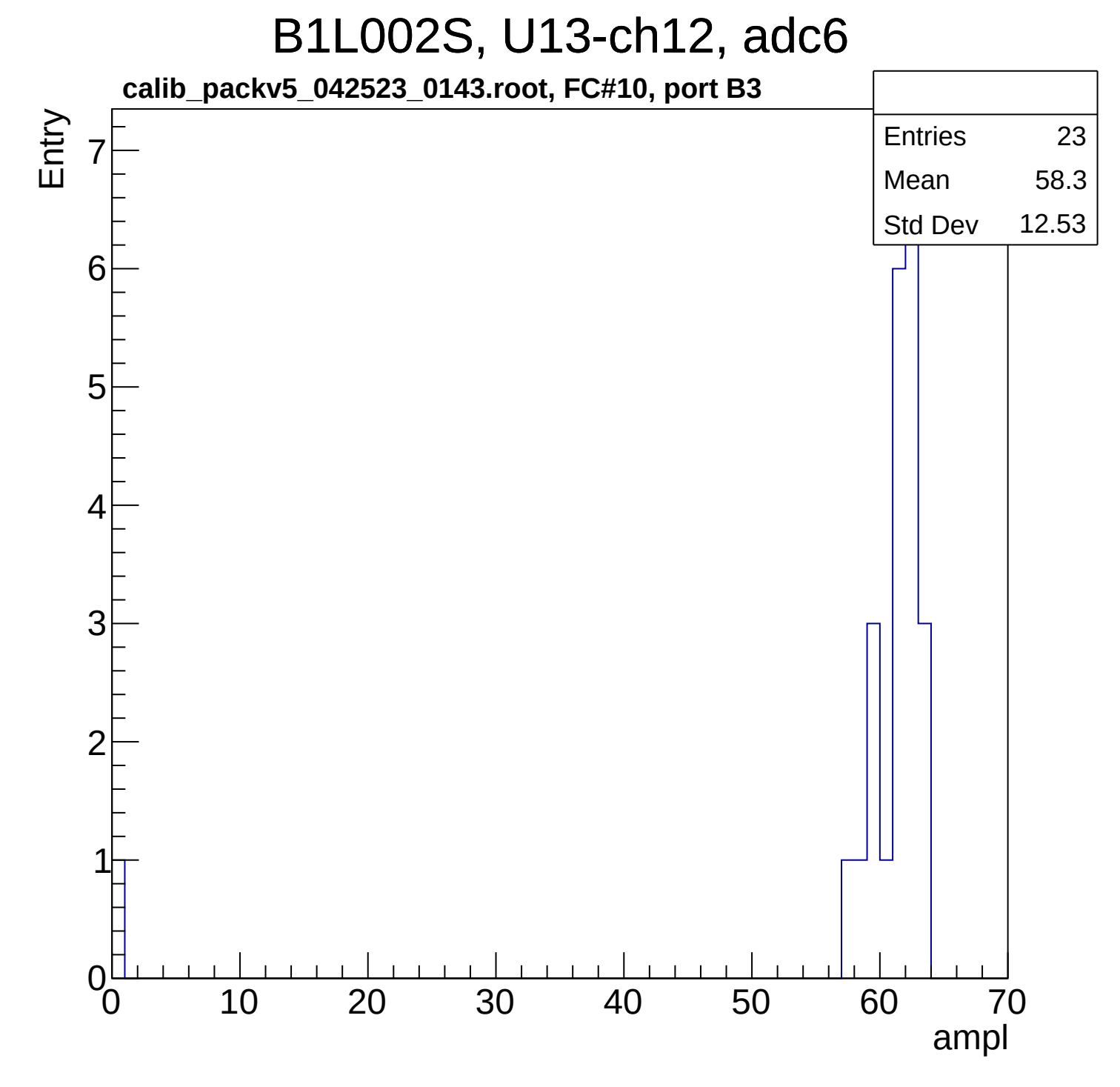
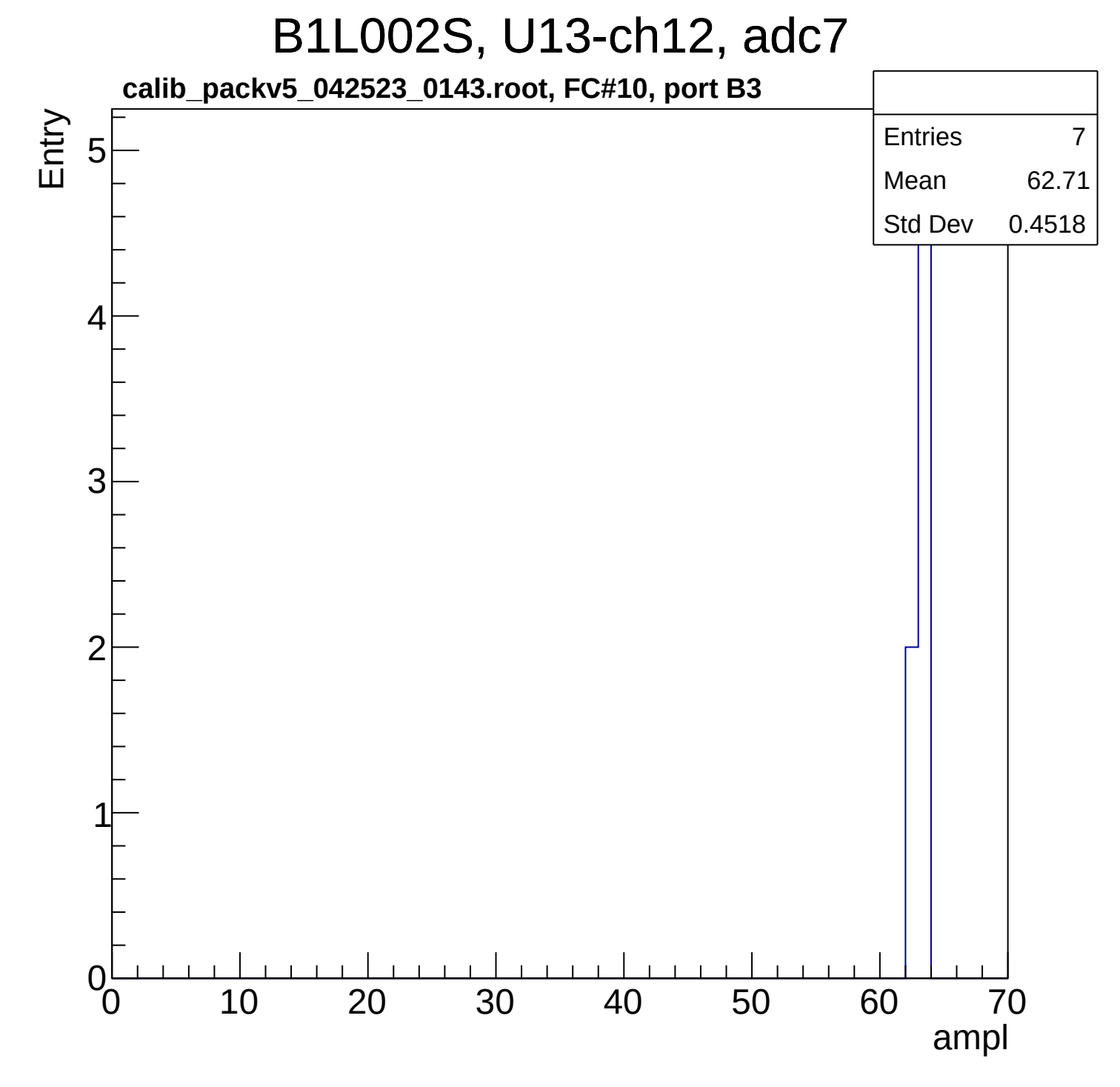

B1L002S, U13-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.3
Std Dev 12.53
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
ampl
0
1
2
3
4
5
0
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20
30
40
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60
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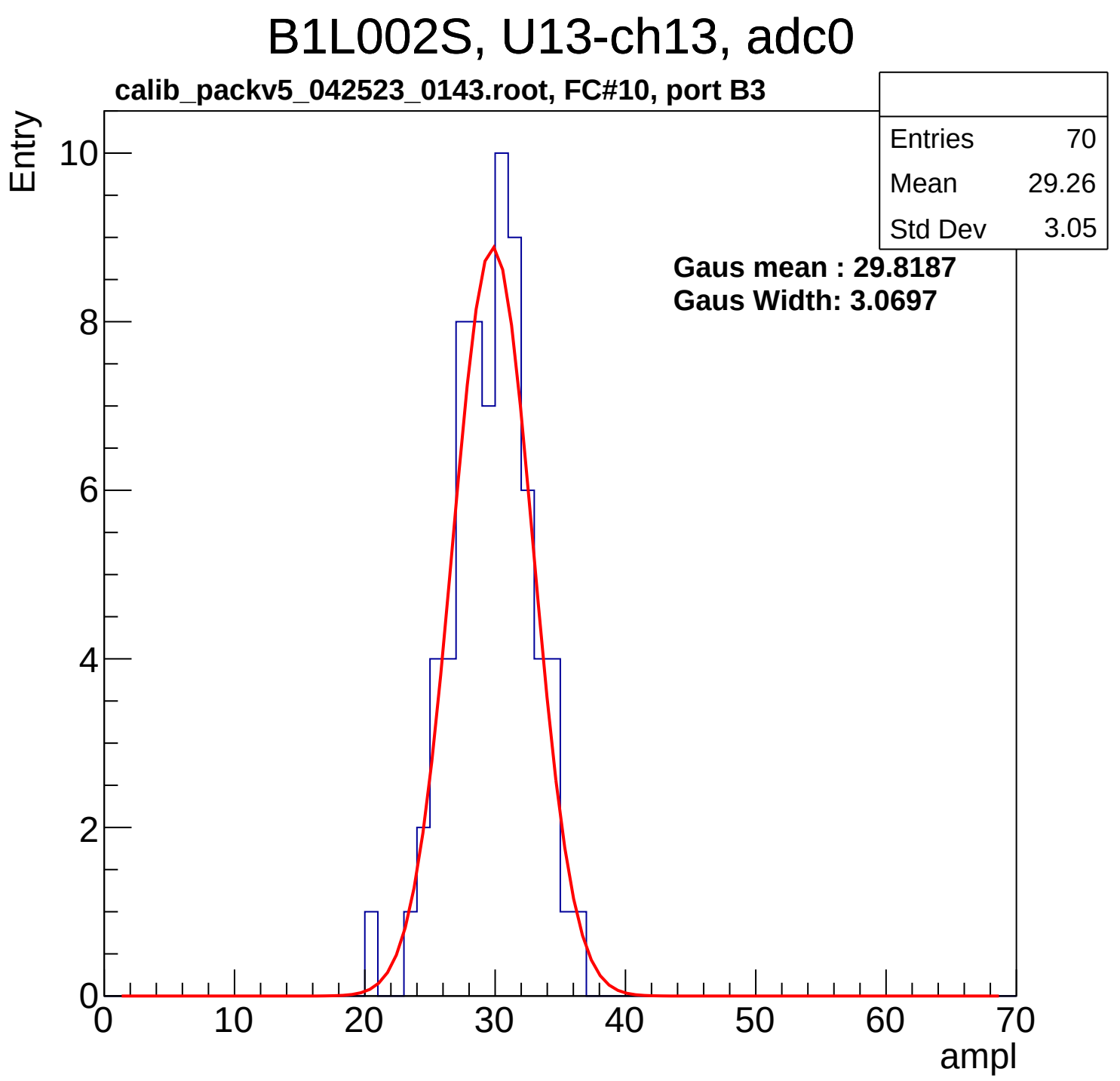
B1L002S, U13-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.26
Std Dev 3.05
Gaus mean : 29.8187
Gaus Width: 3.0697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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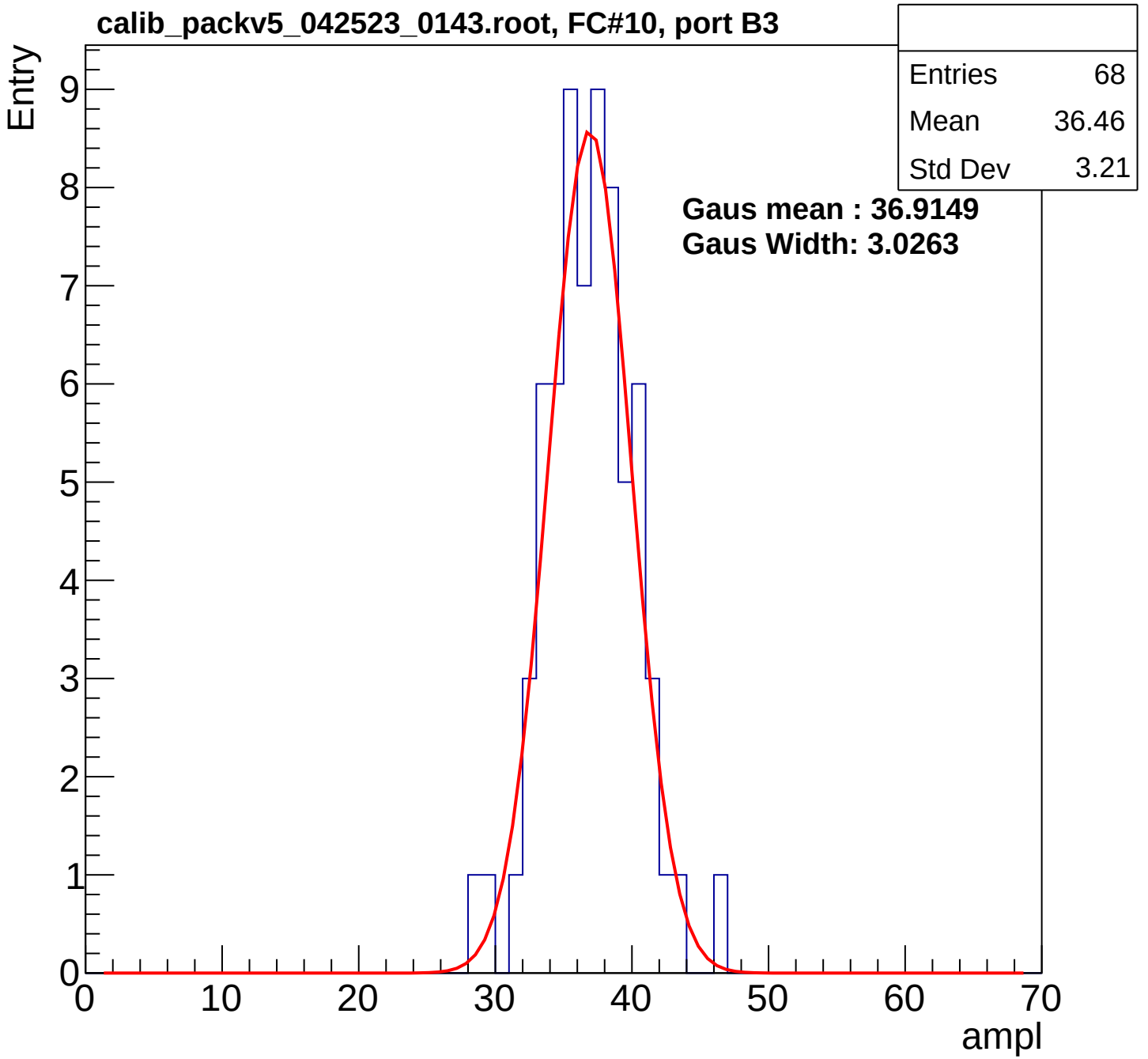
B1L002S, U13-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.46
Std Dev 3.21
Gaus mean : 36.9149
Gaus Width: 3.0263
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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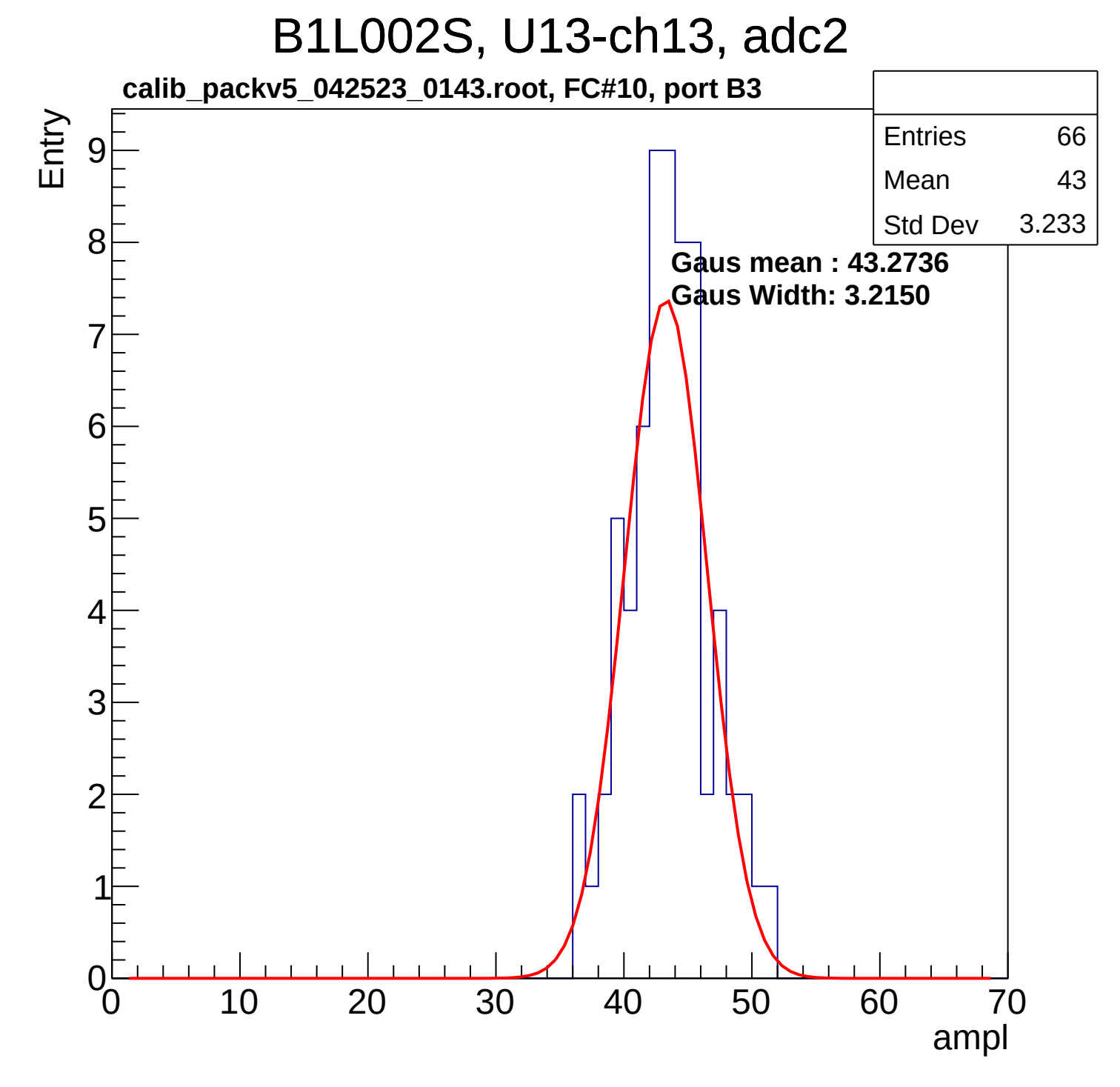
B1L002S, U13-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 43
Std Dev 3.233
Gaus mean : 43.2736
Gaus Width: 3.2150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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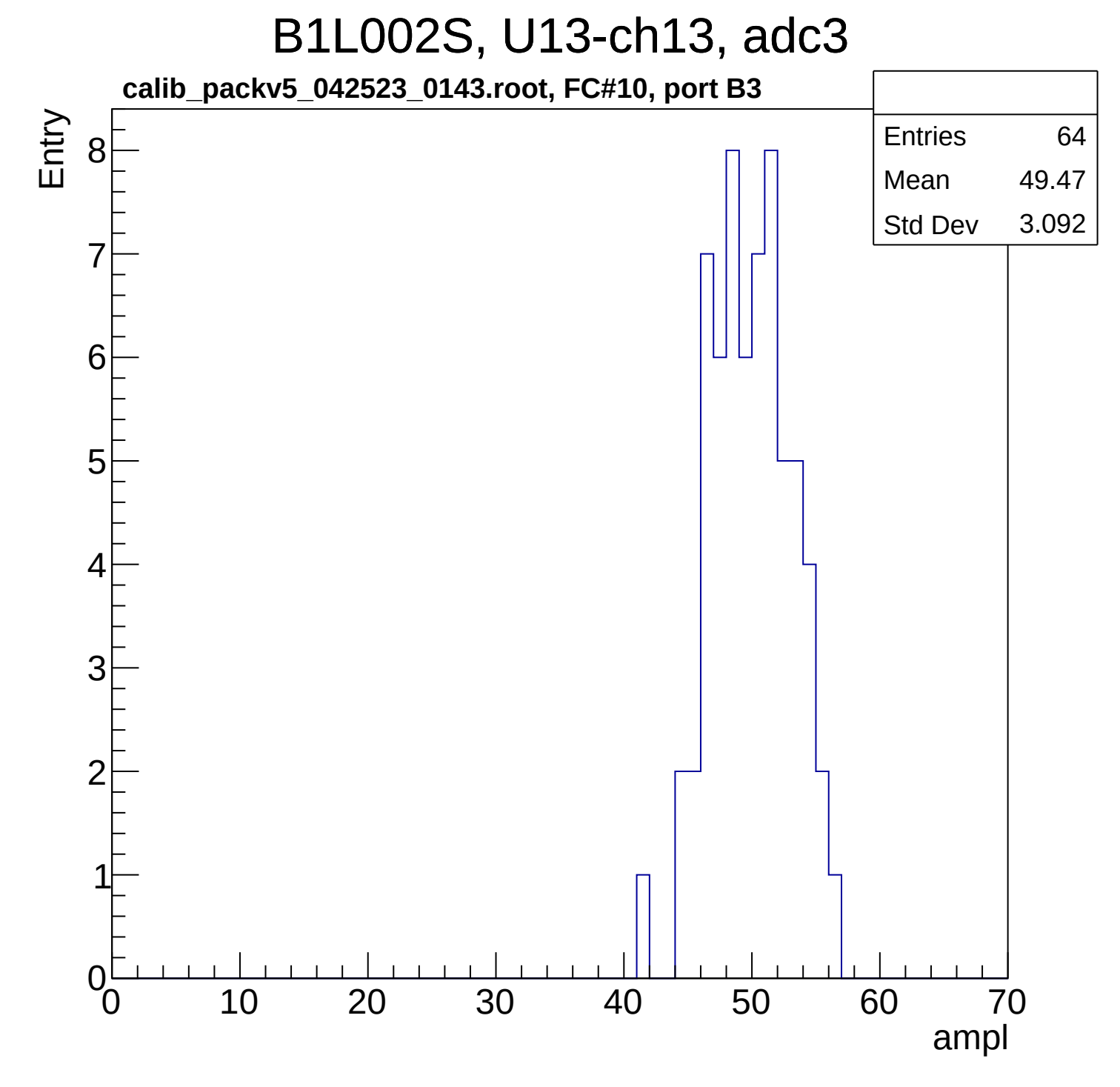
B1L002S, U13-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.47
Std Dev 3.092
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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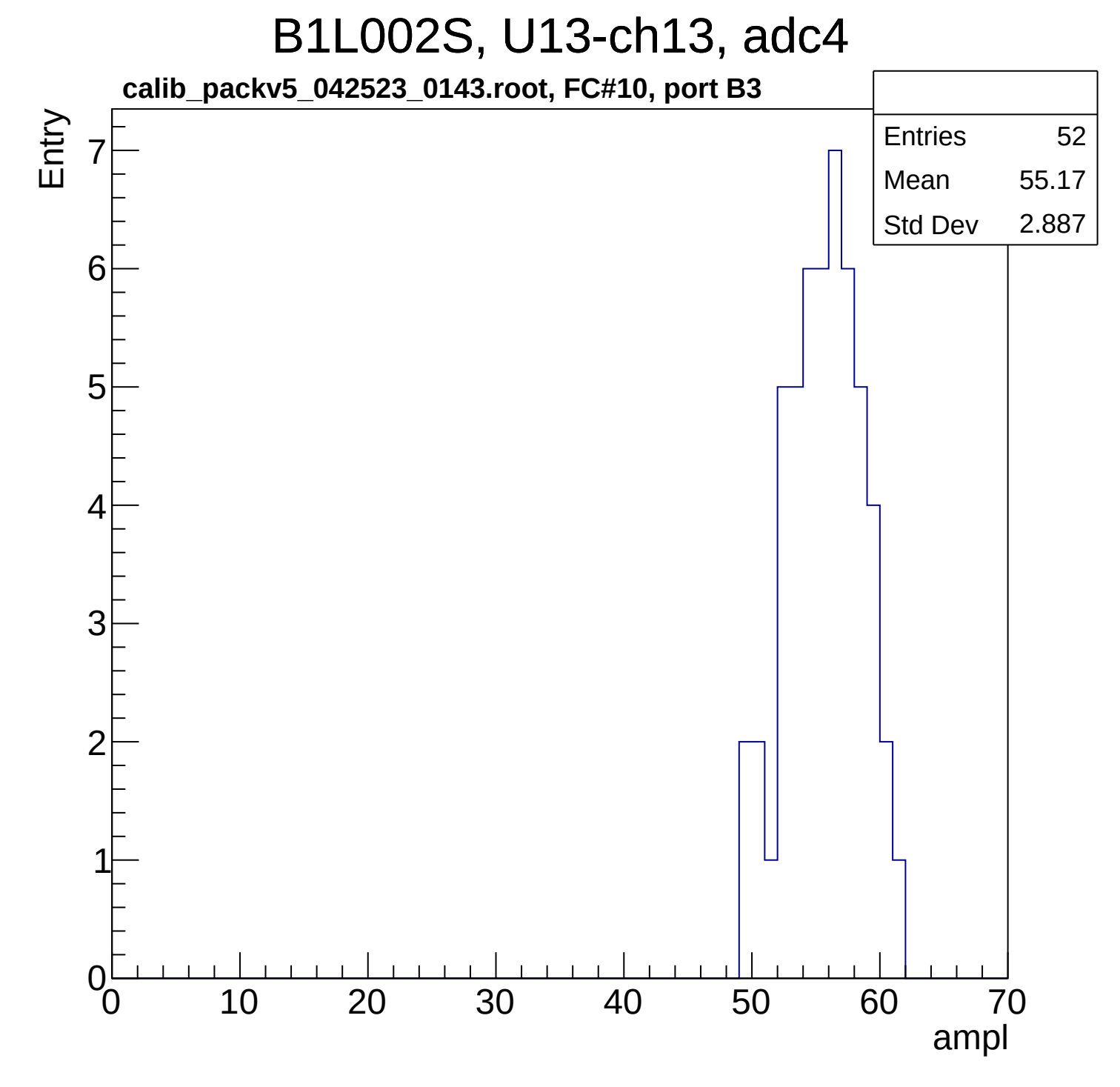
B1L002S, U13-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 55.17
Std Dev 2.887
Entry
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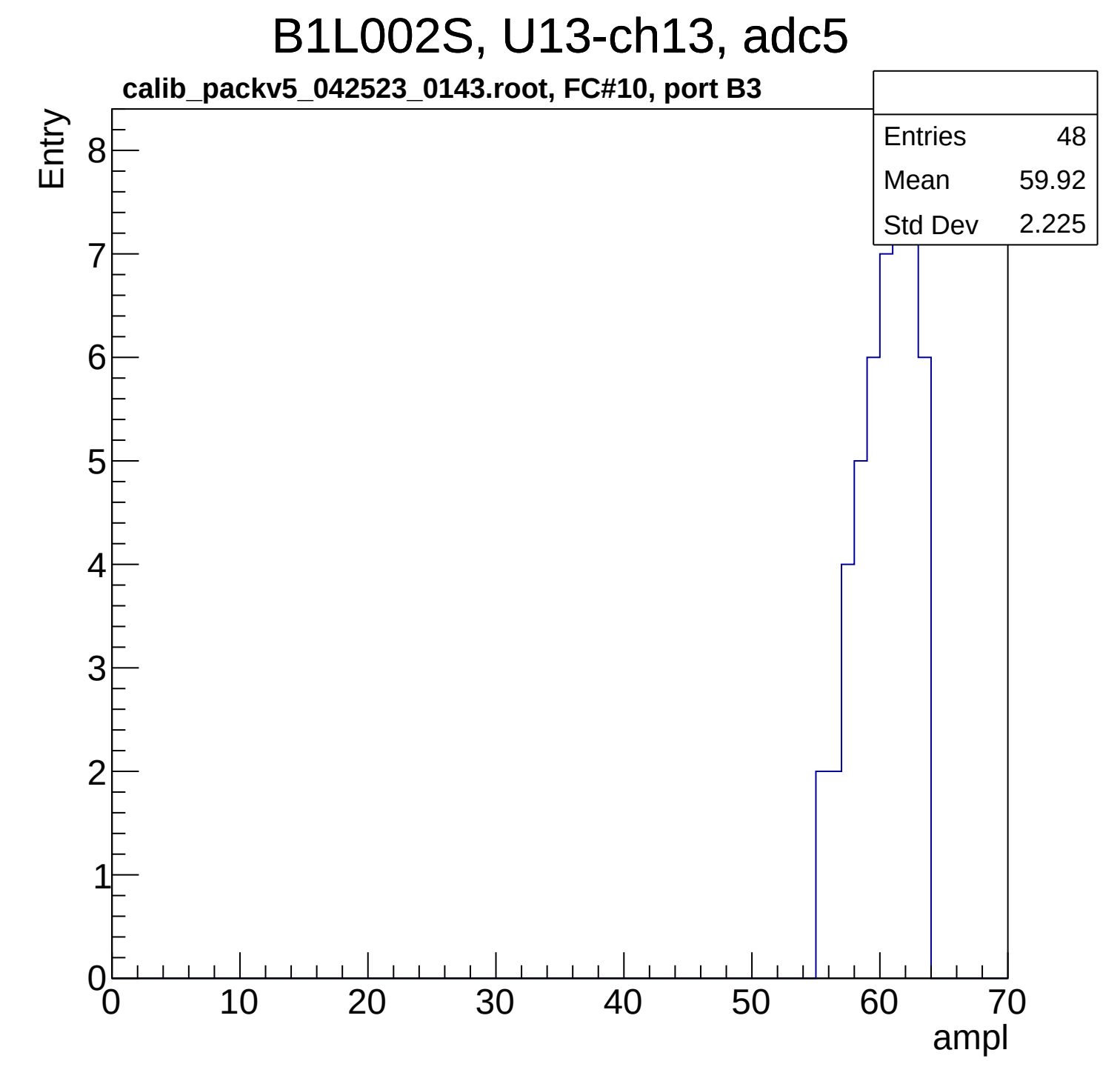
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.92
Std Dev 2.225
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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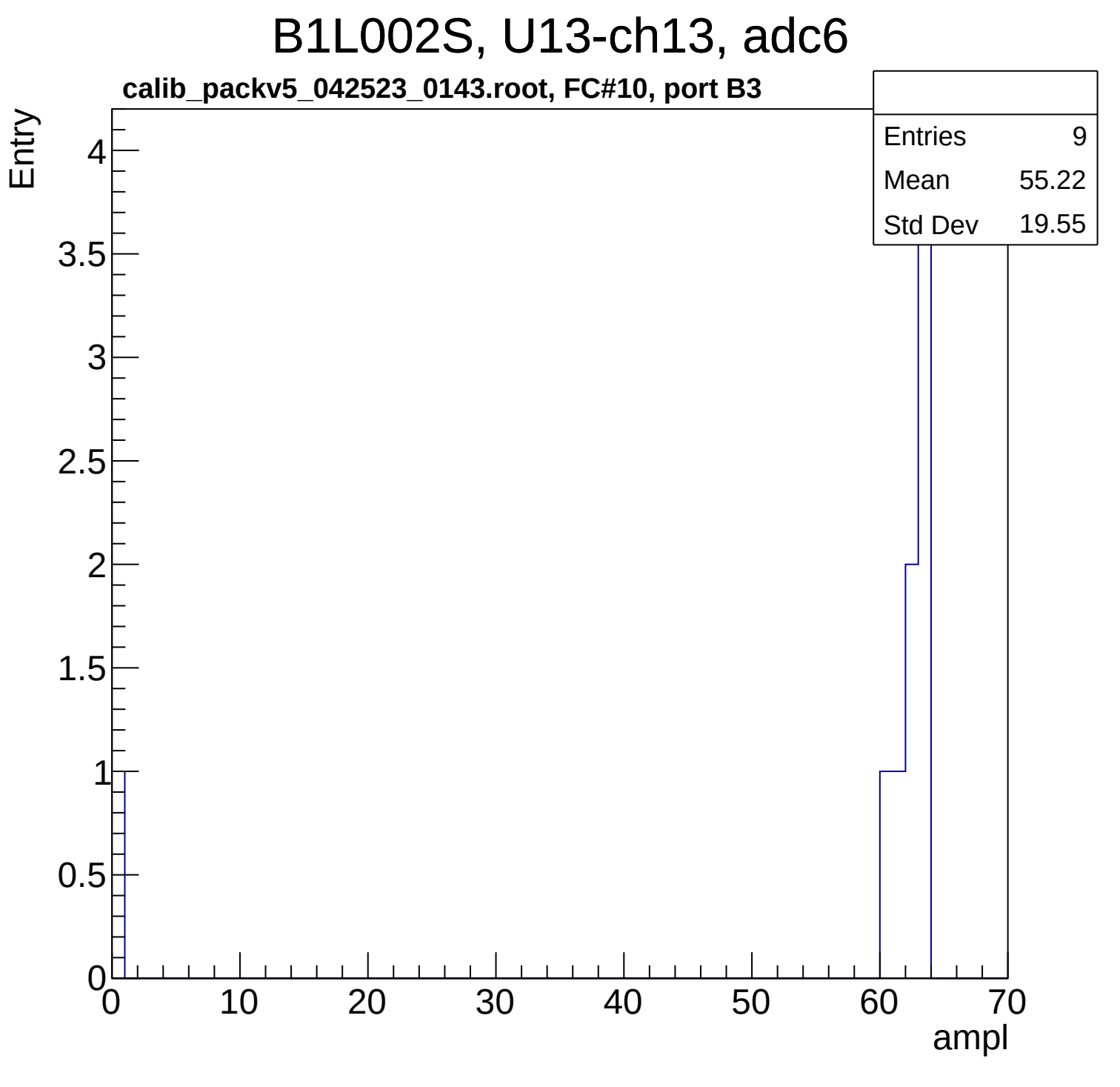
B1L002S, U13-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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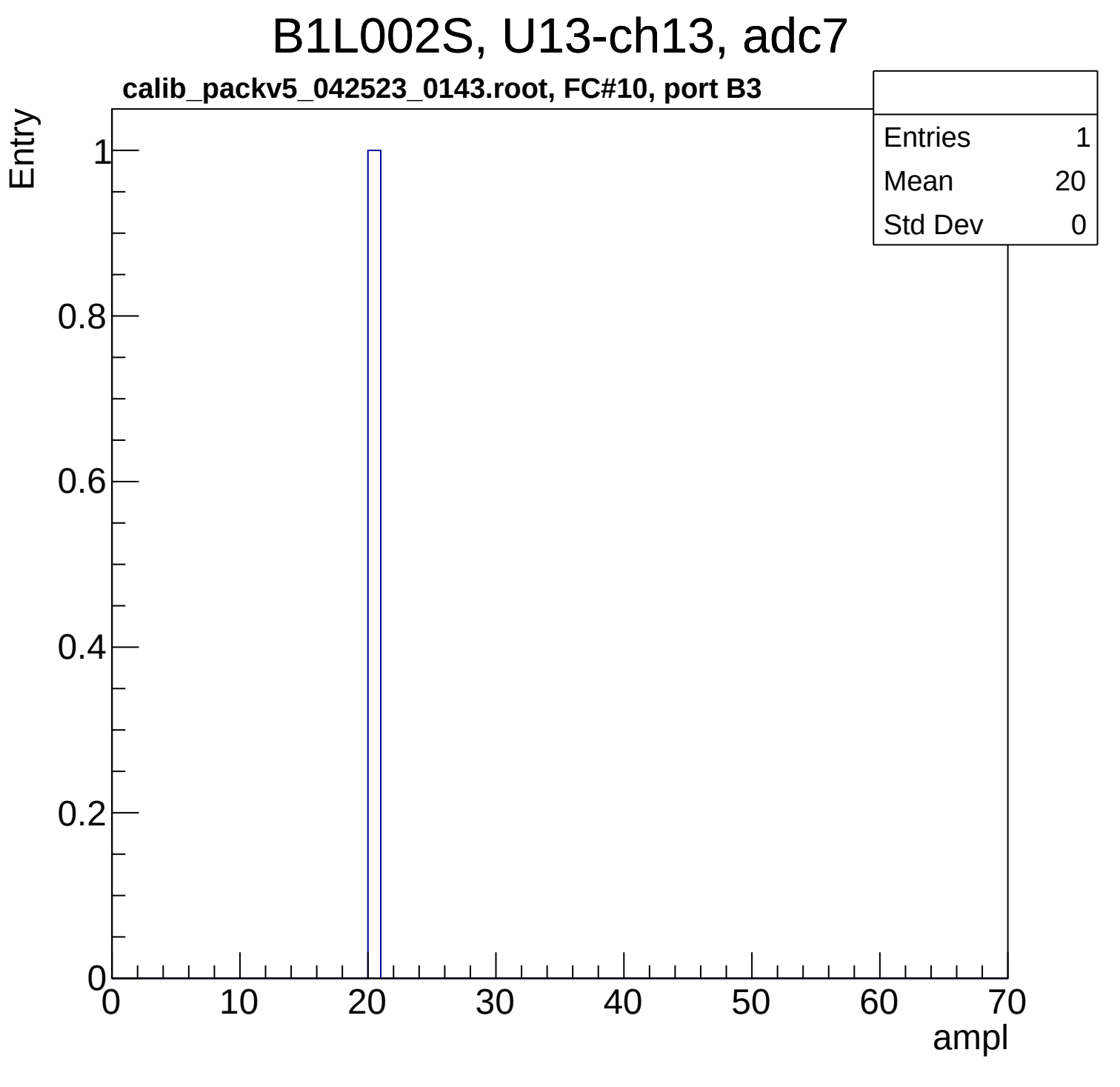
40
50
60
70


B1L002S, U13-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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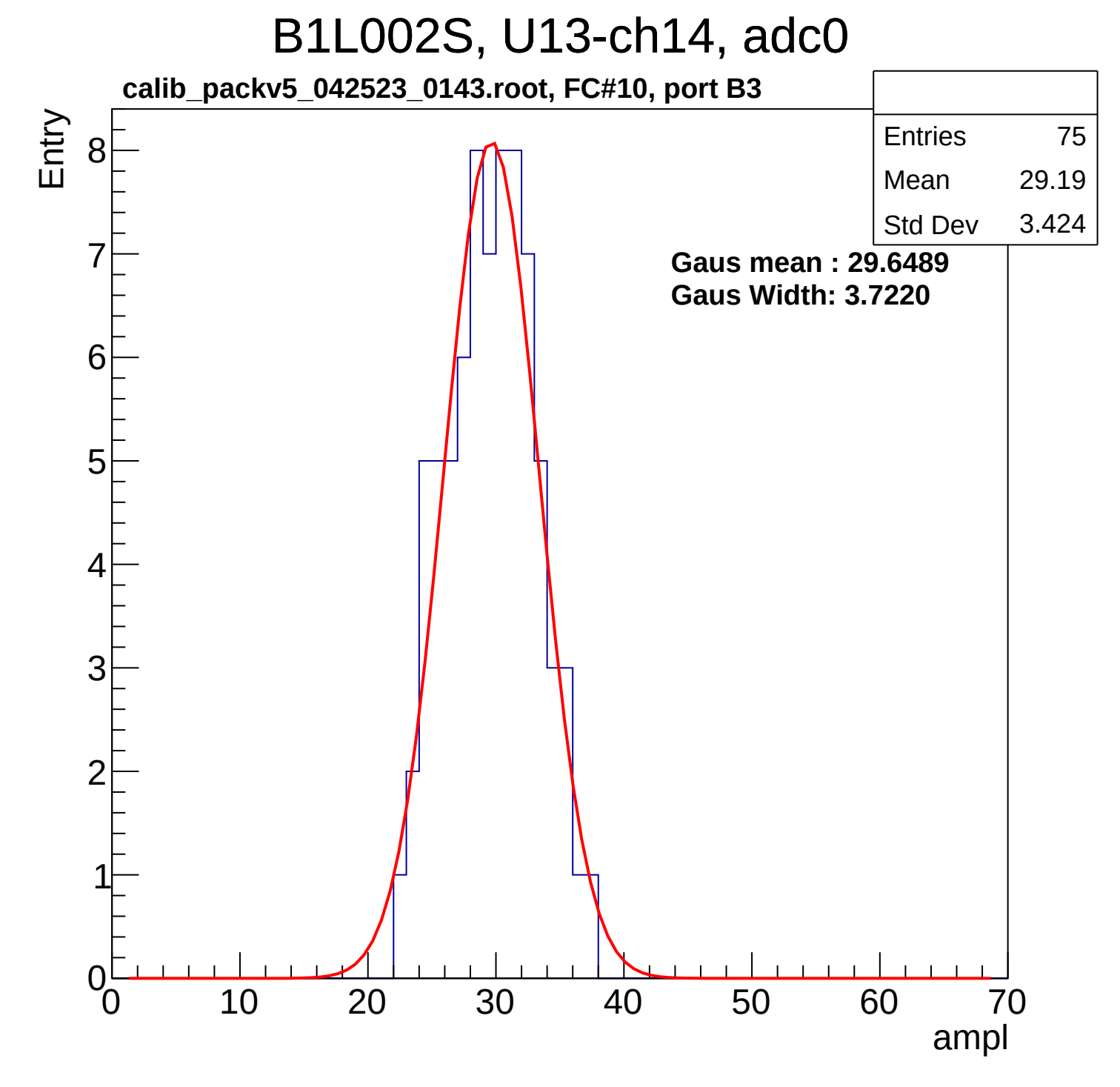
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 29.19
Std Dev 3.424
Gaus mean : 29.6489
Gaus Width: 3.7220
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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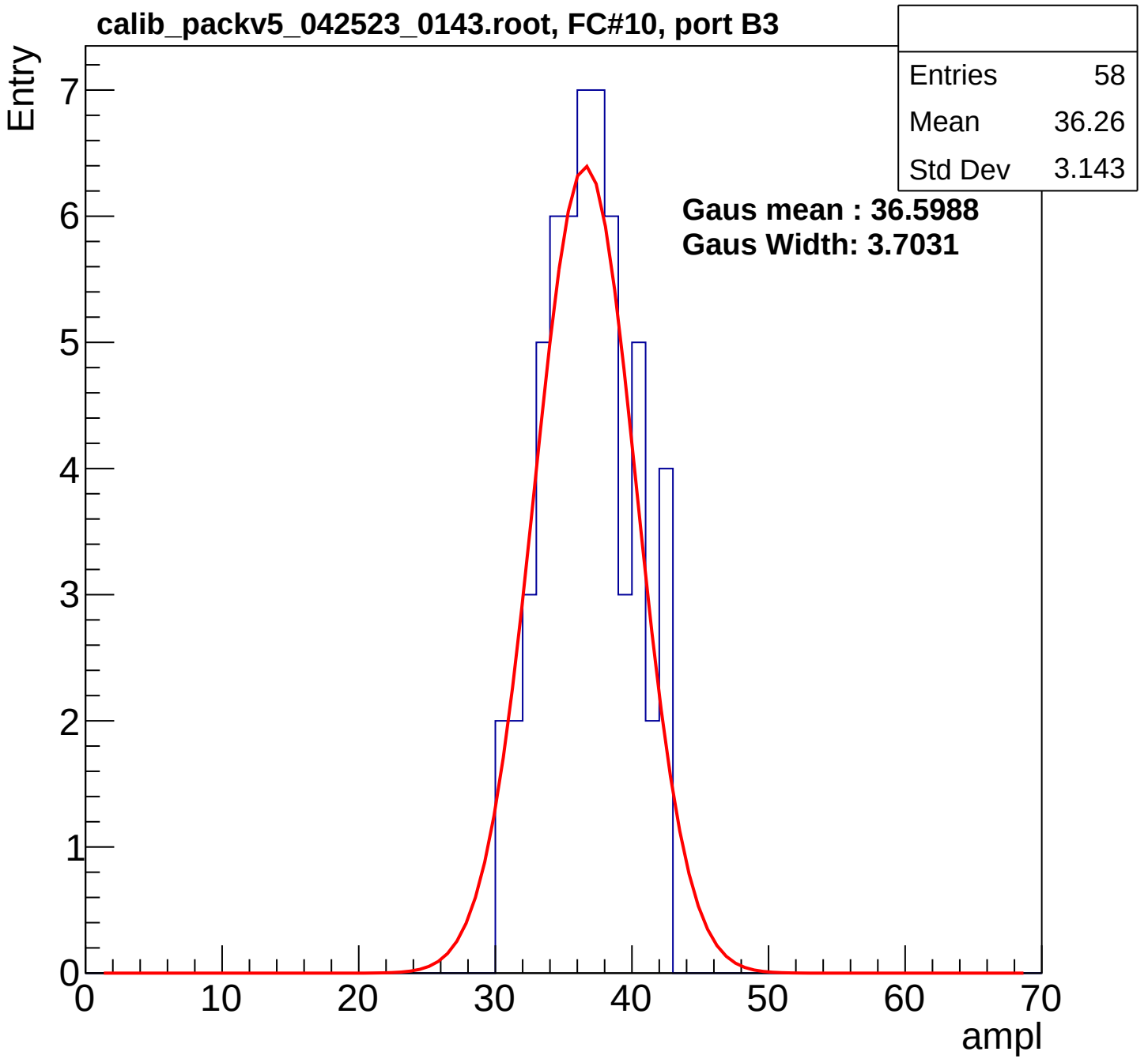
B1L002S, U13-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 36.26
Std Dev 3.143
Gaus mean : 36.5988
Gaus Width: 3.7031
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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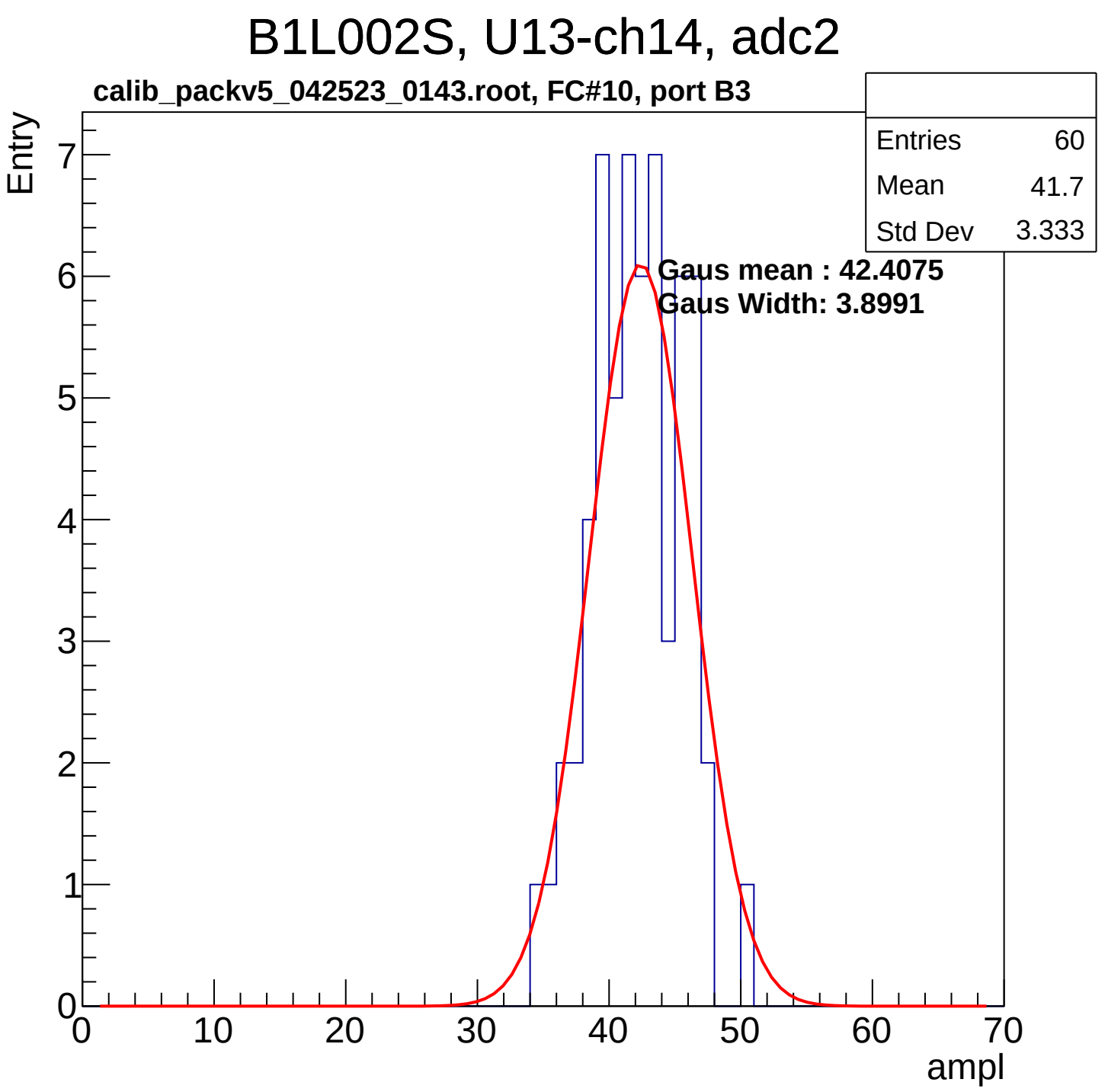
B1L002S, U13-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.7
Std Dev 3.333
Gaus mean : 42.4075
Gaus Width: 3.8991
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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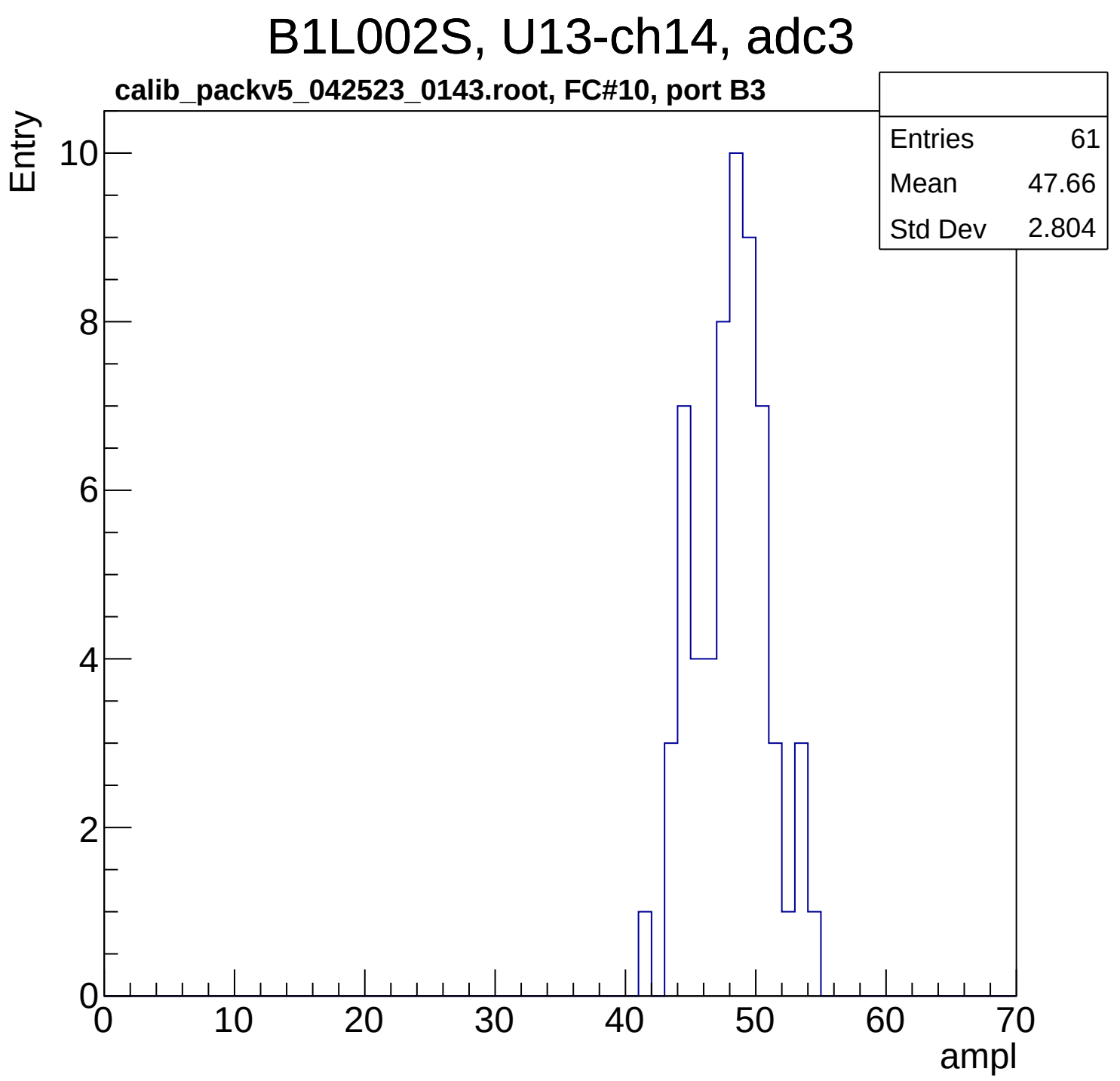
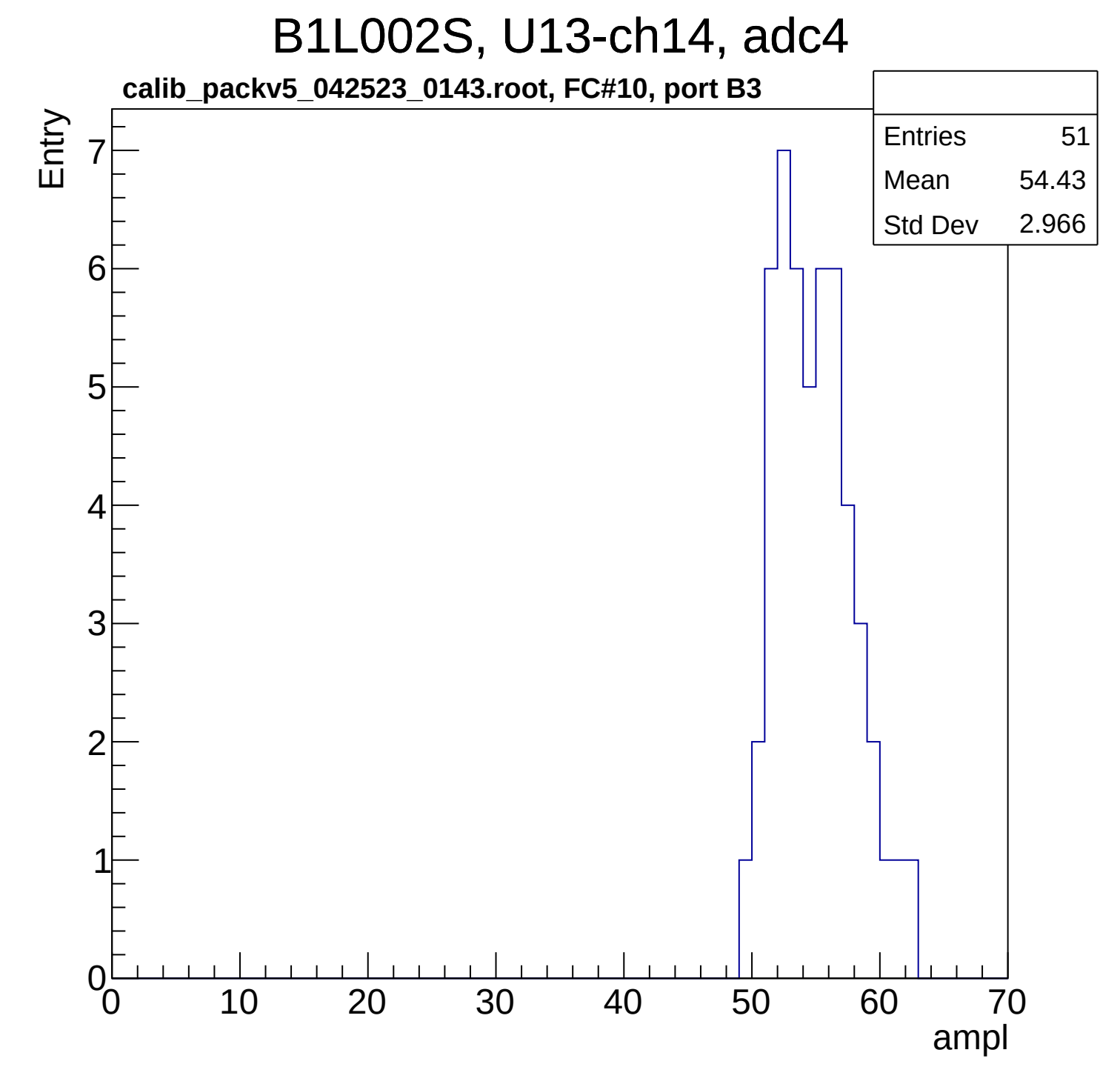

B1L002S, U13-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.66
Std Dev 2.804
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.43
Std Dev 2.966
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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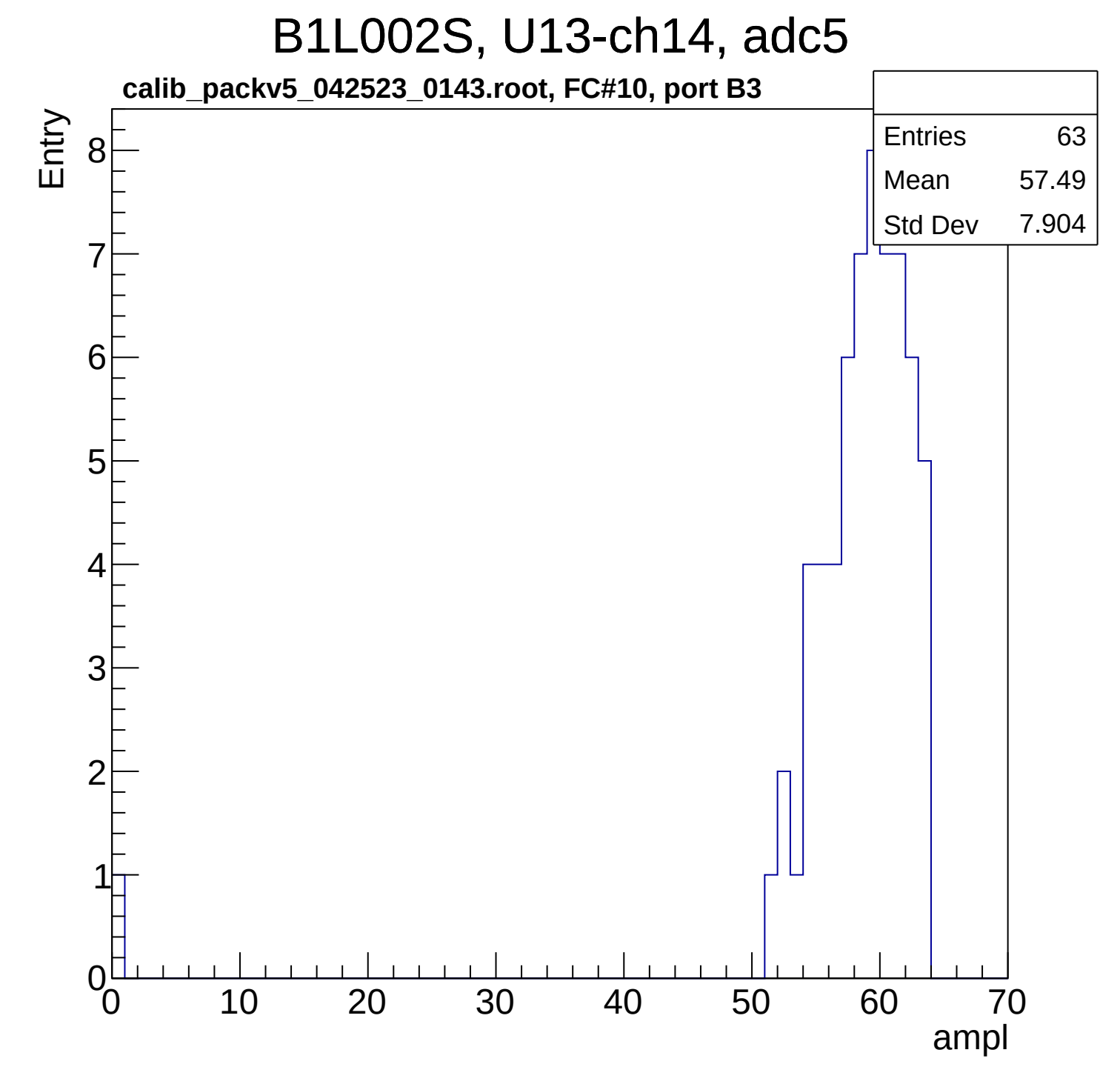
B1L002S, U13-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.49
Std Dev 7.904
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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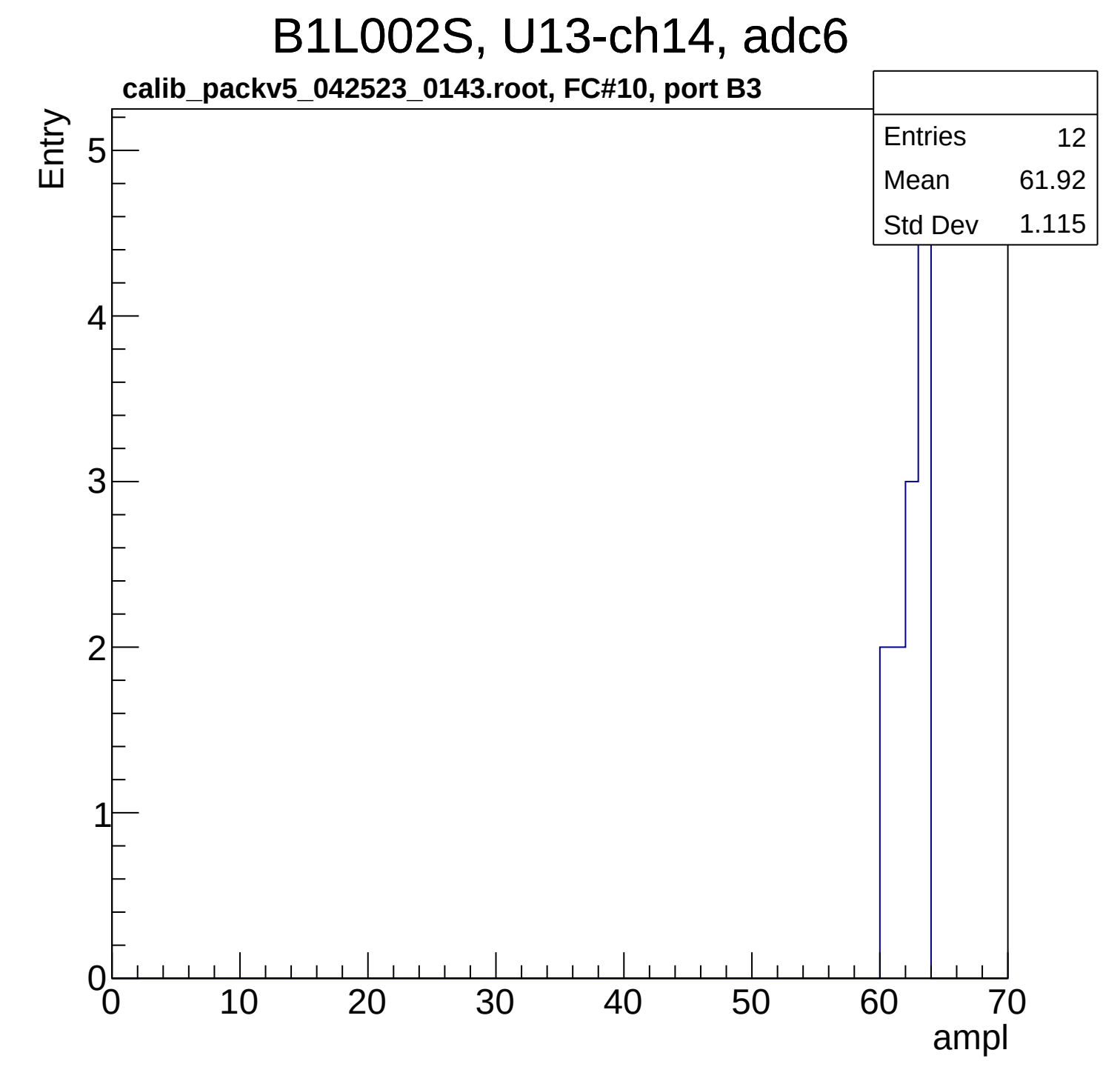
B1L002S, U13-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.92
Std Dev 1.115
Entry
0
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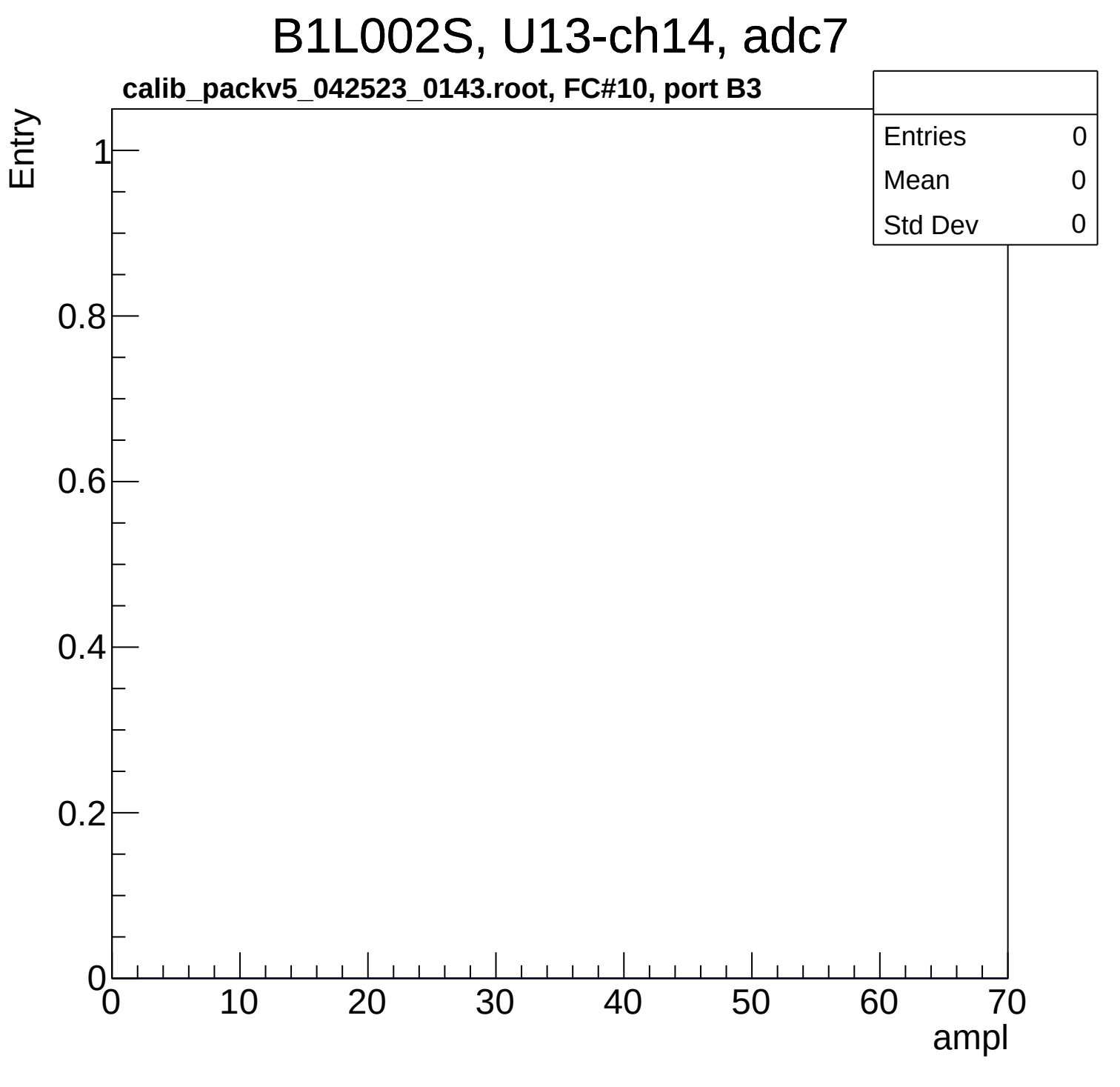
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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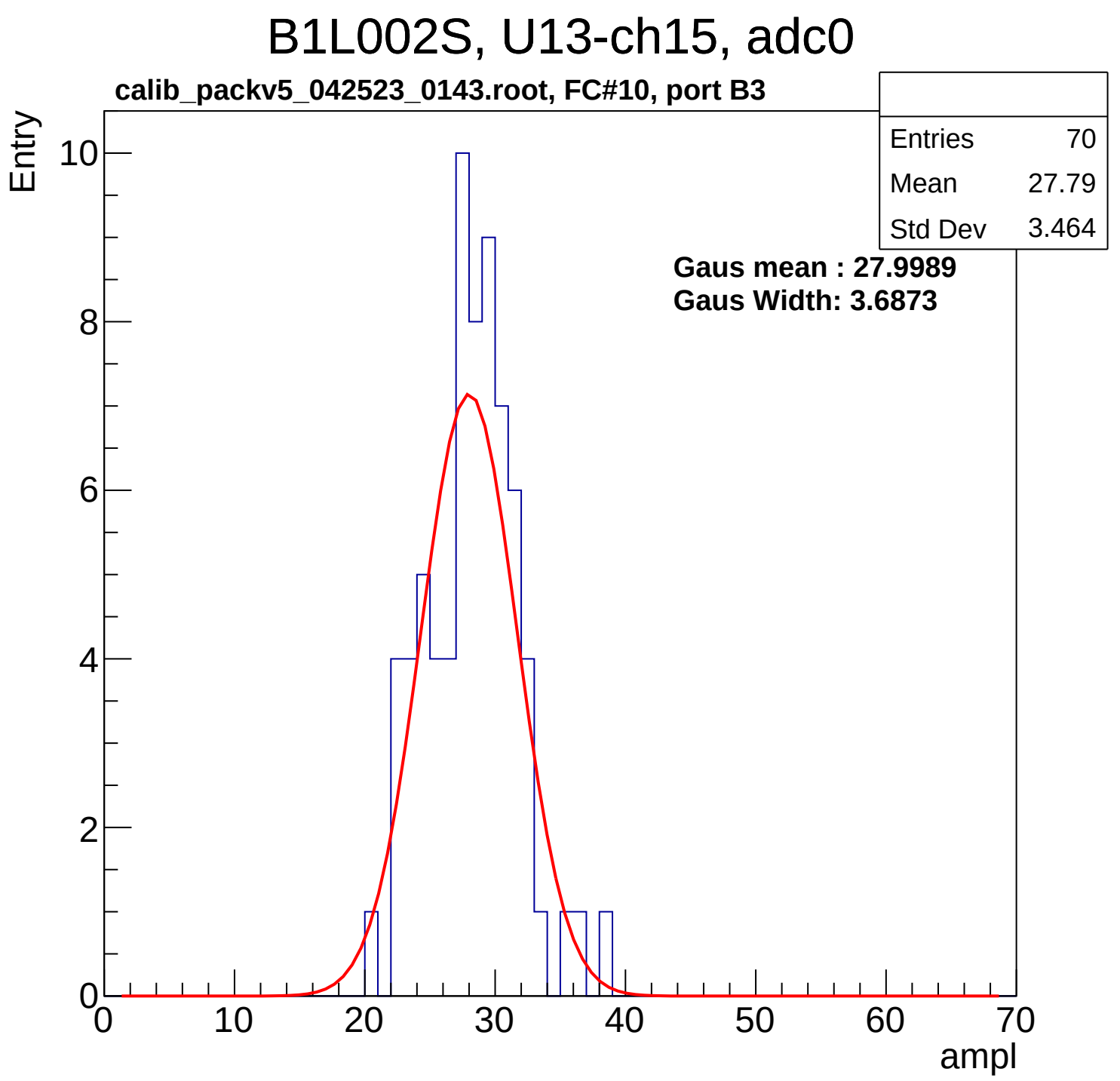
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.79
Std Dev 3.464
Gaus mean : 27.9989
Gaus Width: 3.6873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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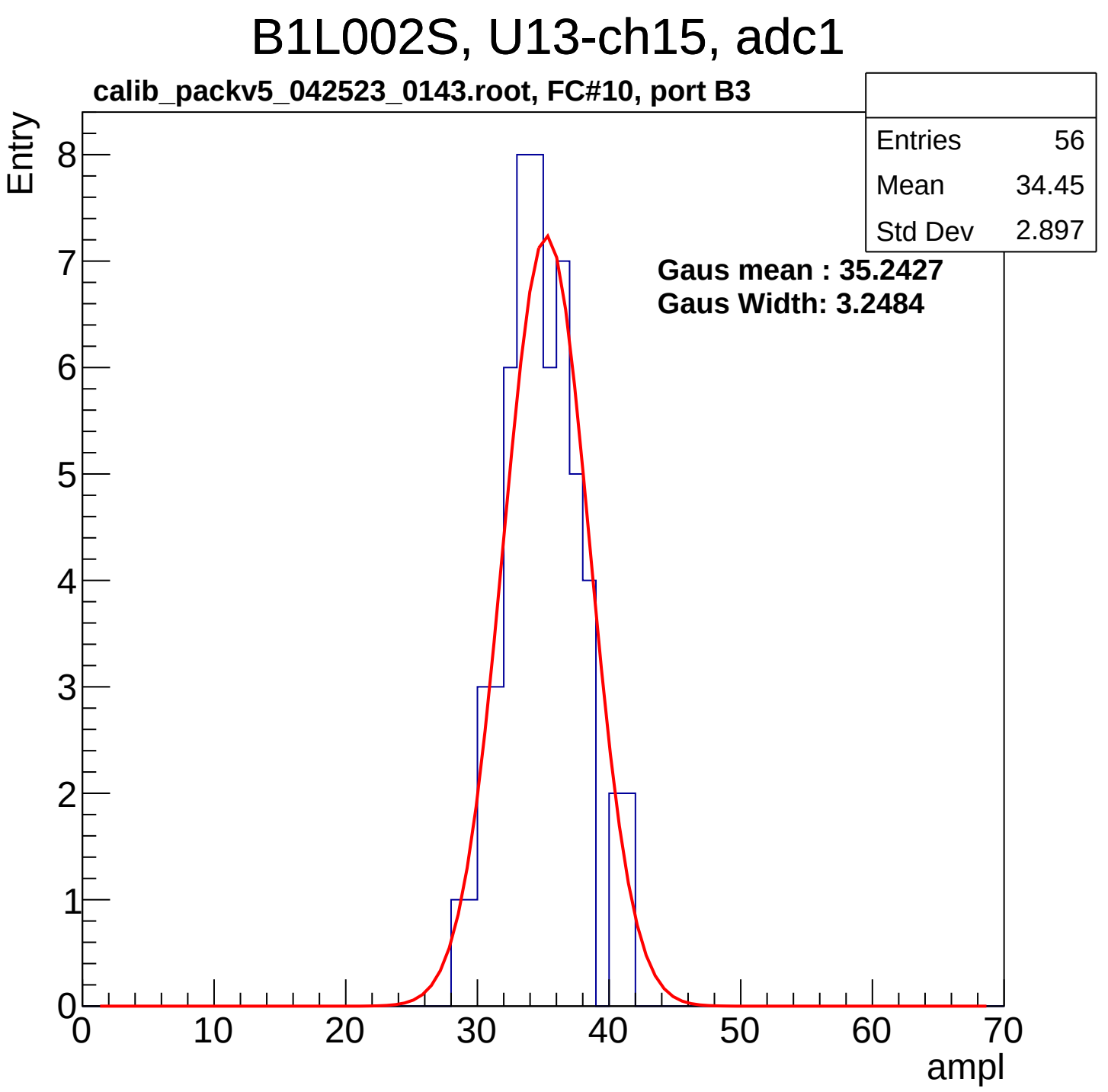
B1L002S, U13-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.45
Std Dev 2.897
Gaus mean : 35.2427
Gaus Width: 3.2484
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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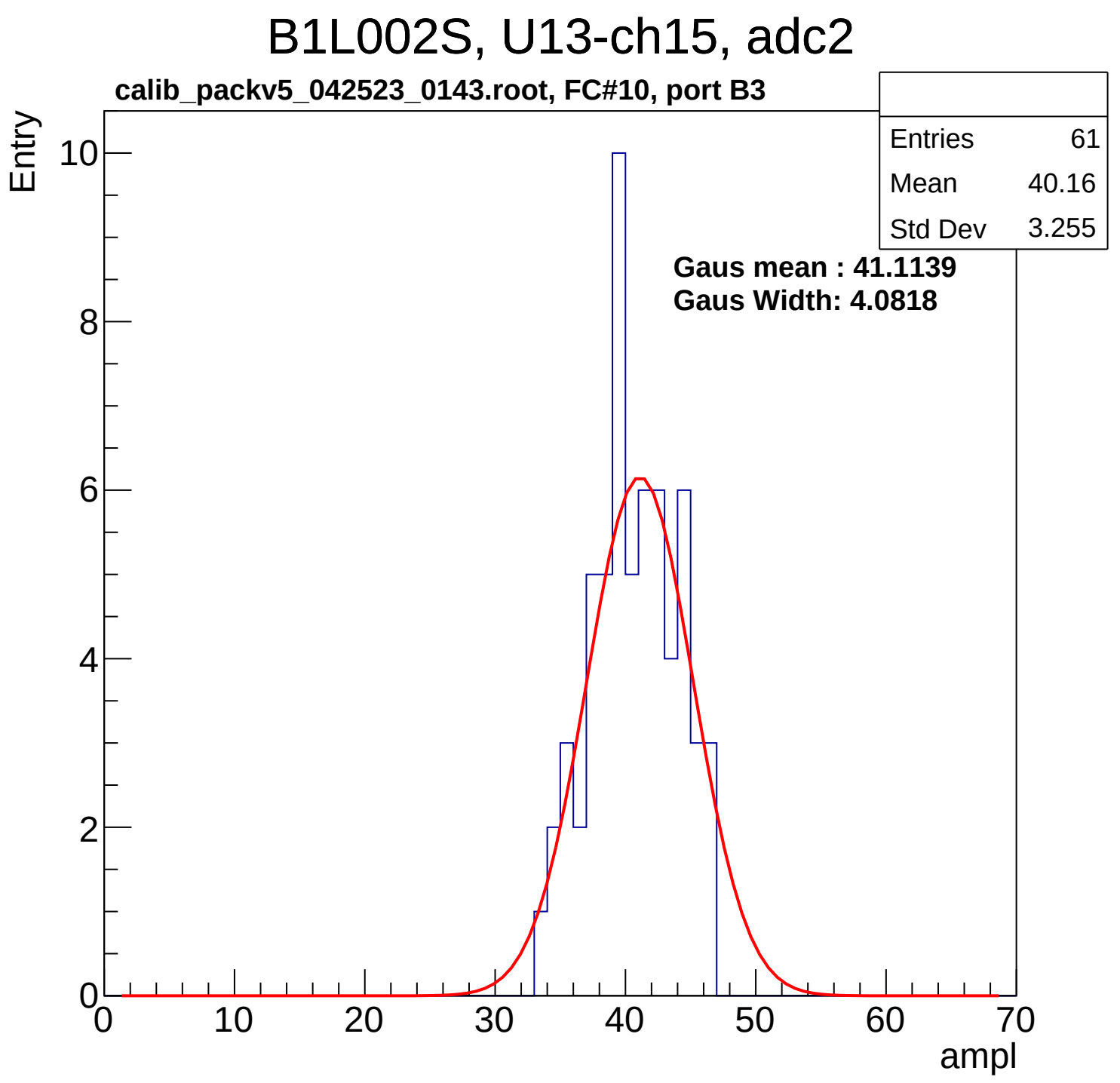
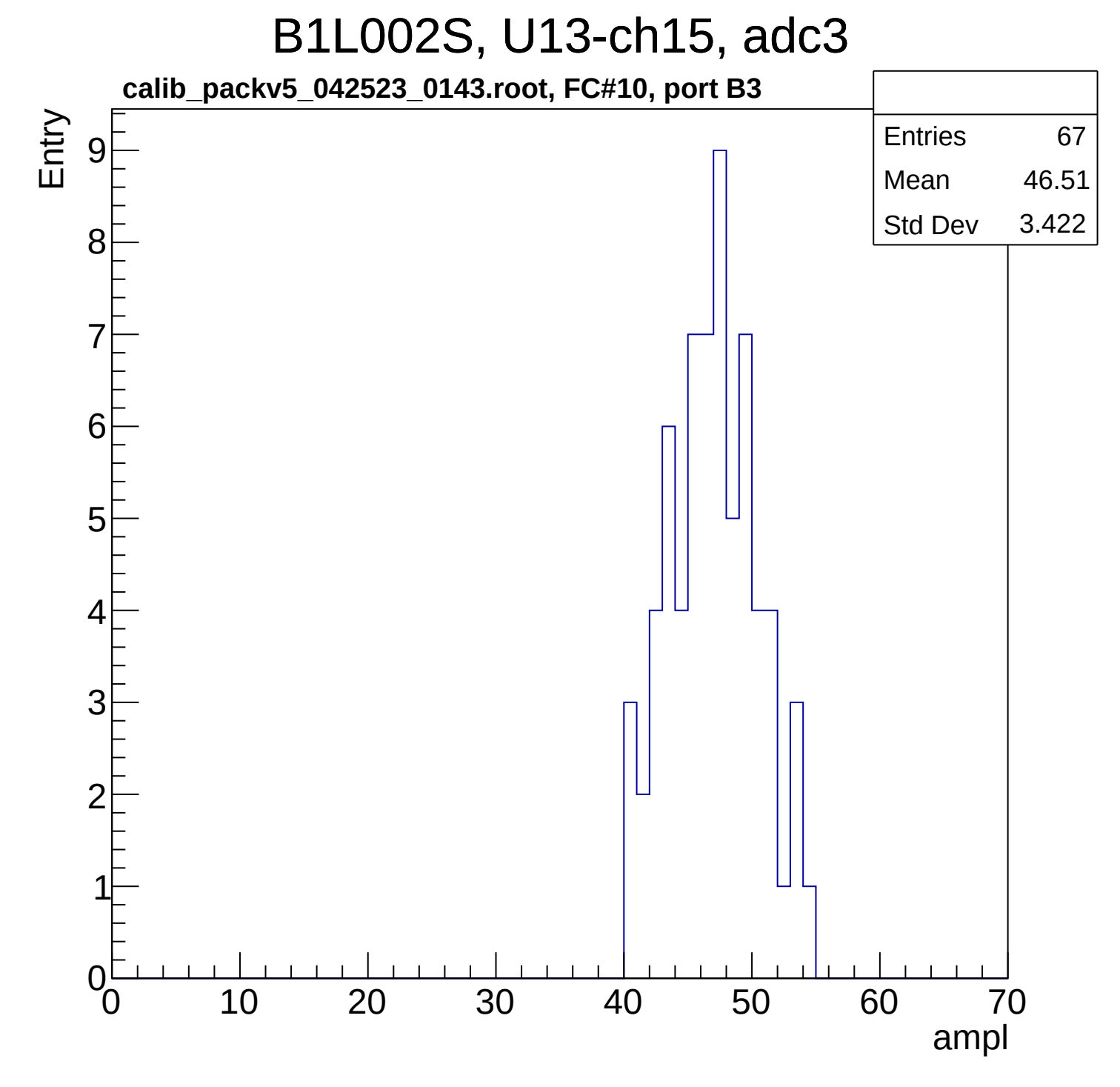

B1L002S, U13-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.16
Std Dev 3.255
Gaus mean : 41.1139
Gaus Width: 4.0818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.51
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
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0
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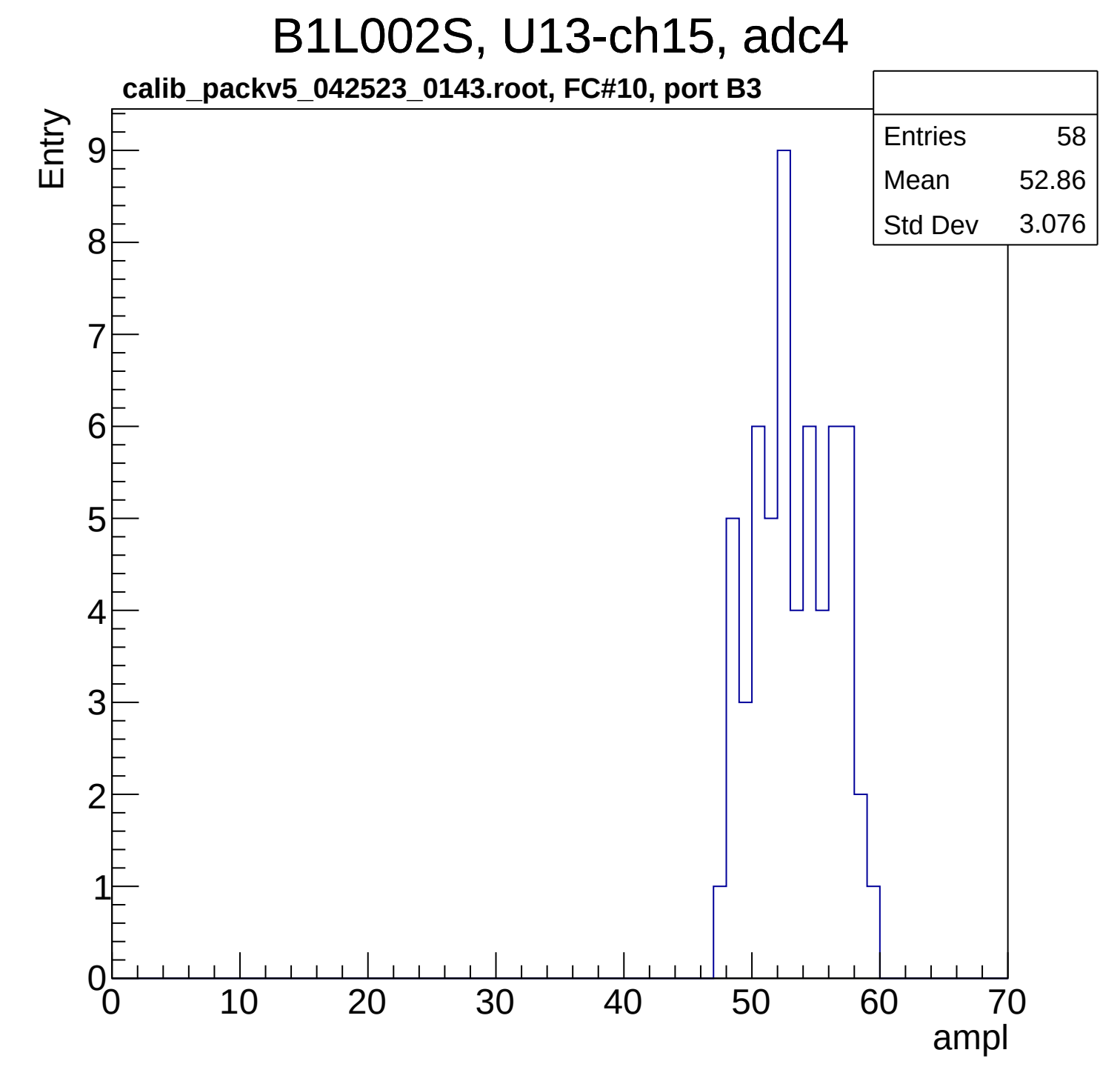
B1L002S, U13-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.86
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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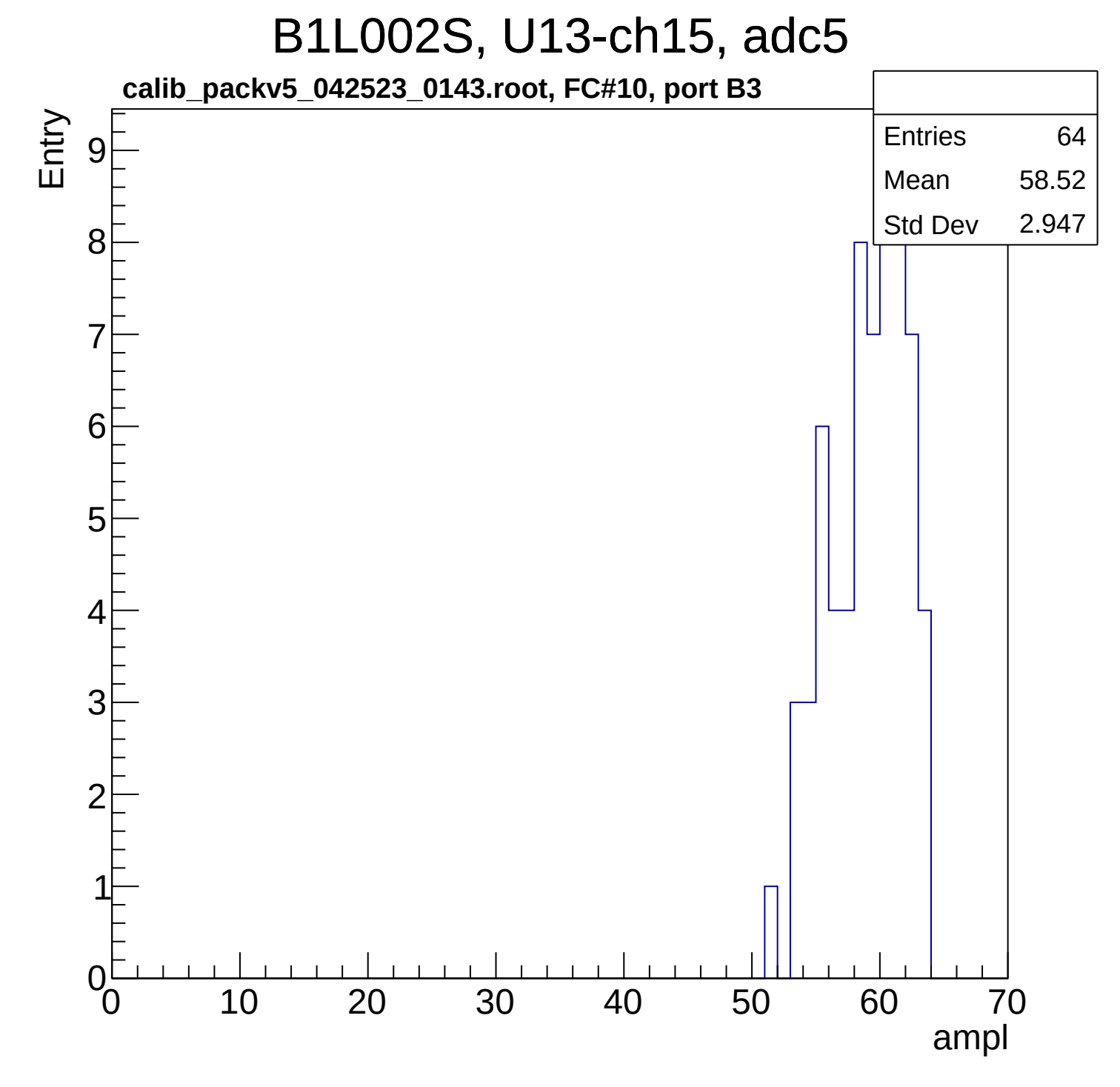
B1L002S, U13-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.52
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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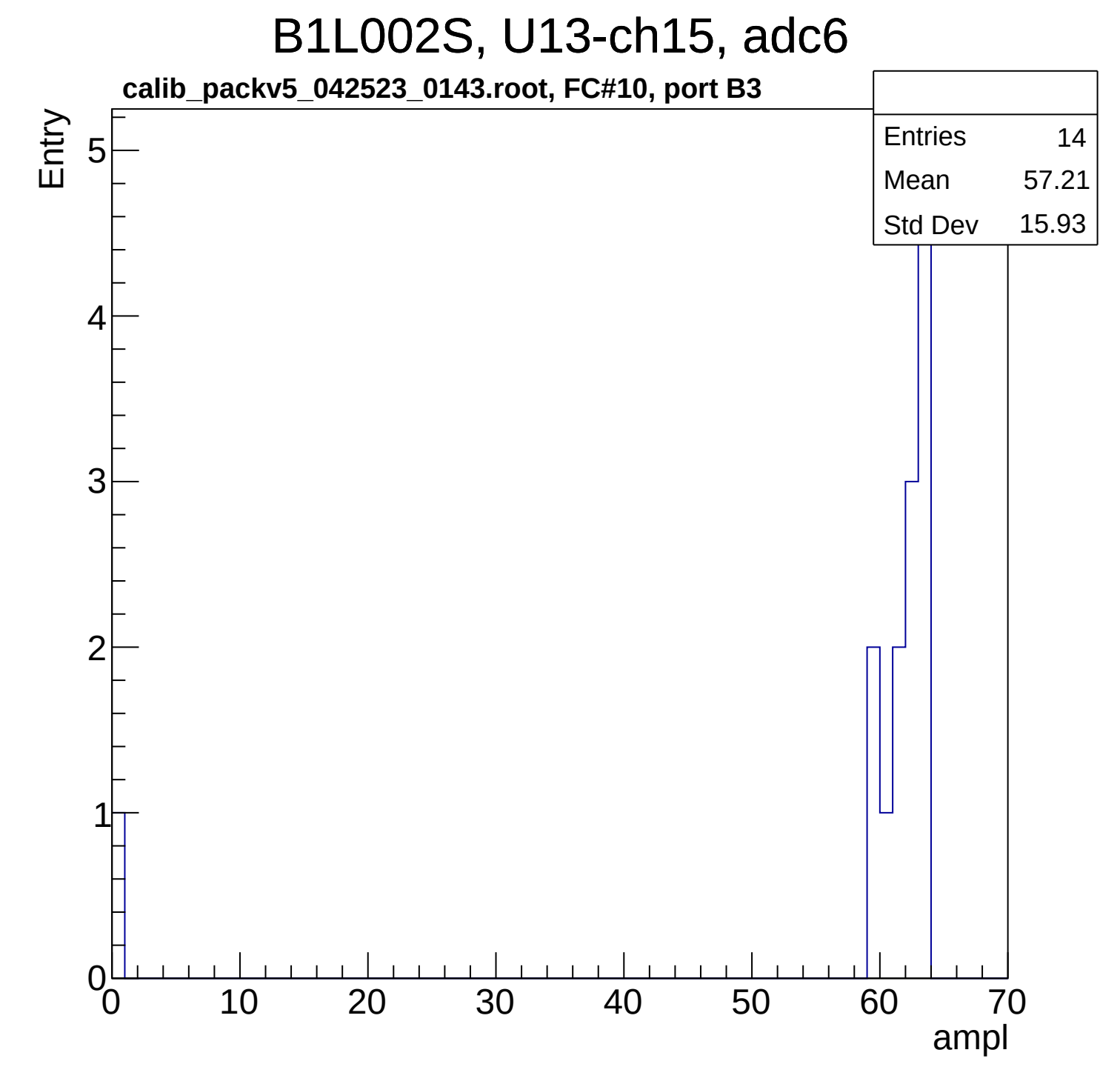
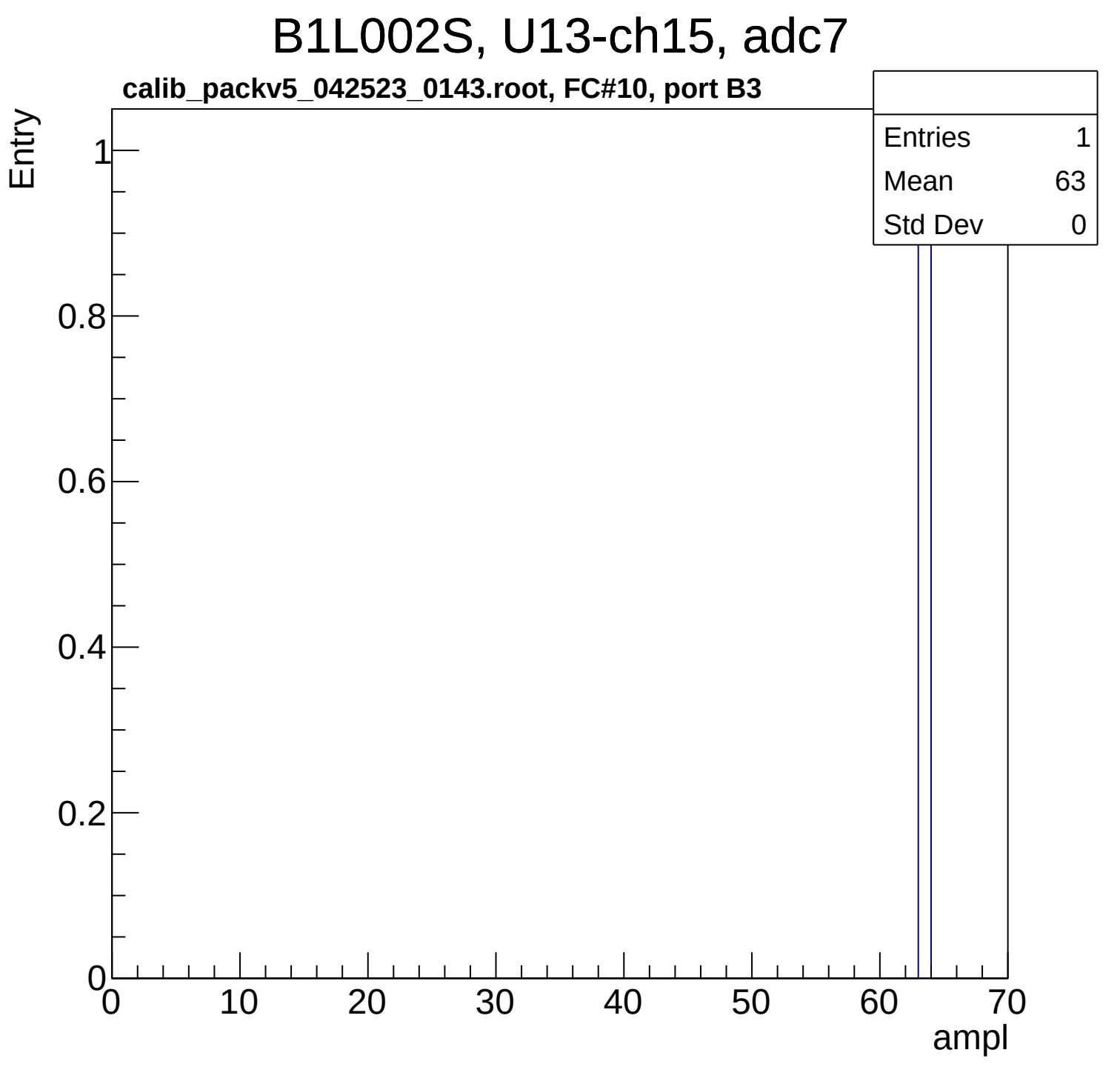
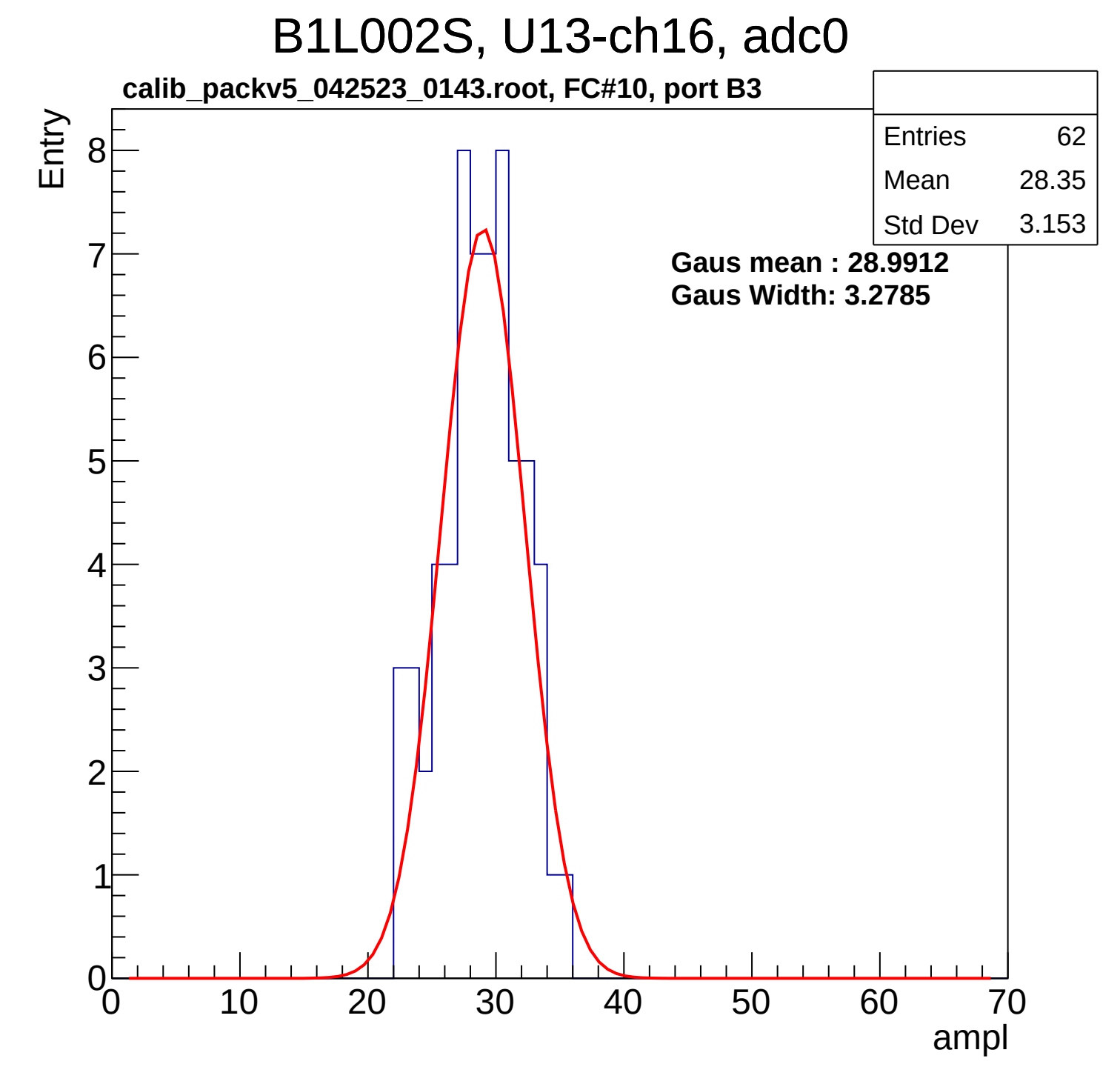

B1L002S, U13-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.21
Std Dev 15.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.35
Std Dev 3.153
Gaus mean : 28.9912
Gaus Width: 3.2785
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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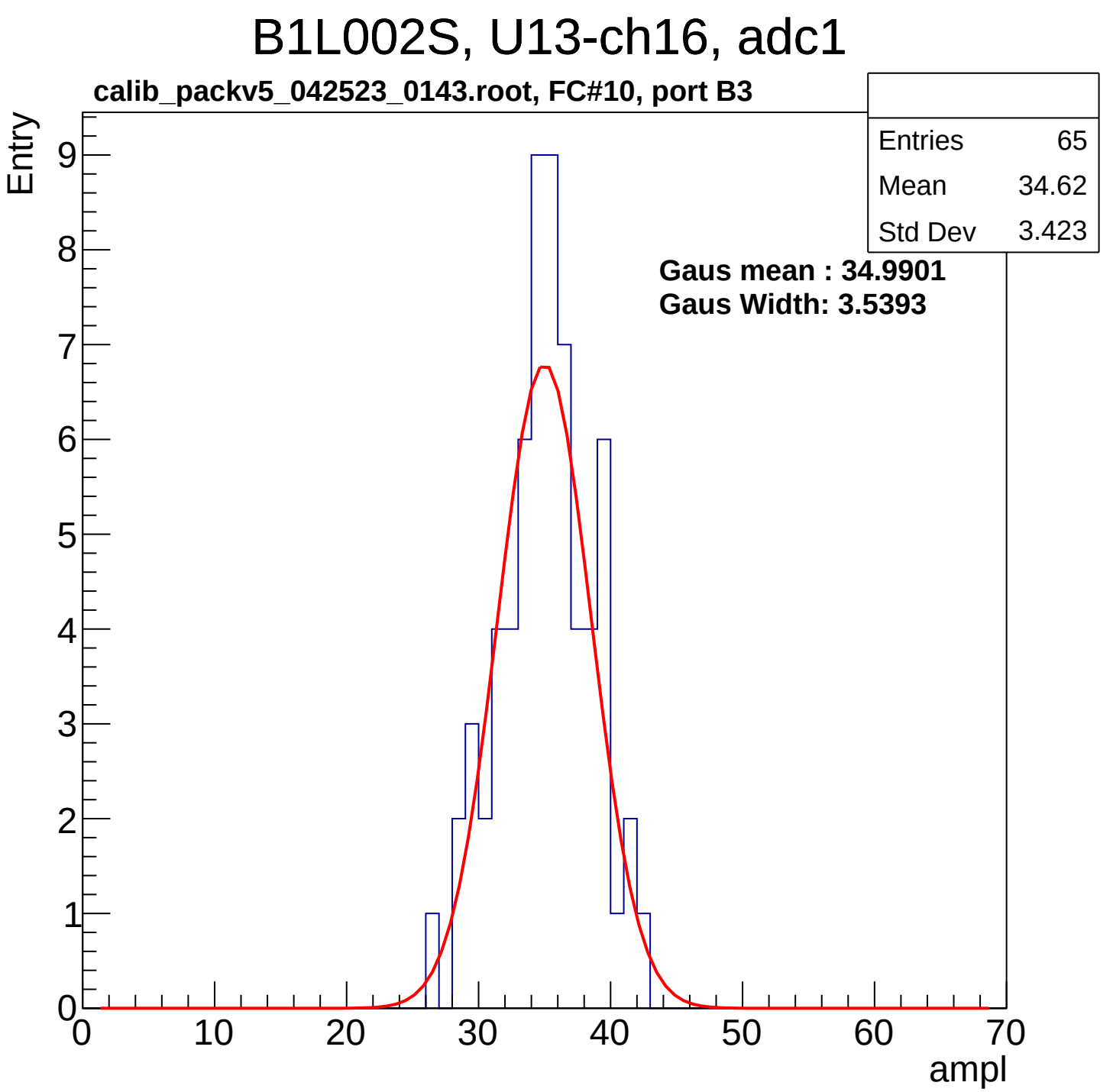
B1L002S, U13-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.62
Std Dev 3.423
Gaus mean : 34.9901
Gaus Width: 3.5393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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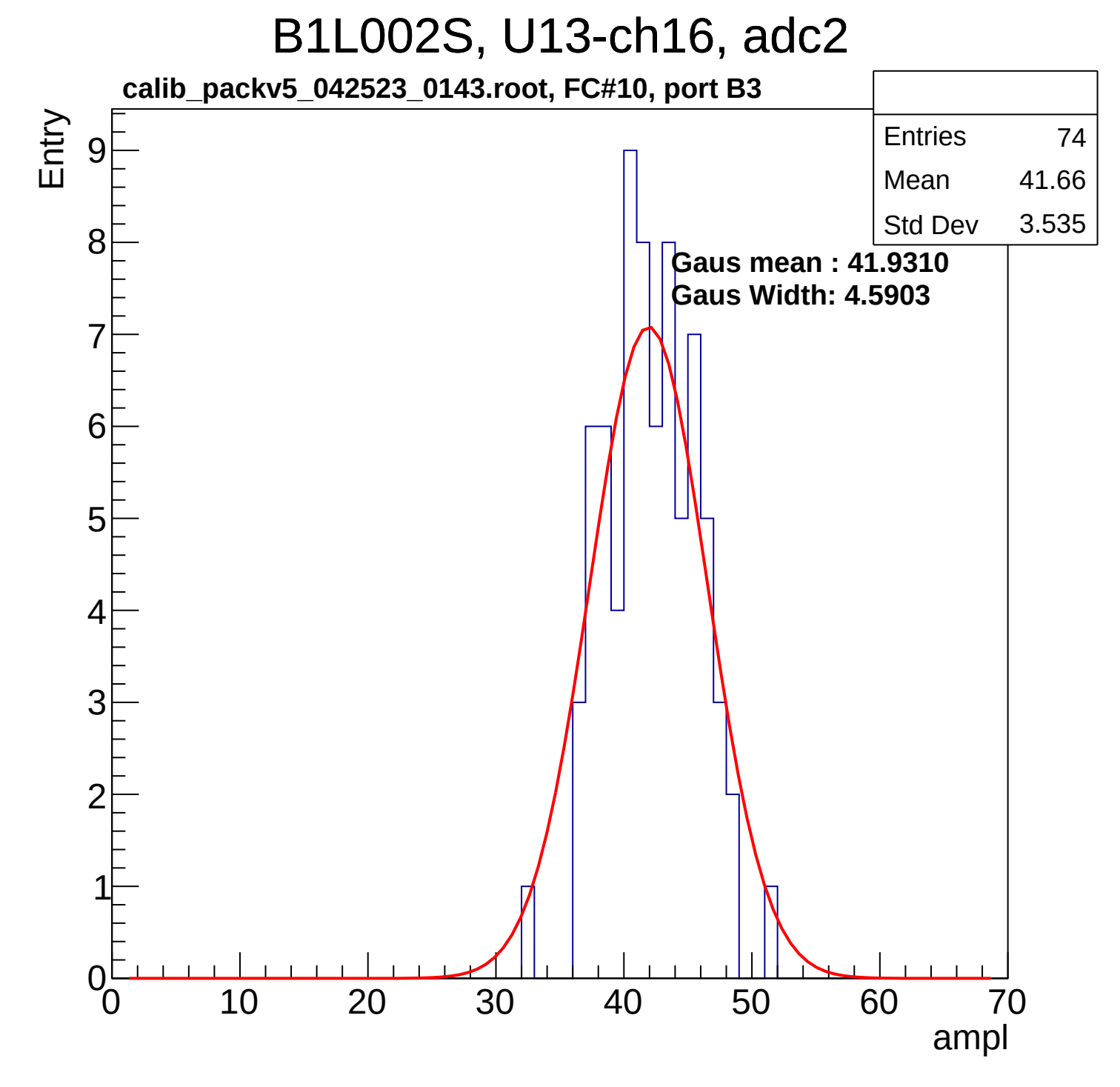
B1L002S, U13-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.66
Std Dev 3.535
Gaus mean : 41.9310
Gaus Width: 4.5903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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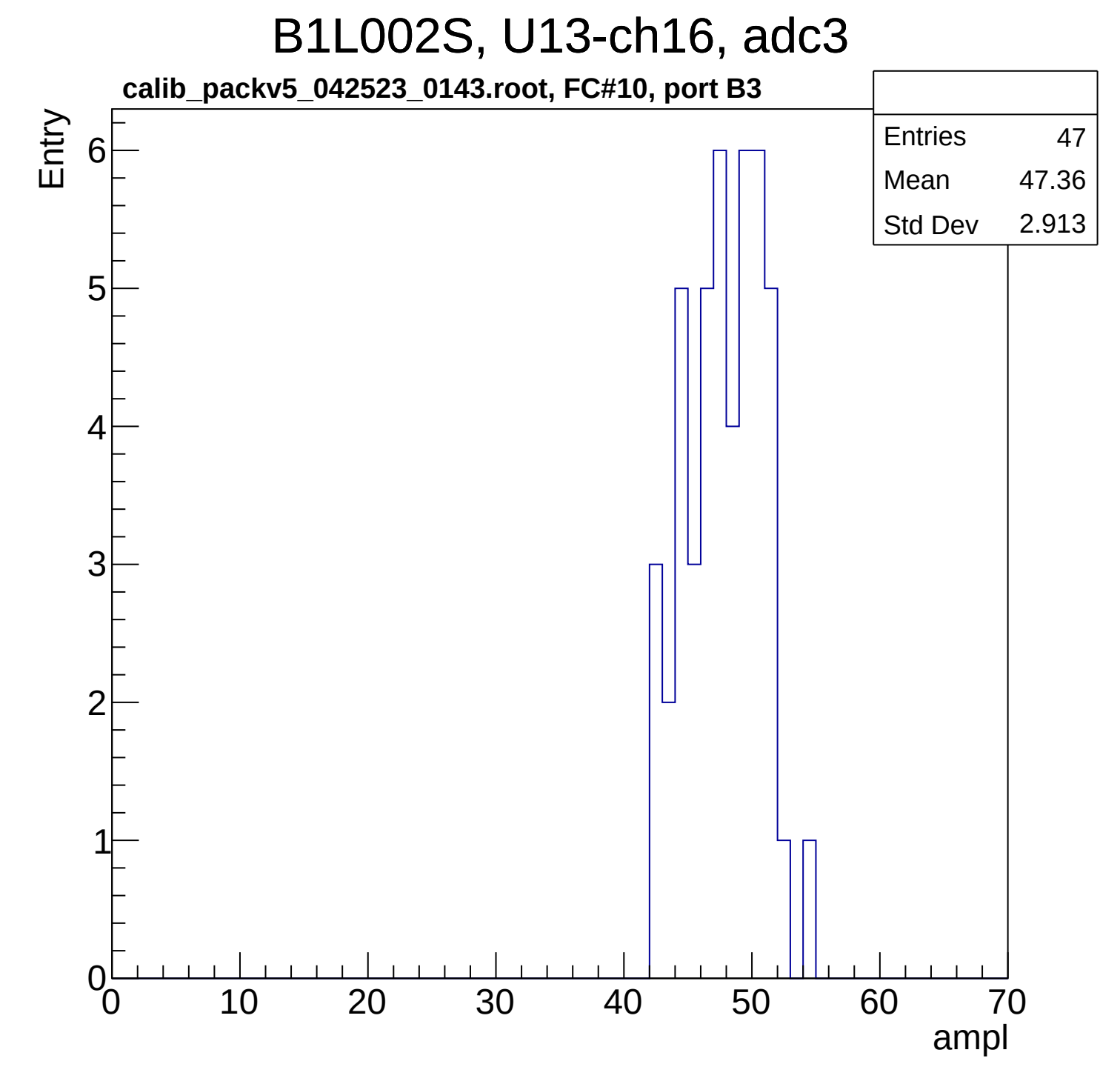
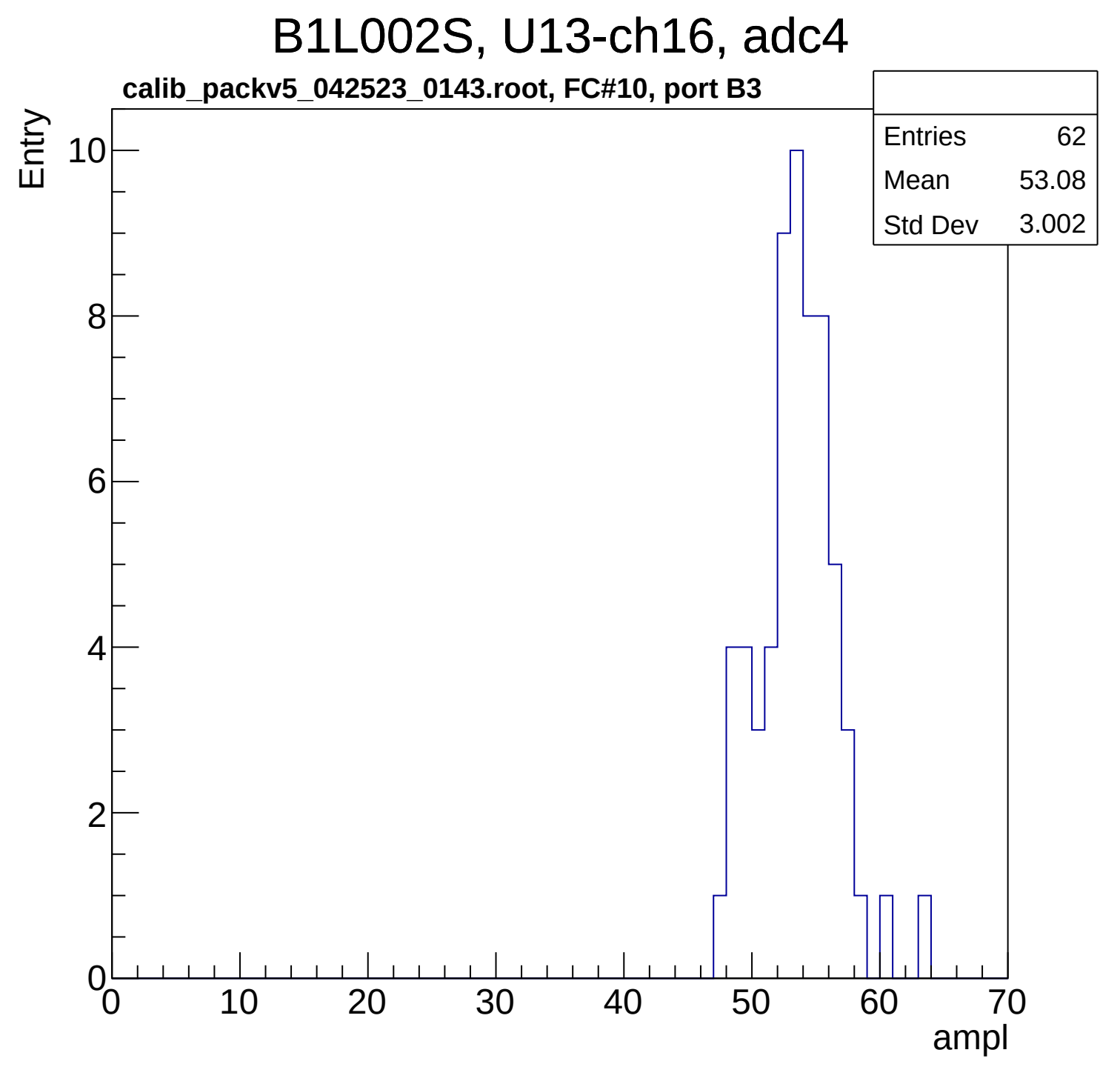
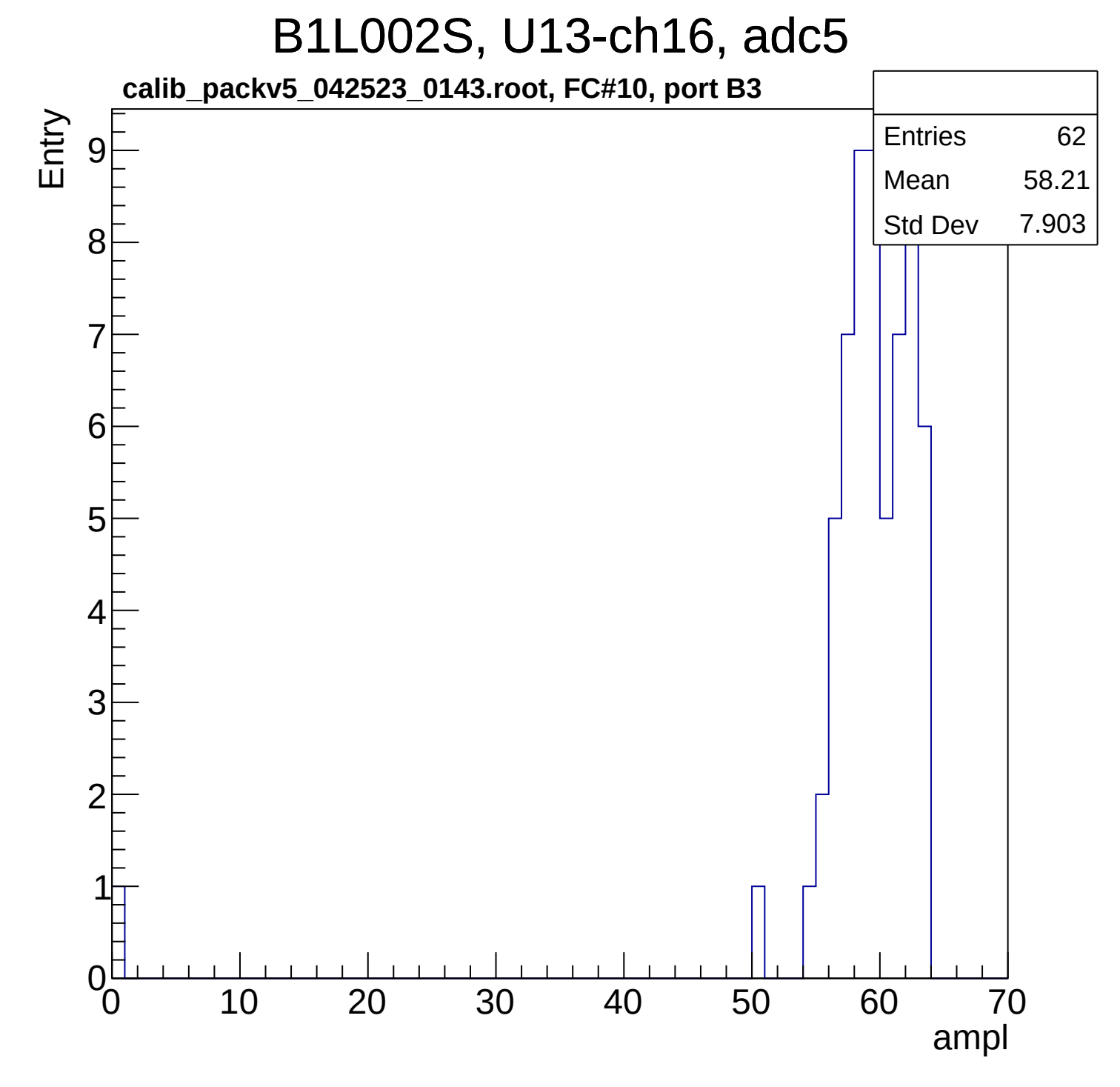
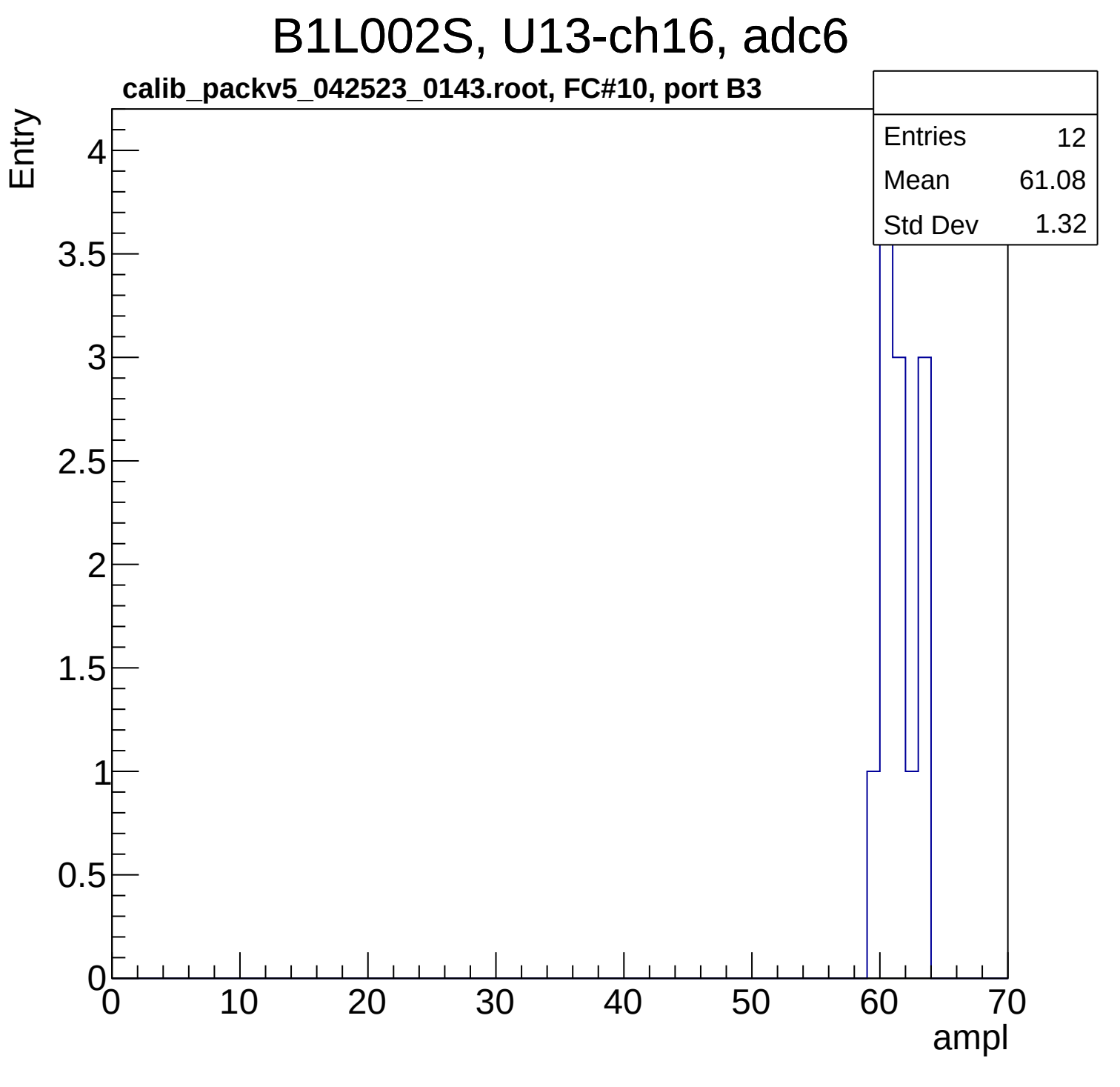
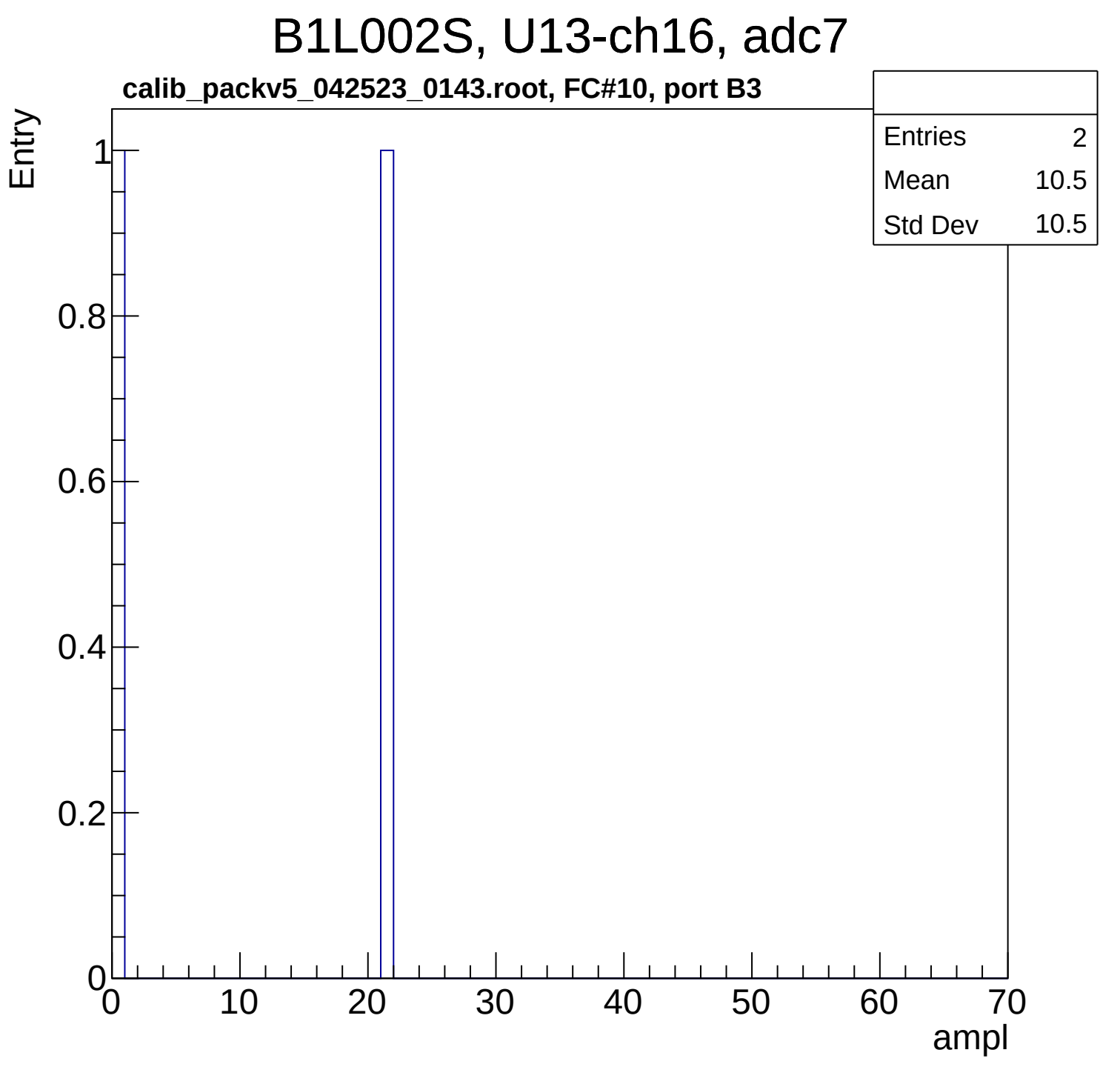
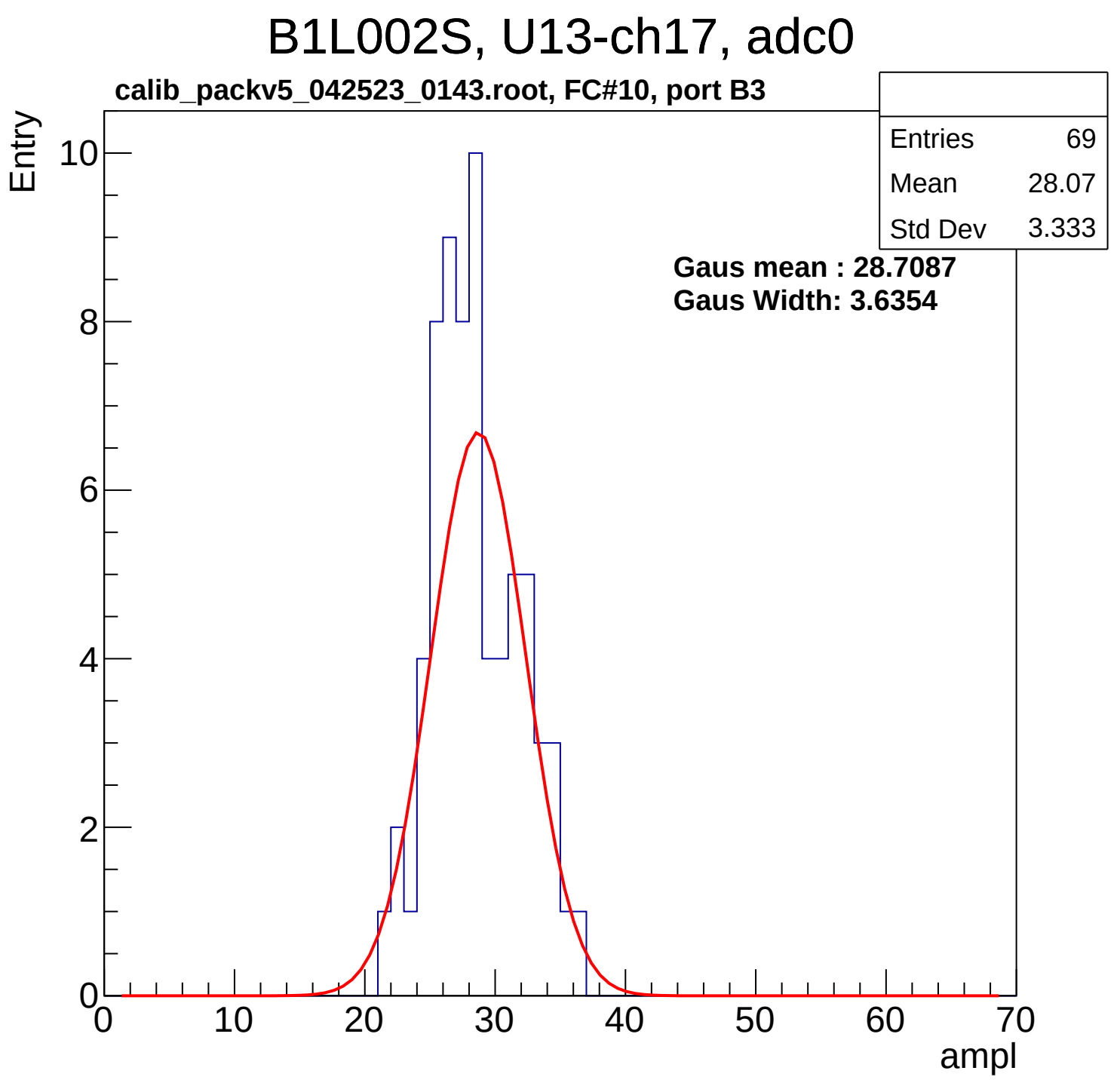

B1L002S, U13-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.36
Std Dev 2.913
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

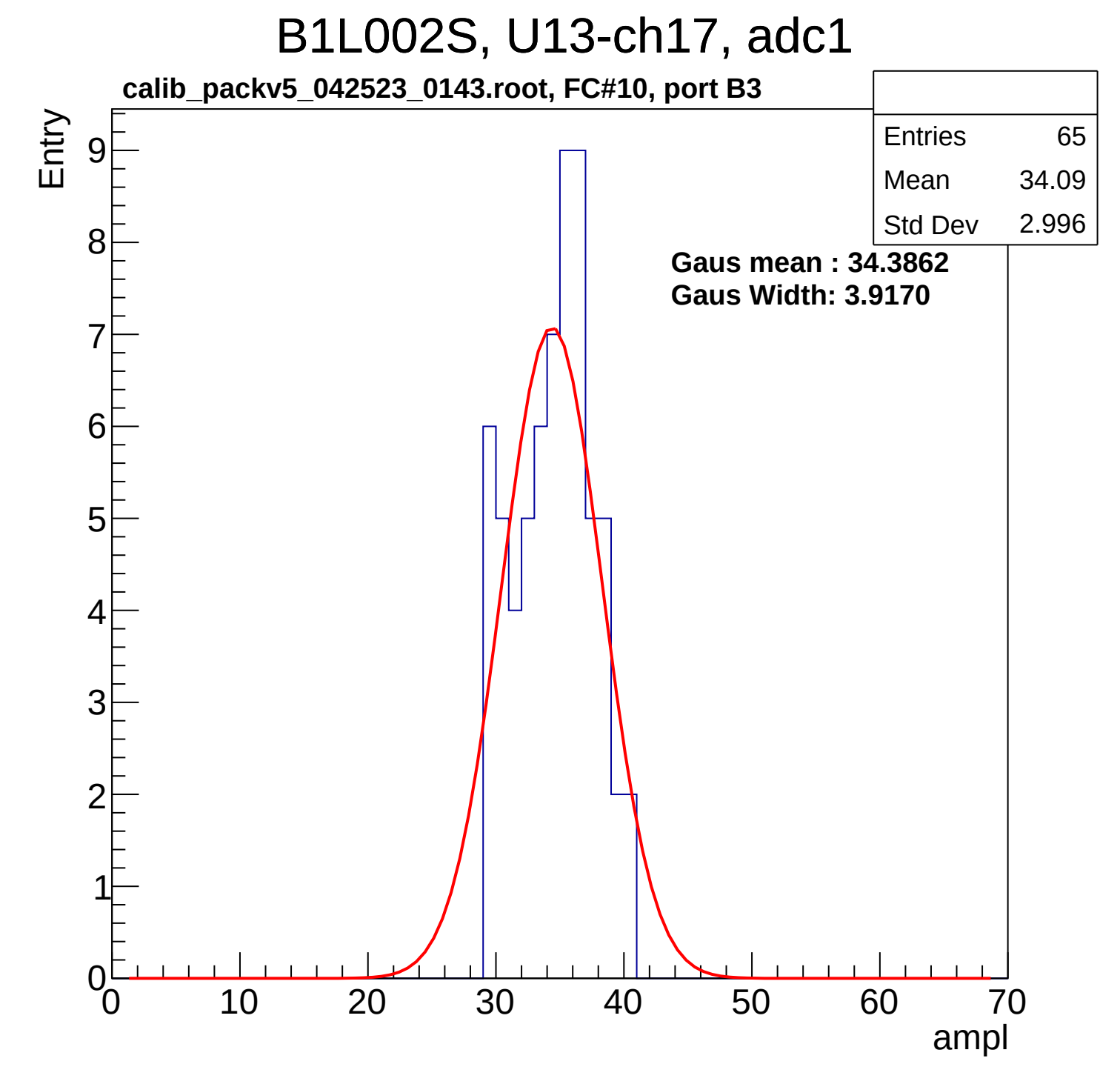

B1L002S, U13-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.08
Std Dev 3.002
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

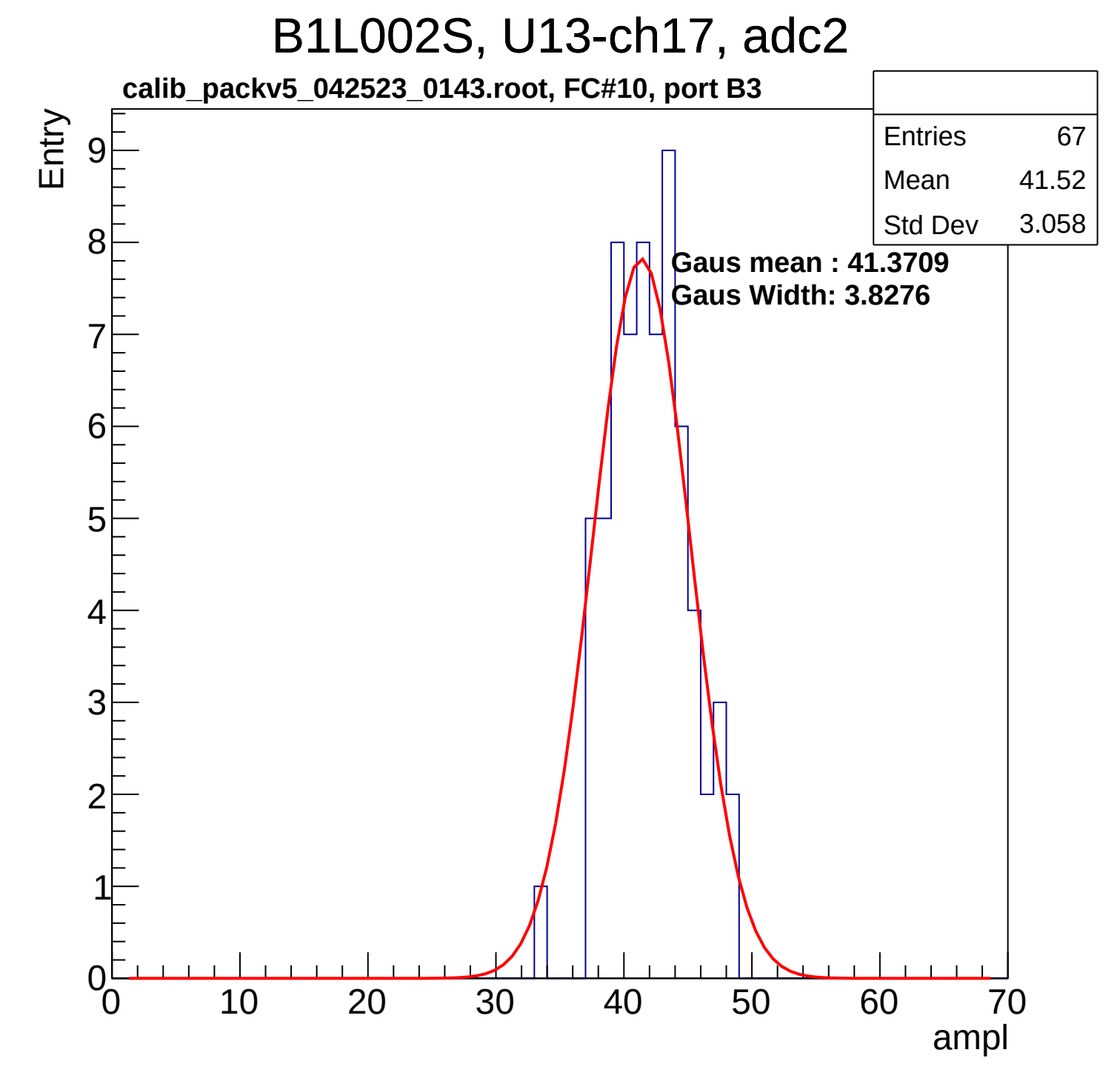

B1L002S, U13-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.21
Std Dev 7.903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.08
Std Dev 1.32
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.07
Std Dev 3.333
Gaus mean : 28.7087
Gaus Width: 3.6354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.09
Std Dev 2.996
Gaus mean : 34.3862
Gaus Width: 3.9170
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.52
Std Dev 3.058
Gaus mean : 41.3709
Gaus Width: 3.8276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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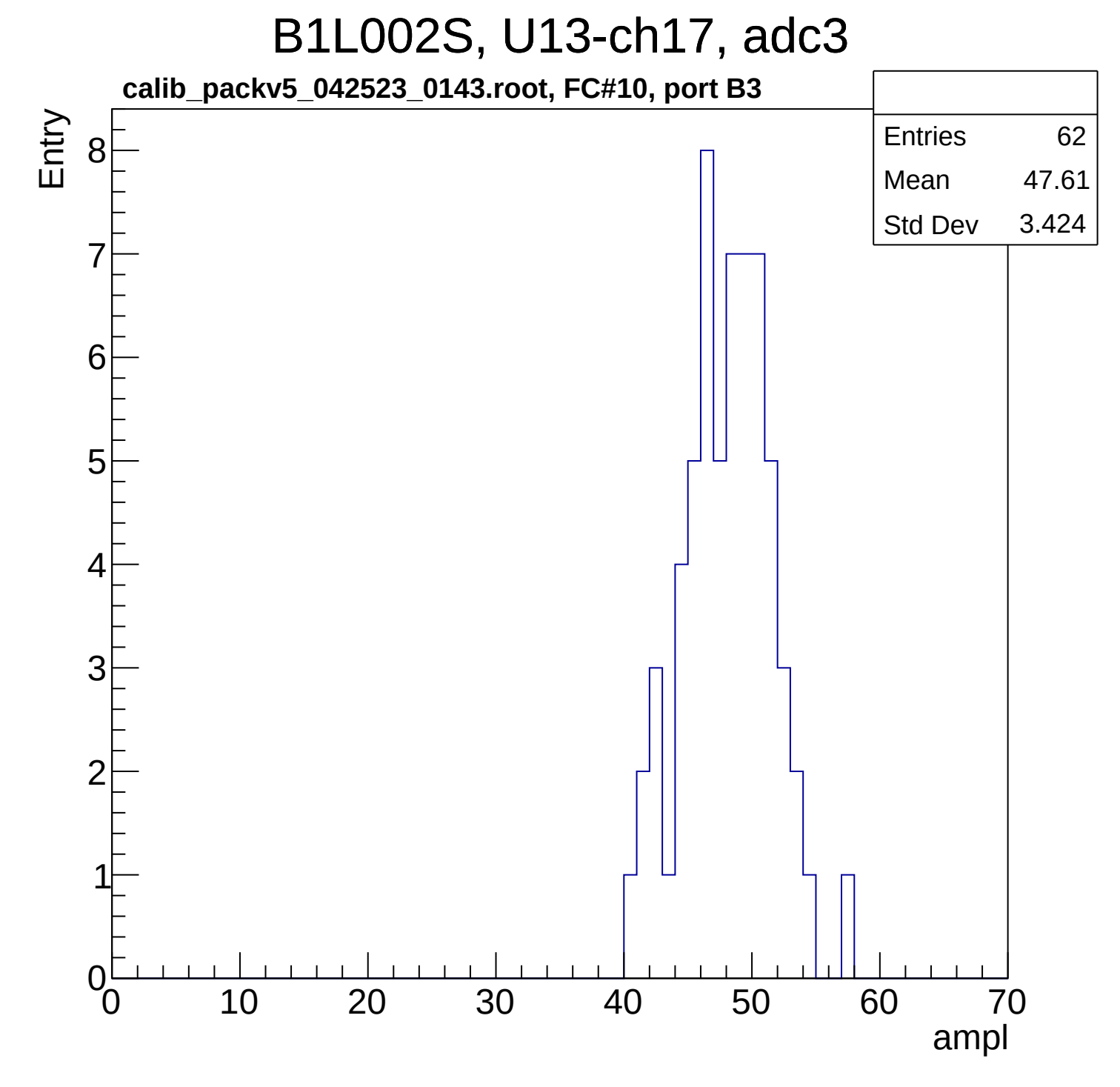
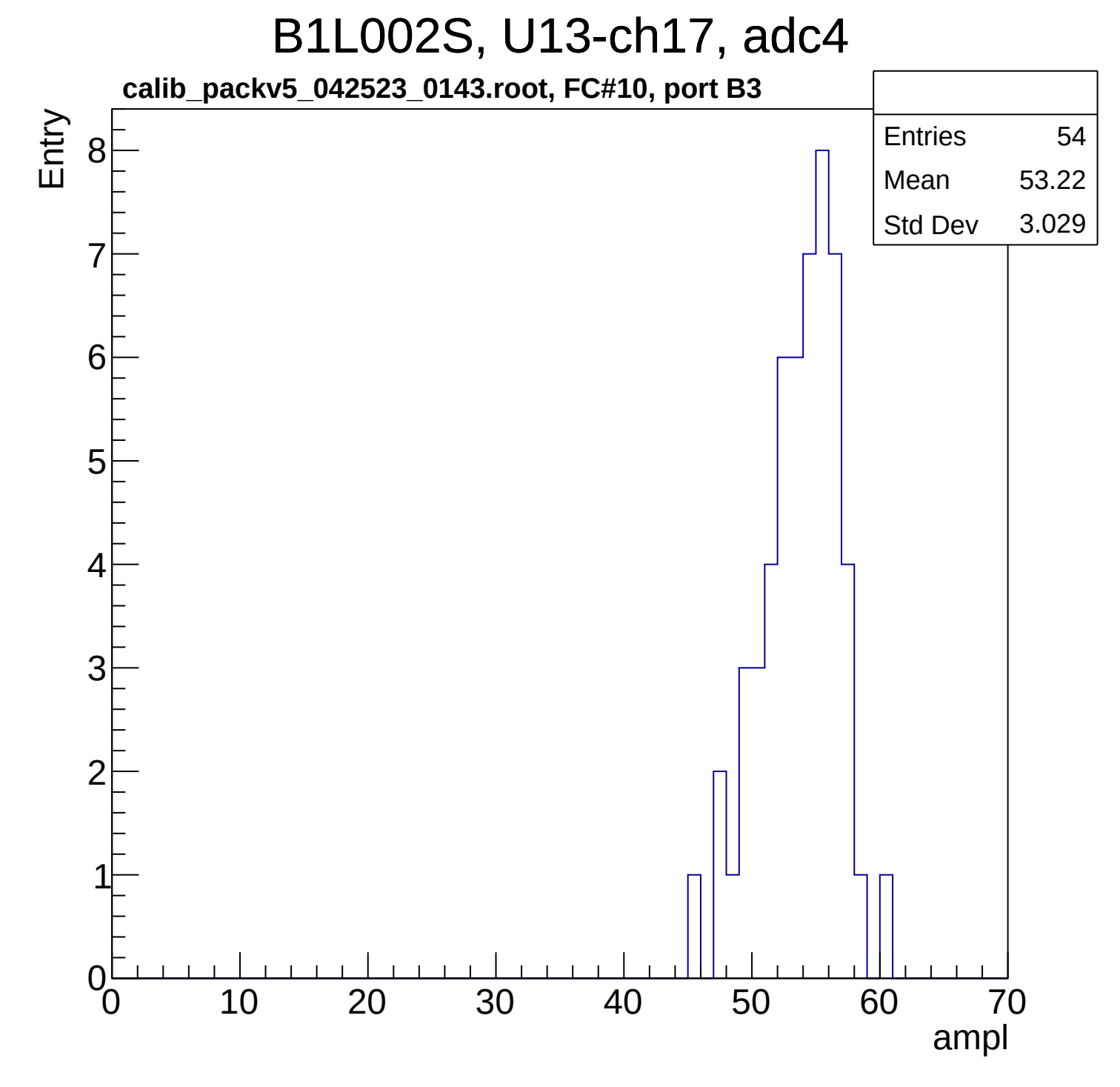
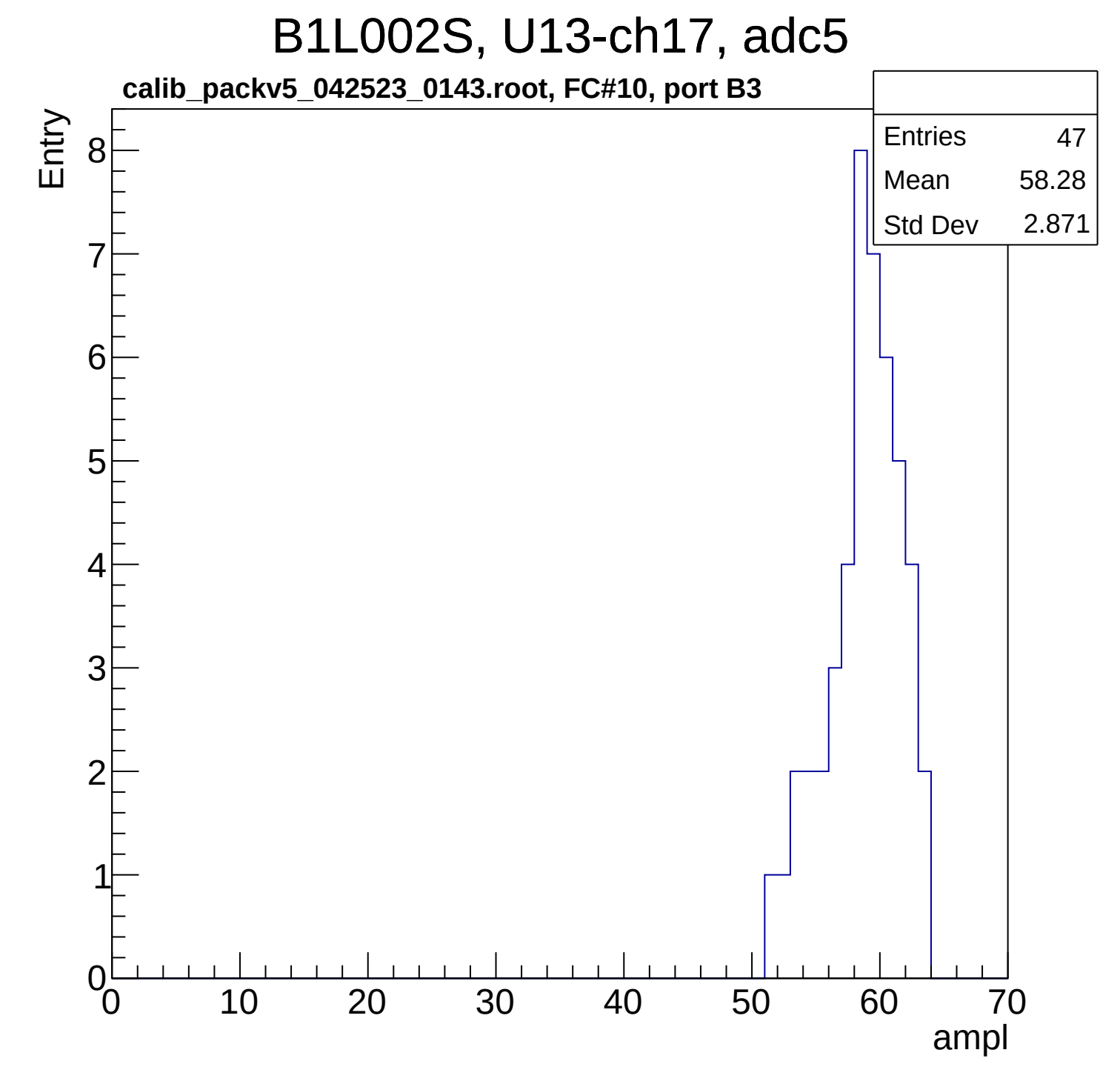

B1L002S, U13-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.61
Std Dev 3.424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.22
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.28
Std Dev 2.871
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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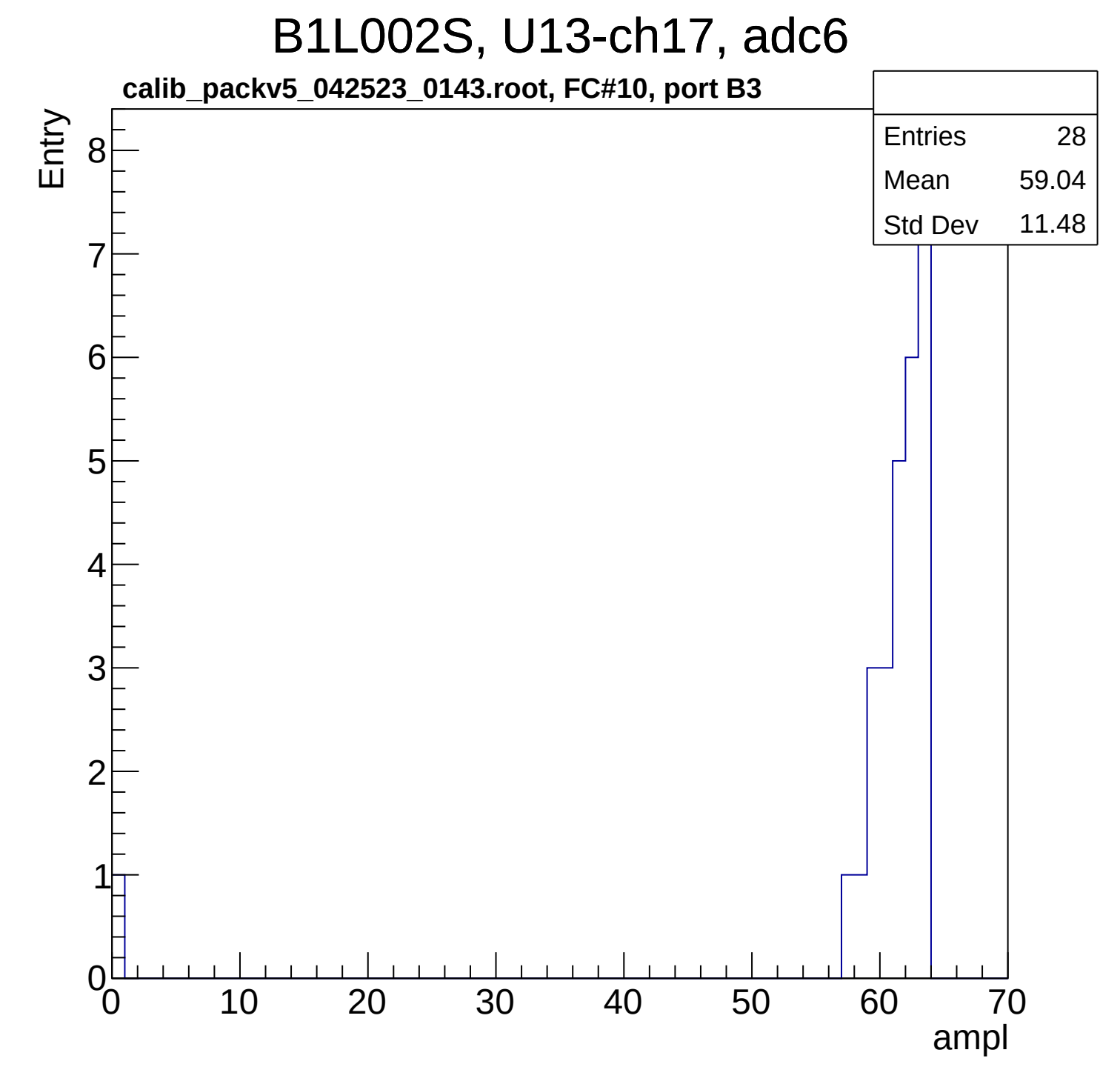
B1L002S, U13-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 59.04
Std Dev 11.48
Entry
0
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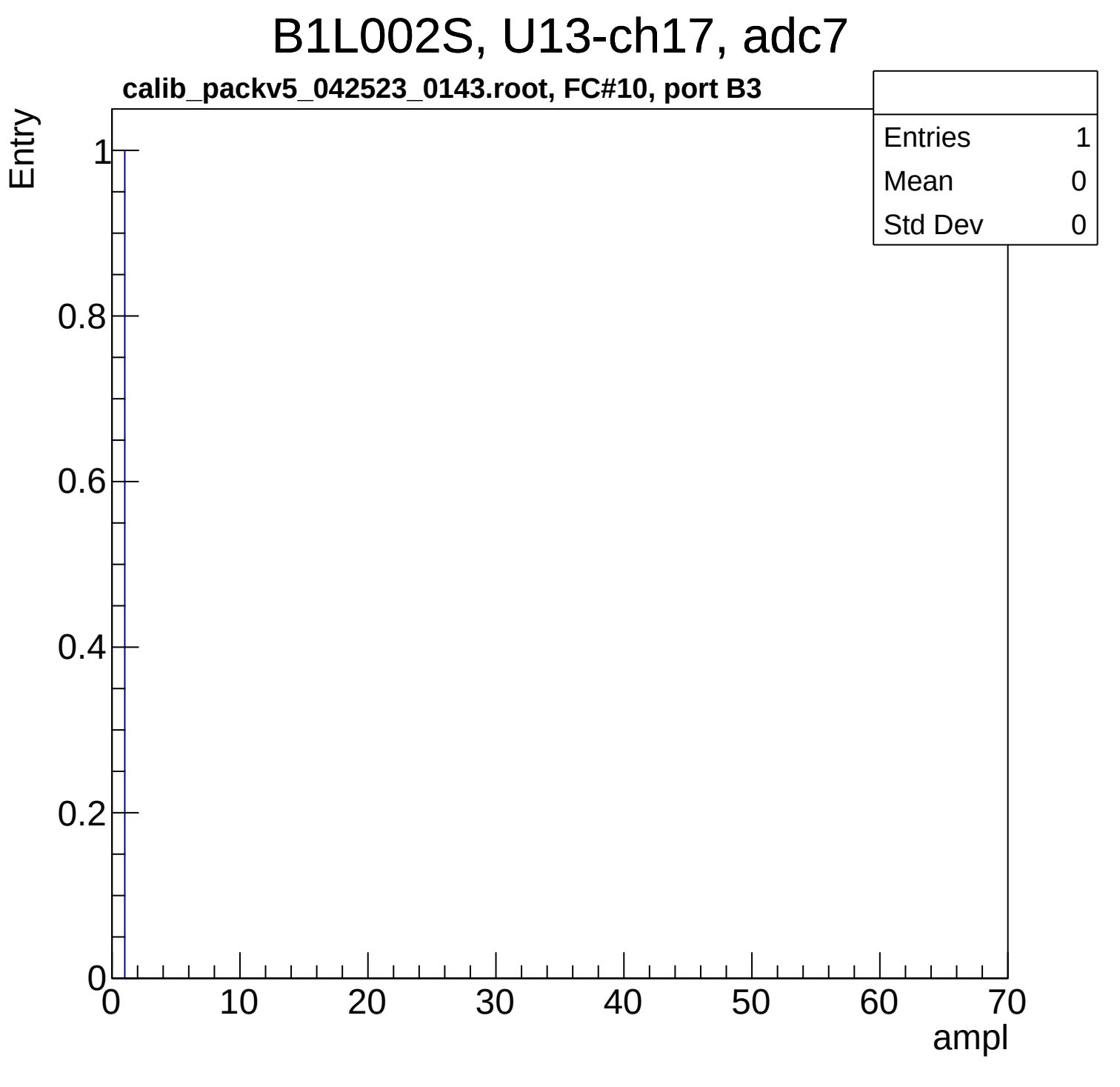
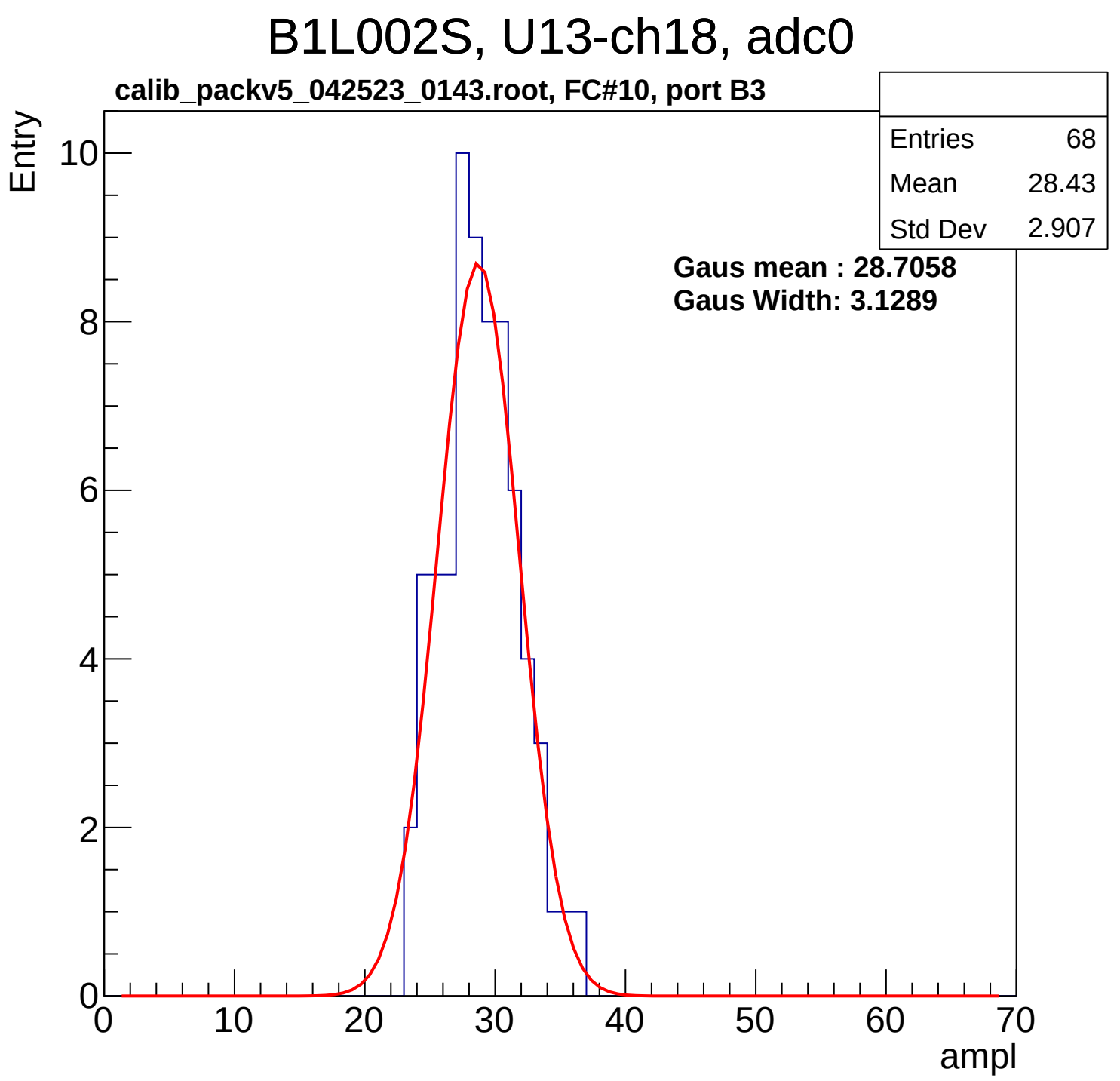
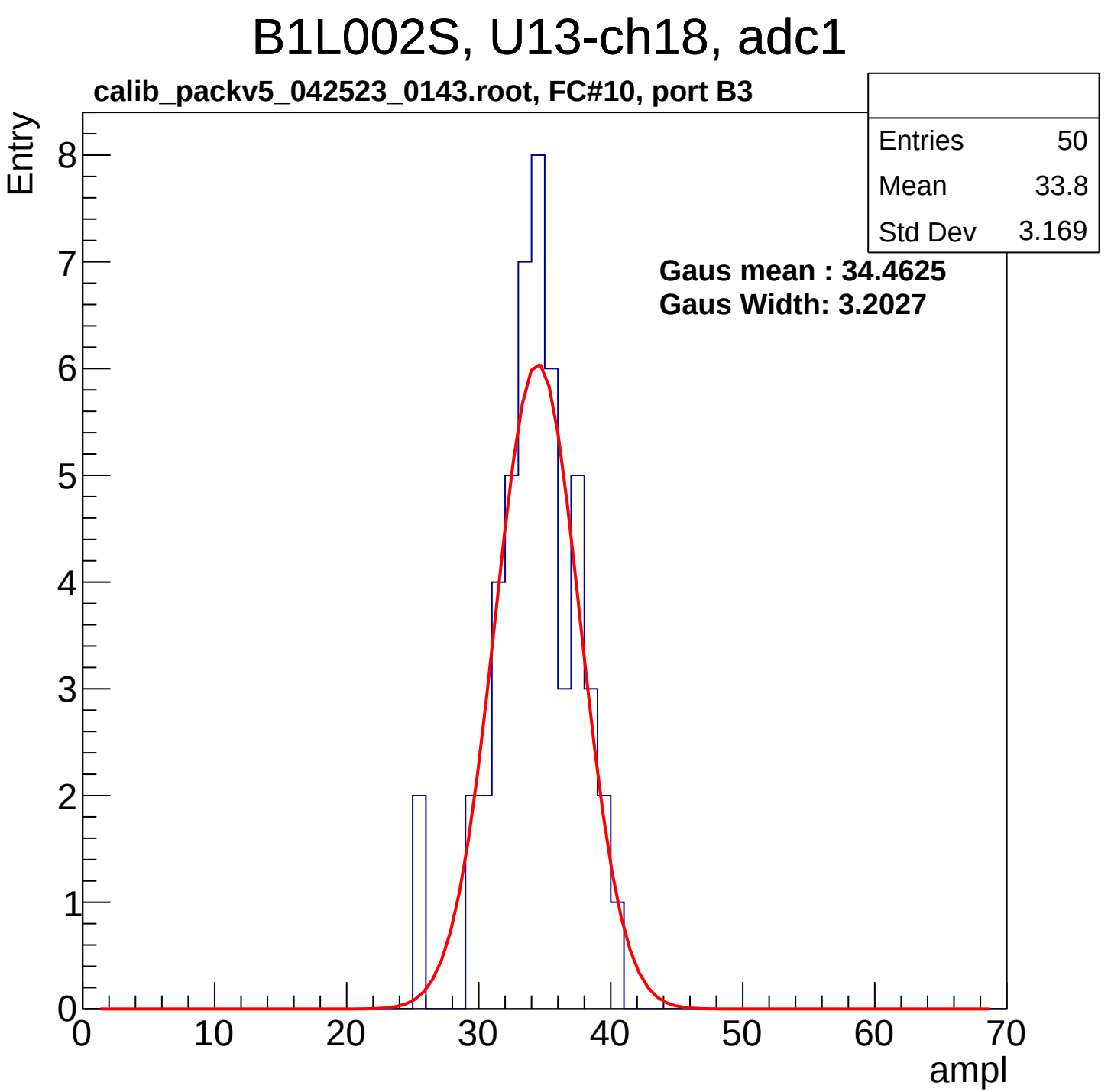
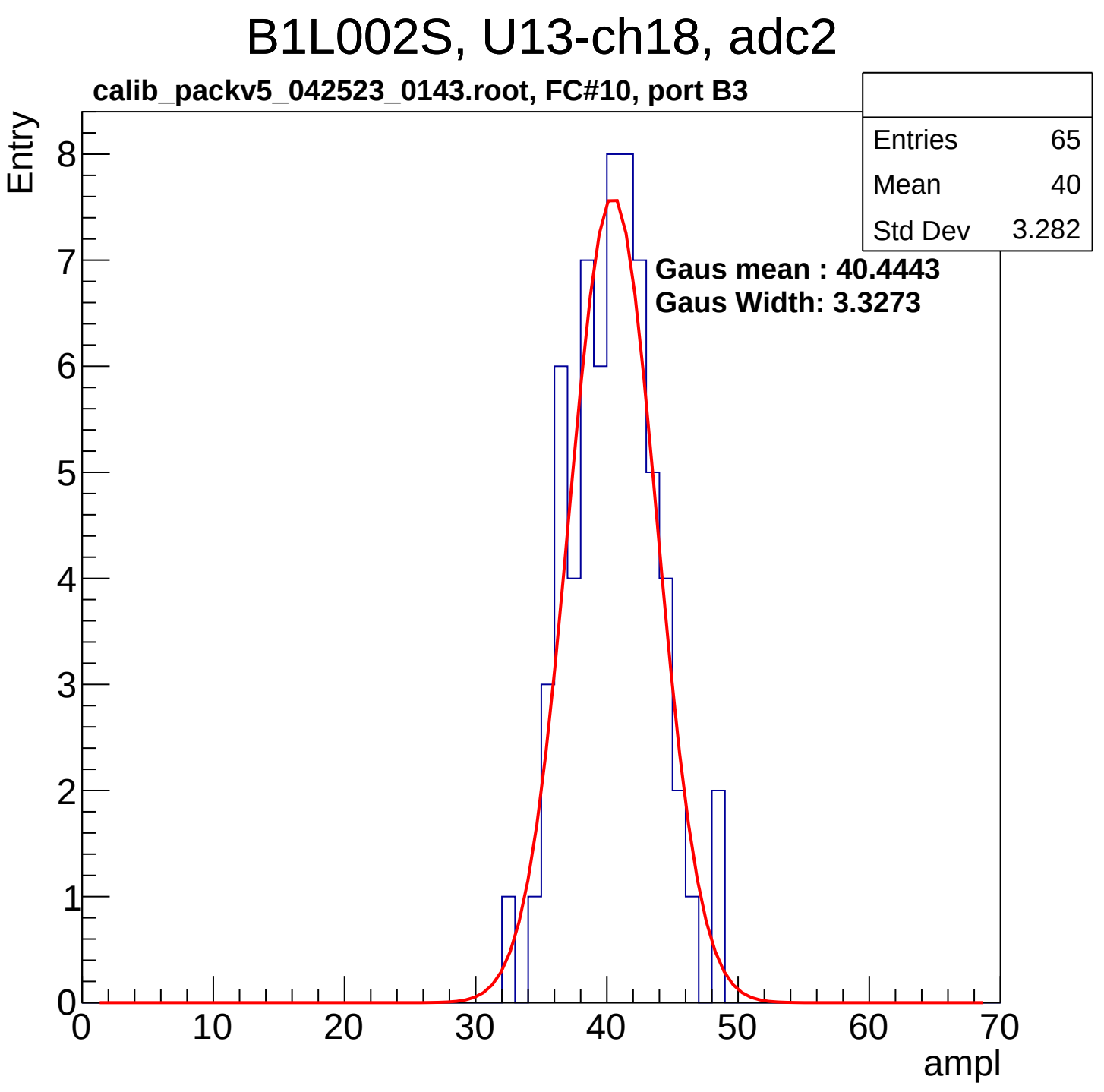
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.43
Std Dev 2.907
Gaus mean : 28.7058
Gaus Width: 3.1289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 33.8
Std Dev 3.169
Gaus mean : 34.4625
Gaus Width: 3.2027
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40
Std Dev 3.282
Gaus mean : 40.4443
Gaus Width: 3.3273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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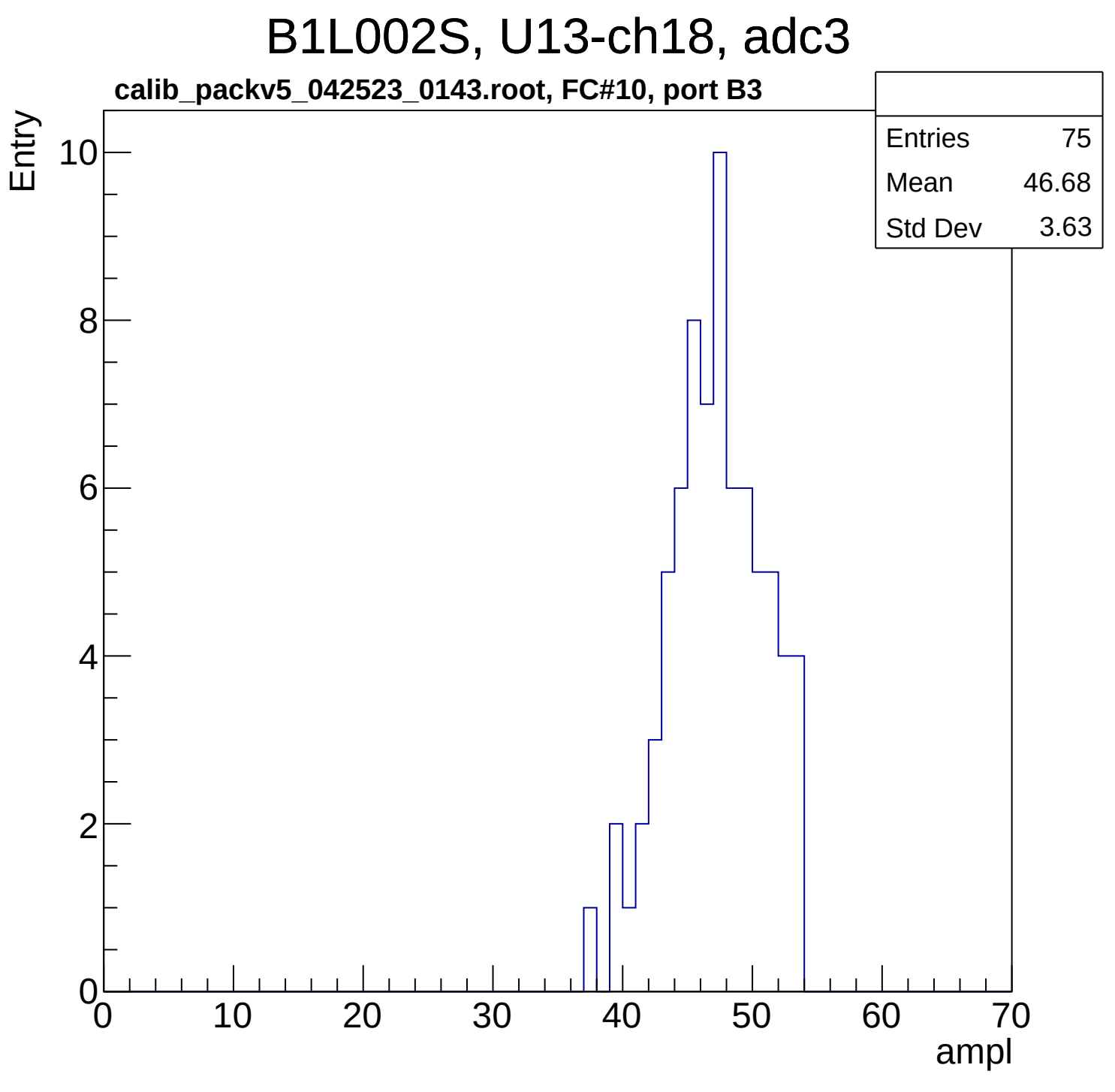
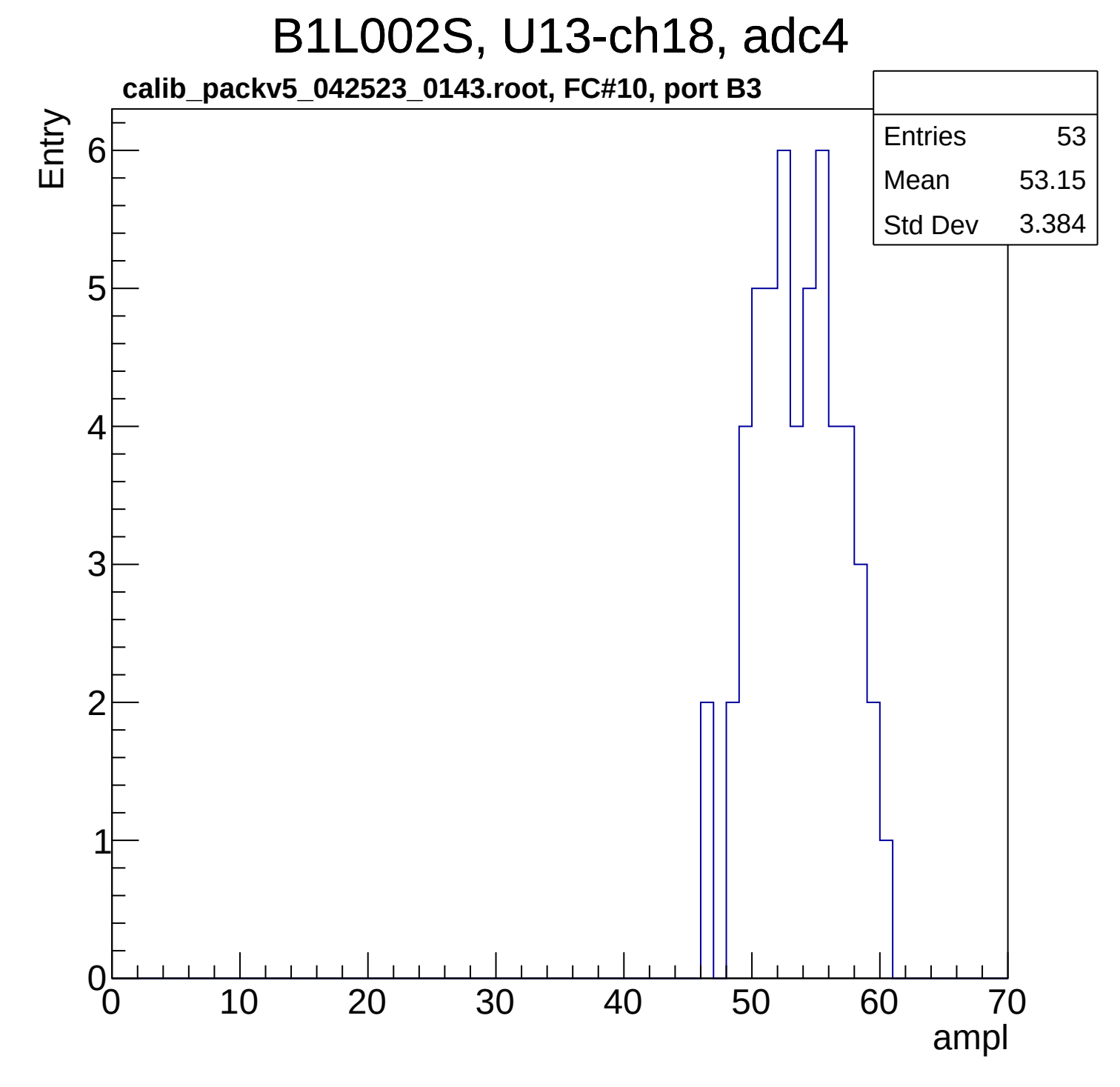
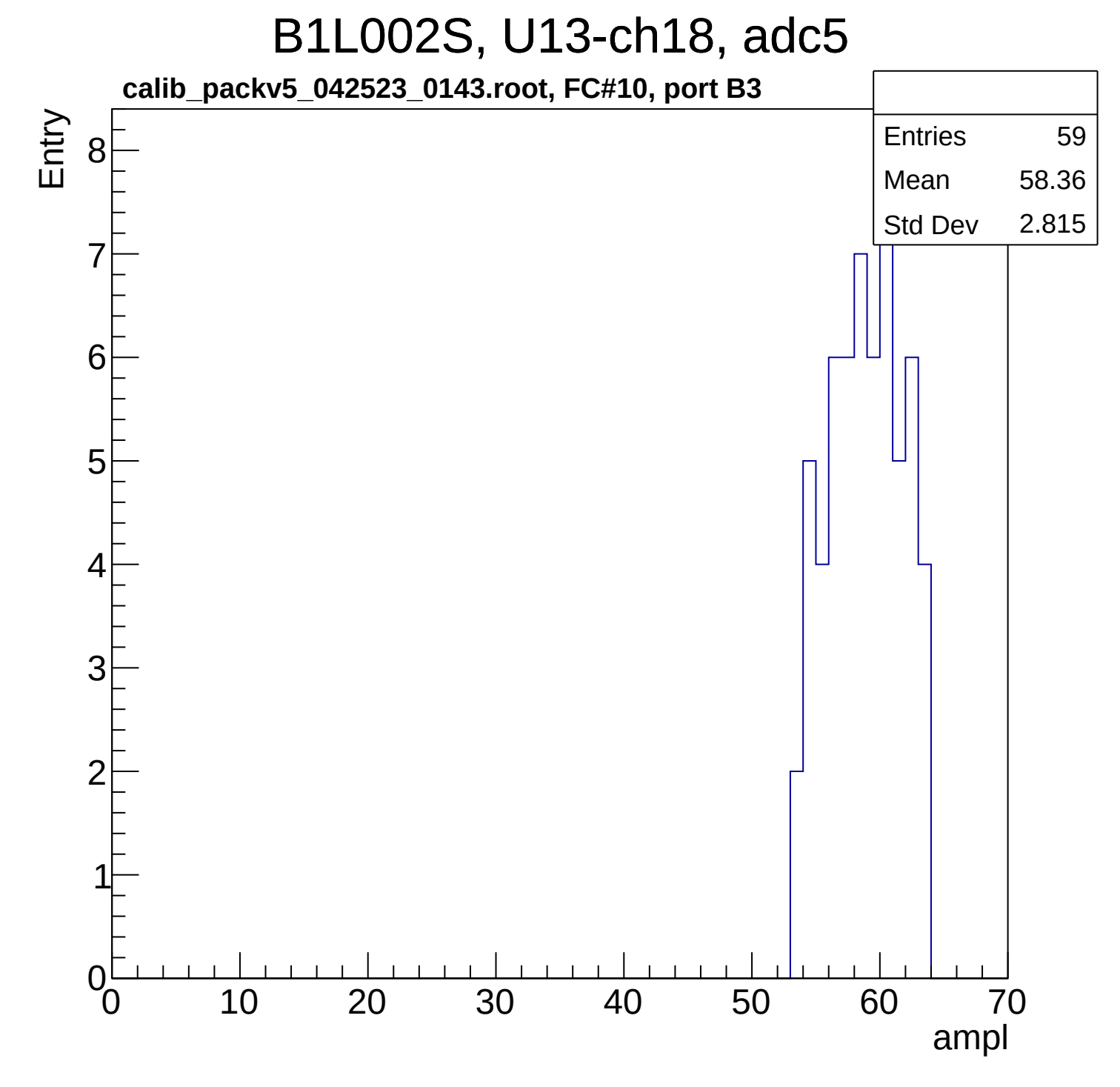
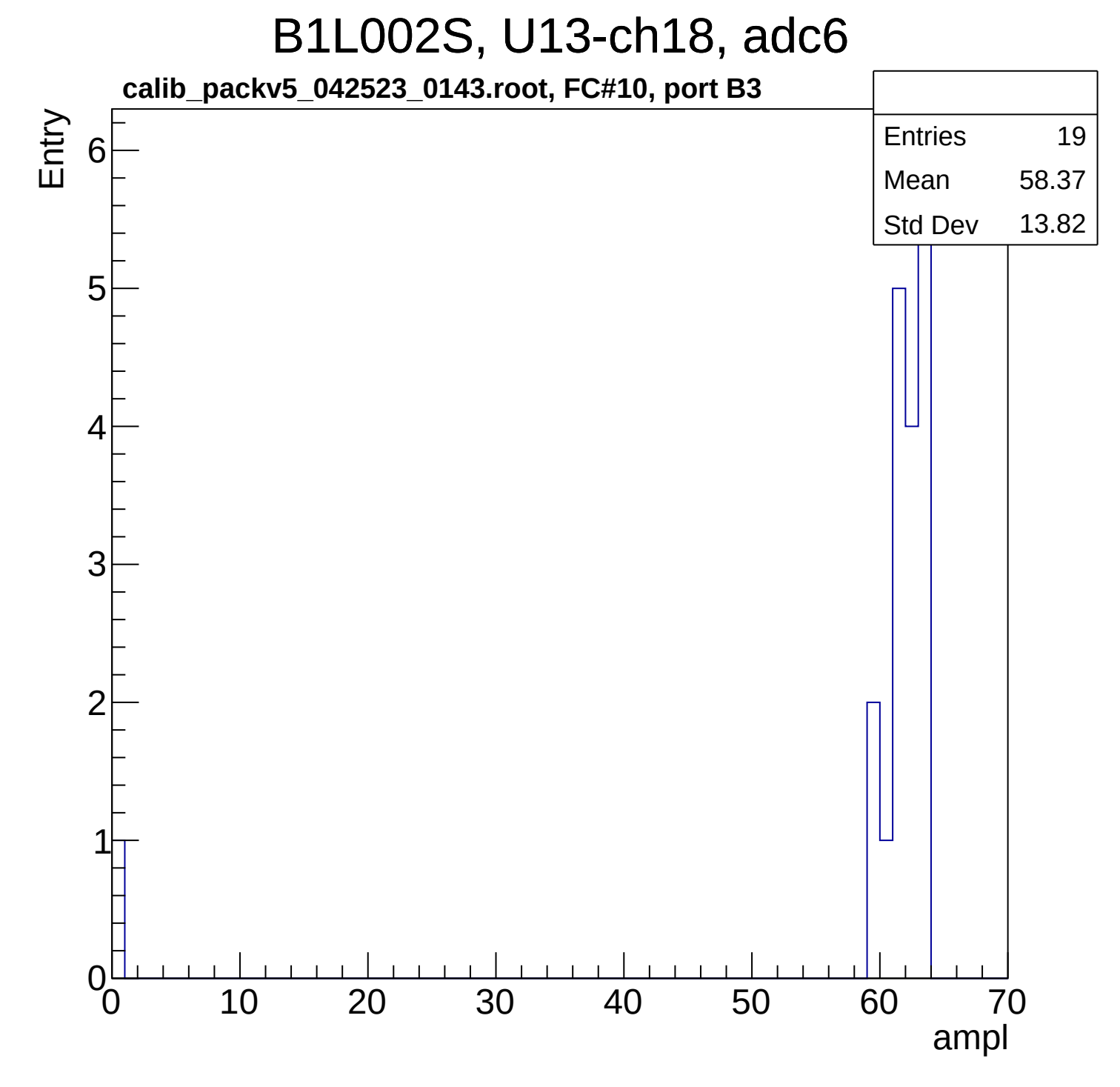
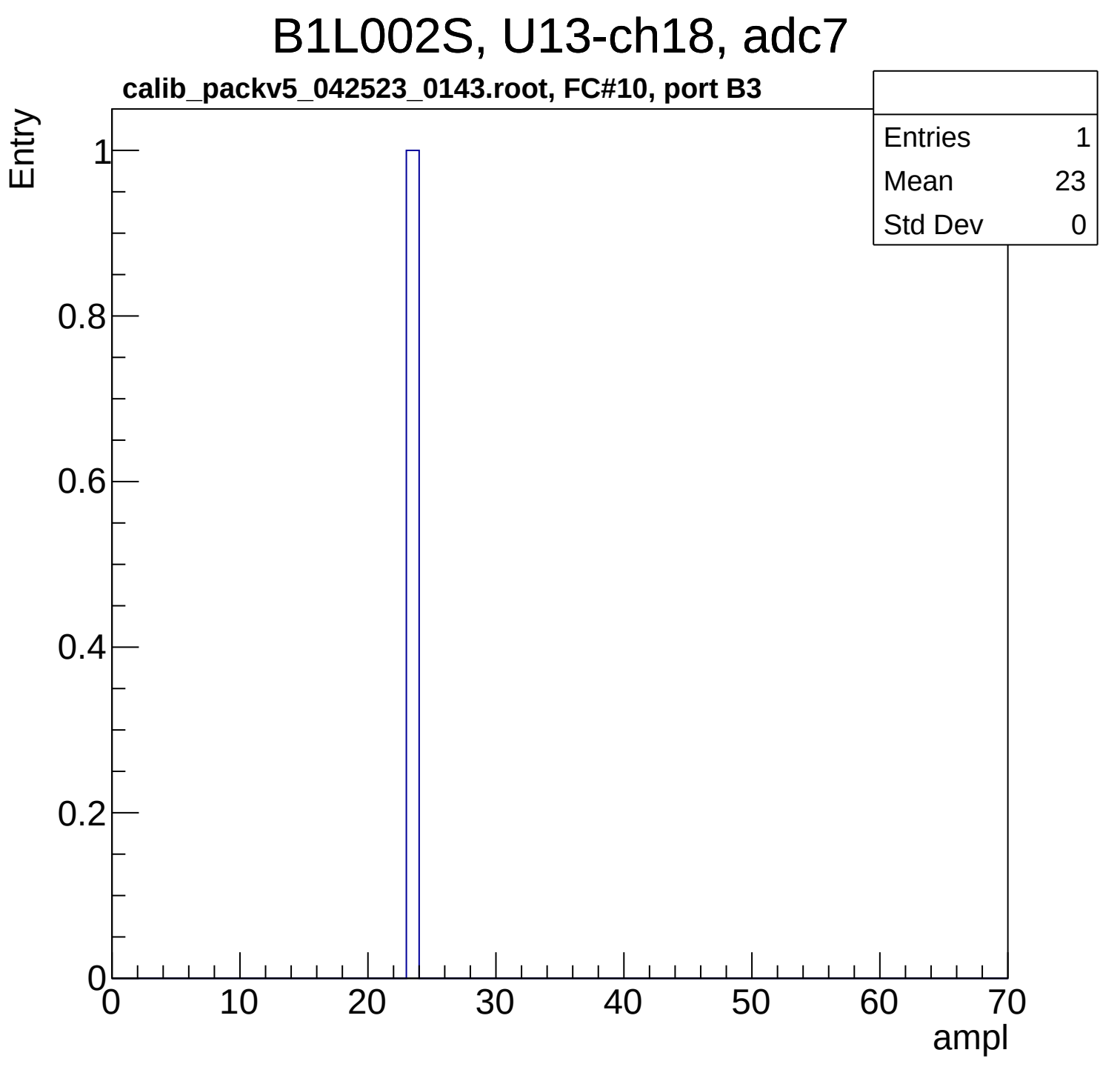

B1L002S, U13-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 46.68
Std Dev 3.63
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.15
Std Dev 3.384
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.36
Std Dev 2.815
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.37
Std Dev 13.82
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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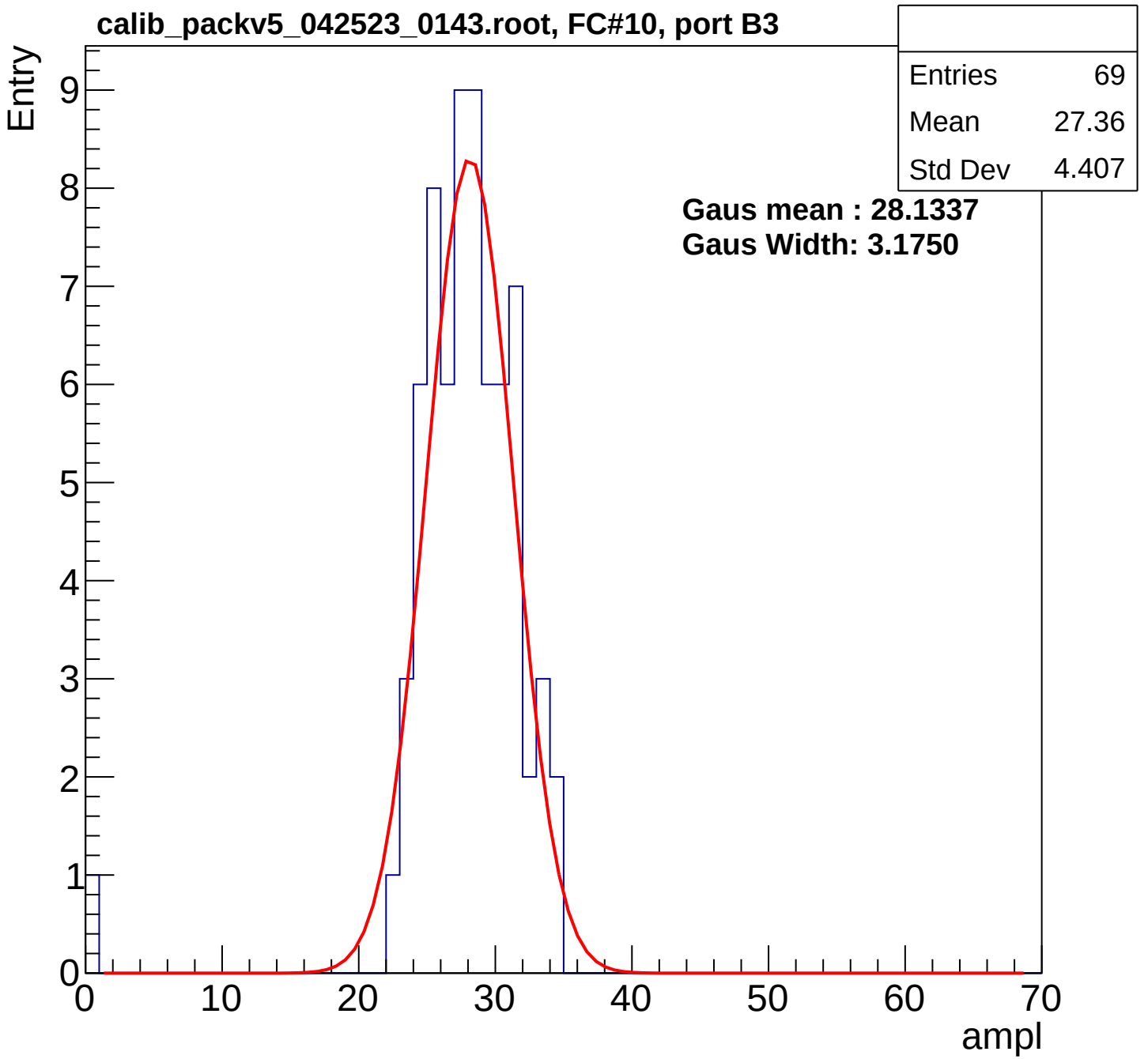
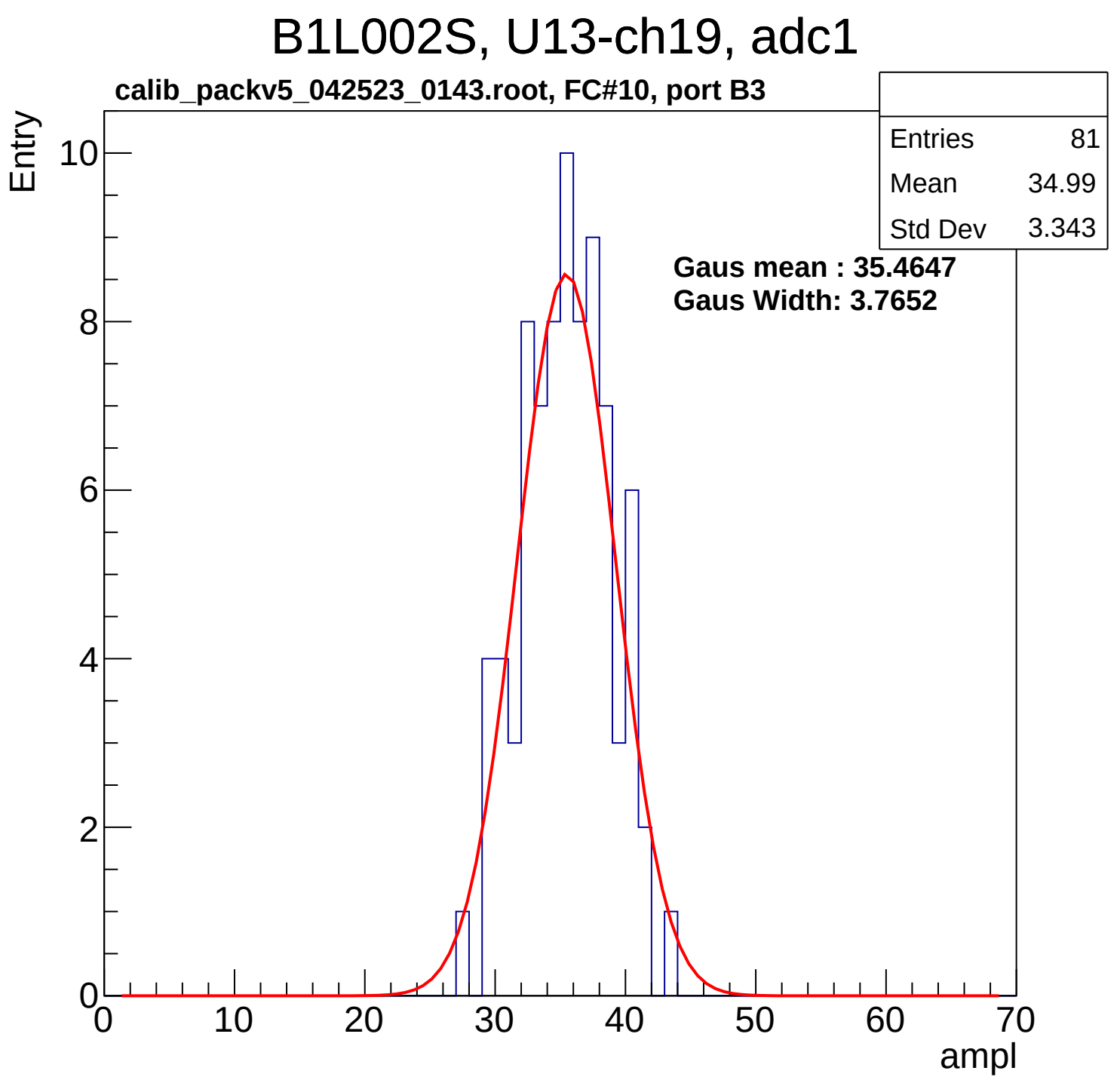
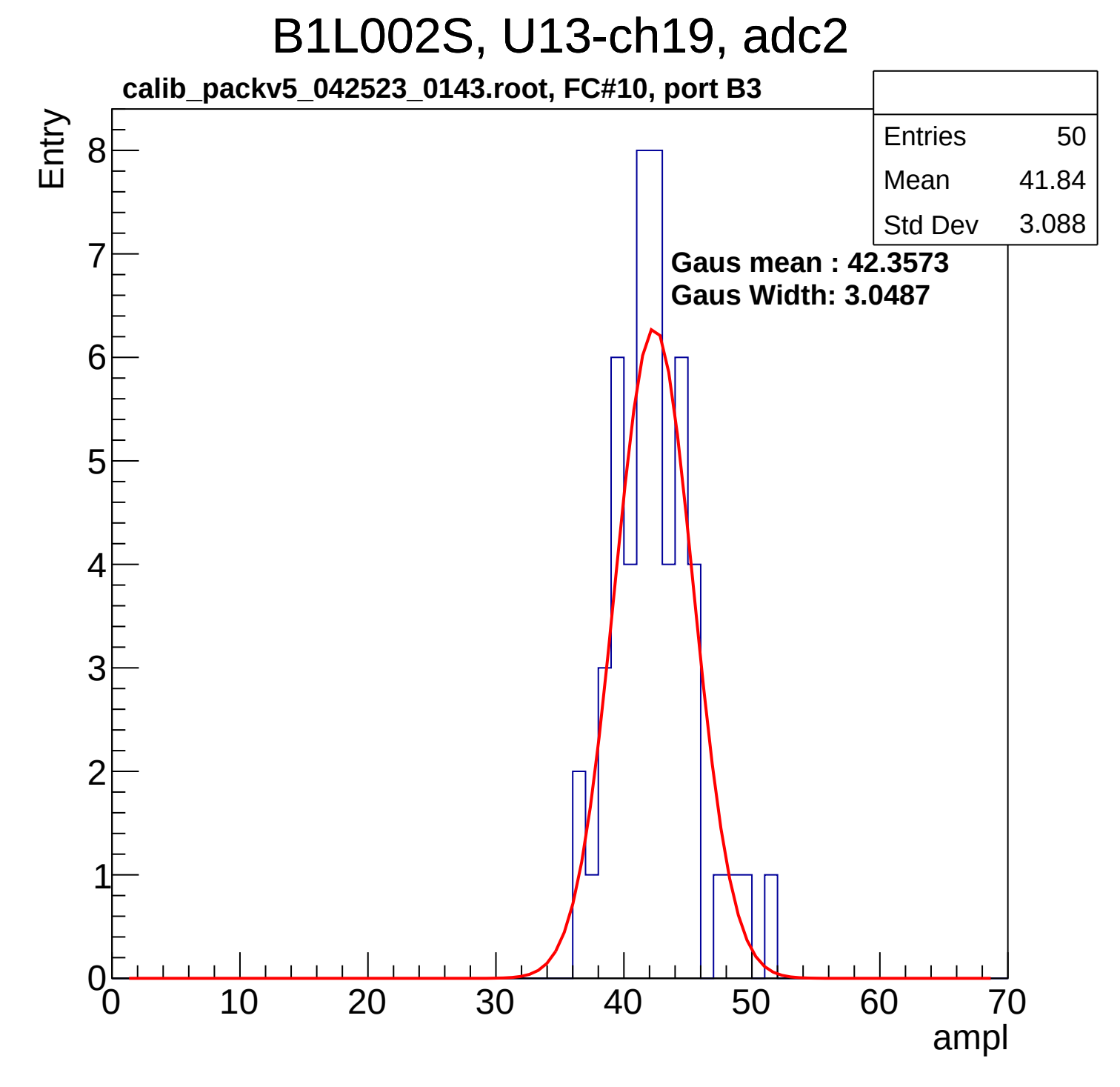
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.36
Std Dev 4.407
Gaus mean : 28.1337
Gaus Width: 3.1750
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.99
Std Dev 3.343
Gaus mean : 35.4647
Gaus Width: 3.7652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.84
Std Dev 3.088
Gaus mean : 42.3573
Gaus Width: 3.0487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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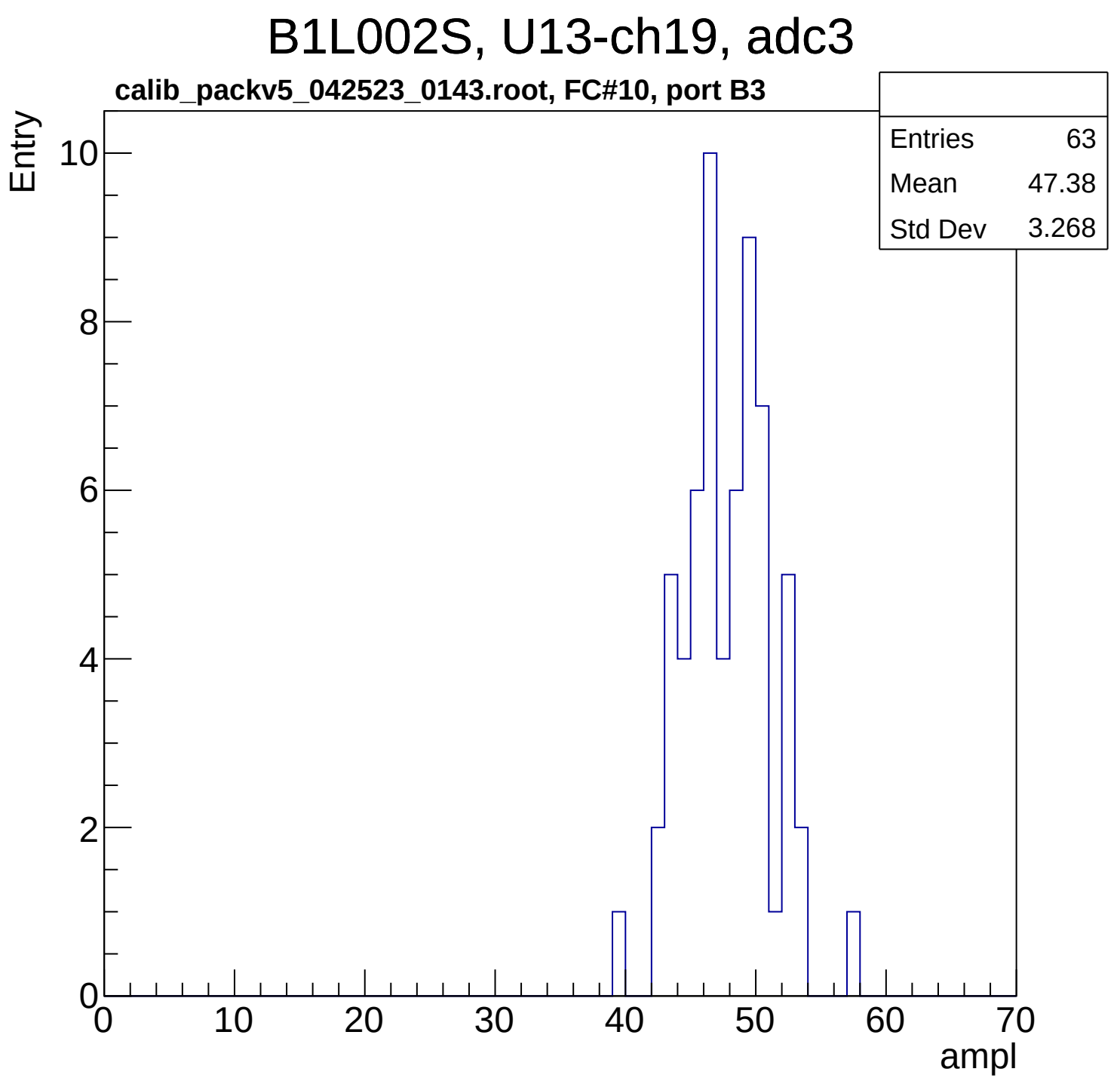
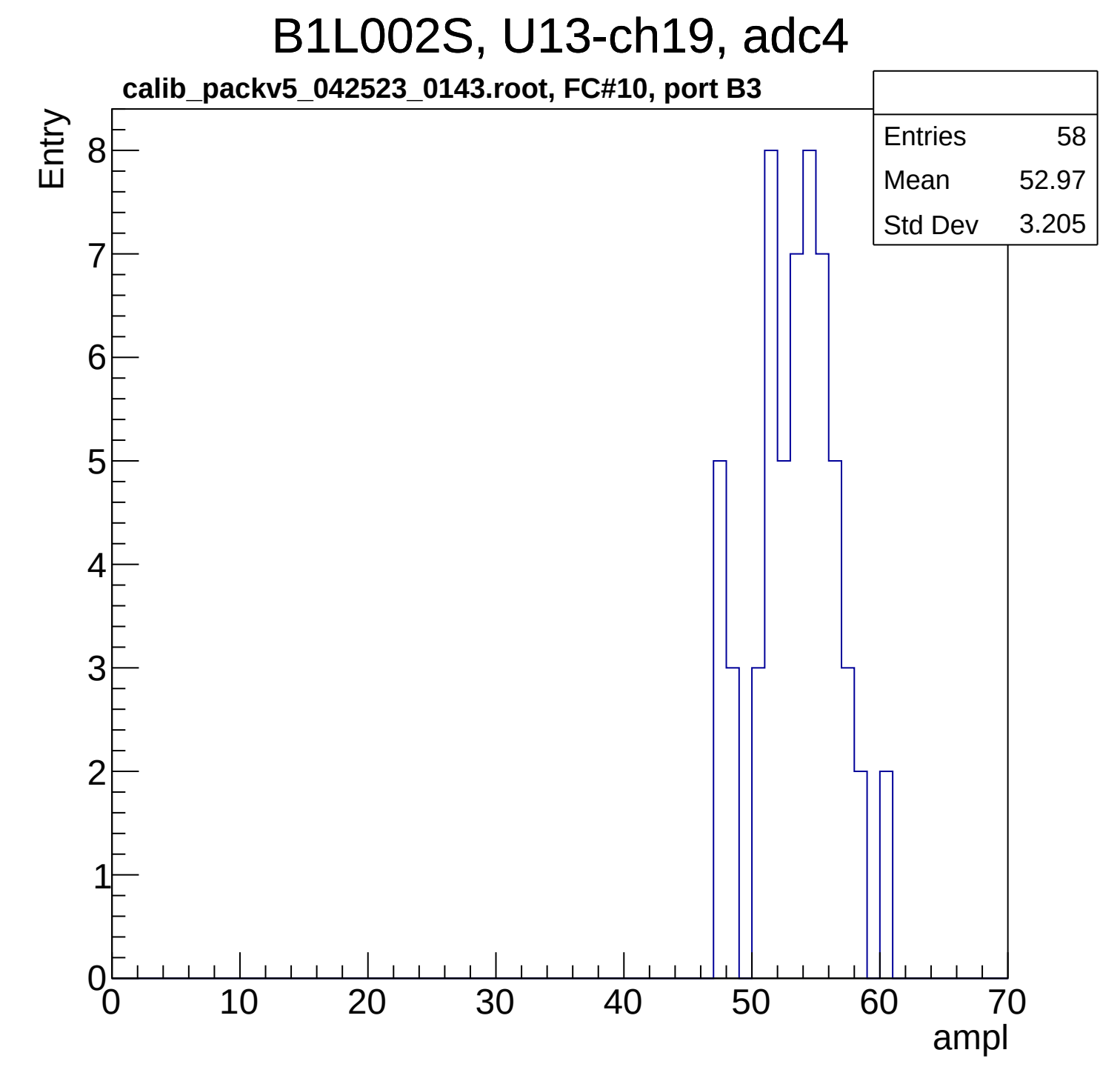

B1L002S, U13-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.38
Std Dev 3.268
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.97
Std Dev 3.205
Entry
ampl
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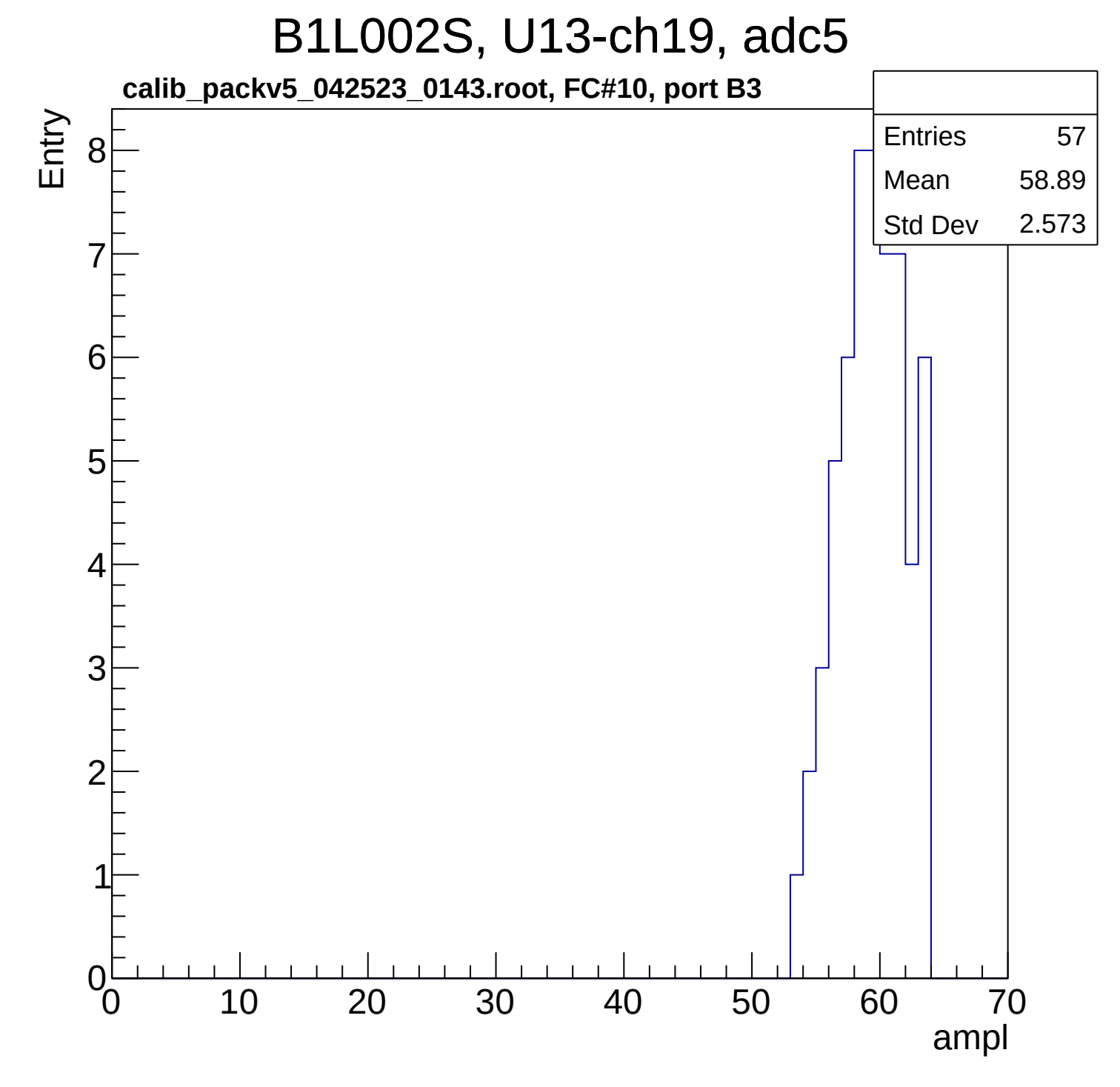
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.89
Std Dev 2.573
Entry
ampl
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2
3
4
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7
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0
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40
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60
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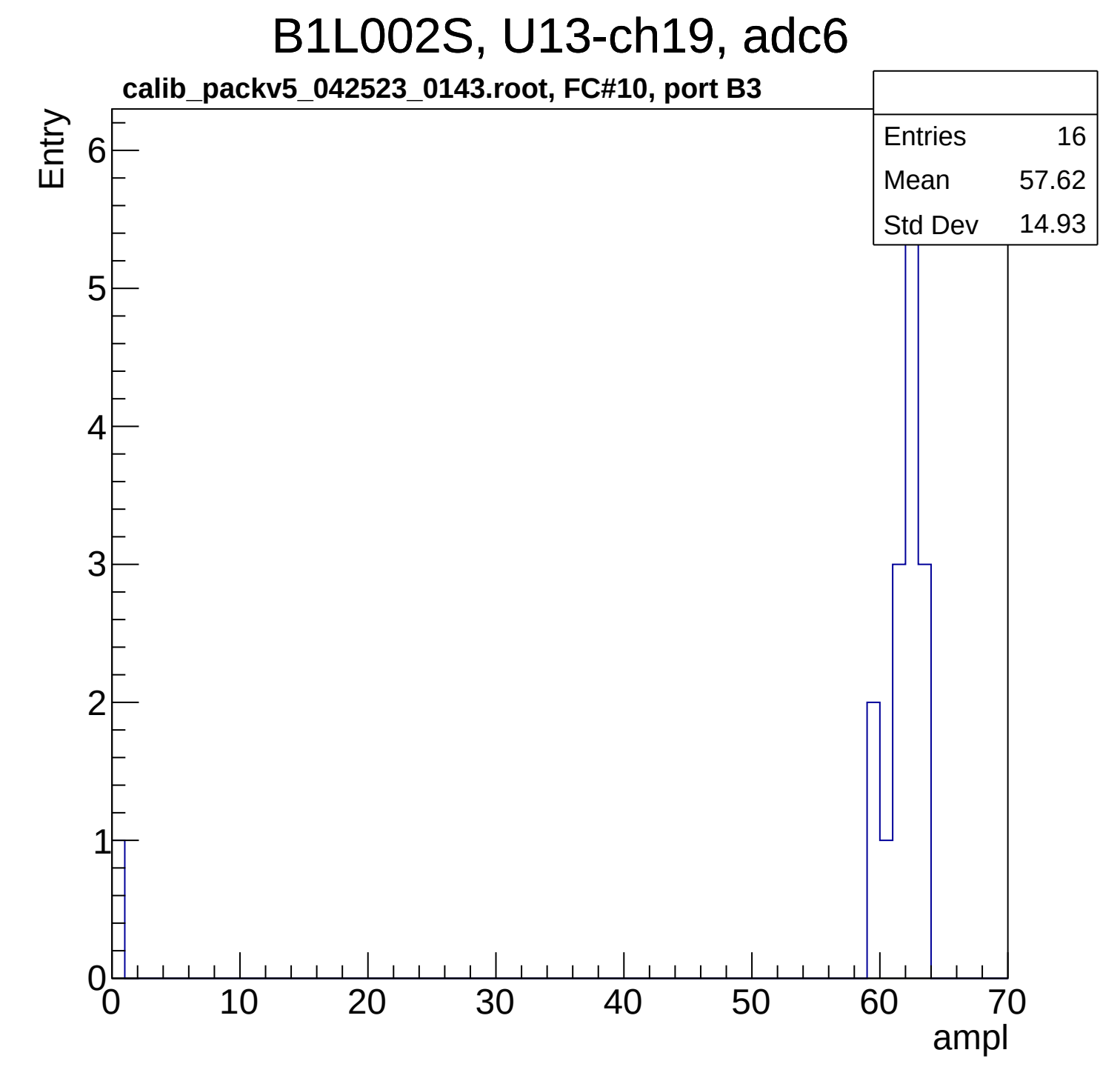
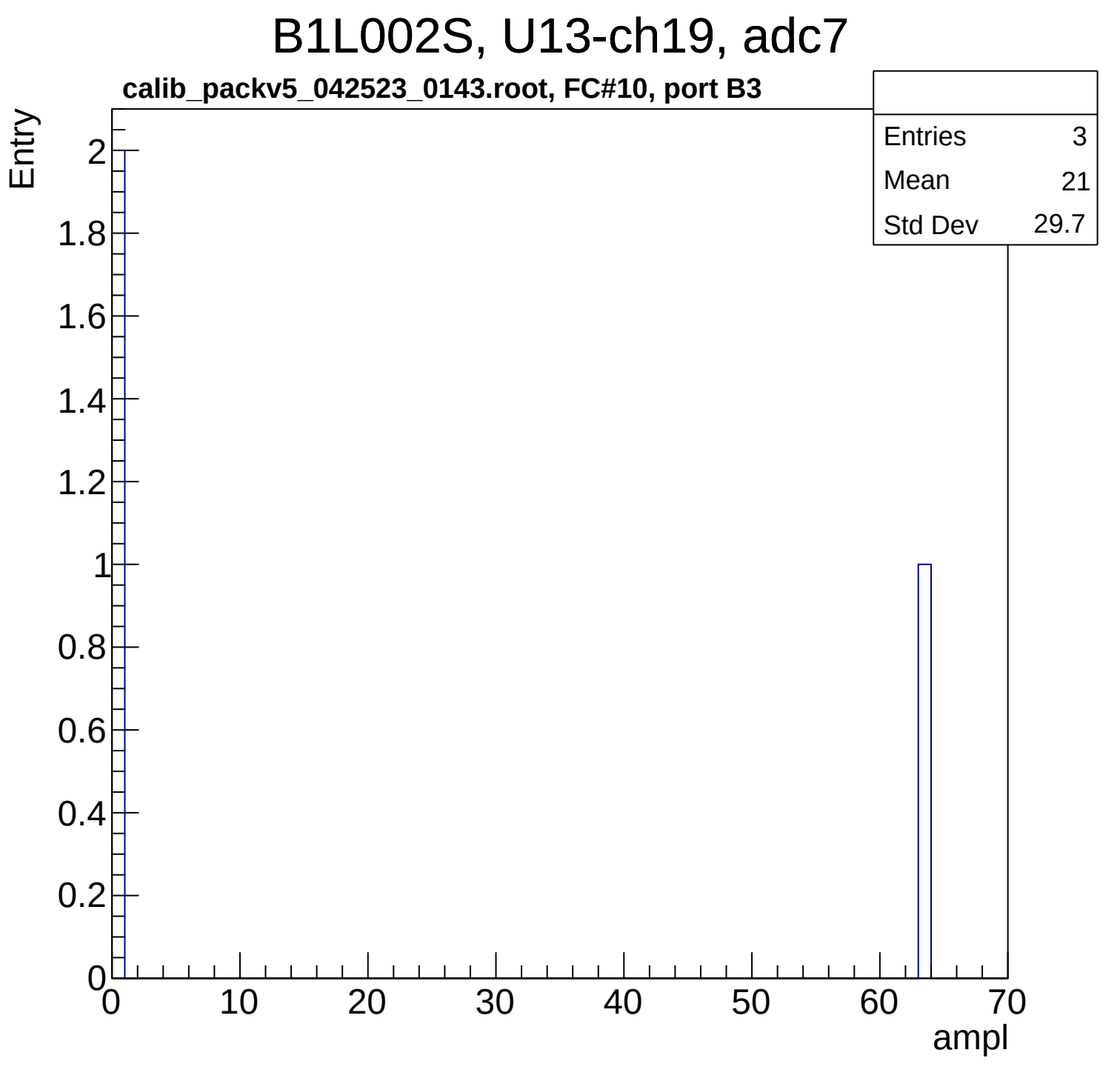
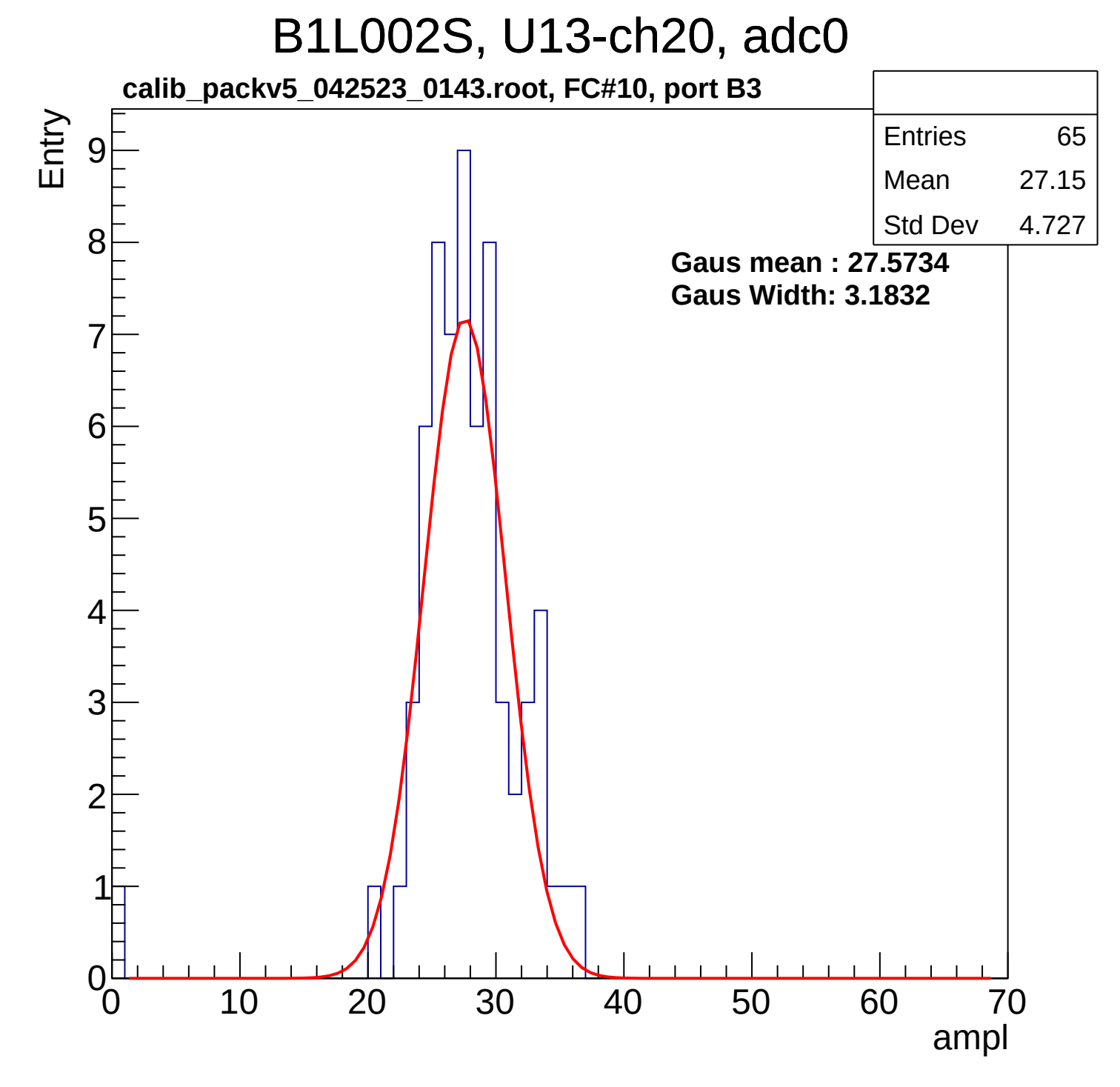

B1L002S, U13-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.62
Std Dev 14.93
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.15
Std Dev 4.727
Gaus mean : 27.5734
Gaus Width: 3.1832
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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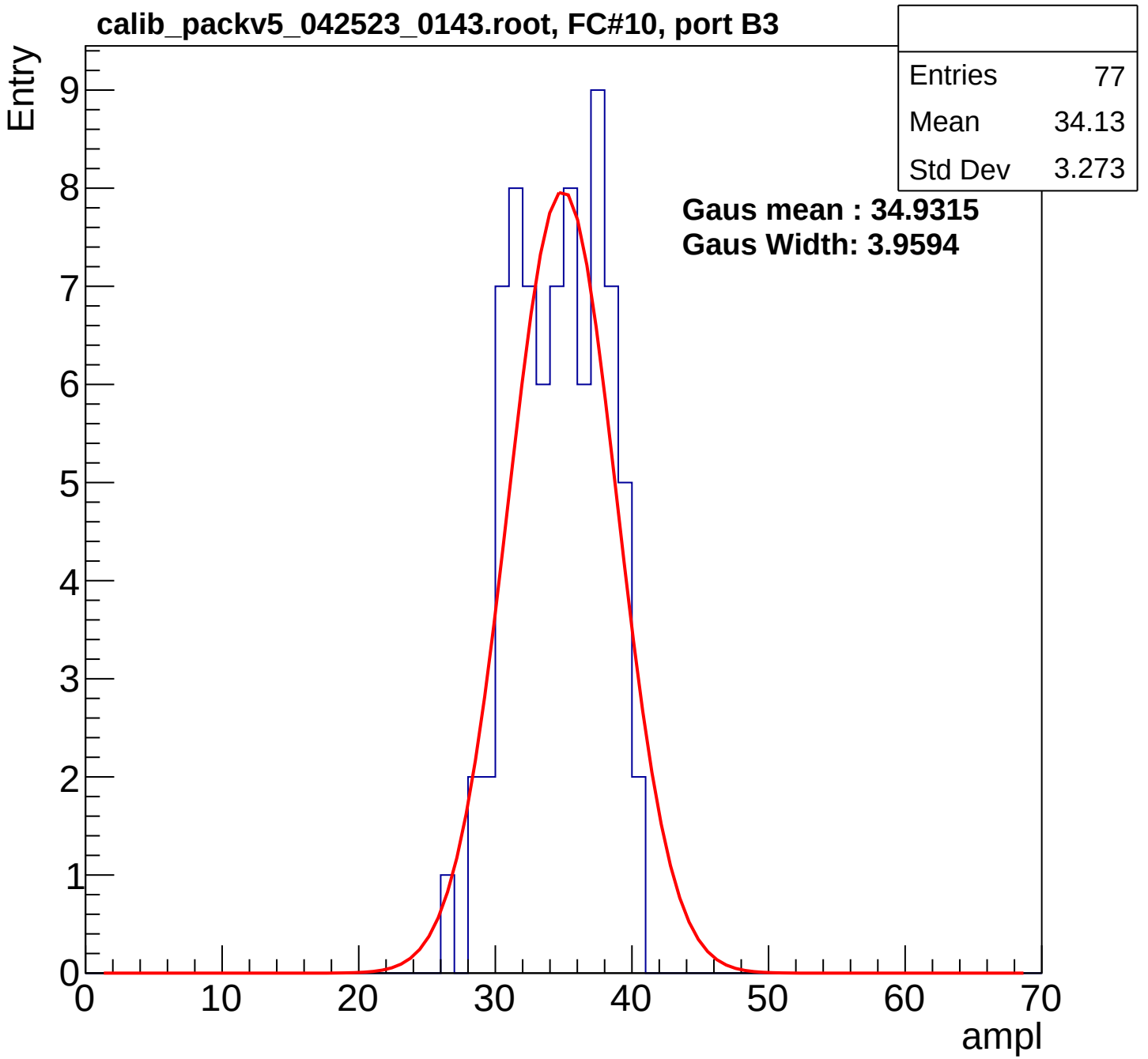
B1L002S, U13-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.13
Std Dev 3.273
Gaus mean : 34.9315
Gaus Width: 3.9594
Entry
ampl
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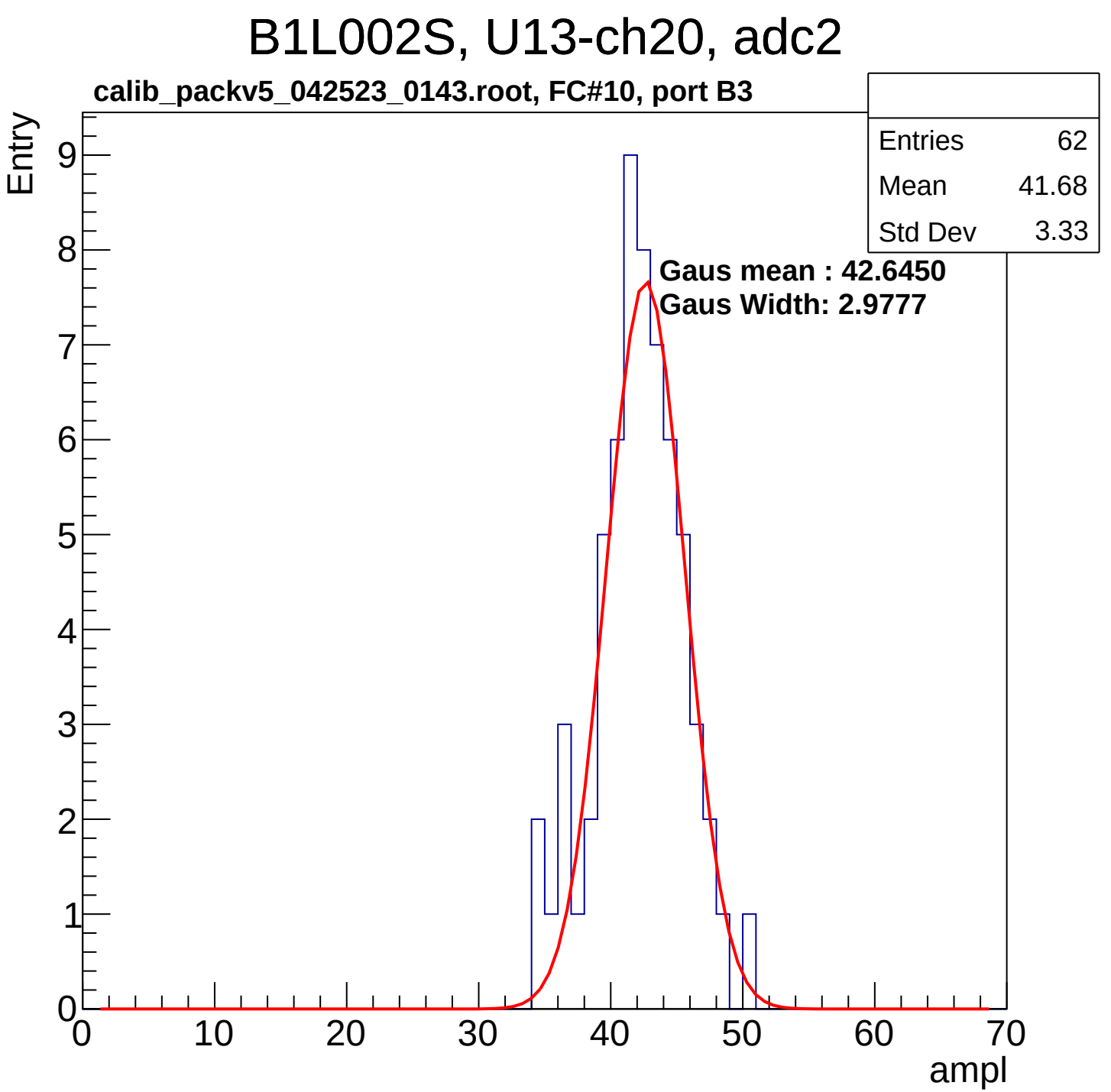
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.68
Std Dev 3.33
Gaus mean : 42.6450
Gaus Width: 2.9777
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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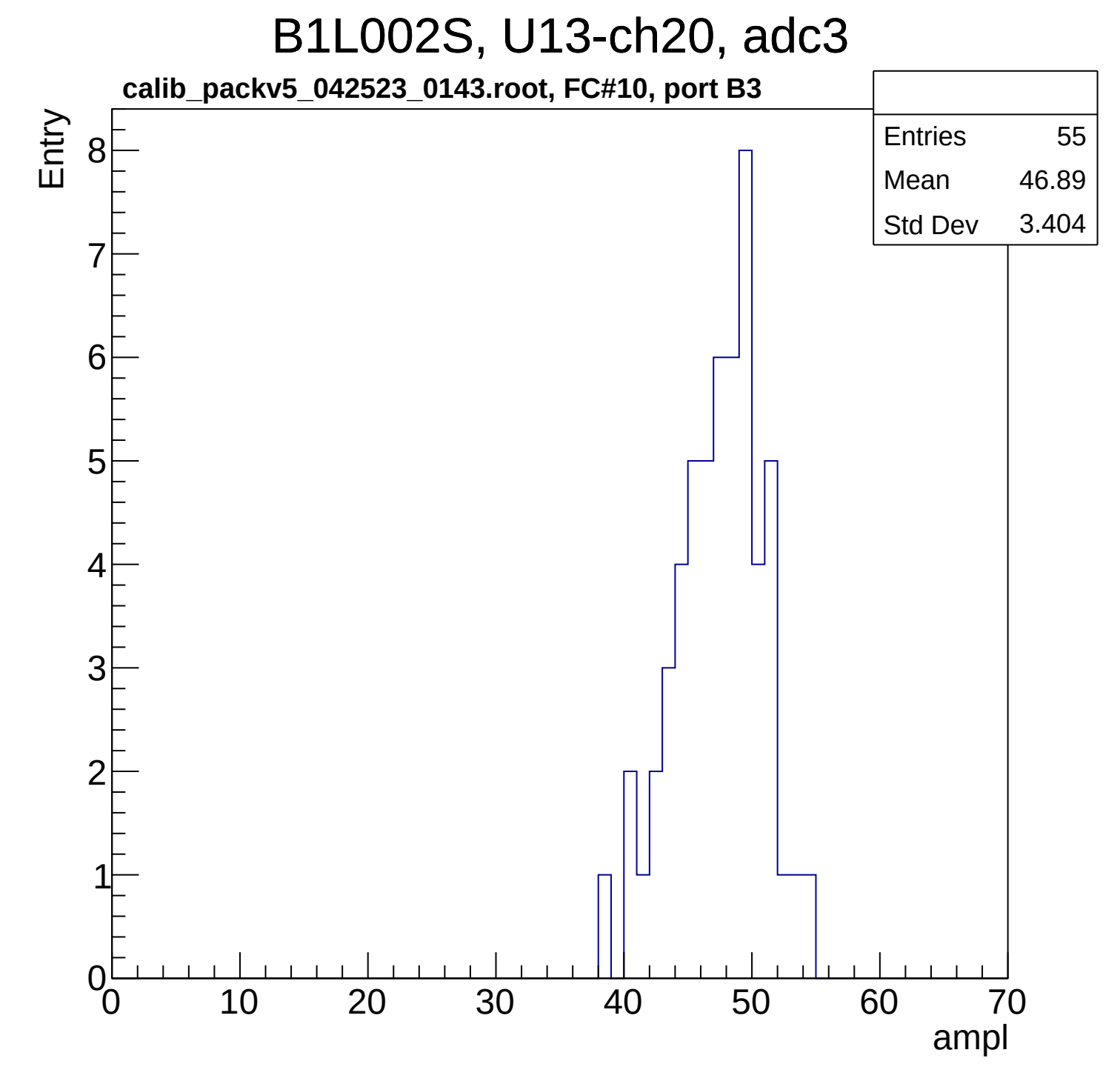
B1L002S, U13-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.89
Std Dev 3.404
Entry
ampl
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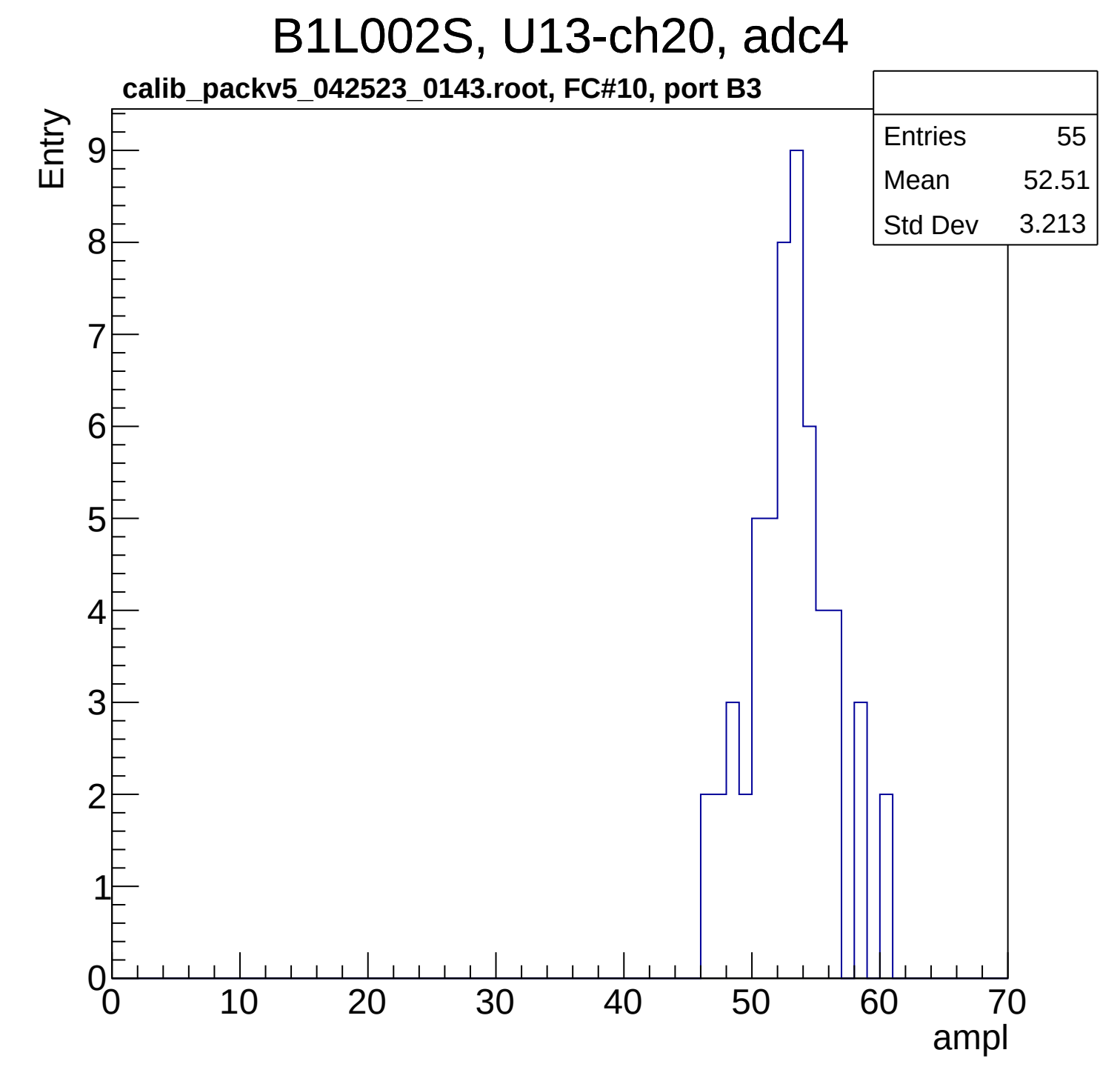
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.51
Std Dev 3.213
Entry
ampl
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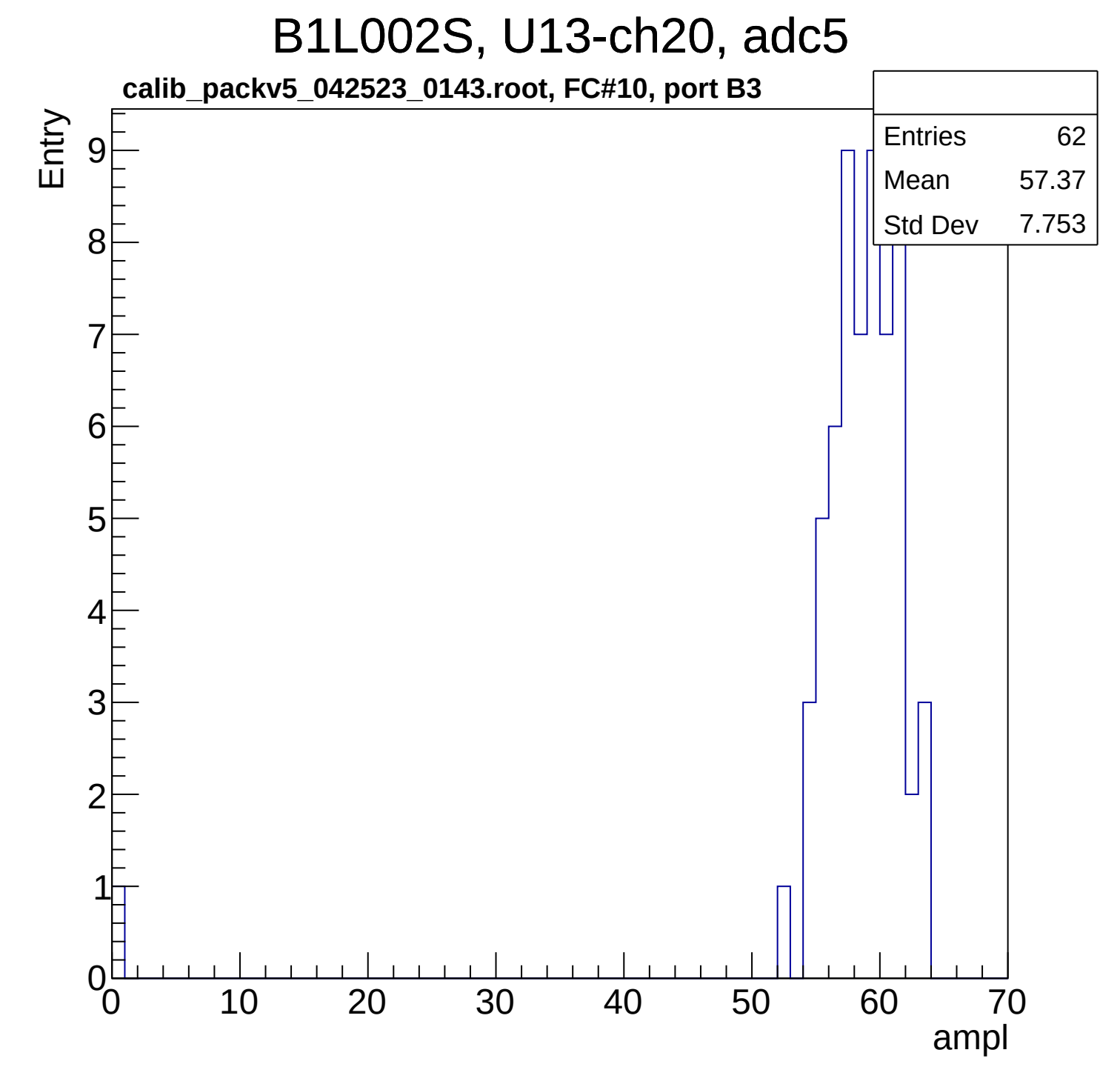
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.37
Std Dev 7.753
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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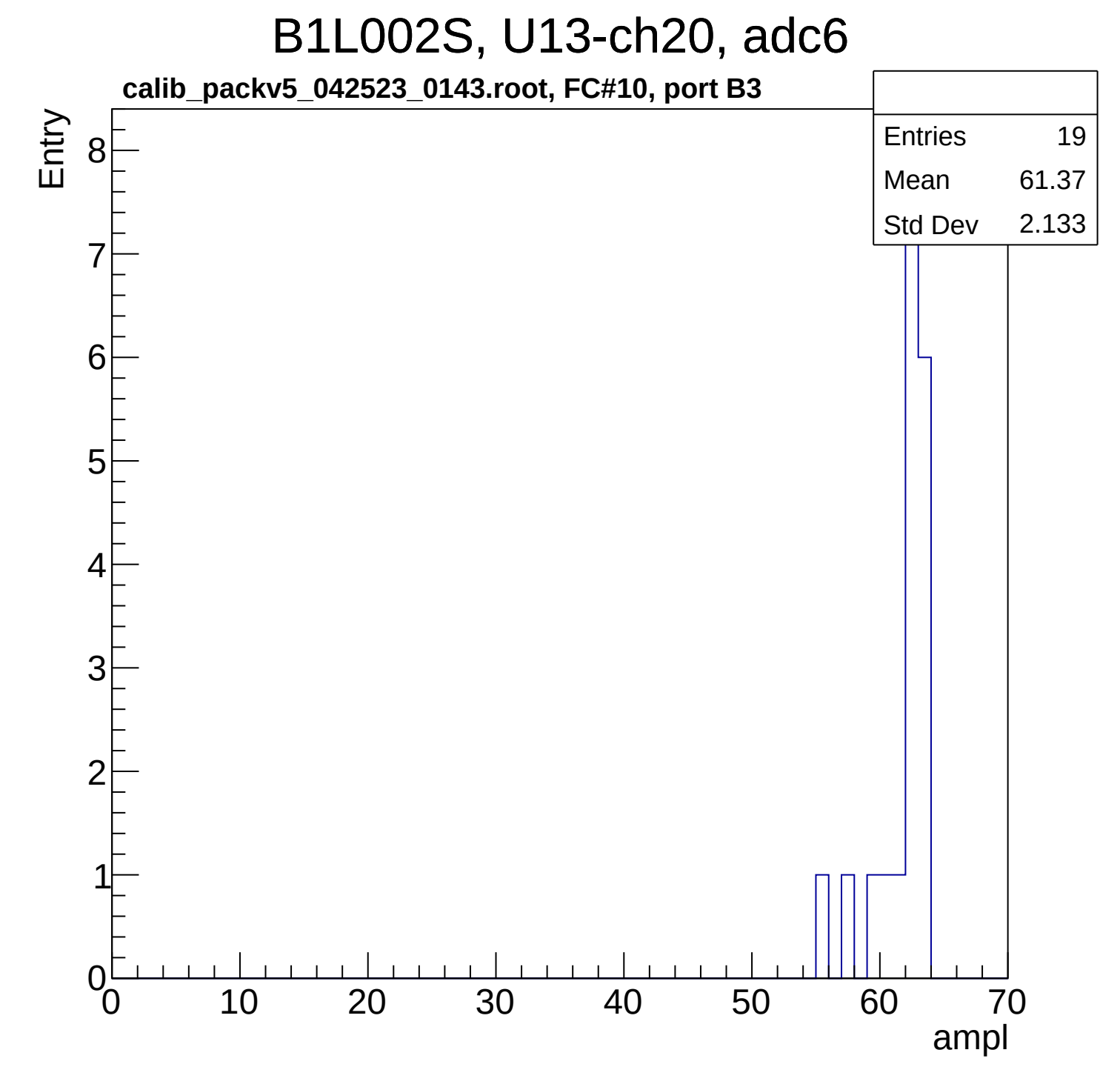
B1L002S, U13-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.37
Std Dev 2.133
Entry
ampl
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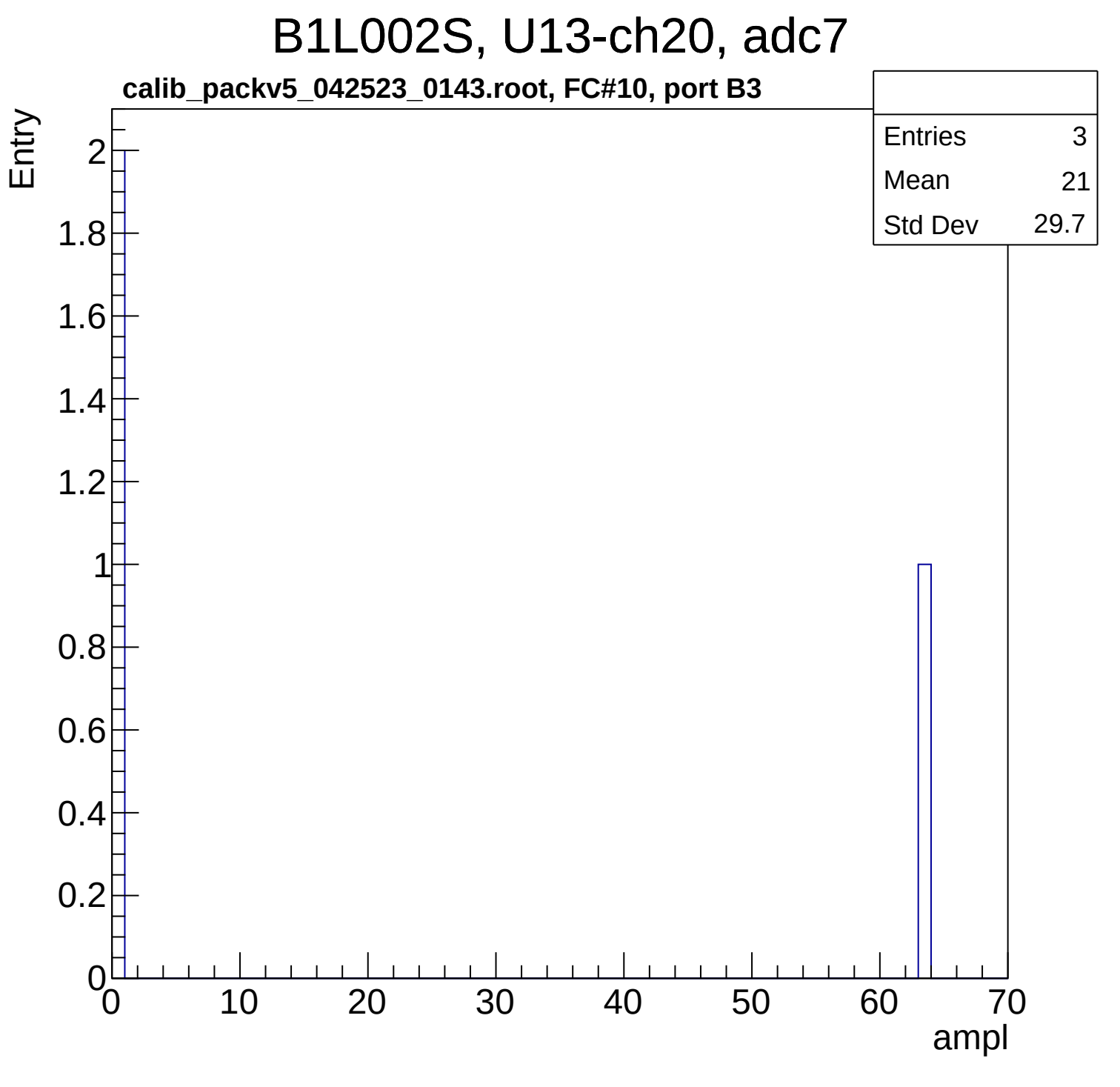
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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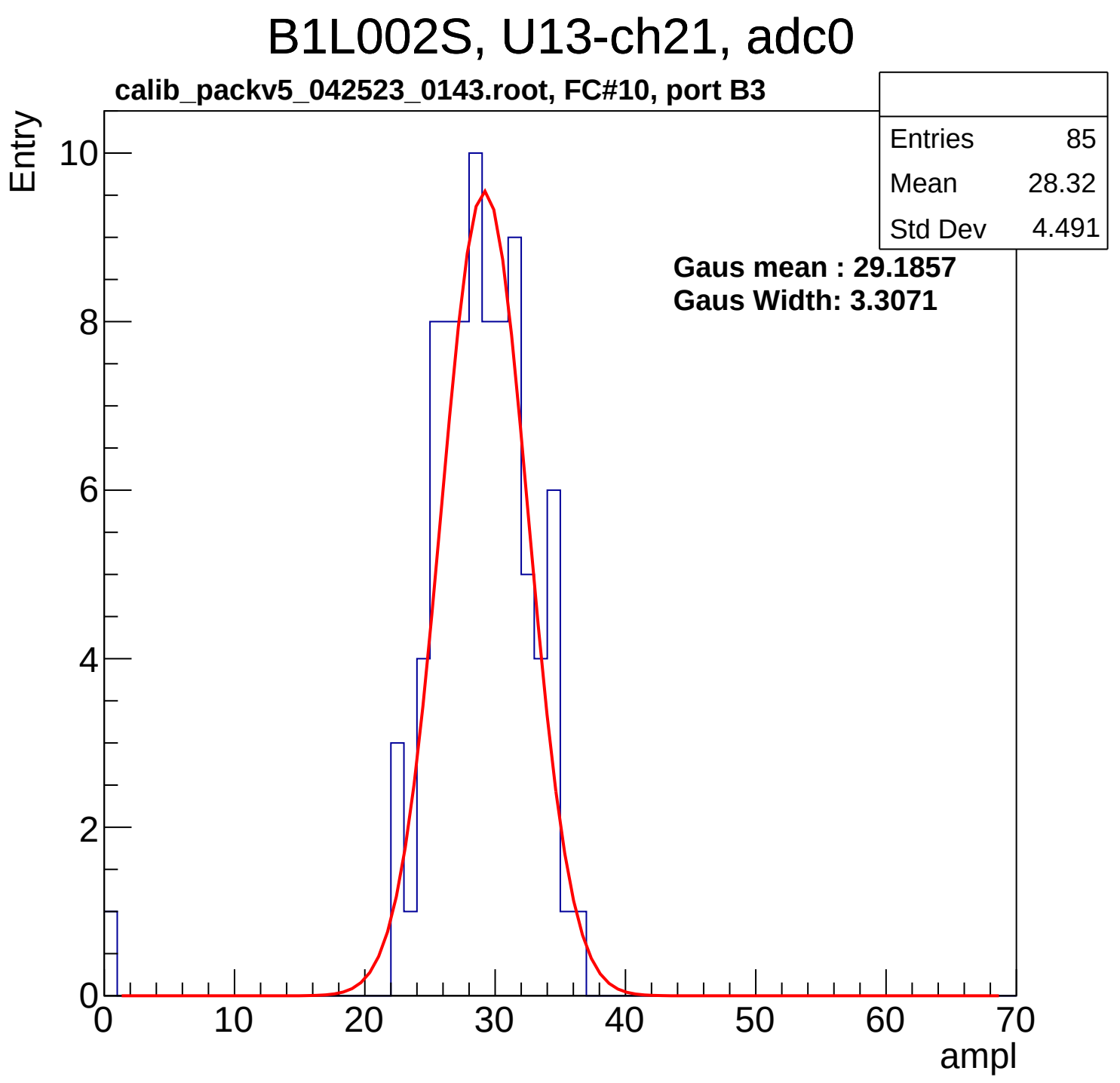
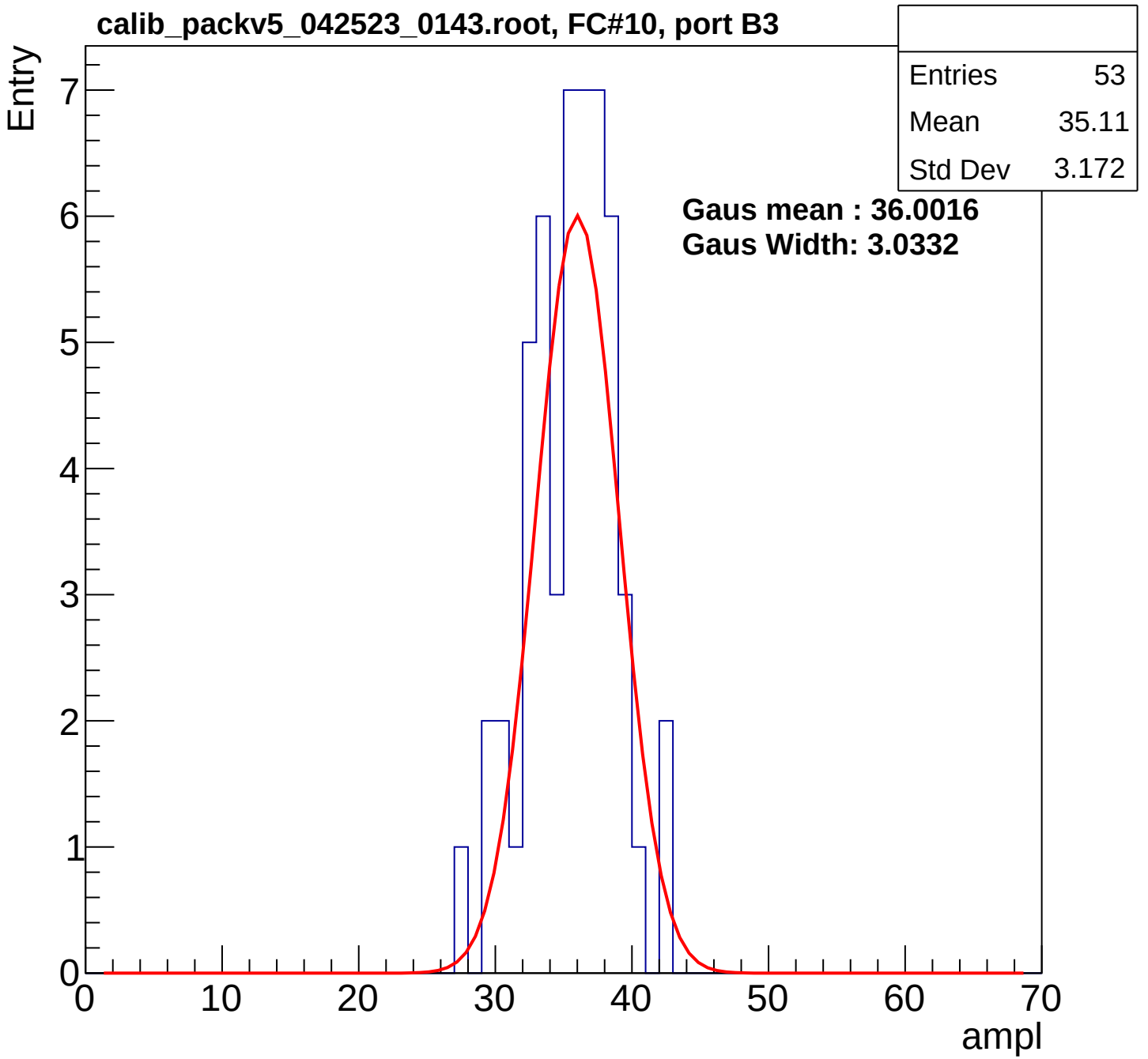
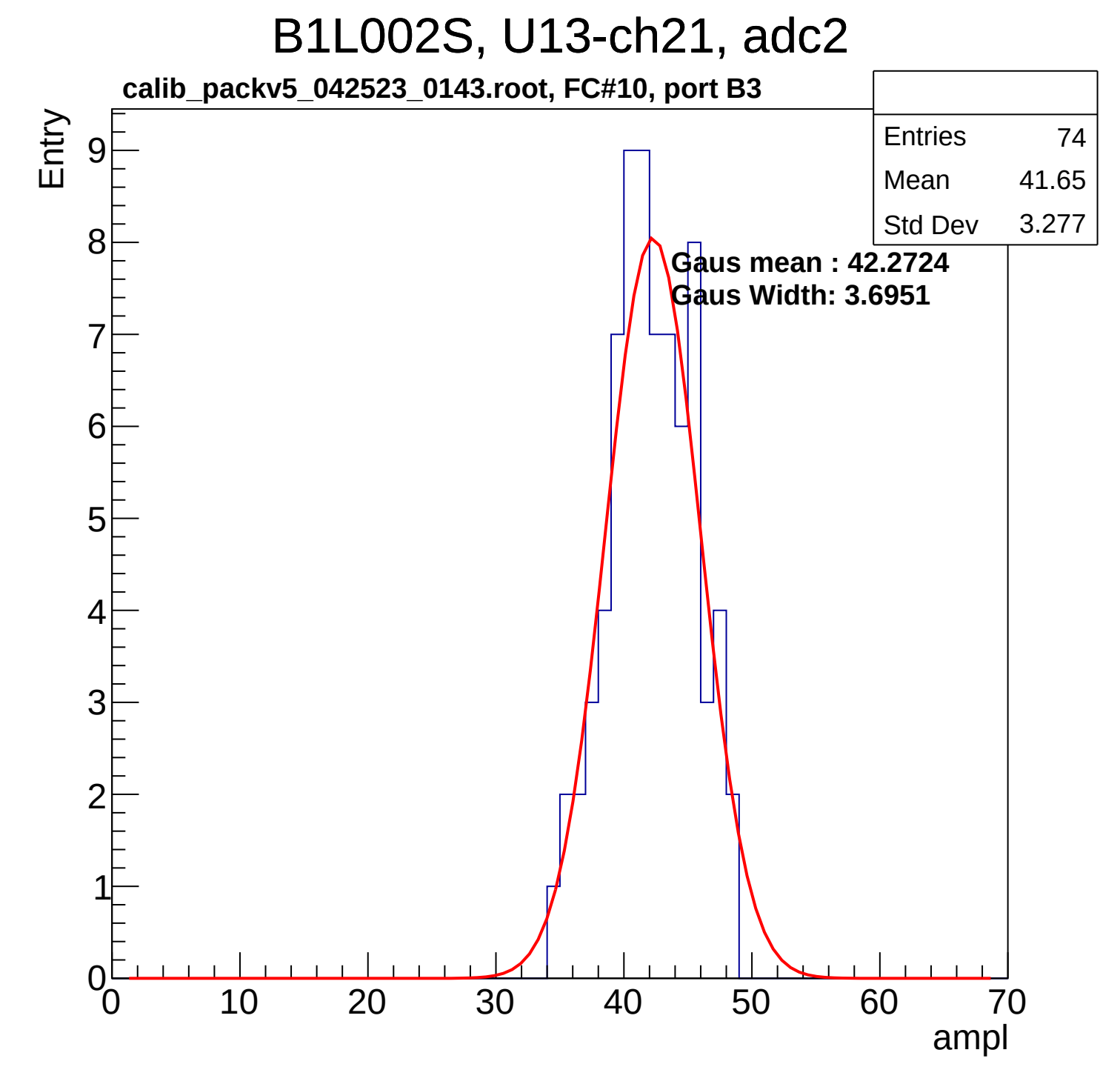

B1L002S, U13-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 28.32
Std Dev 4.491
Gaus mean : 29.1857
Gaus Width: 3.3071
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 35.11
Std Dev 3.172
Gaus mean : 36.0016
Gaus Width: 3.0332
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.65
Std Dev 3.277
Gaus mean : 42.2724
Gaus Width: 3.6951
Entry
ampl
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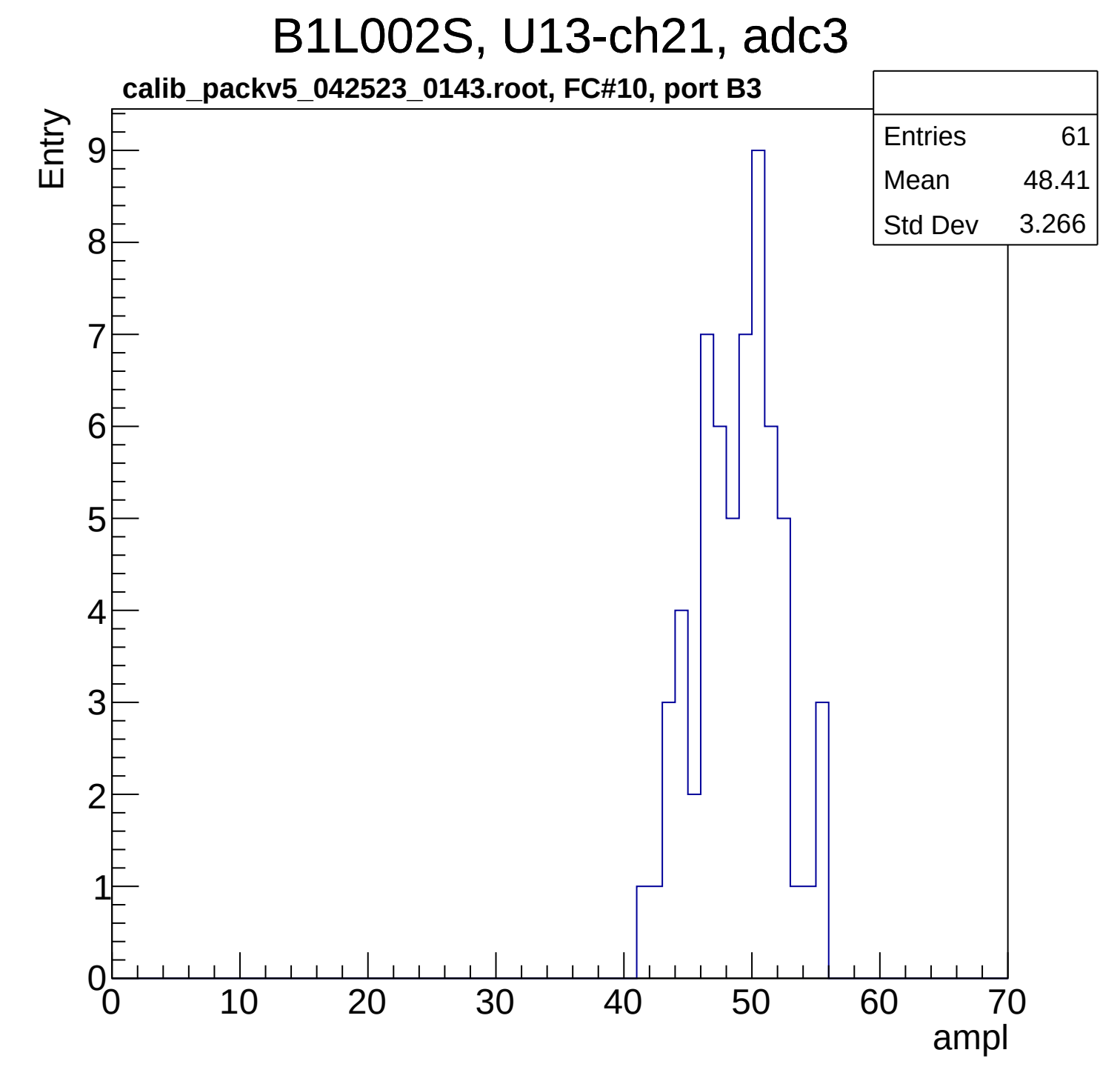
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.41
Std Dev 3.266
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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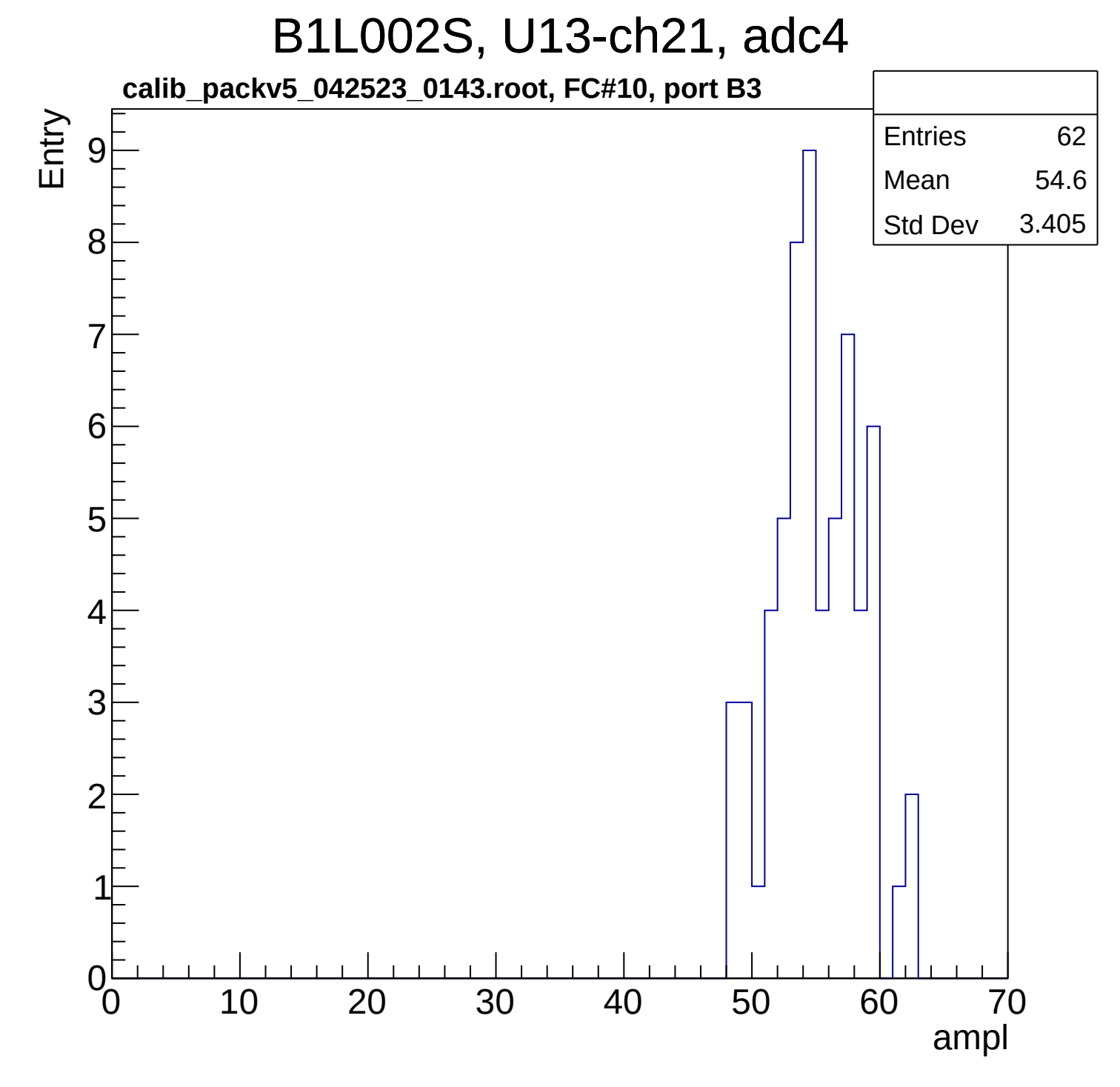
60
70


B1L002S, U13-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.6
Std Dev 3.405
Entry
ampl
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3
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5
6
7
8
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0
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20
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40
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60
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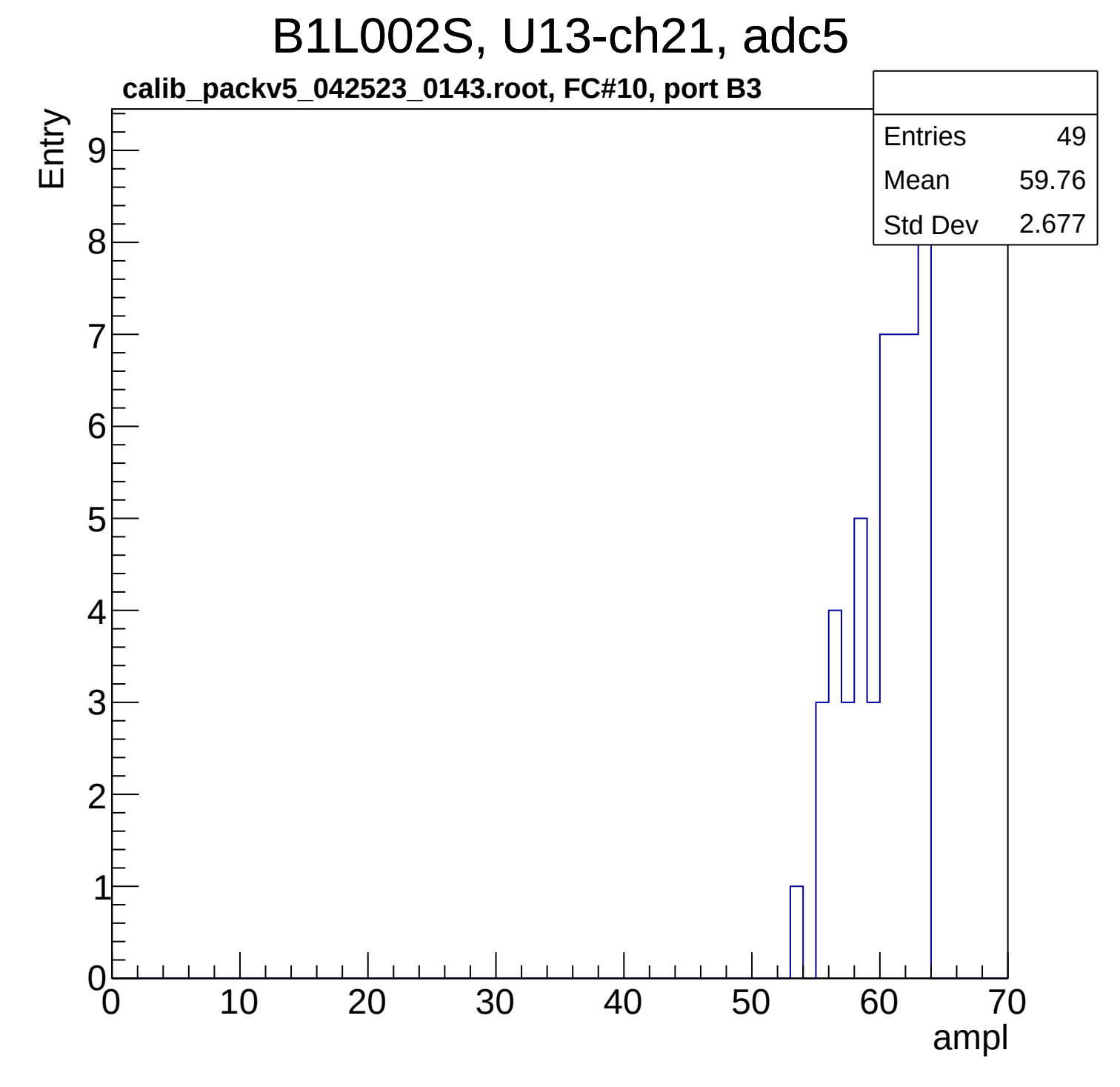
B1L002S, U13-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.76
Std Dev 2.677
Entry
ampl
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2
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4
5
6
7
8
9
0
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60
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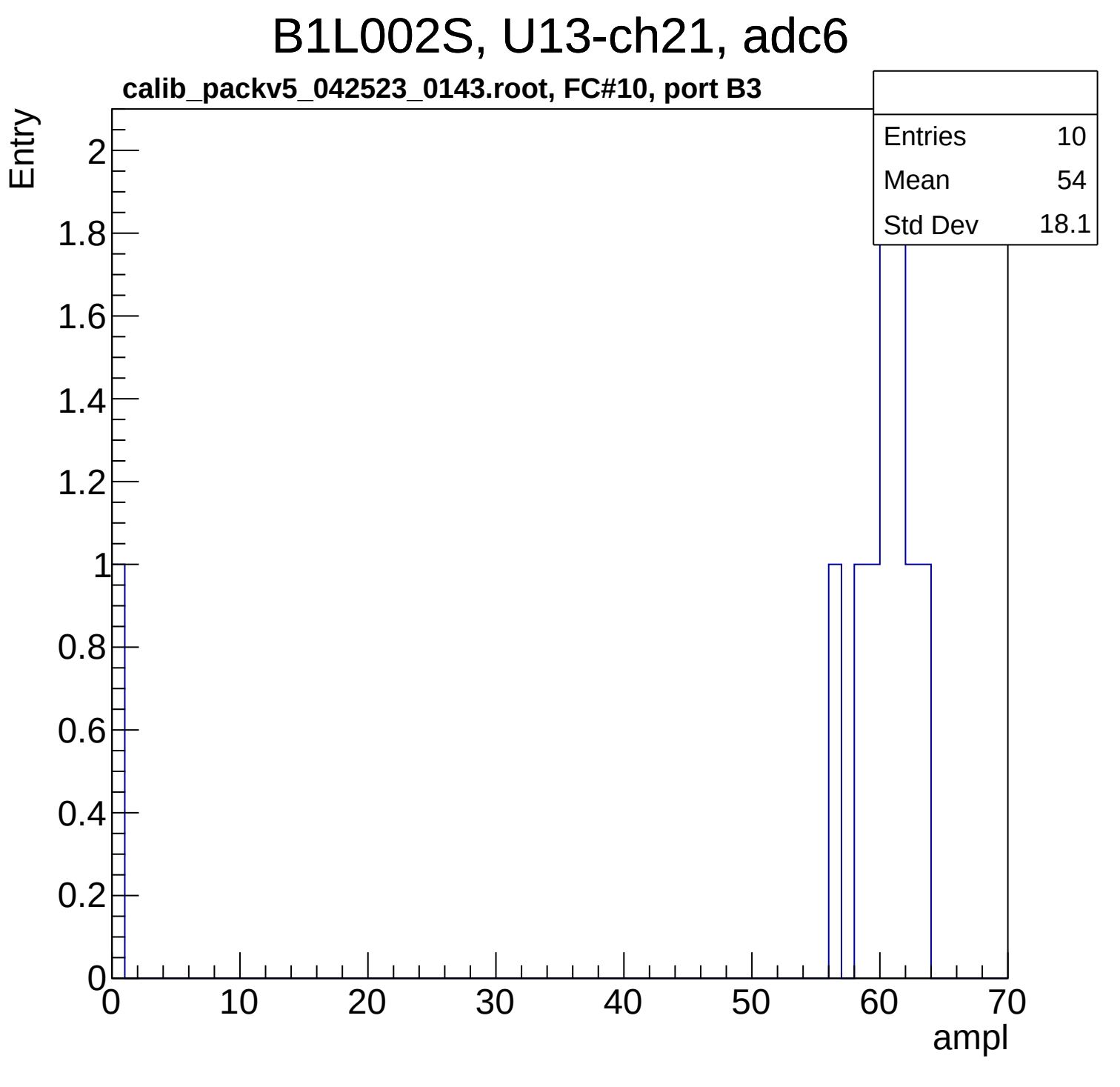
B1L002S, U13-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 54
Std Dev 18.1
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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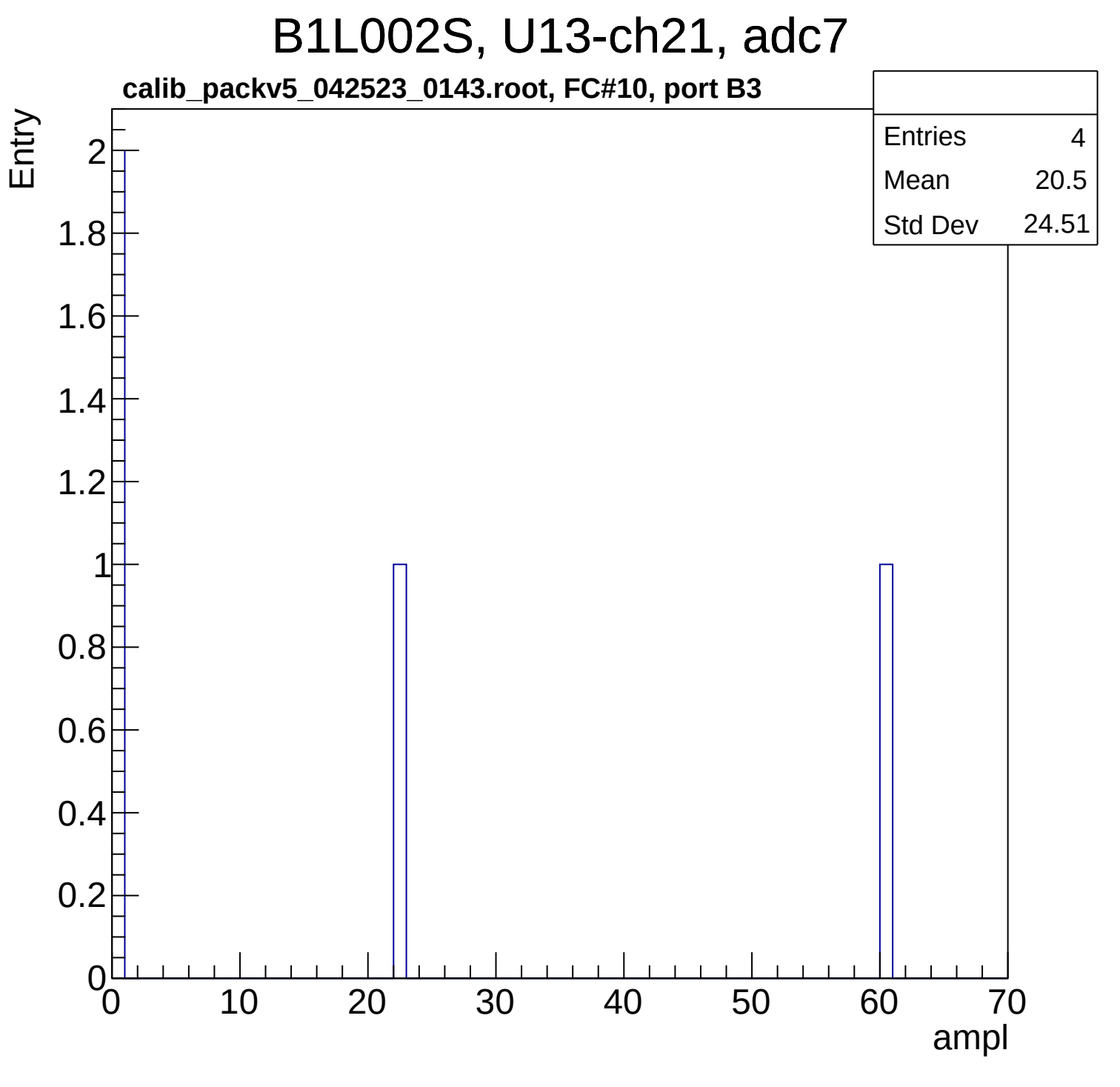
B1L002S, U13-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.5
Std Dev 24.51
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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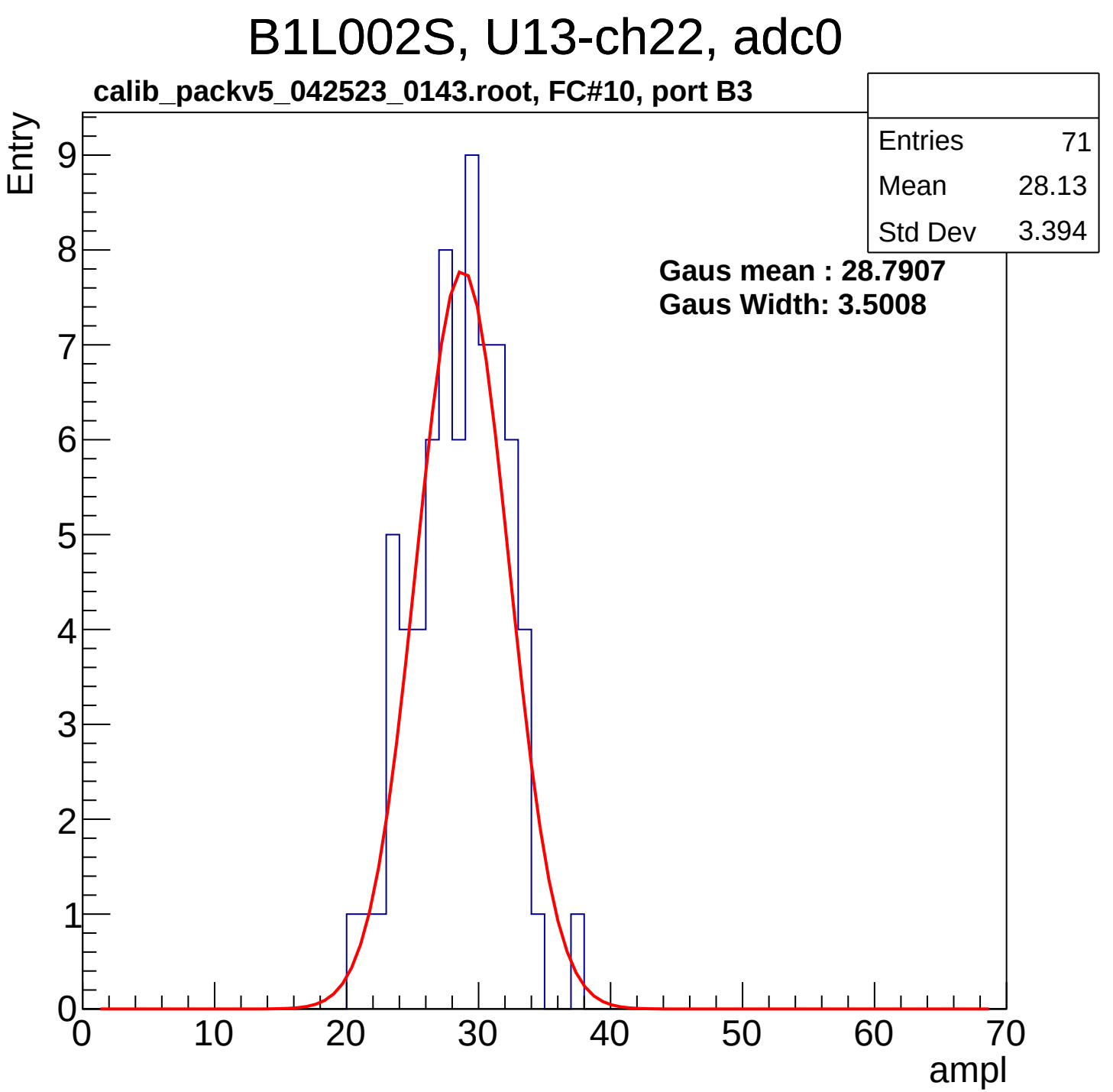
B1L002S, U13-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.13
Std Dev 3.394
Gaus mean : 28.7907
Gaus Width: 3.5008
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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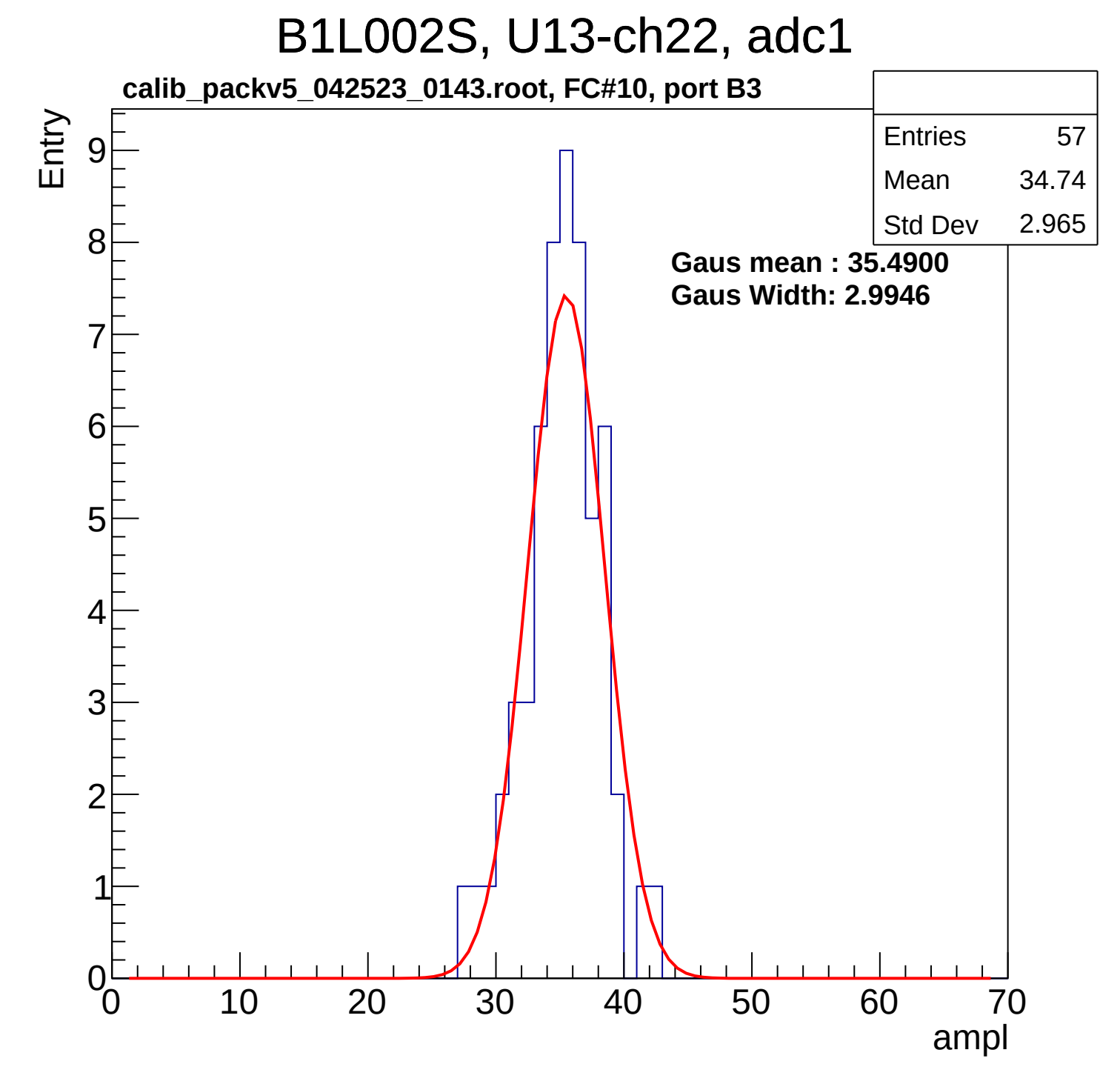
B1L002S, U13-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.74
Std Dev 2.965
Gaus mean : 35.4900
Gaus Width: 2.9946
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
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60
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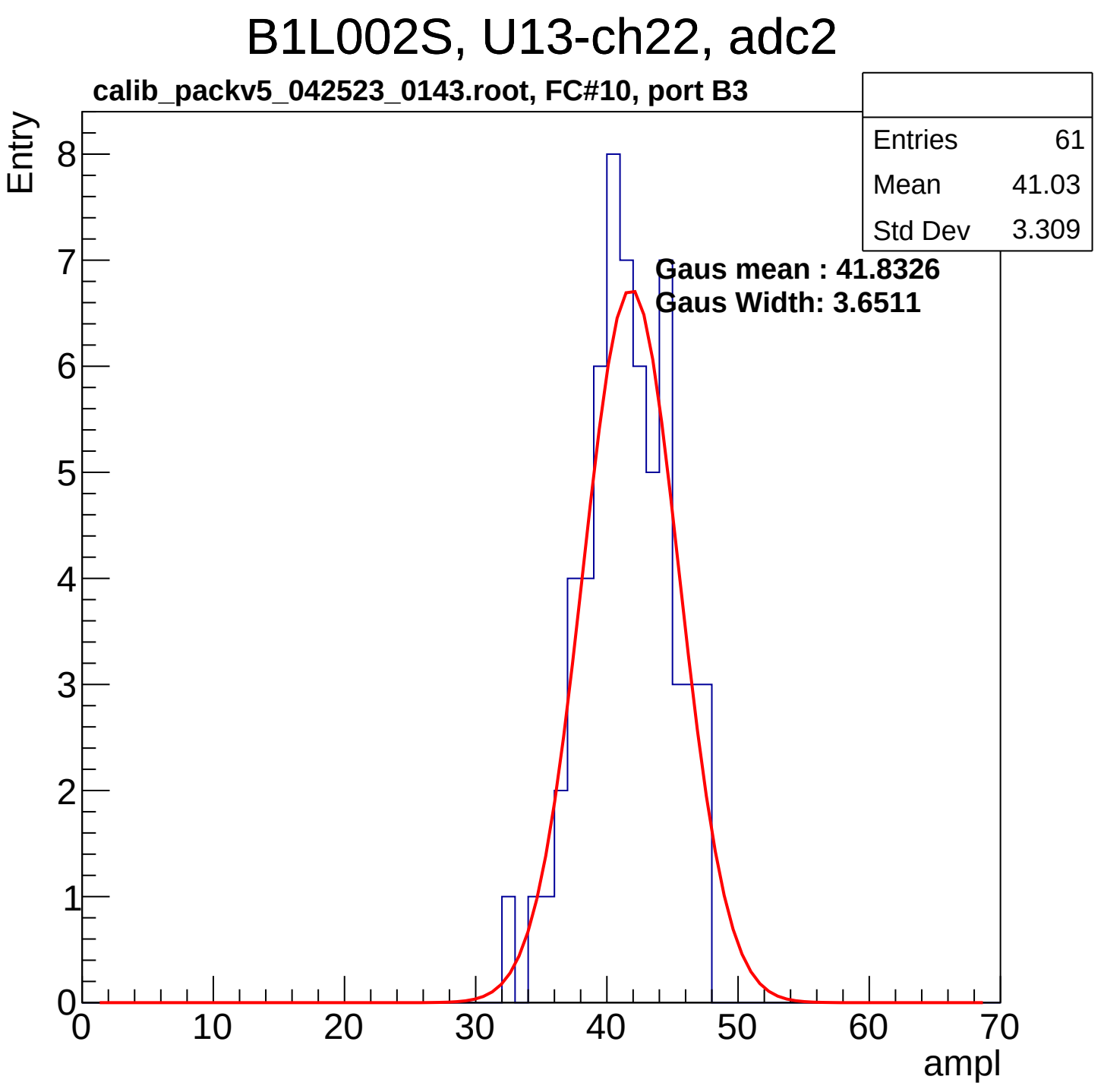
B1L002S, U13-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.03
Std Dev 3.309
Gaus mean : 41.8326
Gaus Width: 3.6511
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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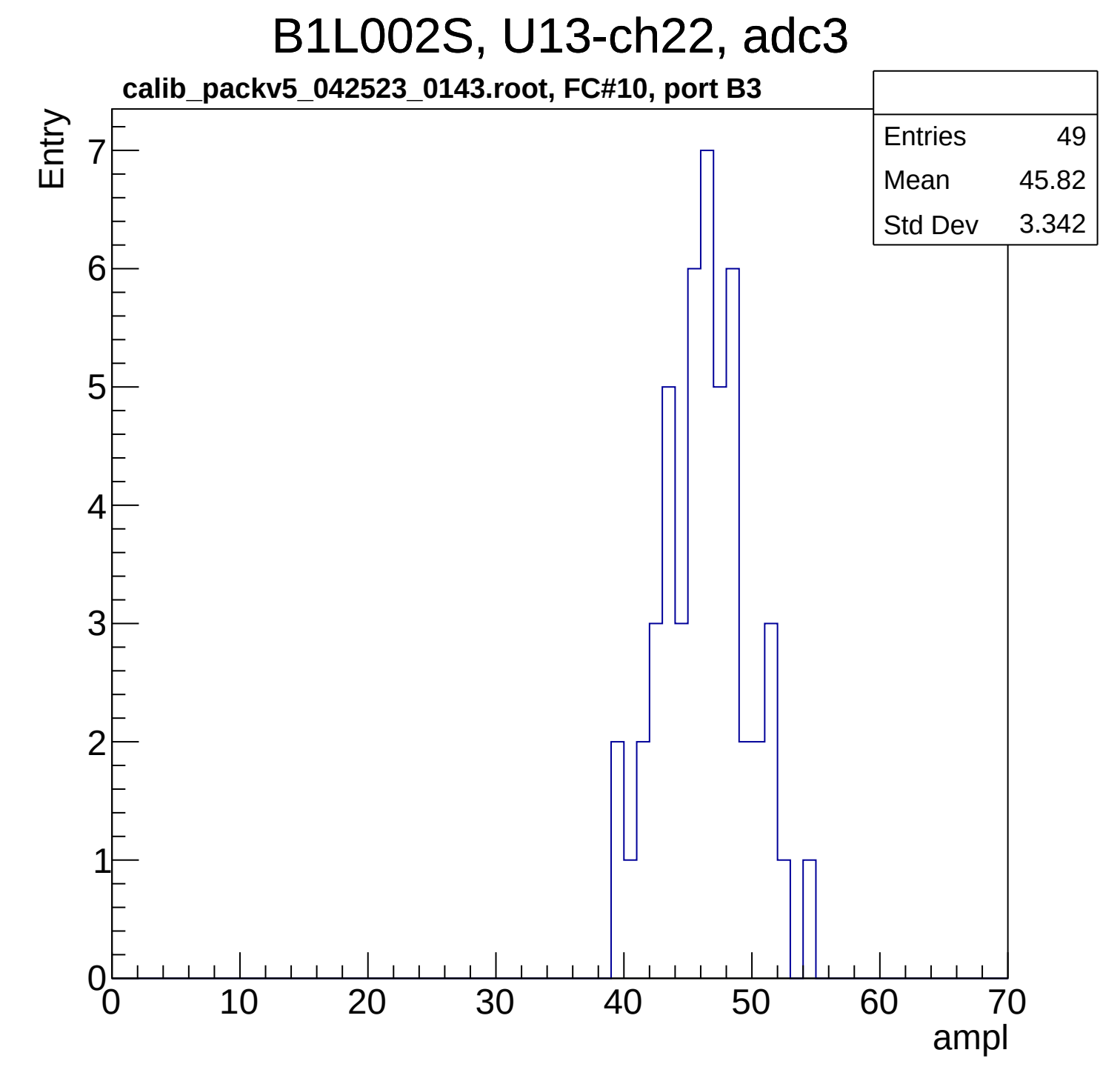
B1L002S, U13-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 45.82
Std Dev 3.342
Entry
ampl
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3
4
5
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0
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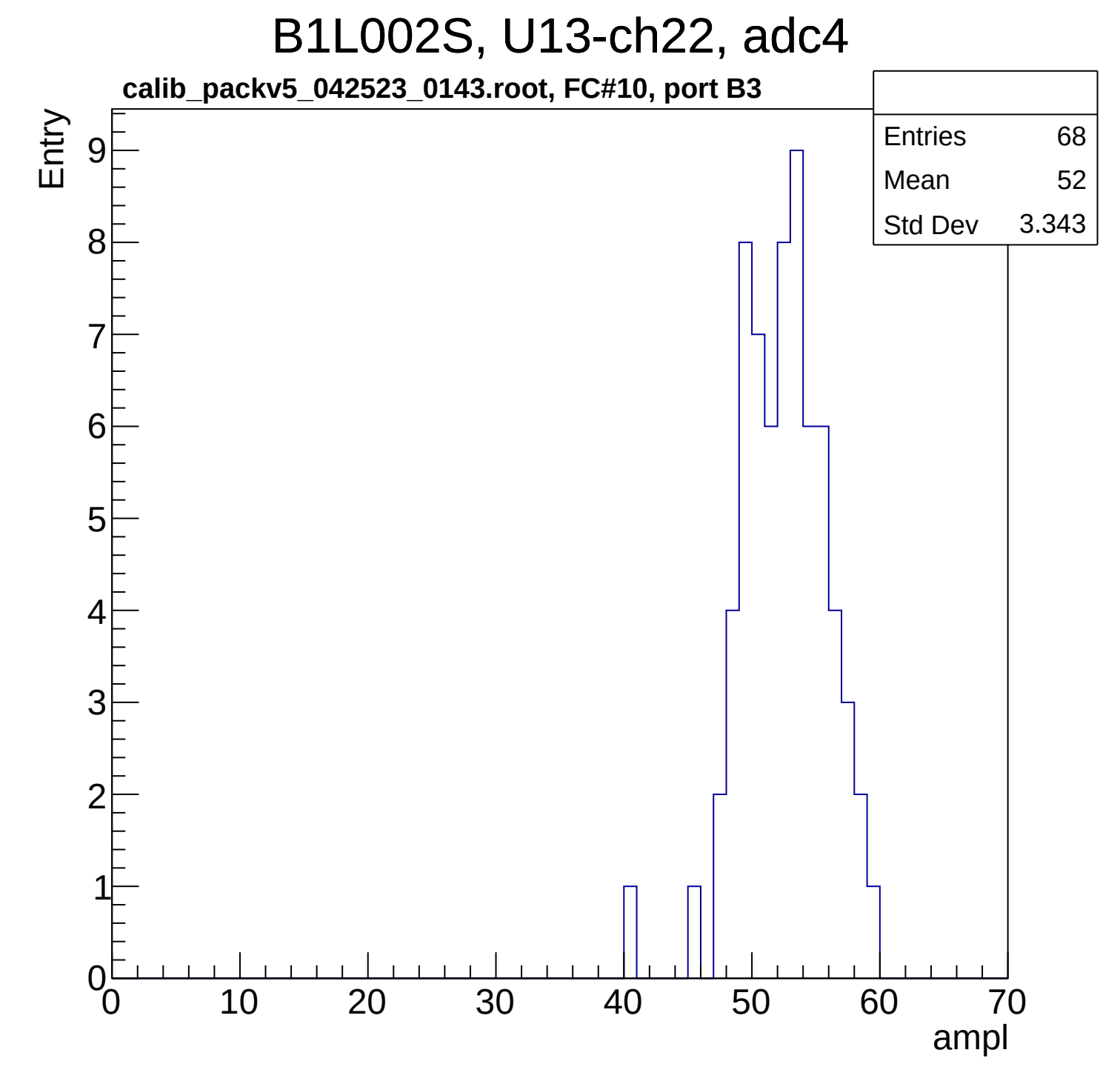
B1L002S, U13-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52
Std Dev 3.343
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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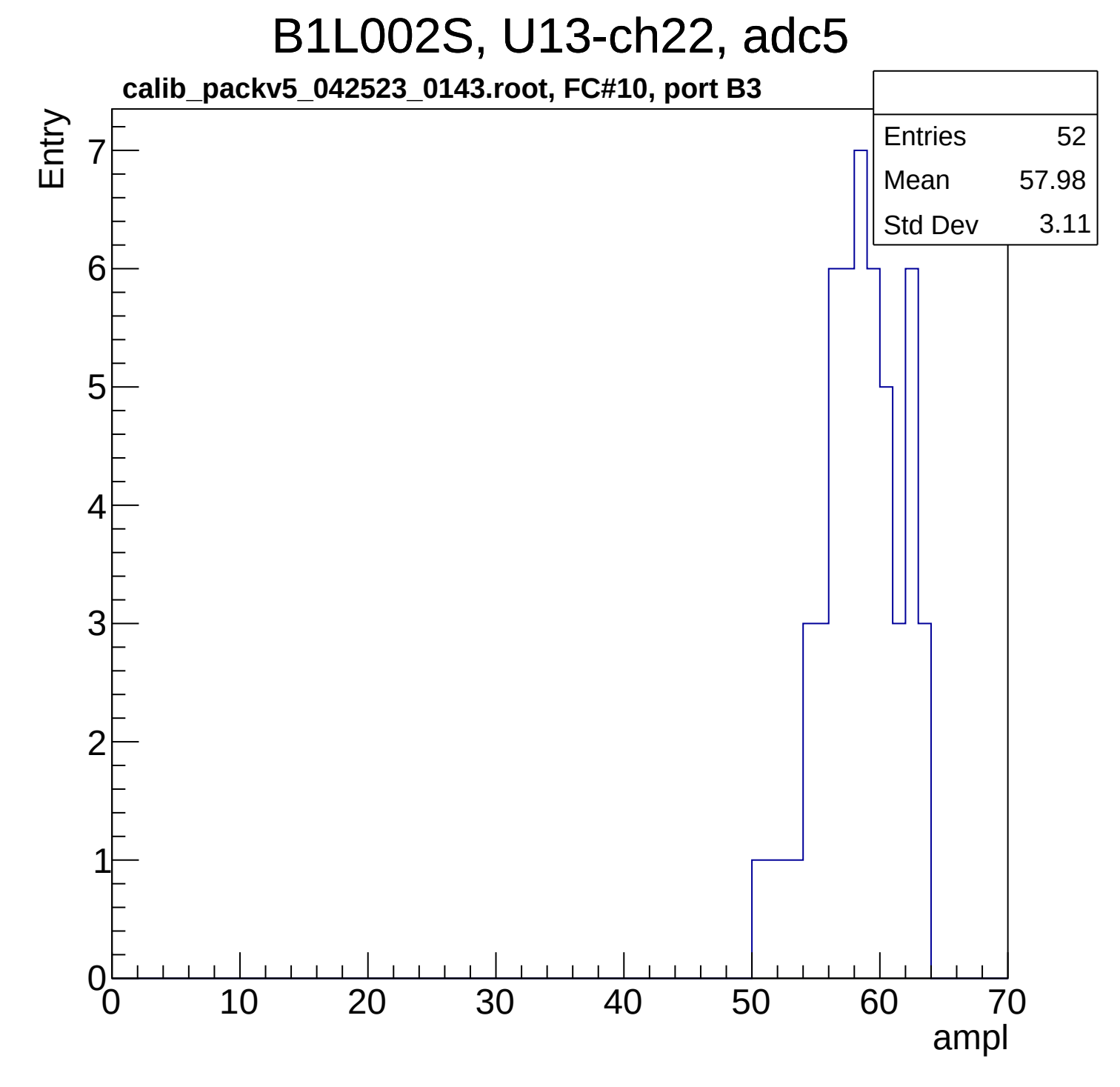
B1L002S, U13-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.98
Std Dev 3.11
Entry
ampl
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1
2
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4
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6
7
0
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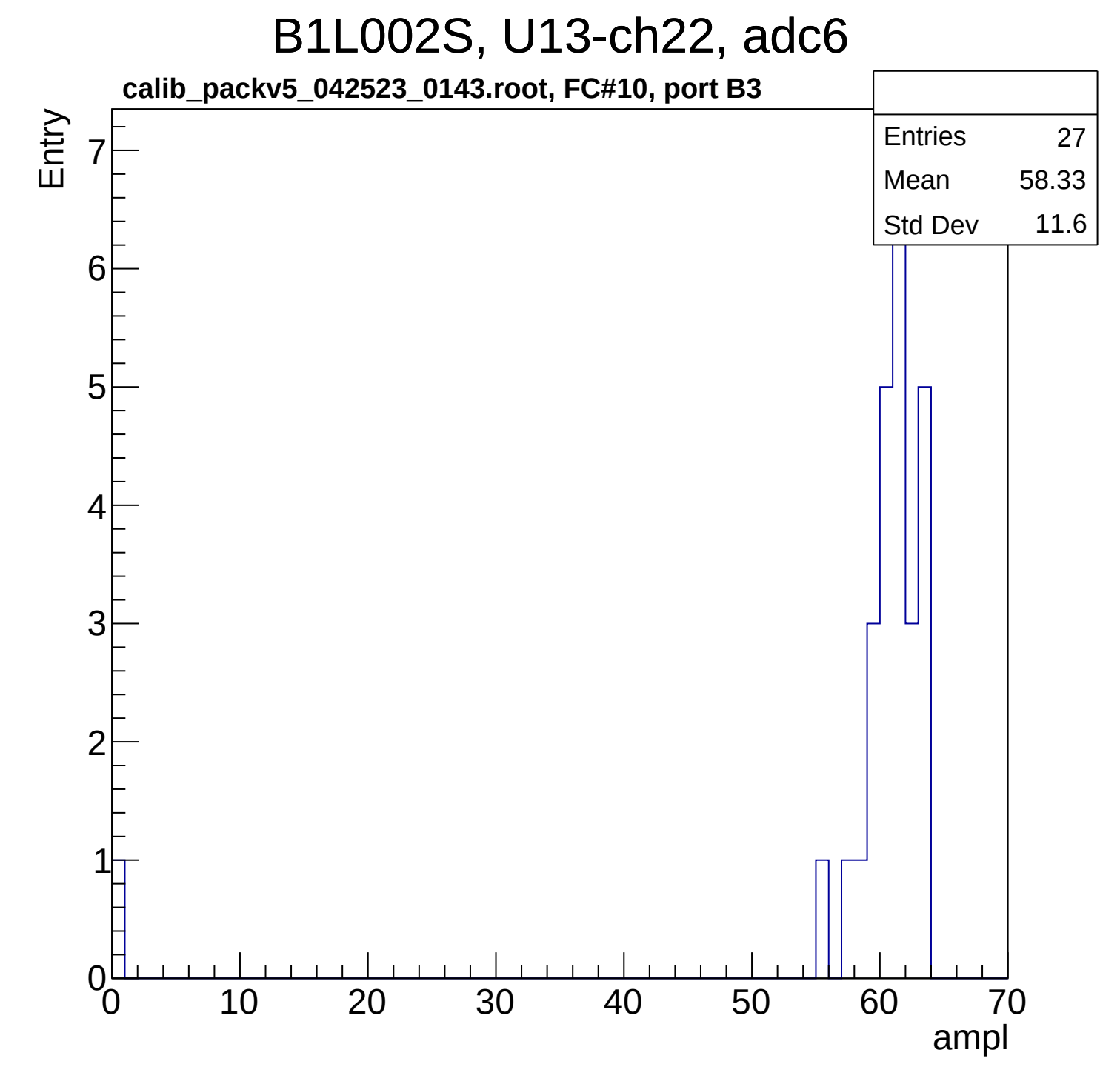
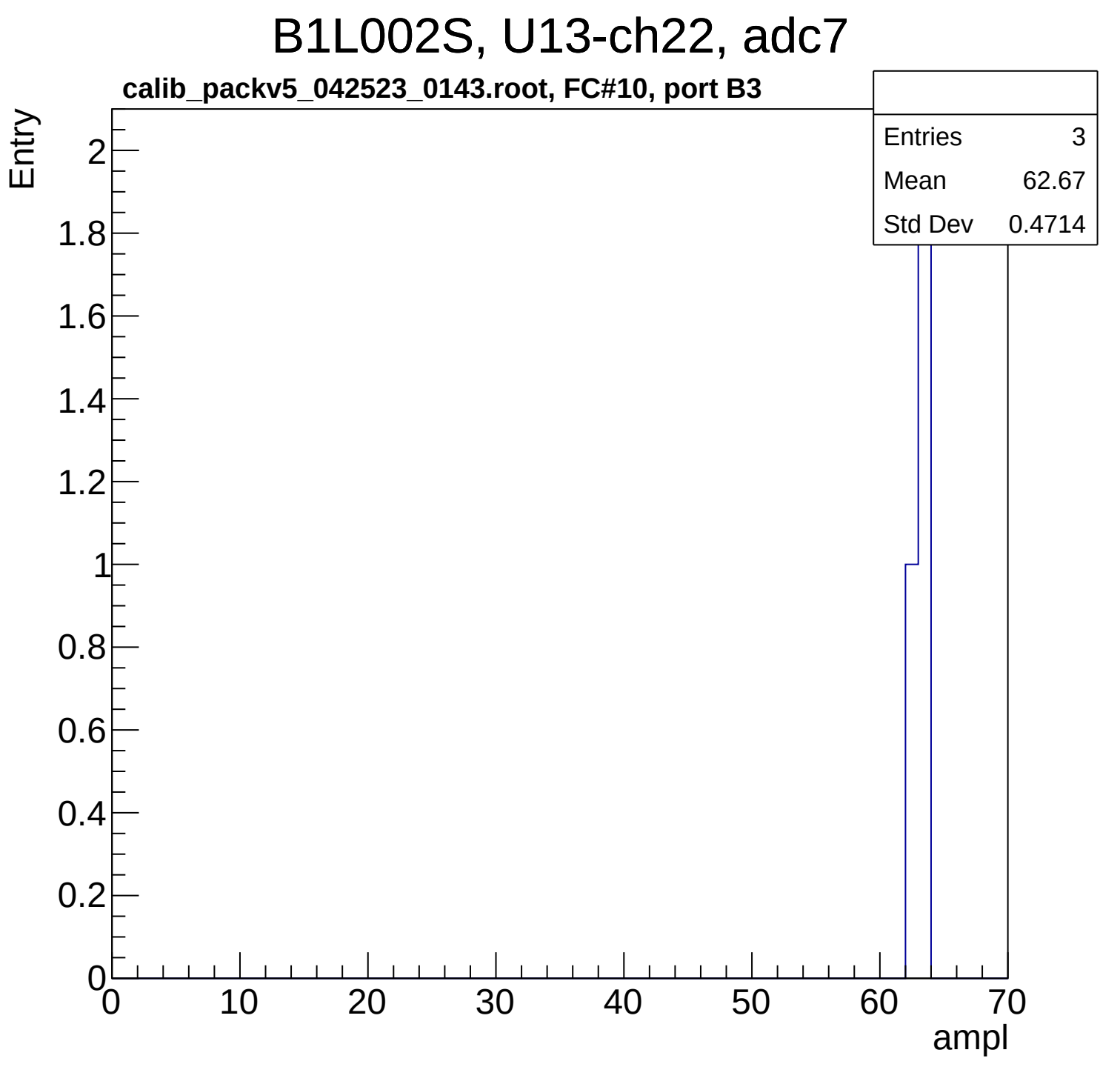

B1L002S, U13-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.33
Std Dev 11.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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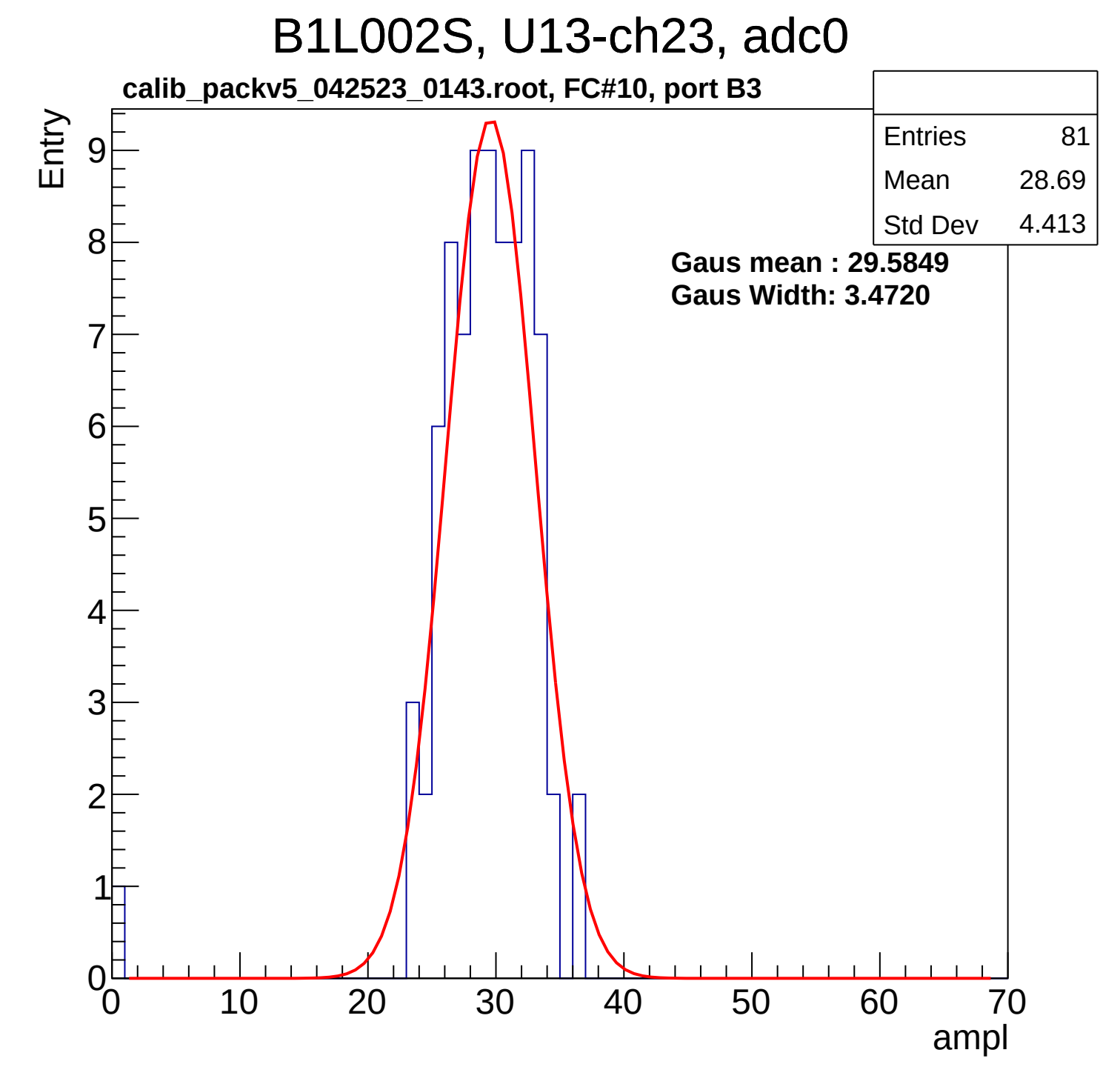
B1L002S, U13-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 28.69
Std Dev 4.413
Gaus mean : 29.5849
Gaus Width: 3.4720
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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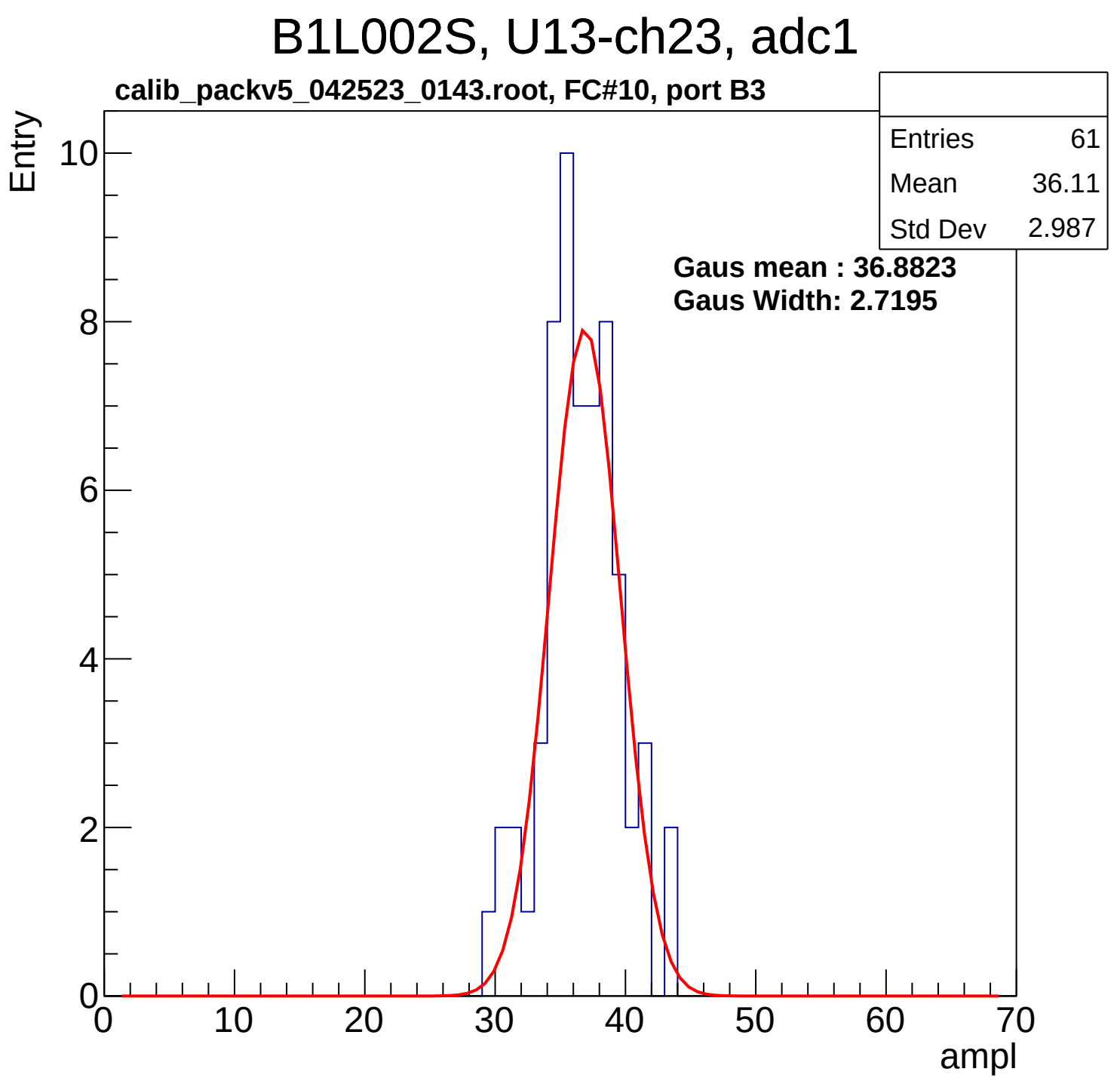
B1L002S, U13-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 36.11
Std Dev 2.987
Gaus mean : 36.8823
Gaus Width: 2.7195
Entry
ampl
0
2
4
6
8
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0
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20
30
40
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60
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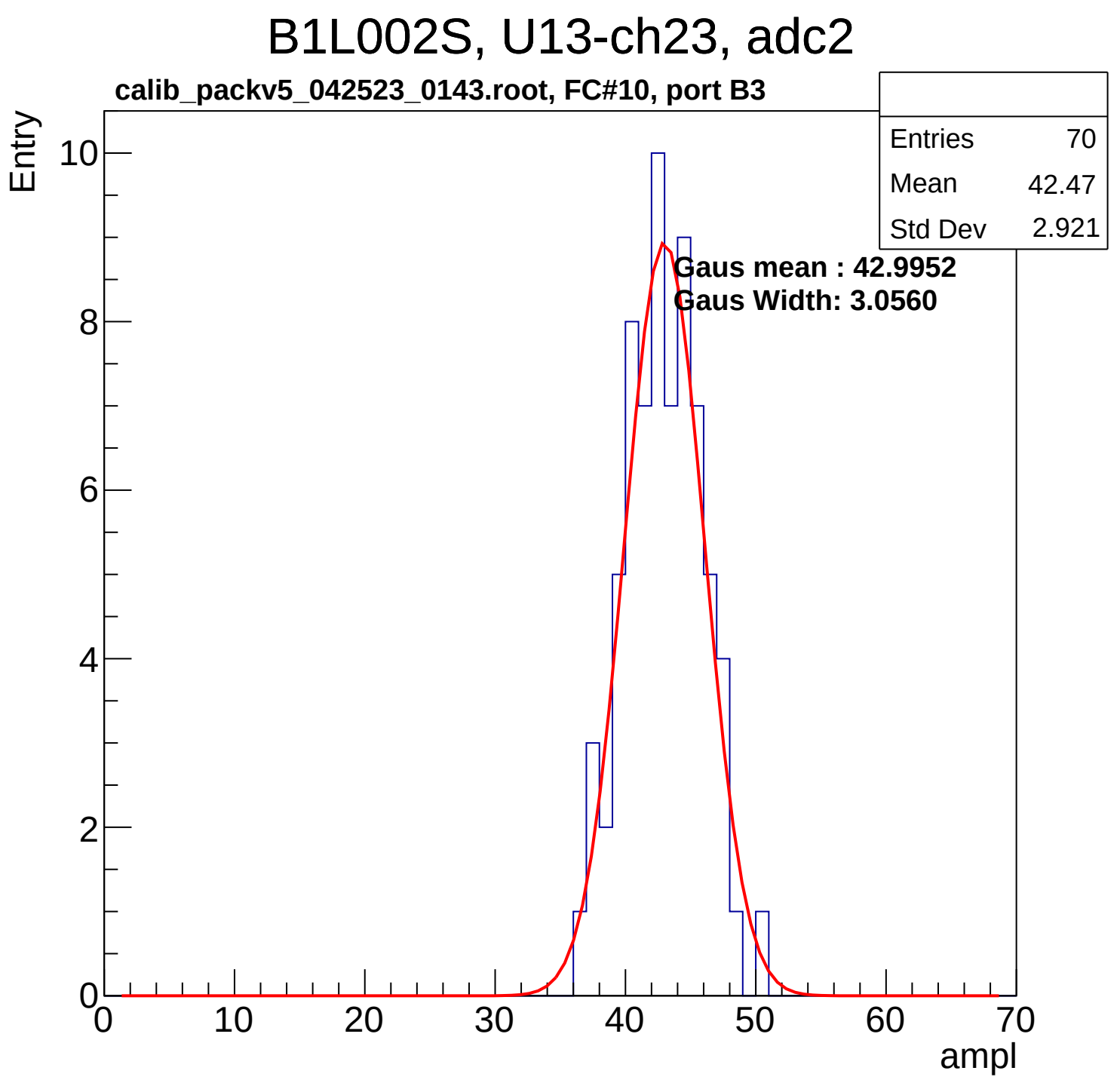
B1L002S, U13-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 42.47
Std Dev 2.921
Gaus mean : 42.9952
Gaus Width: 3.0560
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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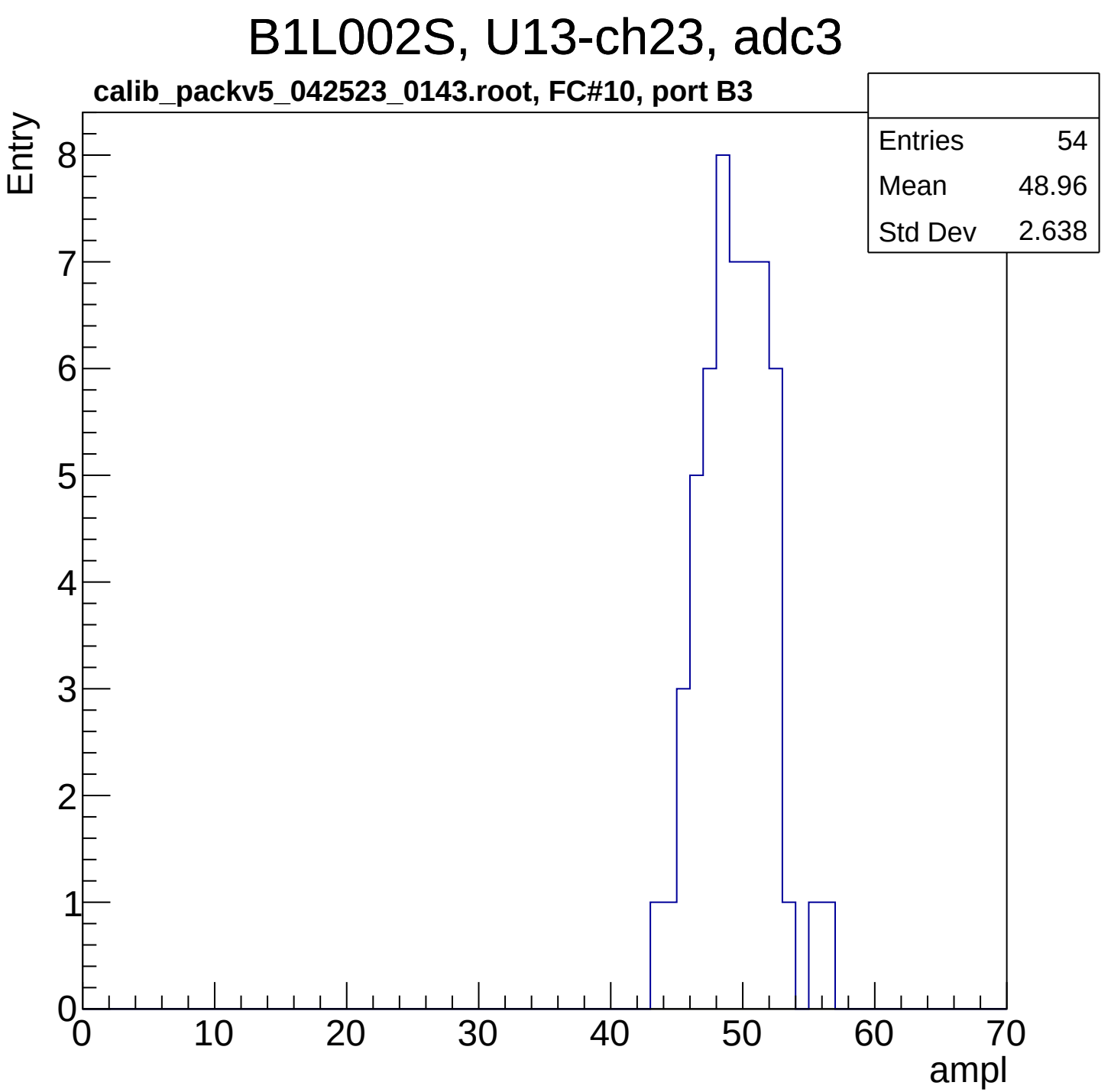
B1L002S, U13-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.96
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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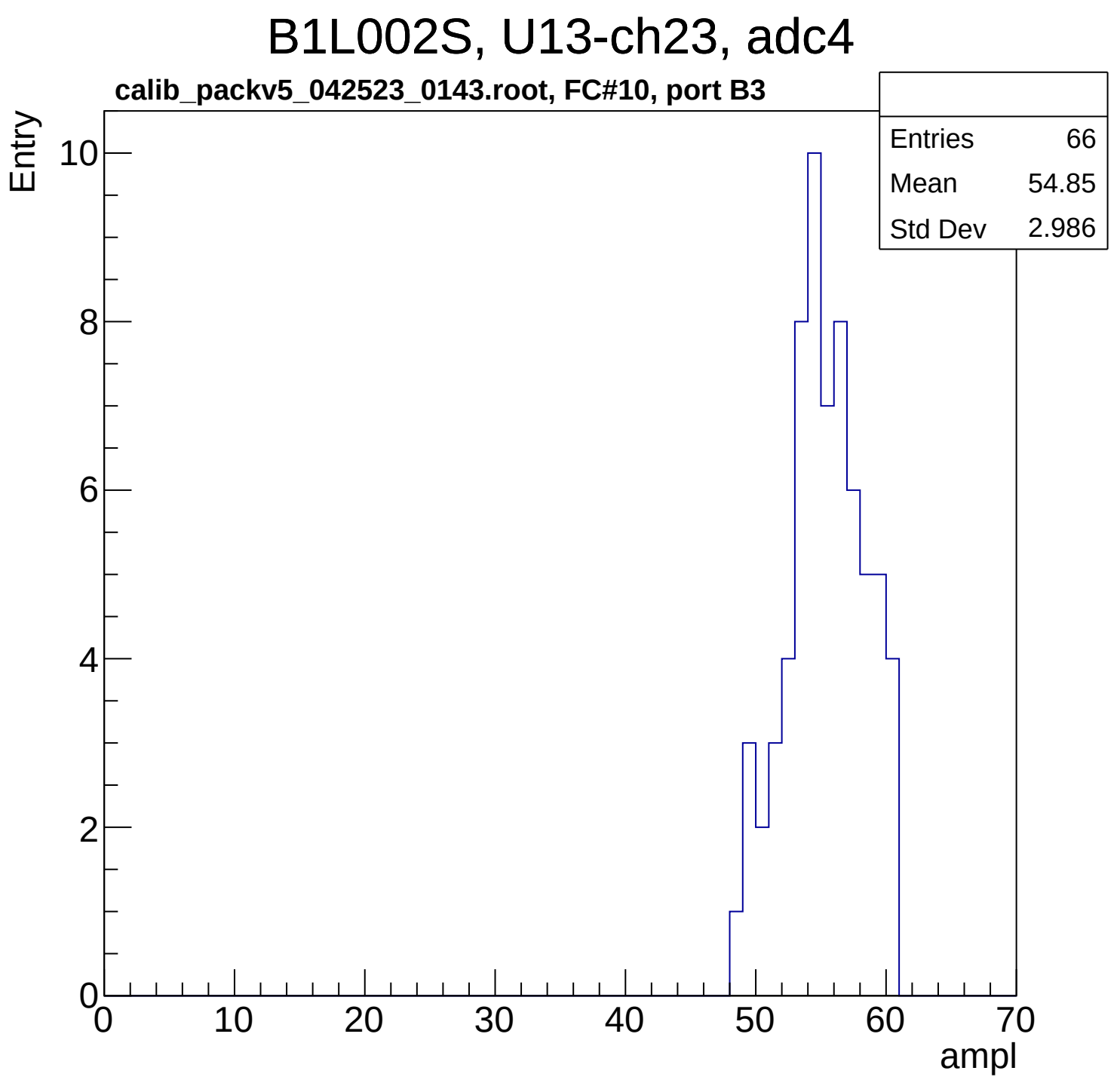
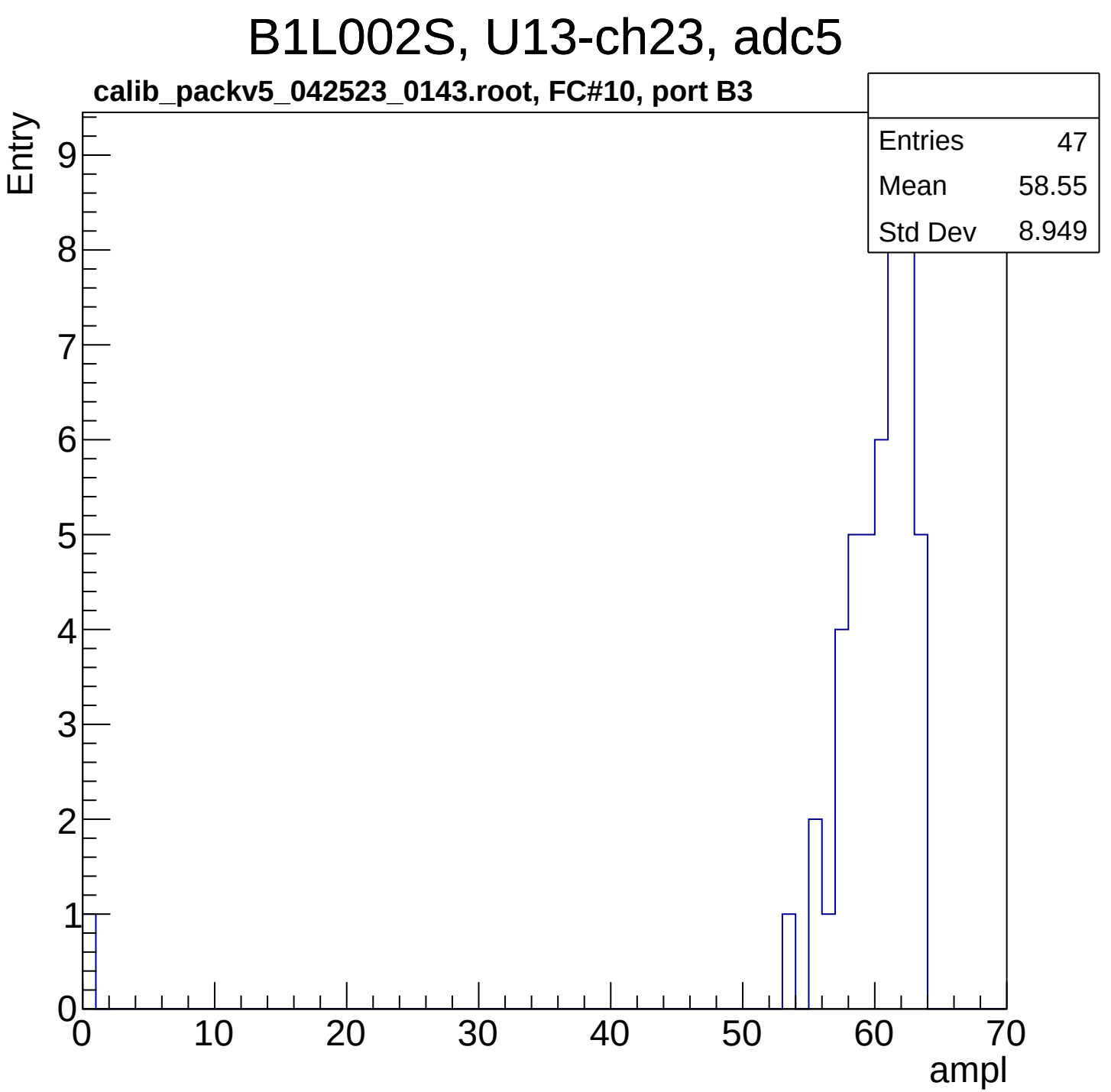
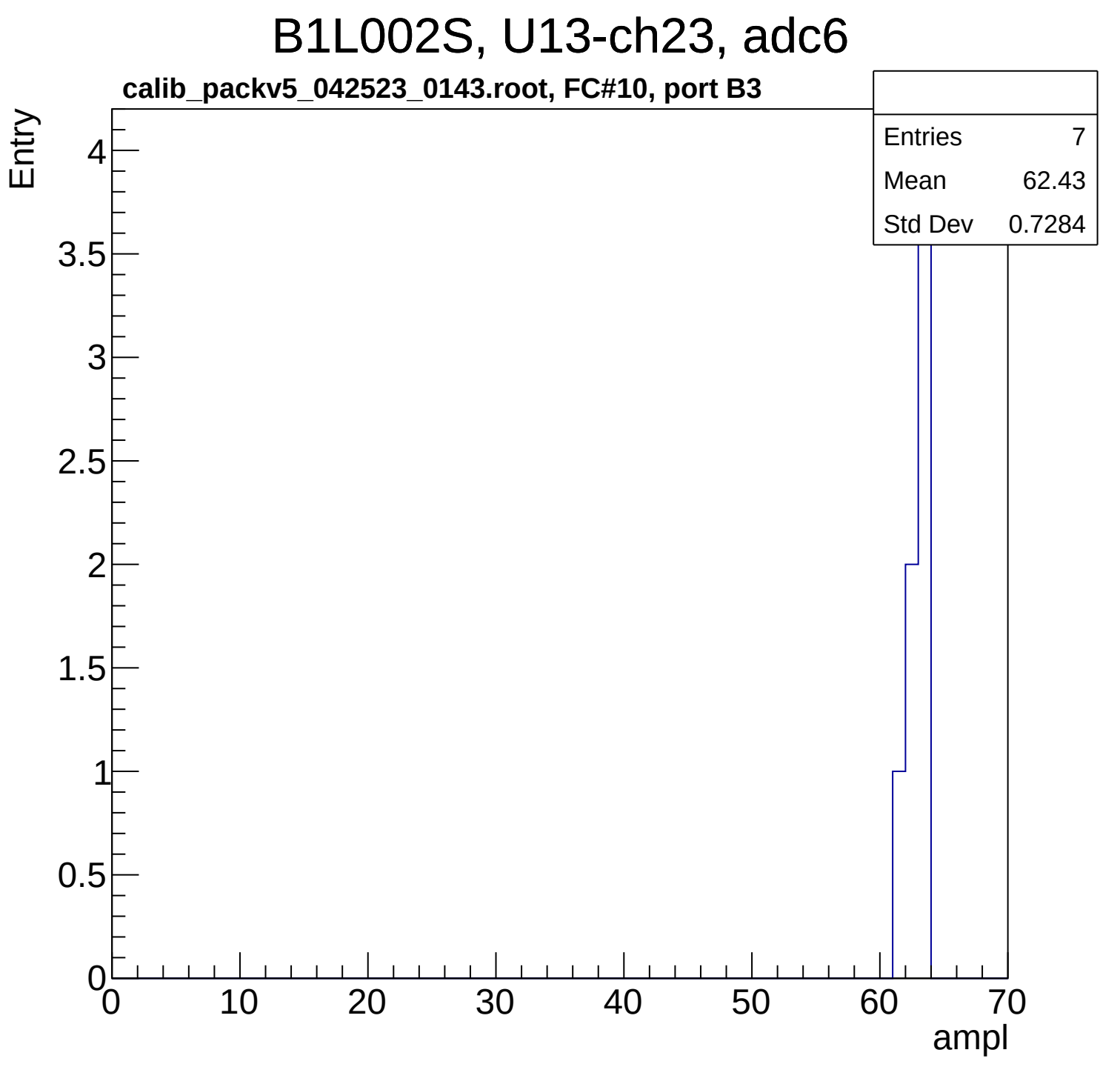

B1L002S, U13-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.85
Std Dev 2.986
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.55
Std Dev 8.949
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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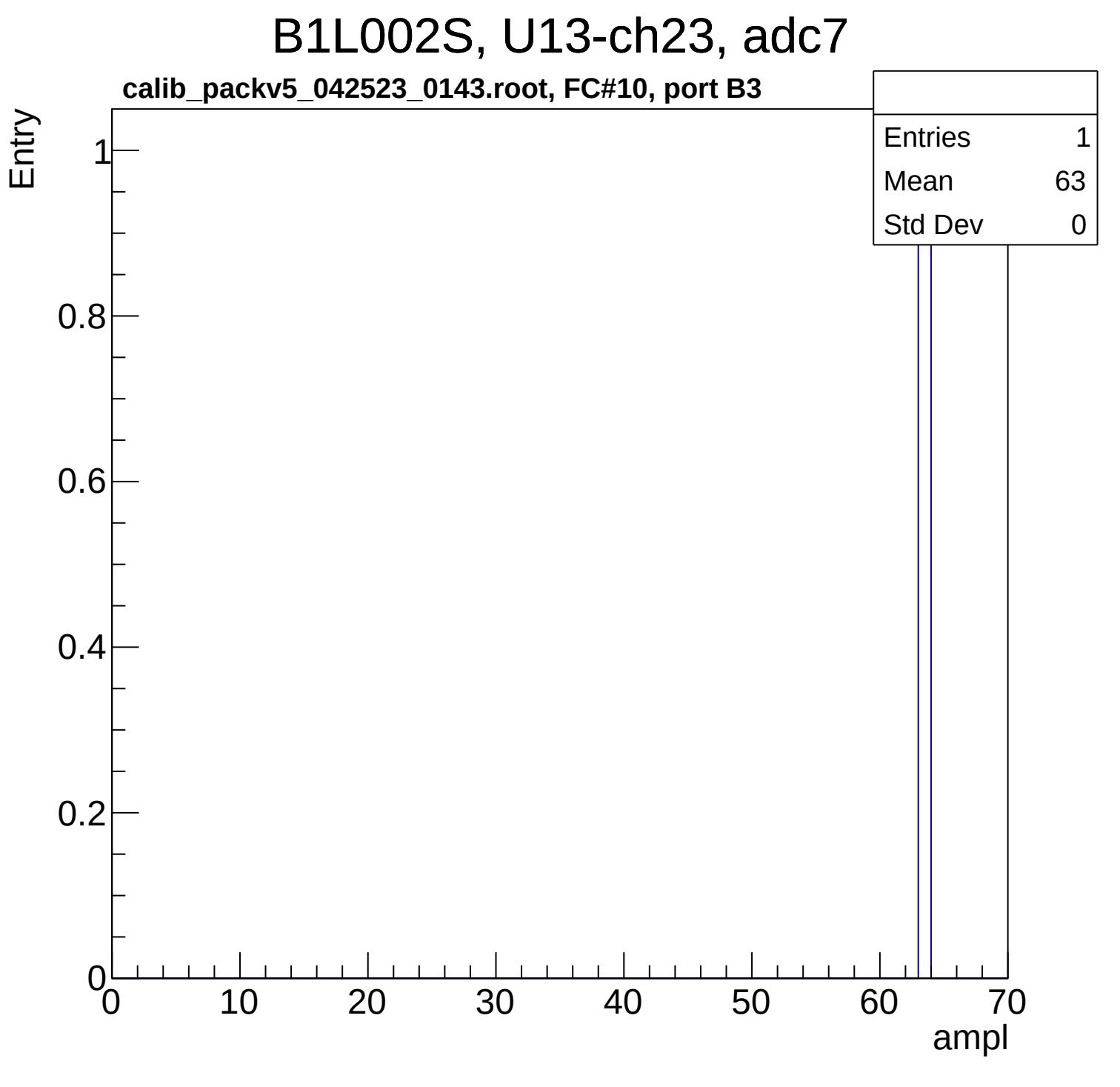
B1L002S, U13-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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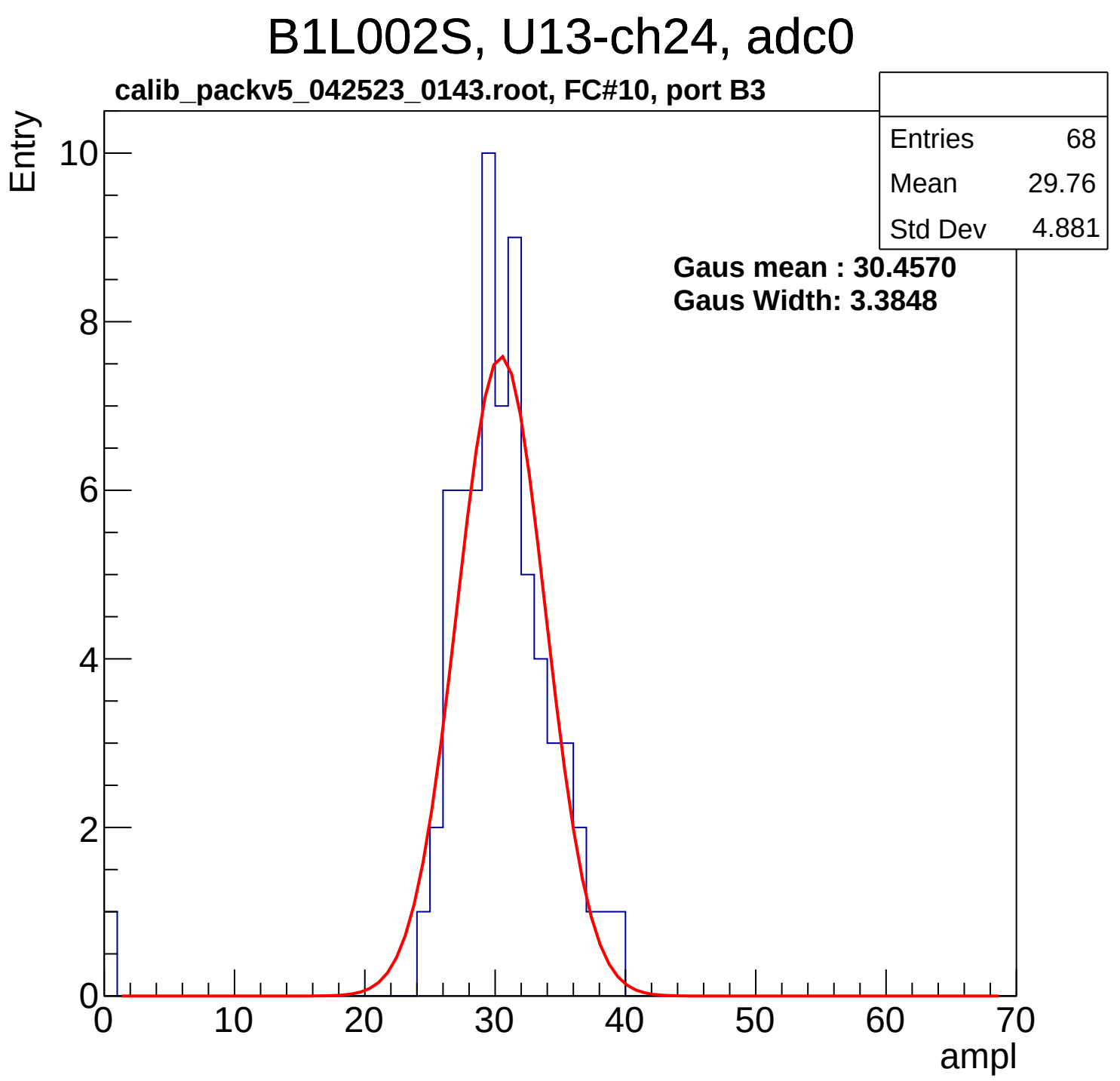
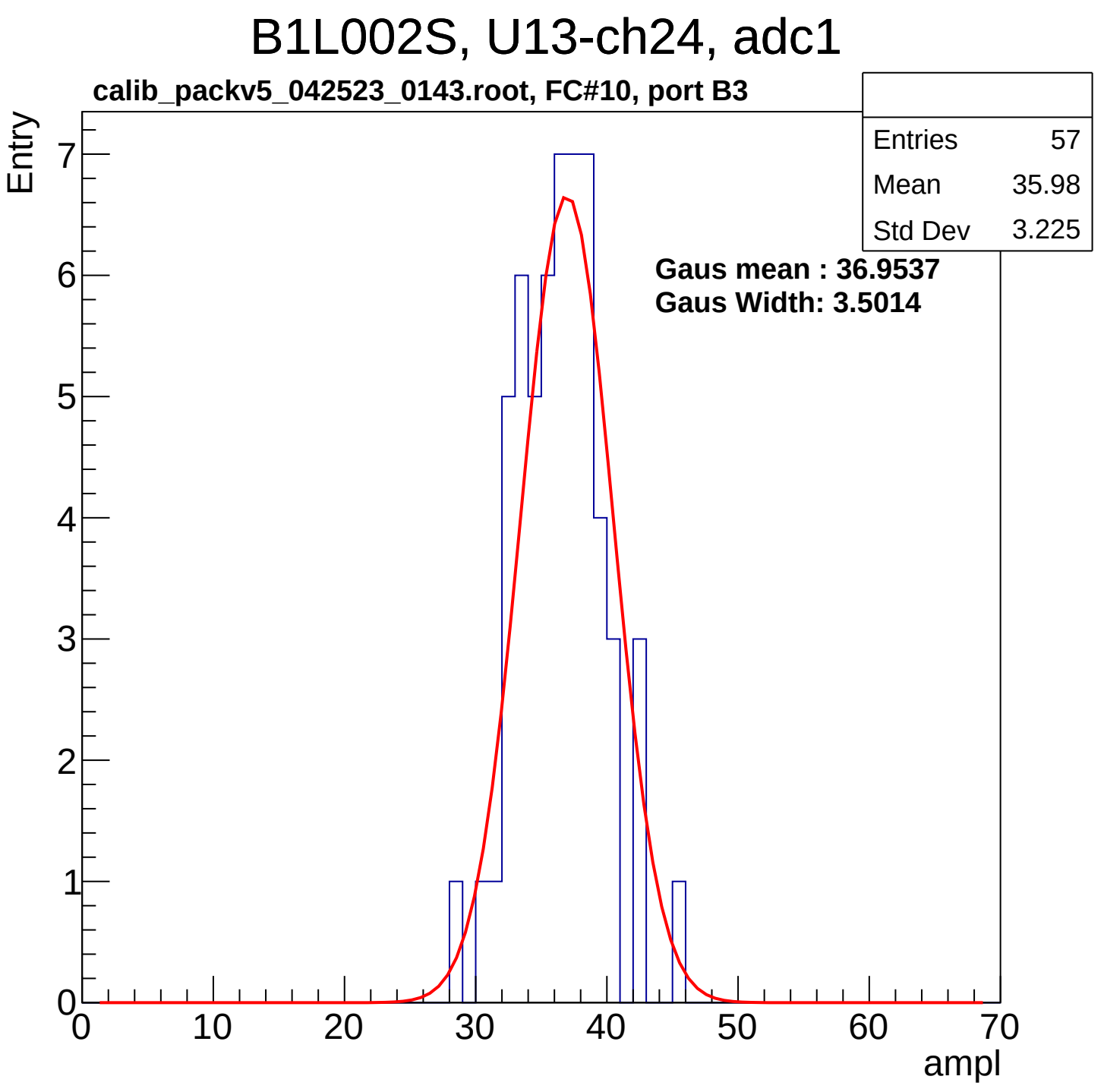
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.76
Std Dev 4.881
Gaus mean : 30.4570
Gaus Width: 3.3848
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.98
Std Dev 3.225
Gaus mean : 36.9537
Gaus Width: 3.5014
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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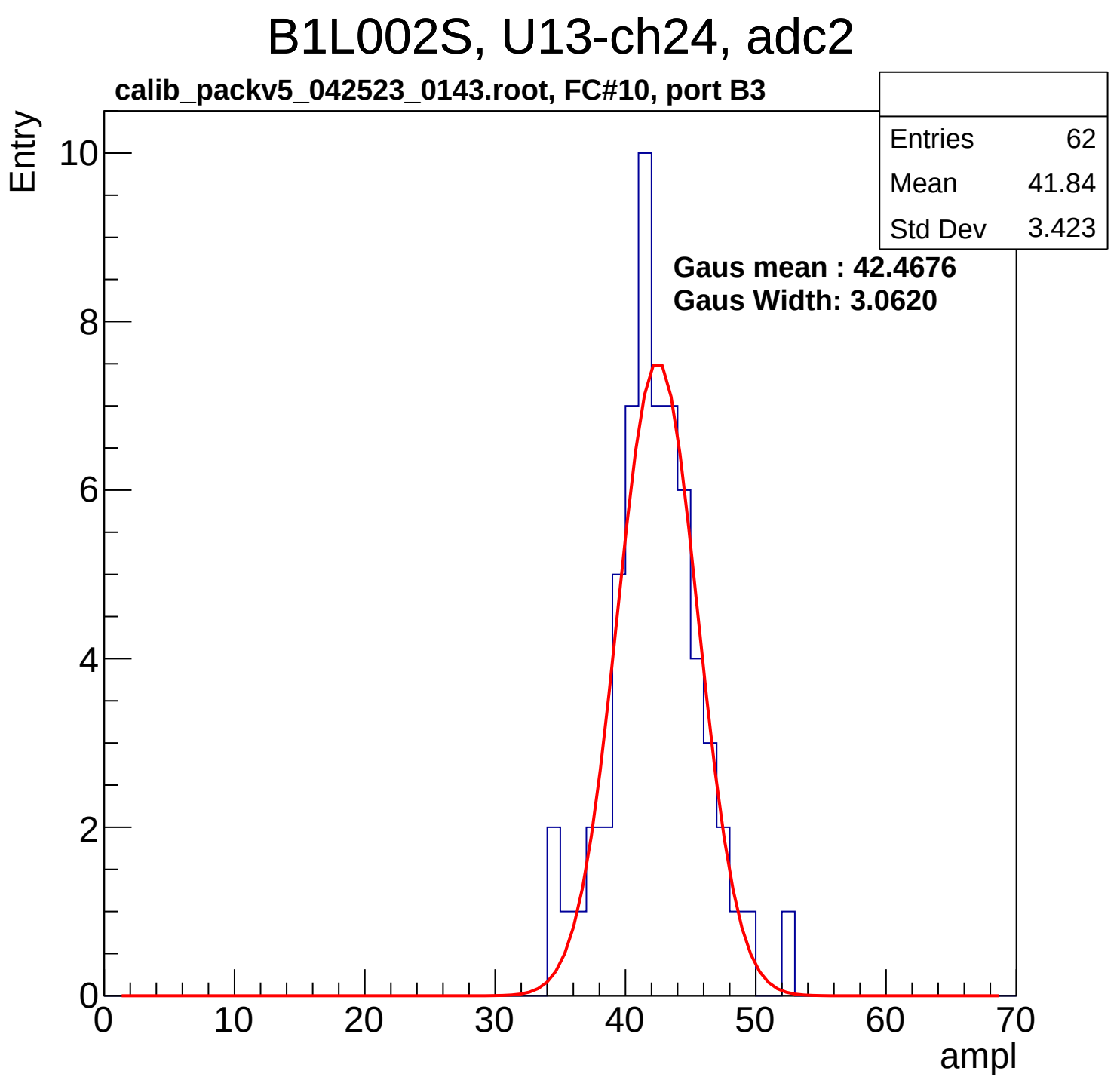
B1L002S, U13-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.84
Std Dev 3.423
Gaus mean : 42.4676
Gaus Width: 3.0620
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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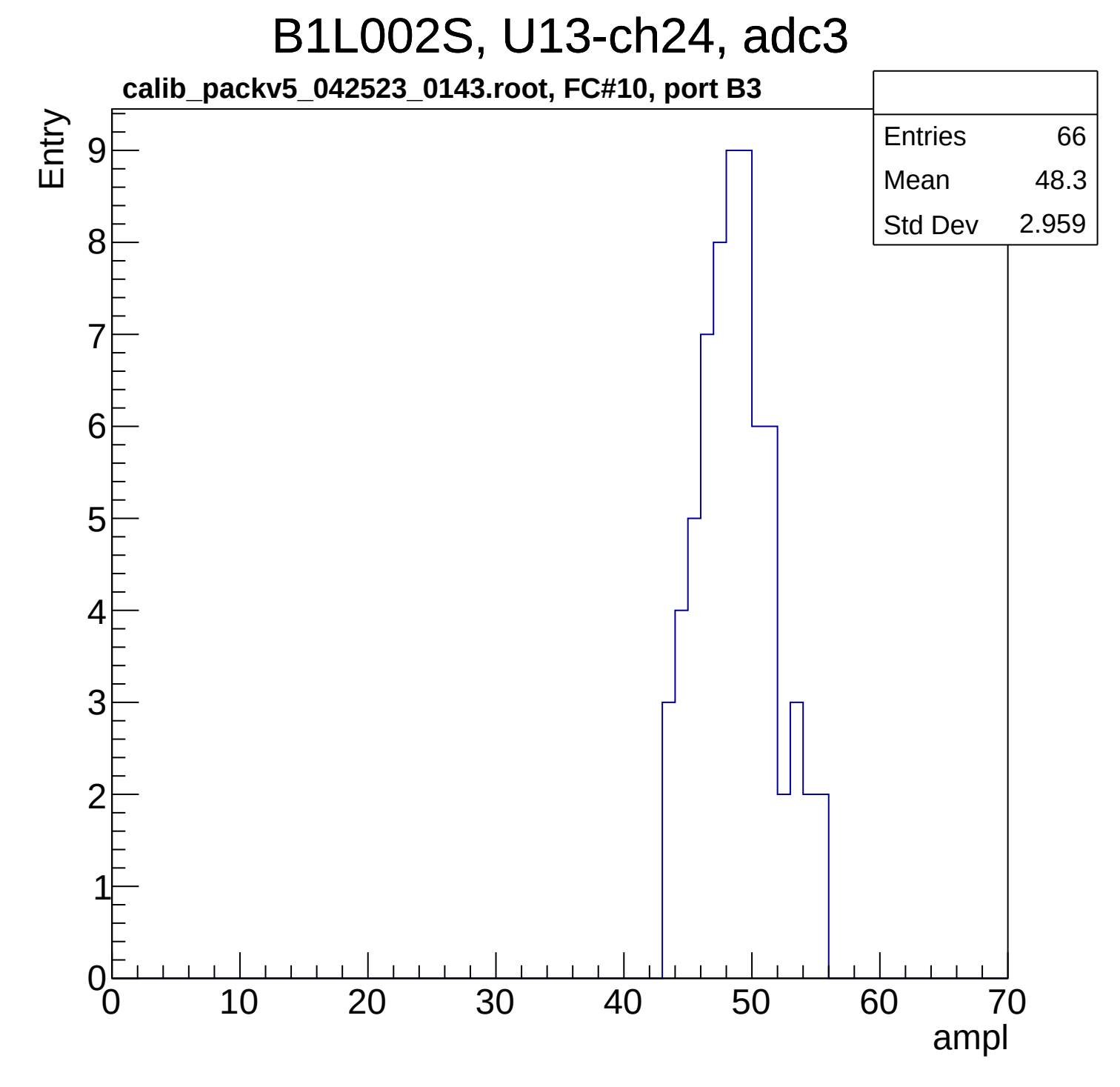
B1L002S, U13-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.3
Std Dev 2.959
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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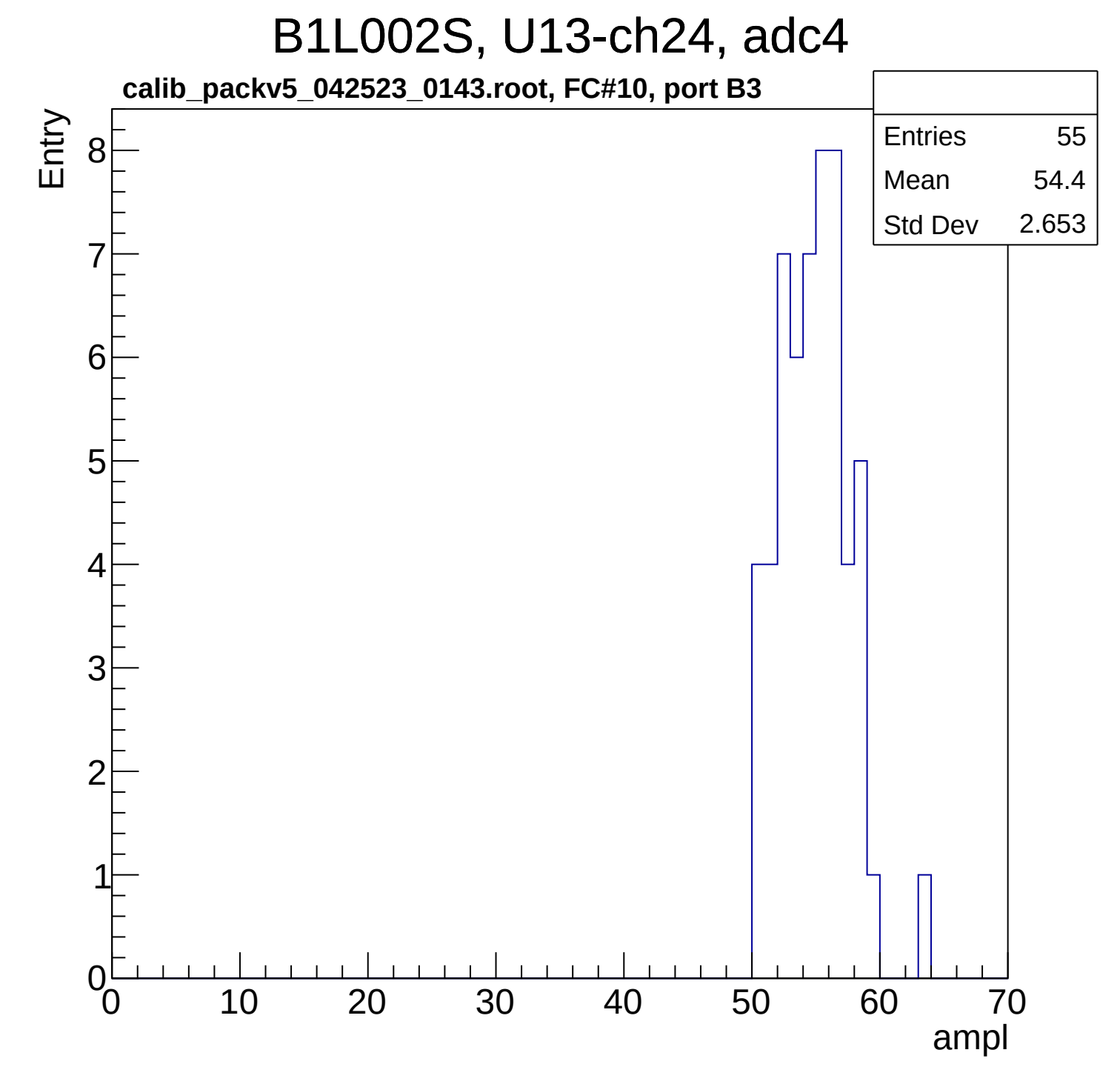
B1L002S, U13-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.4
Std Dev 2.653
Entry
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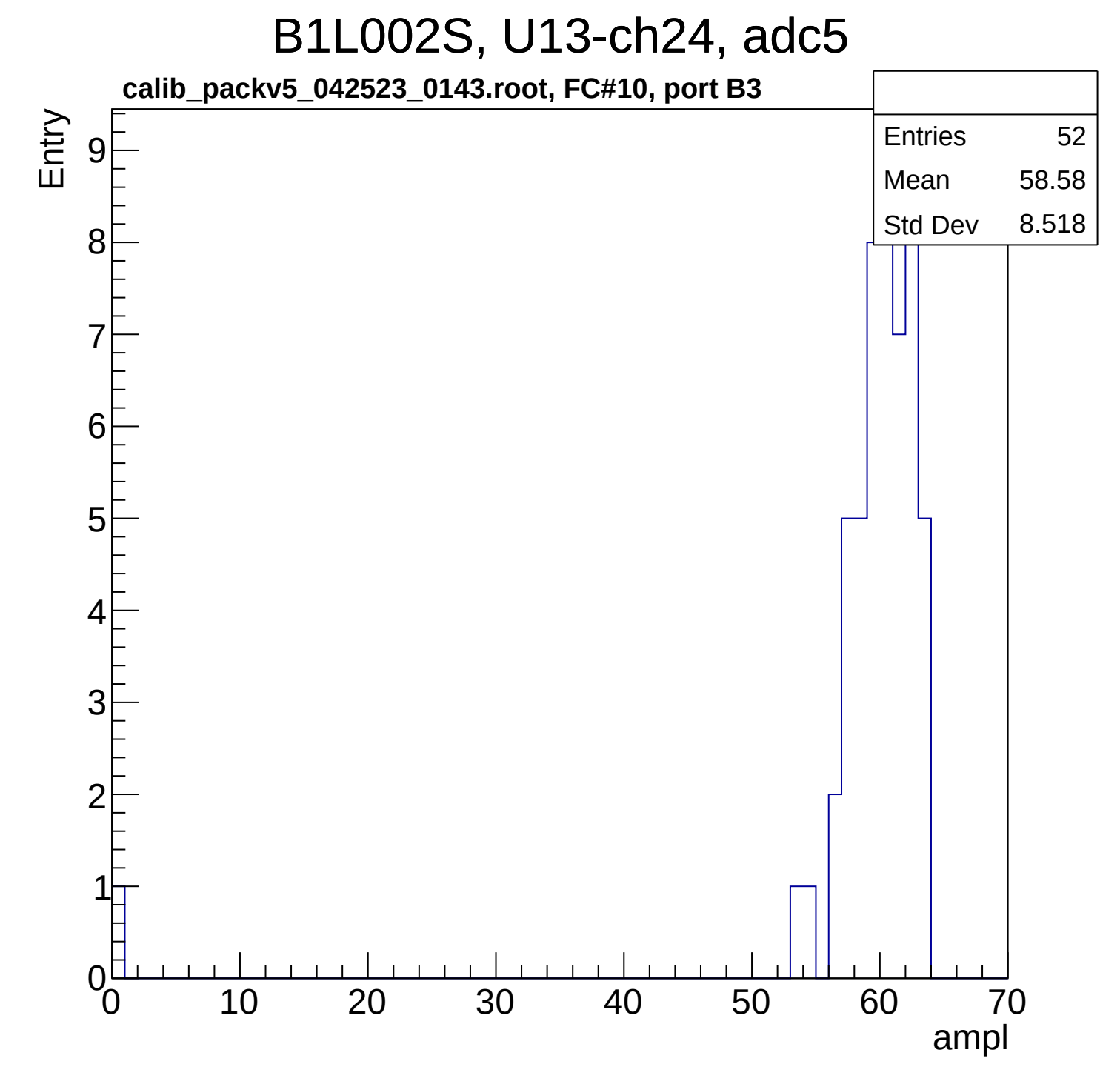
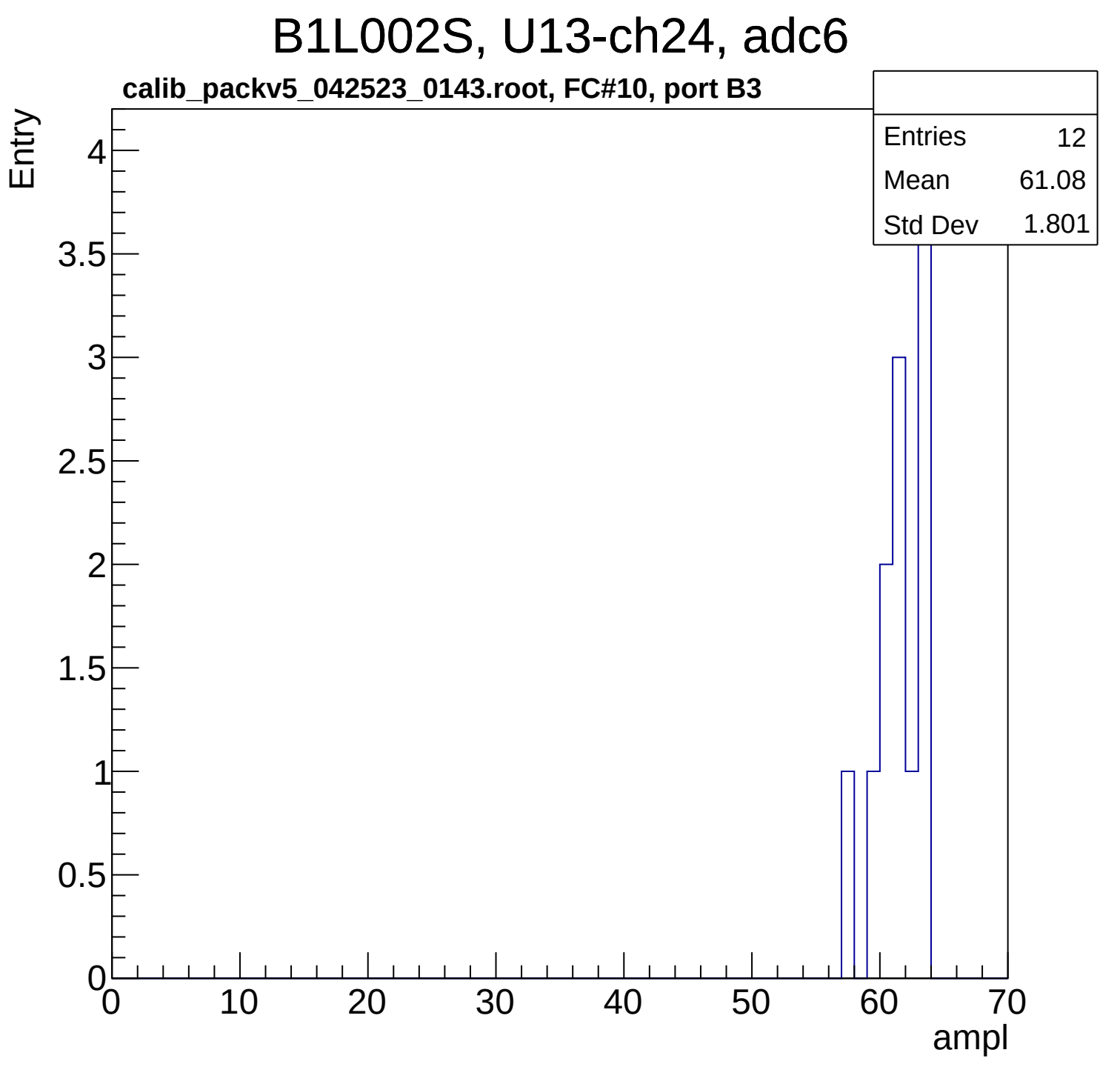
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.58
Std Dev 8.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.08
Std Dev 1.801
Entry
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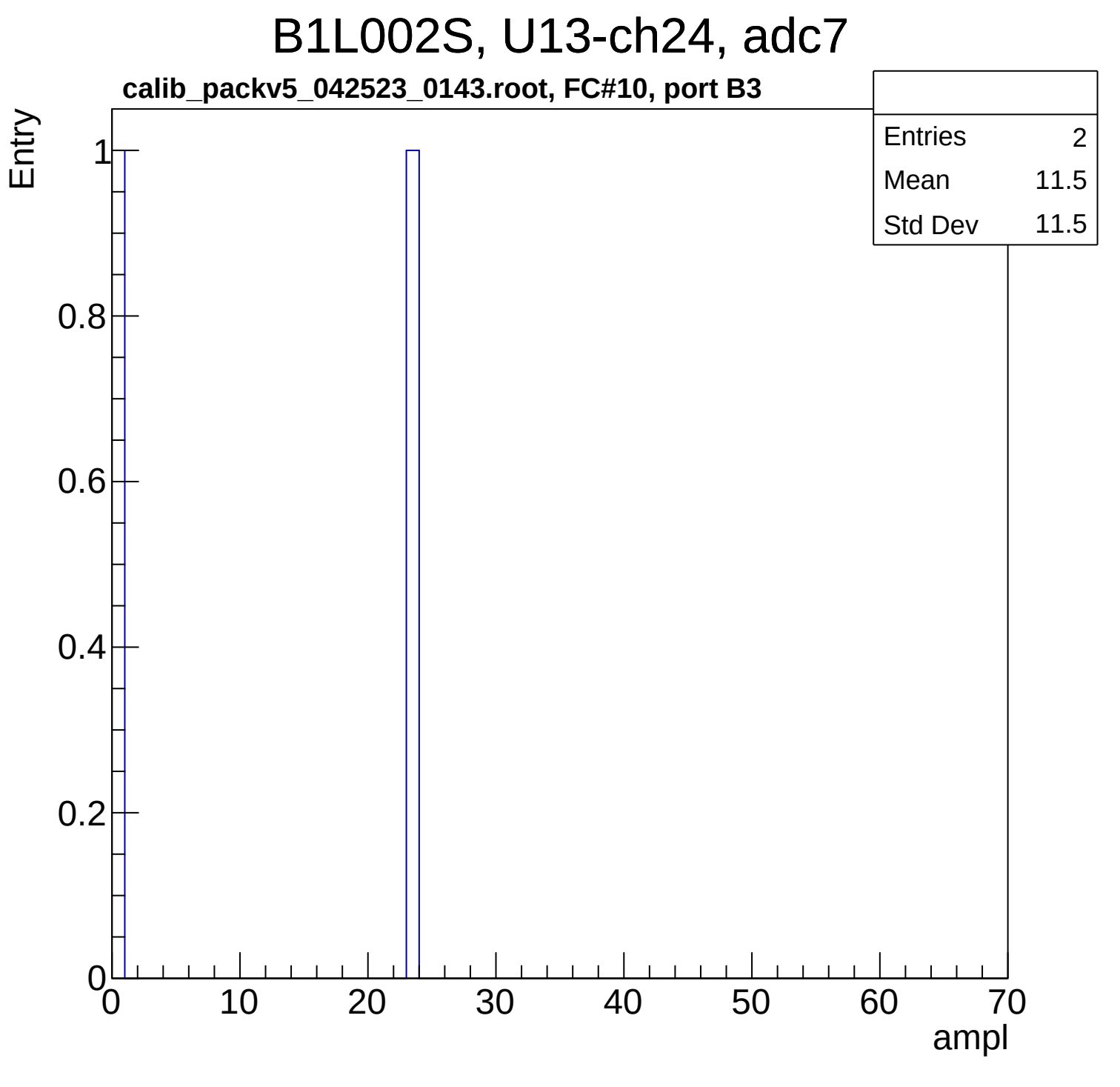
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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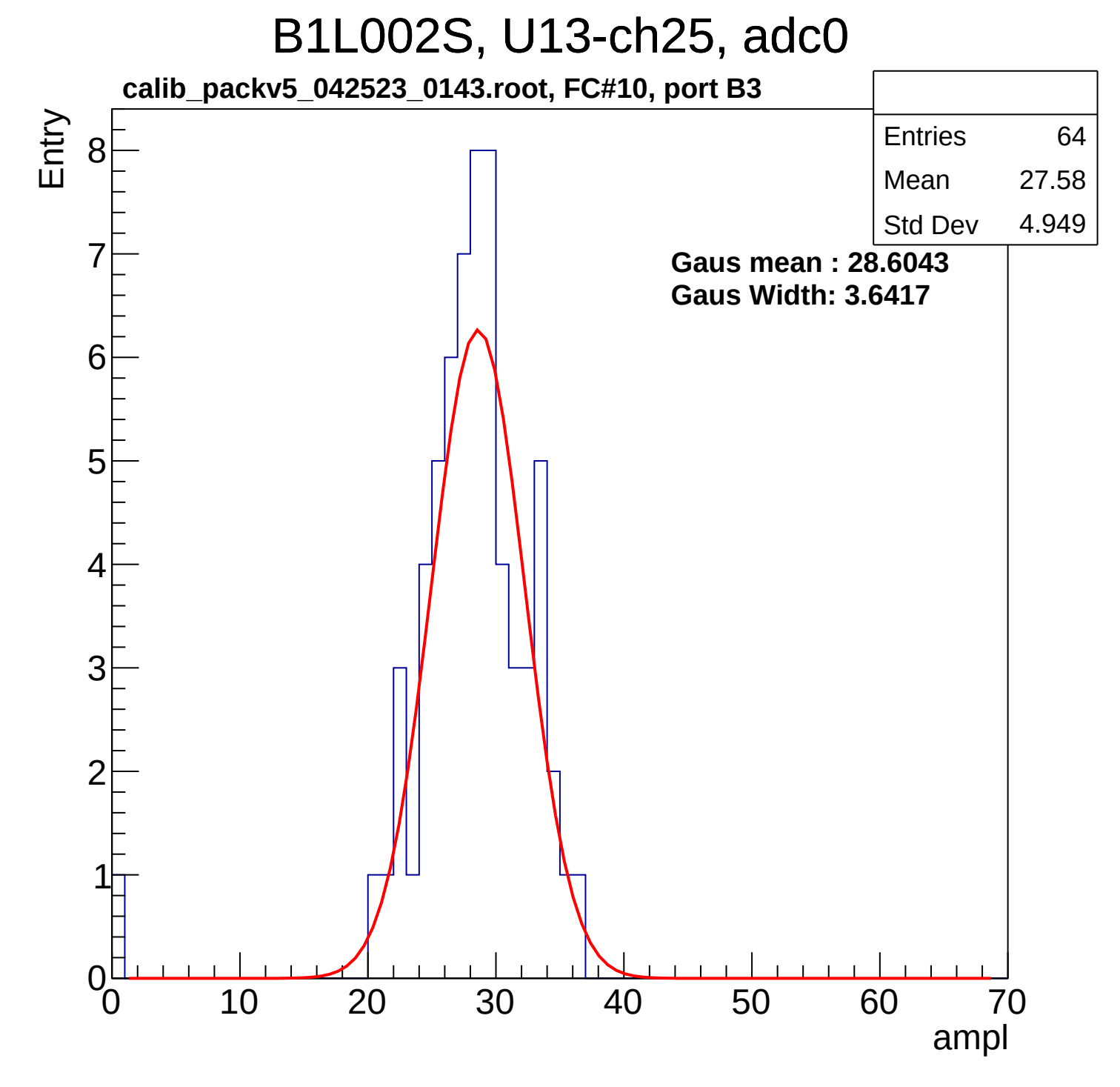
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.58
Std Dev 4.949
Gaus mean : 28.6043
Gaus Width: 3.6417
Entry
ampl
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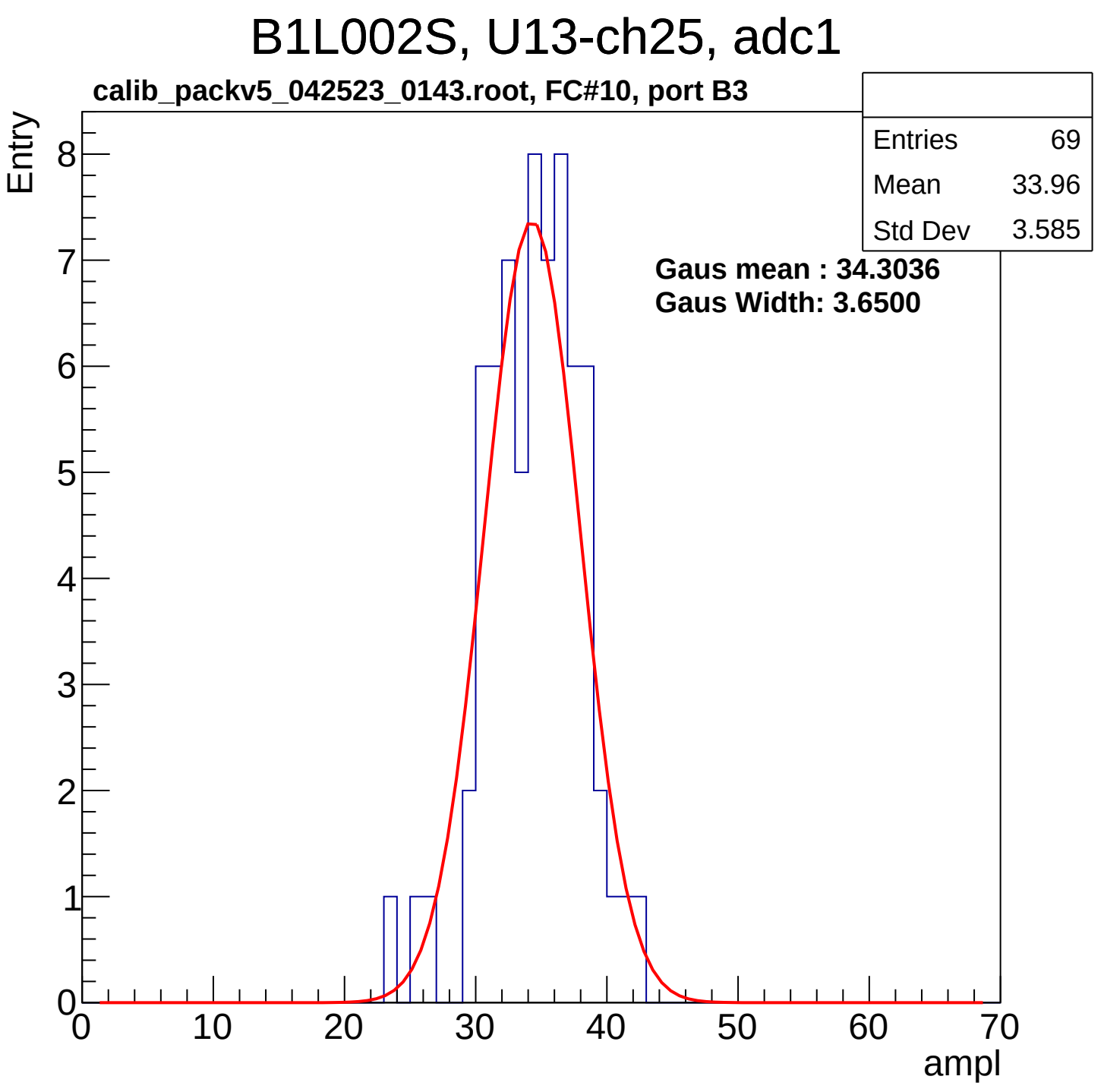
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.96
Std Dev 3.585
Gaus mean : 34.3036
Gaus Width: 3.6500
Entry
ampl
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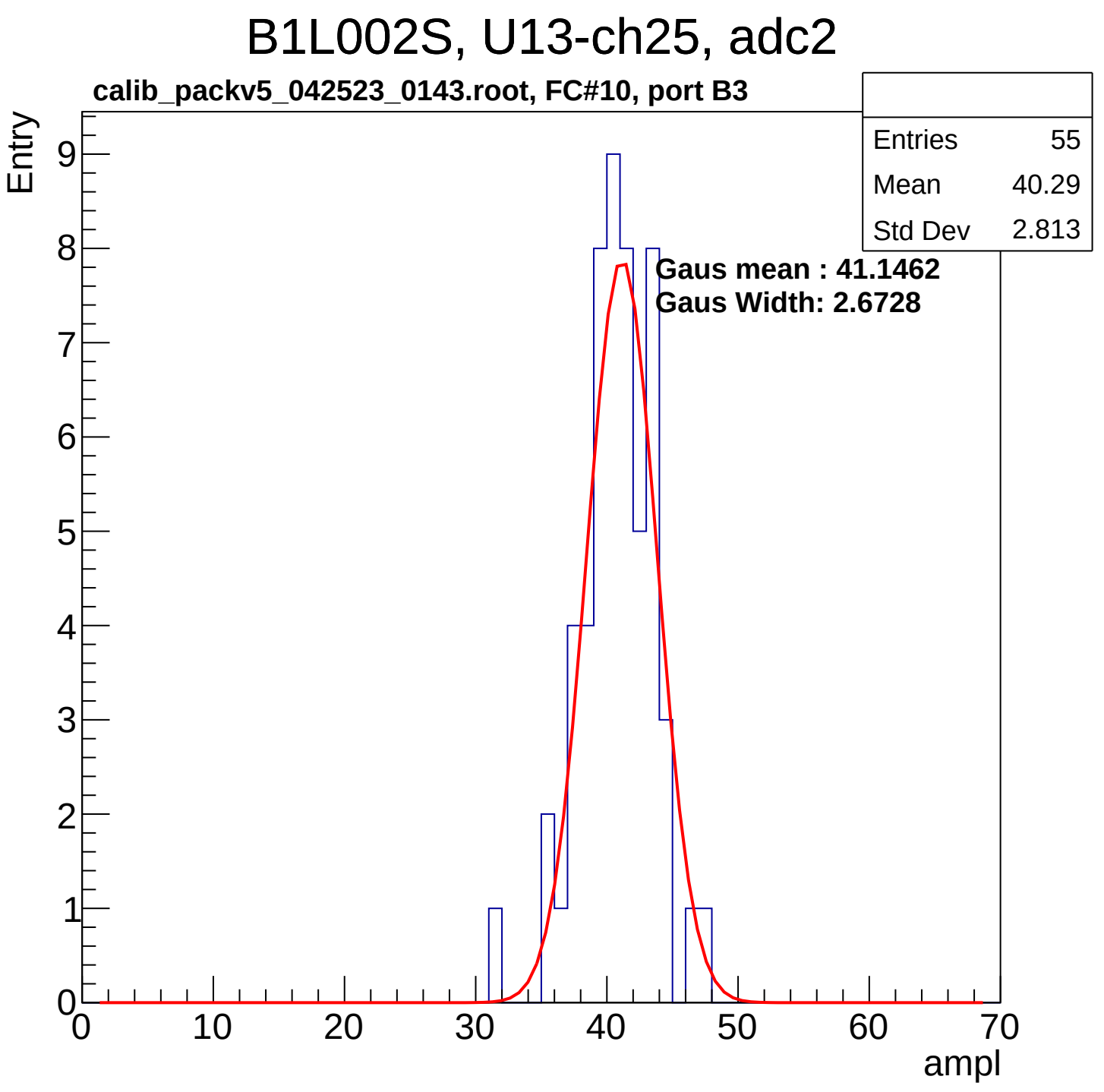
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.29
Std Dev 2.813
Gaus mean : 41.1462
Gaus Width: 2.6728
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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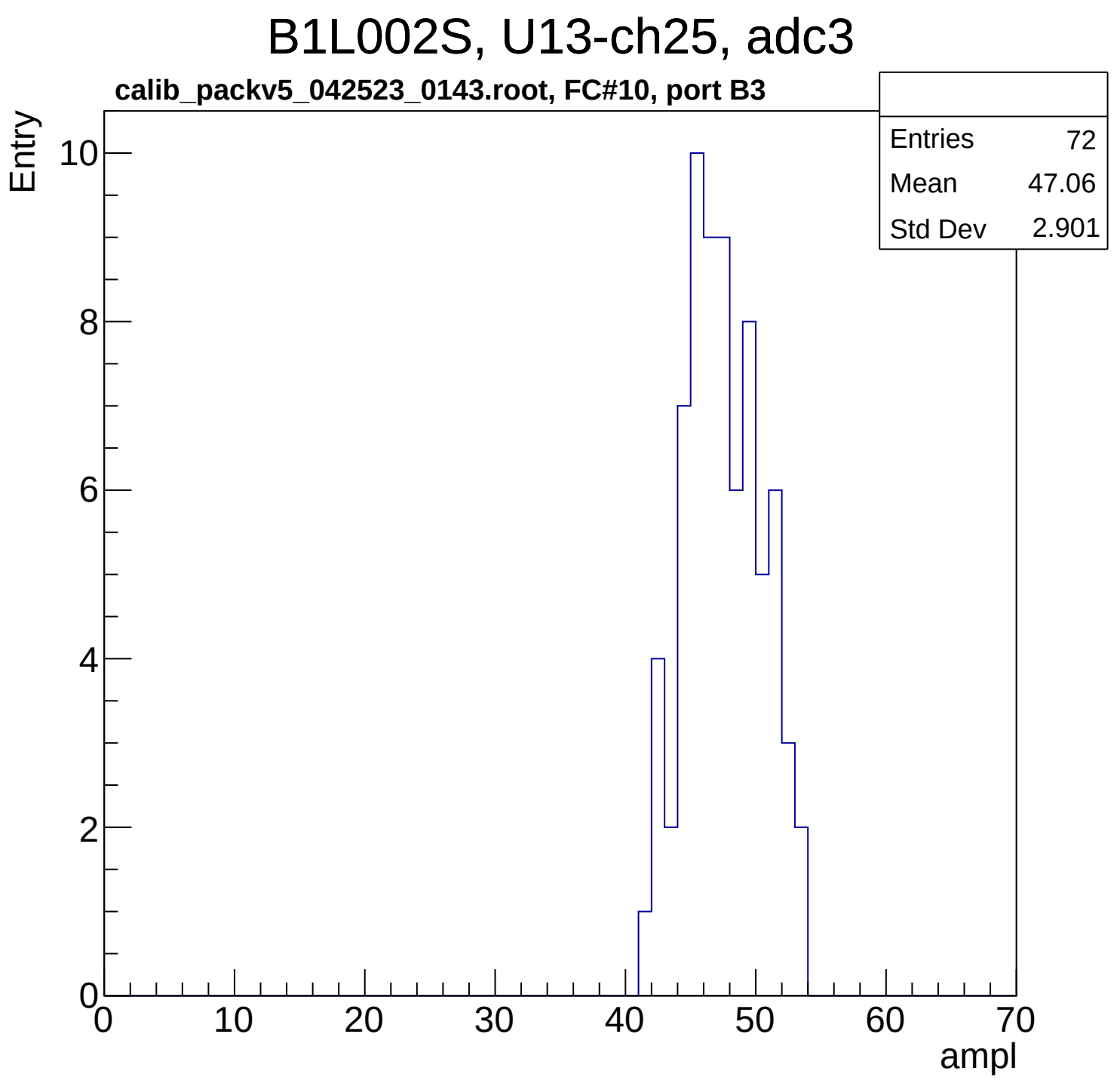
B1L002S, U13-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.06
Std Dev 2.901
Entry
0
2
4
6
8
10
0
10
20
30
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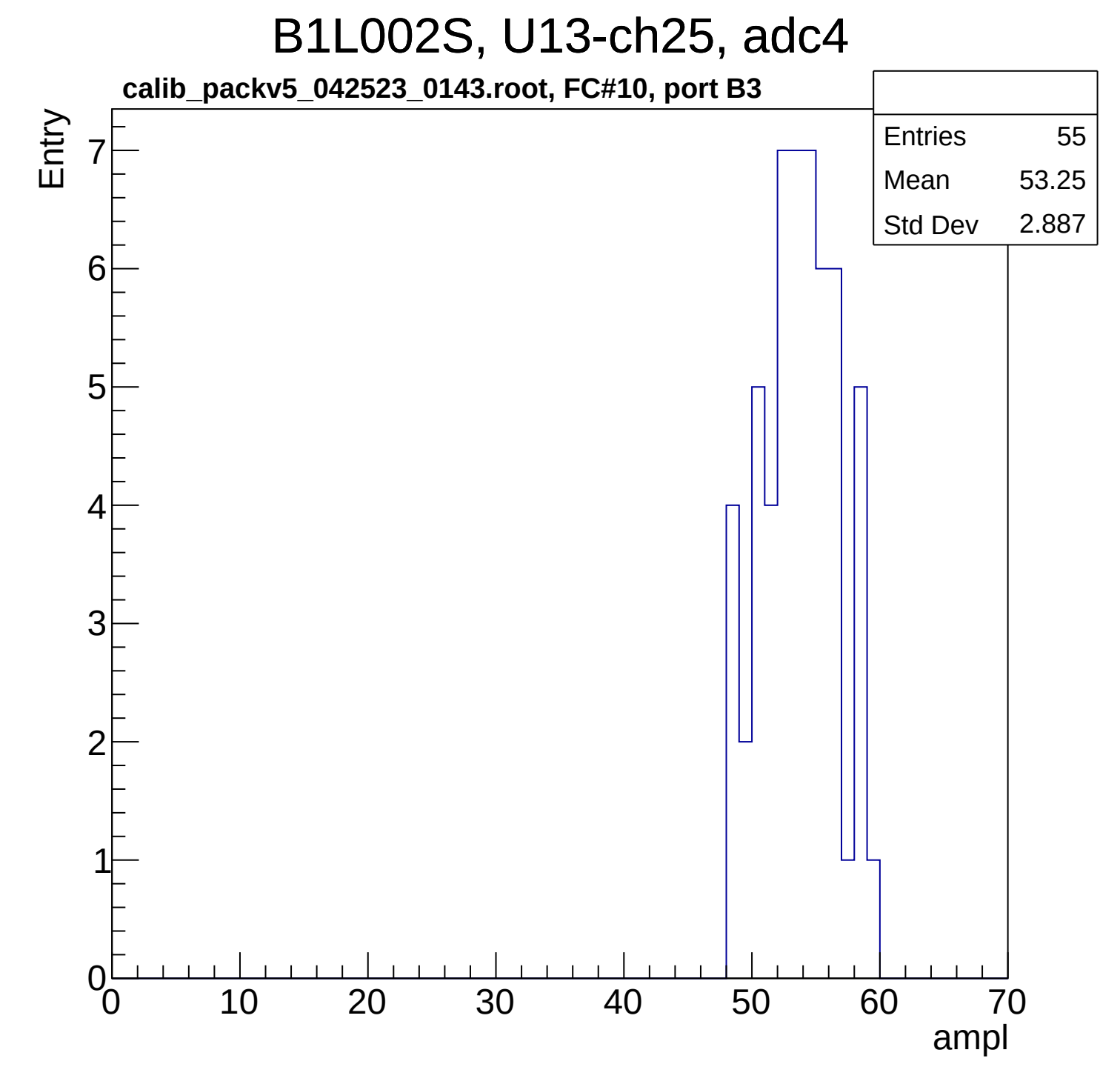
50
60
70
ampl


B1L002S, U13-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.25
Std Dev 2.887
Entry
ampl
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4
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6
7
0
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20
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40
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60
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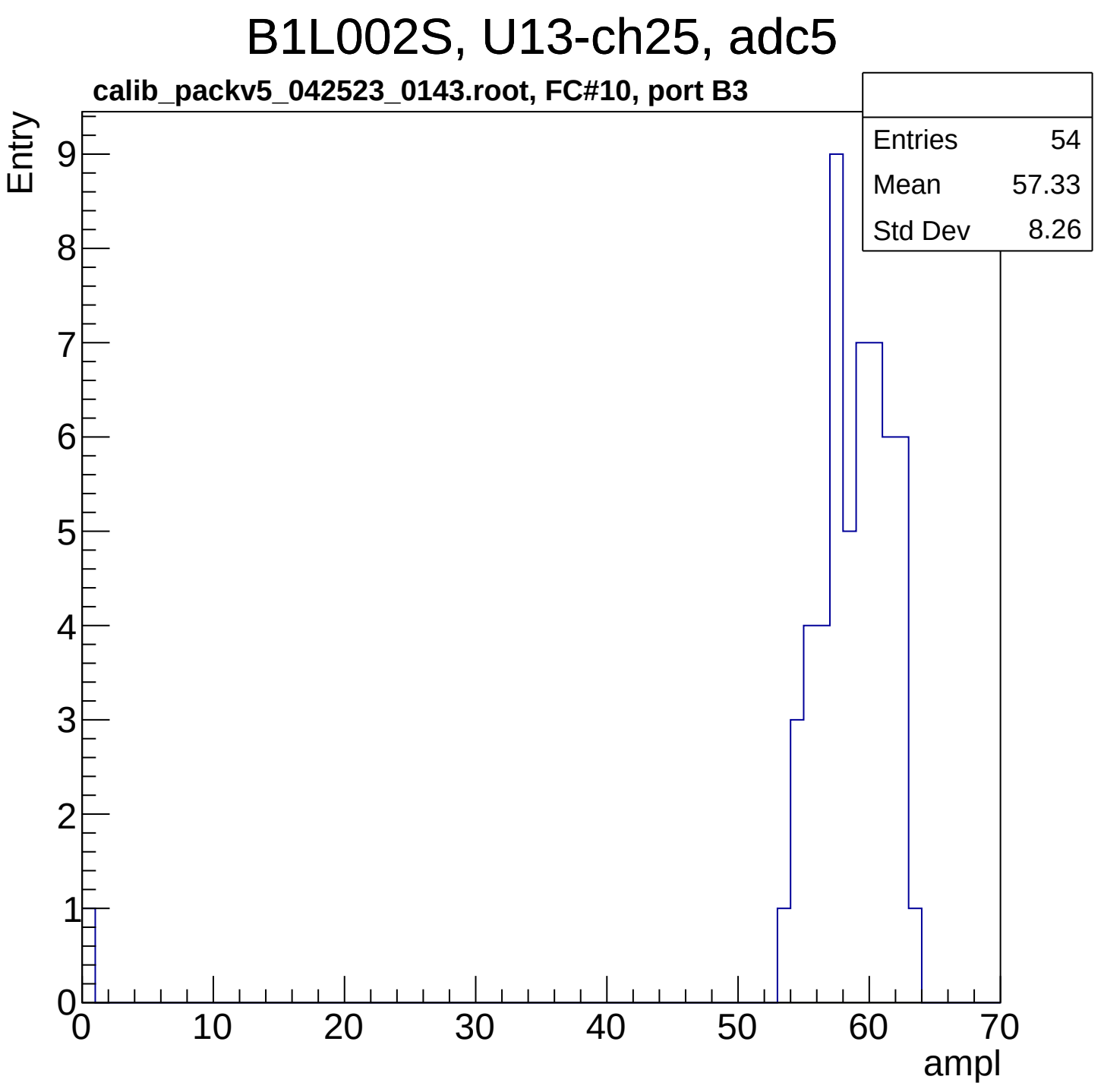
B1L002S, U13-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.33
Std Dev 8.26
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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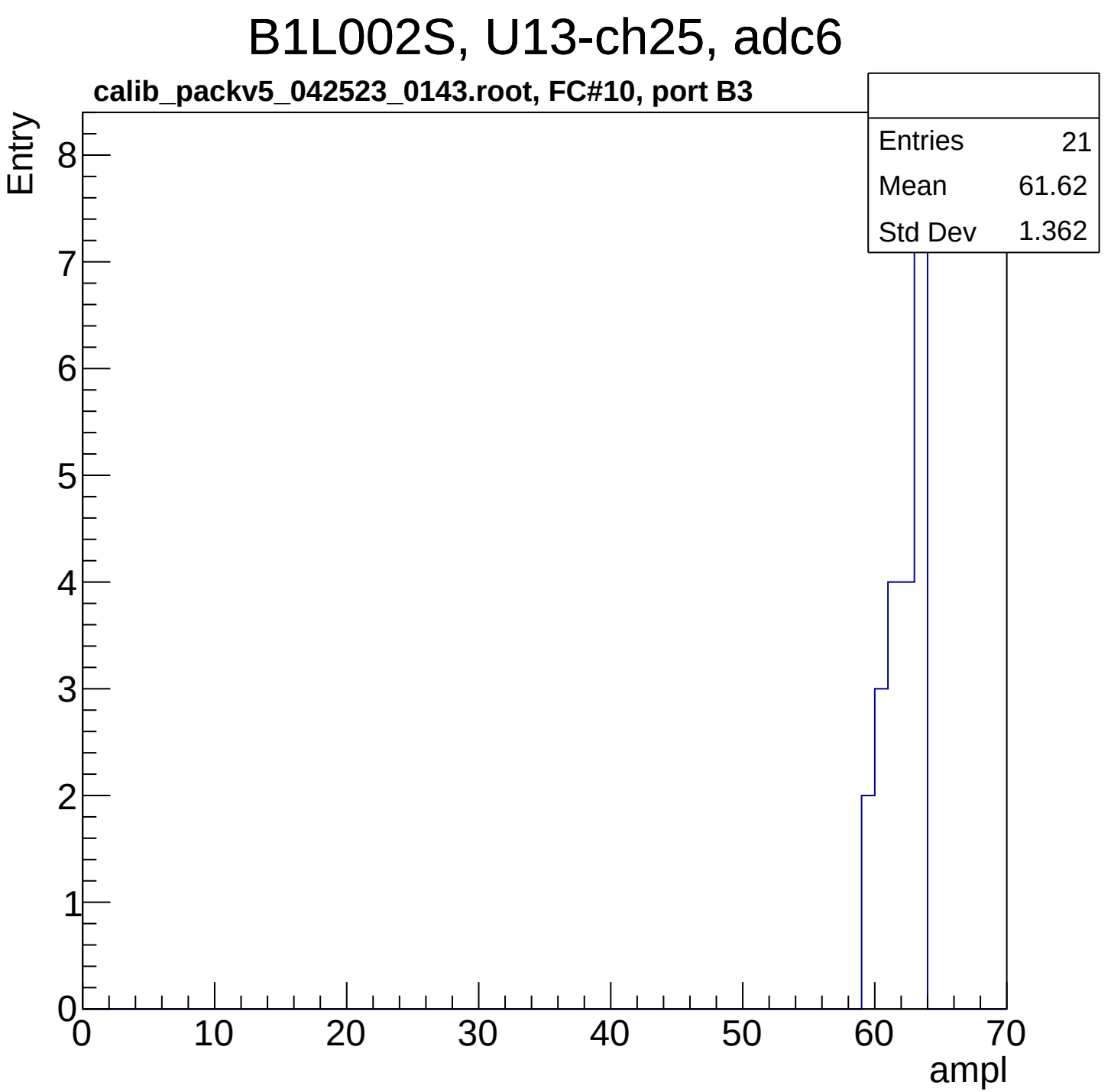
B1L002S, U13-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.62
Std Dev 1.362
Entry
ampl
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3
4
5
6
7
8
0
10
20
30
40
50
60
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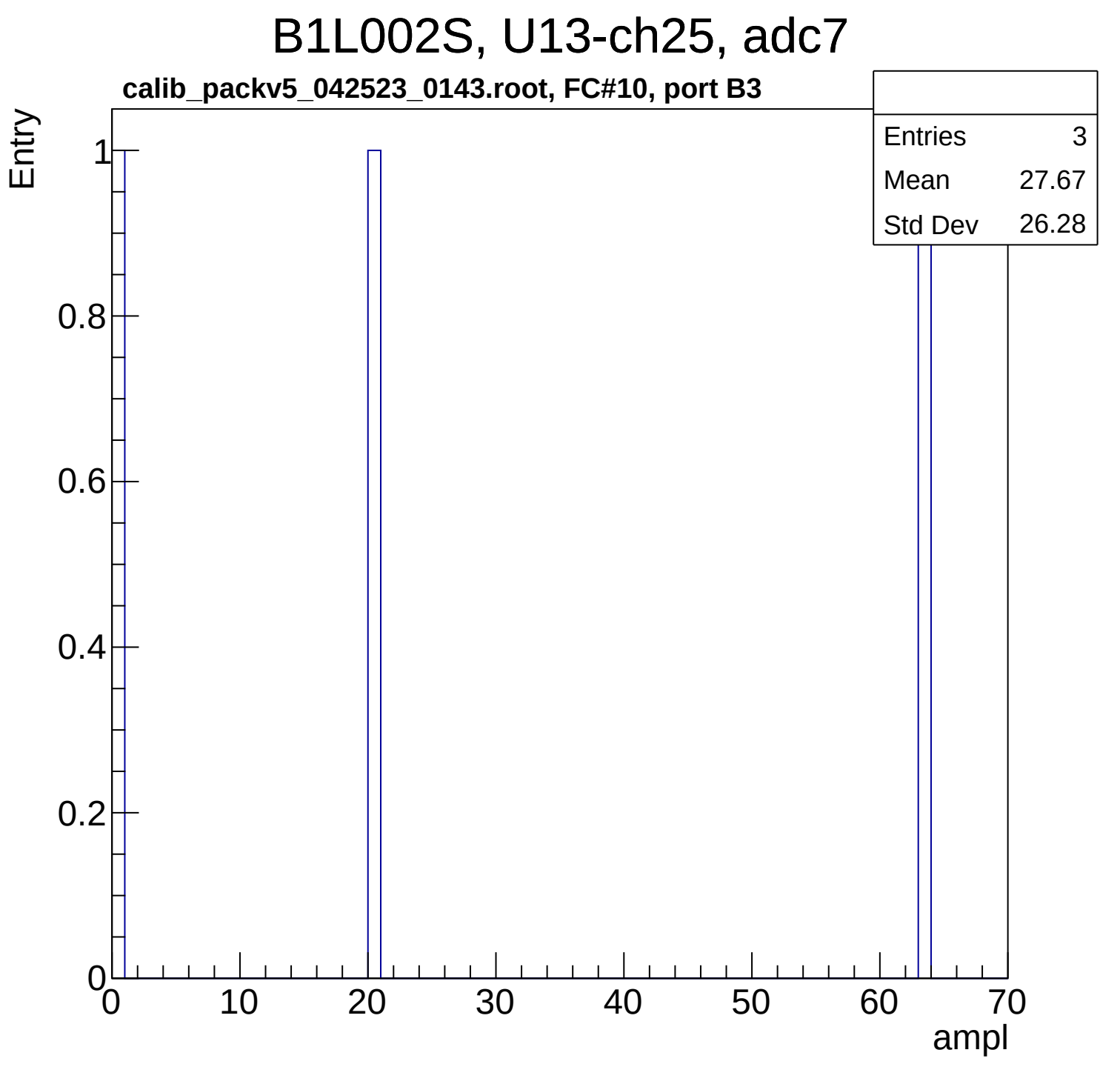
B1L002S, U13-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.67
Std Dev 26.28
Entry
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0.6
0.4
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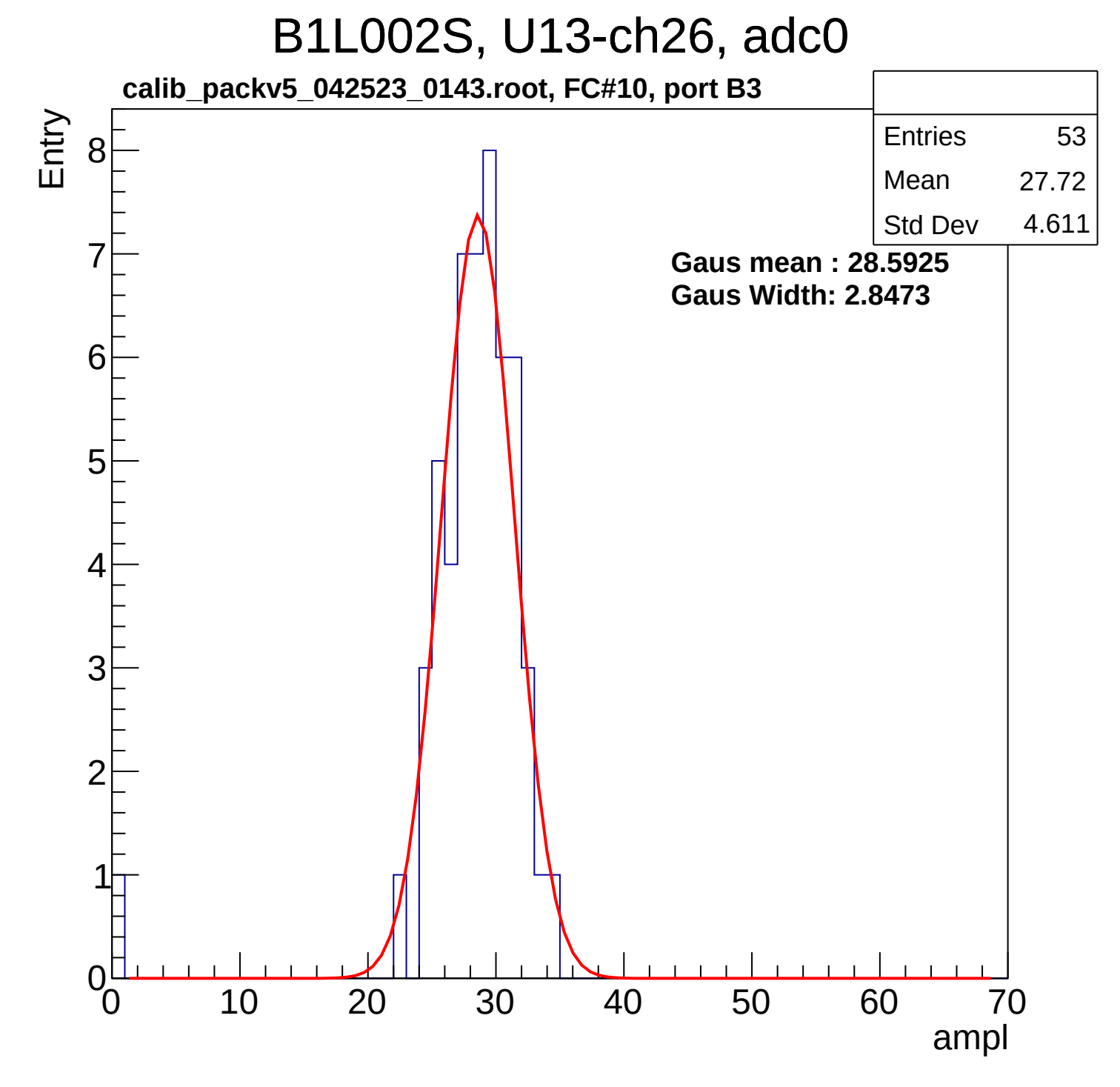
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 27.72
Std Dev 4.611
Gaus mean : 28.5925
Gaus Width: 2.8473
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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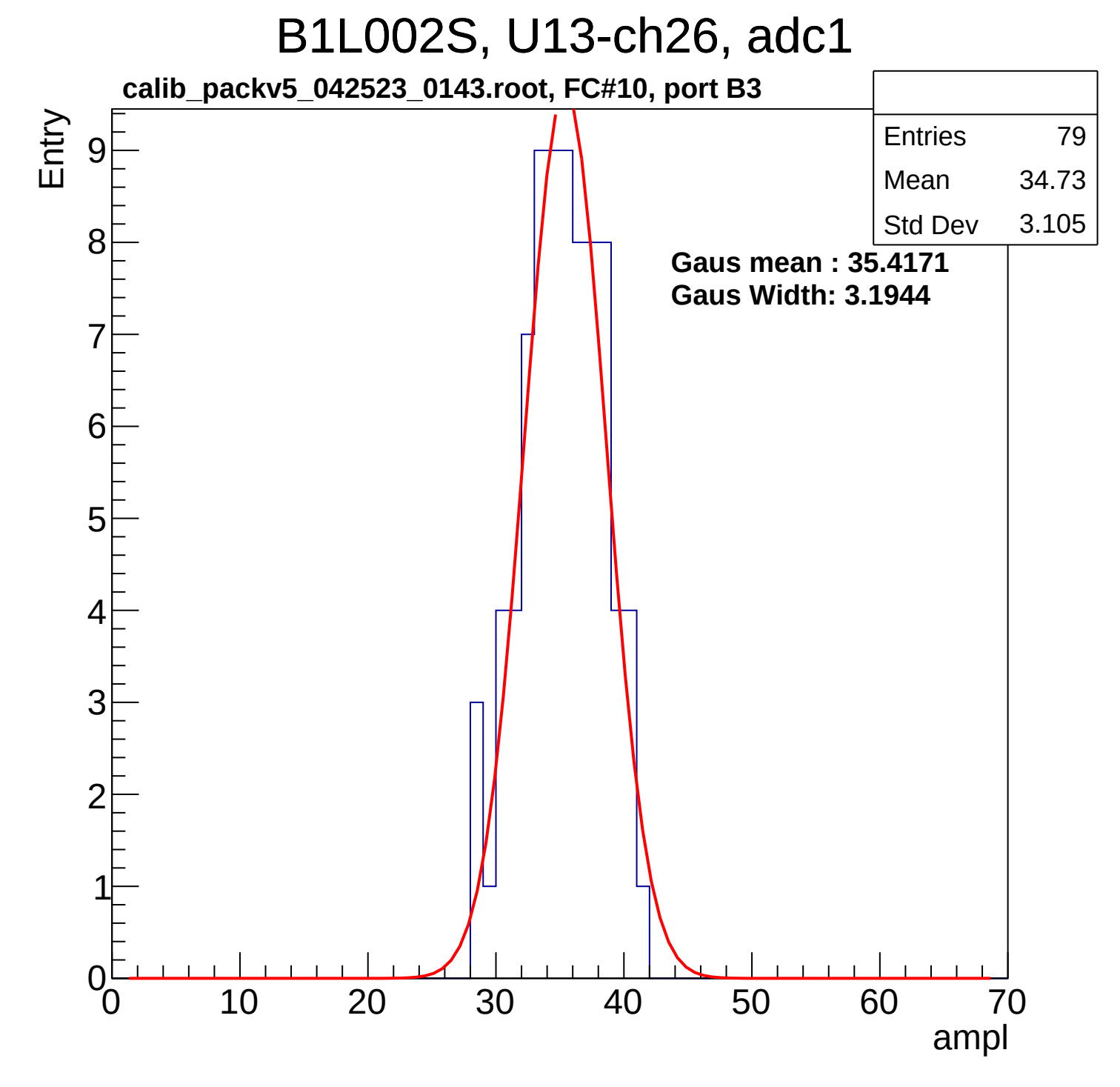
40
50
60
70


B1L002S, U13-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.73
Std Dev 3.105
Gaus mean : 35.4171
Gaus Width: 3.1944
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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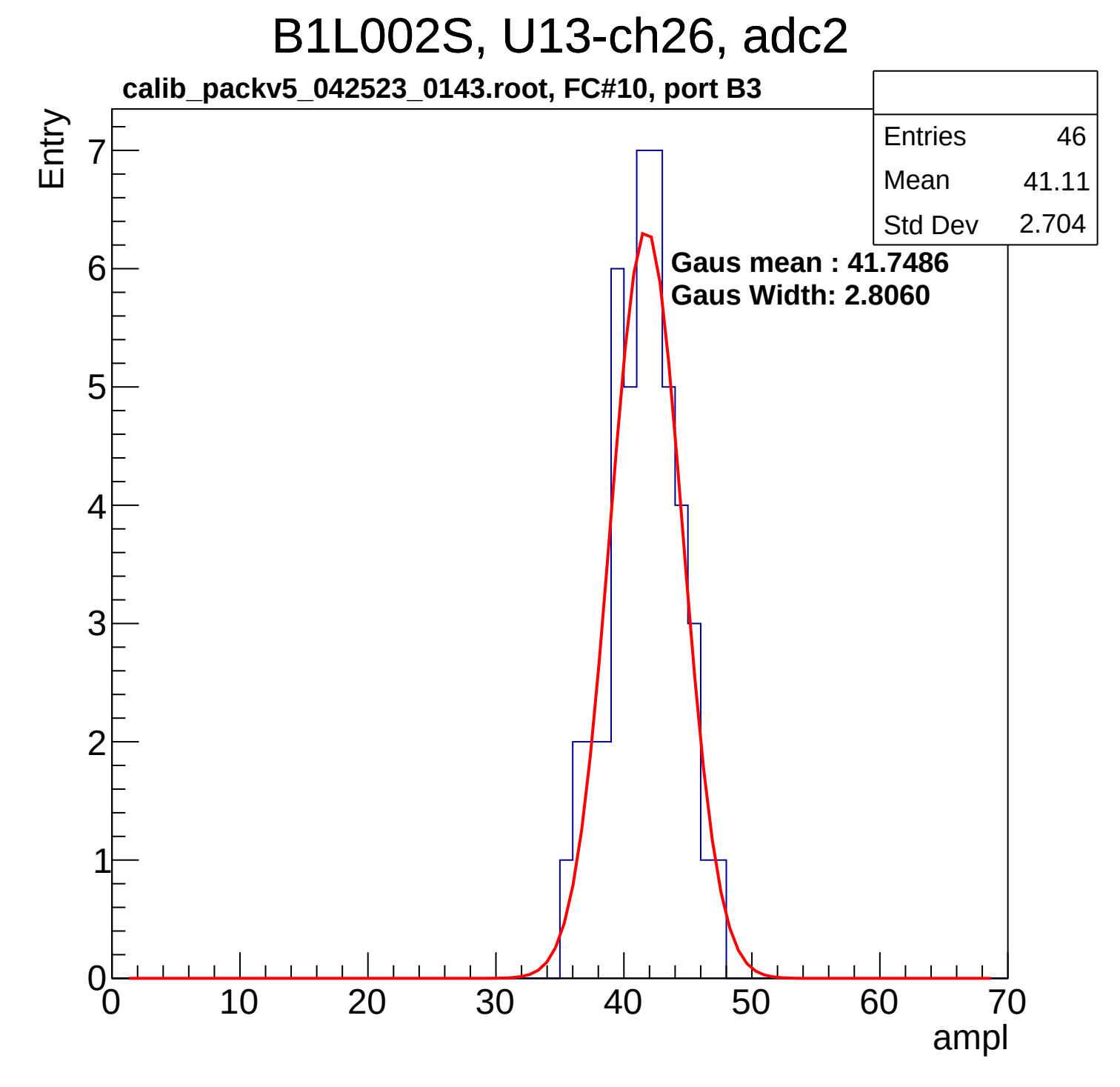
B1L002S, U13-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 41.11
Std Dev 2.704
Gaus mean : 41.7486
Gaus Width: 2.8060
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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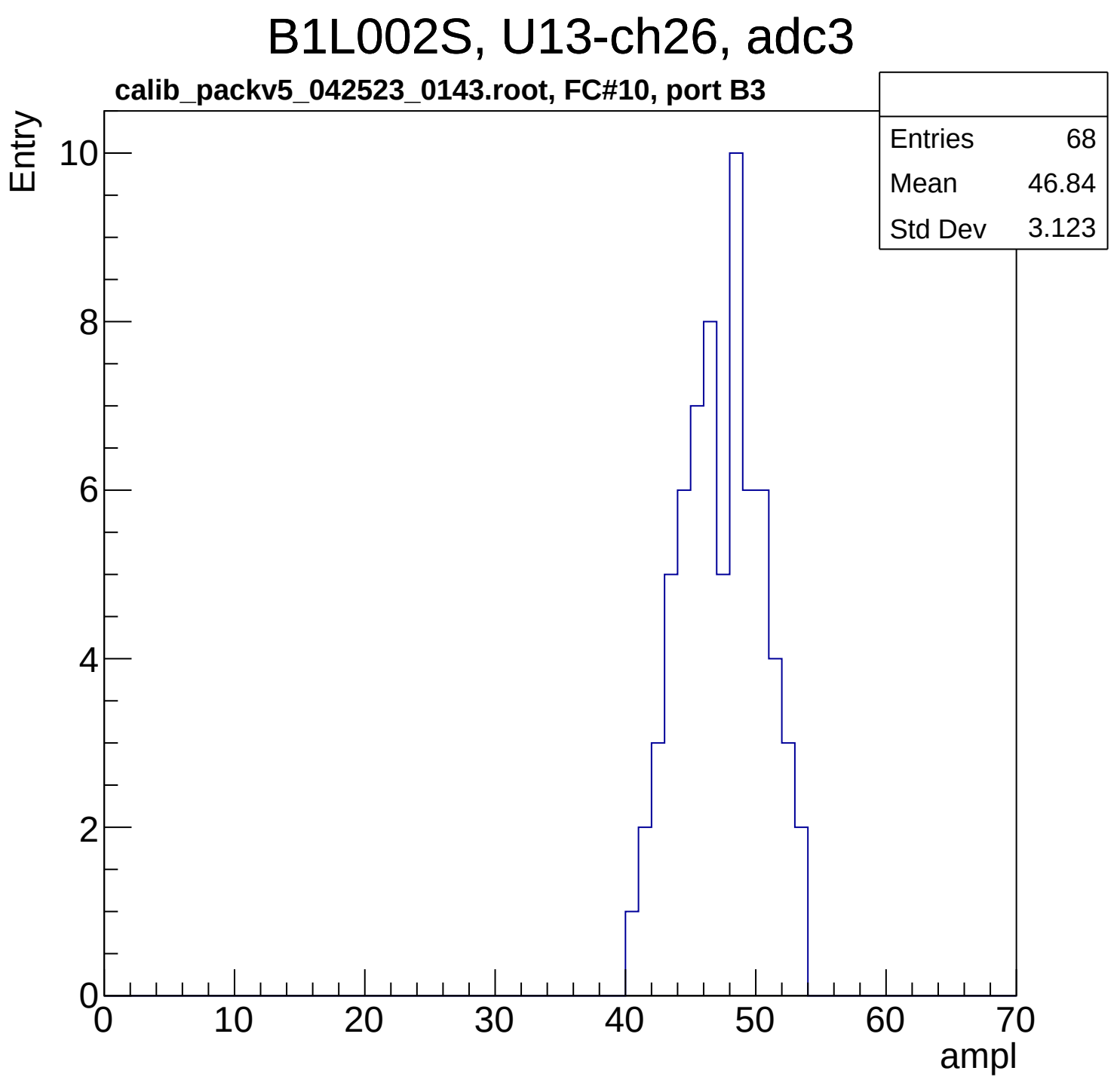
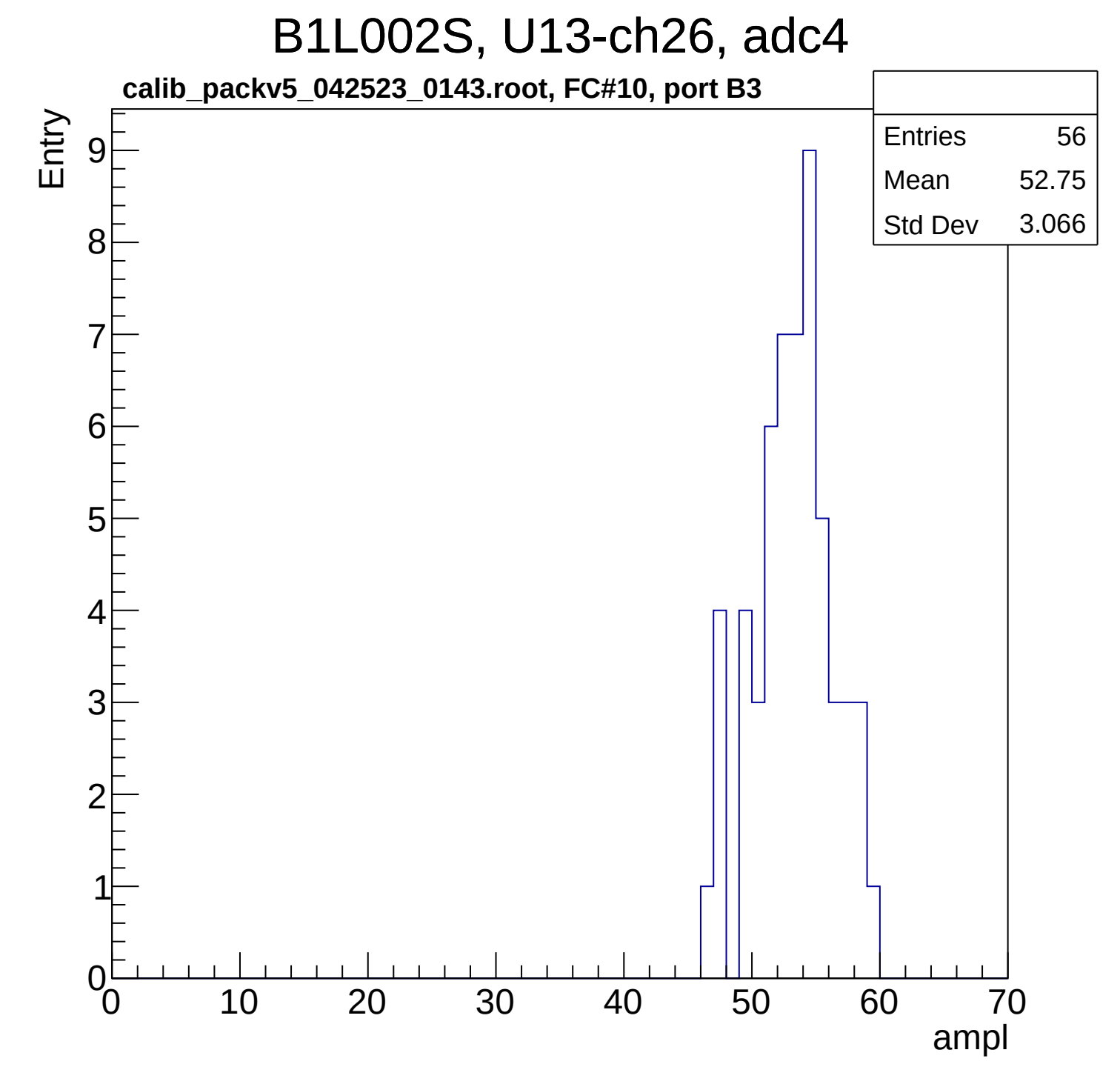

B1L002S, U13-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.84
Std Dev 3.123
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.75
Std Dev 3.066
Entry
ampl
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3
4
5
6
7
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0
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40
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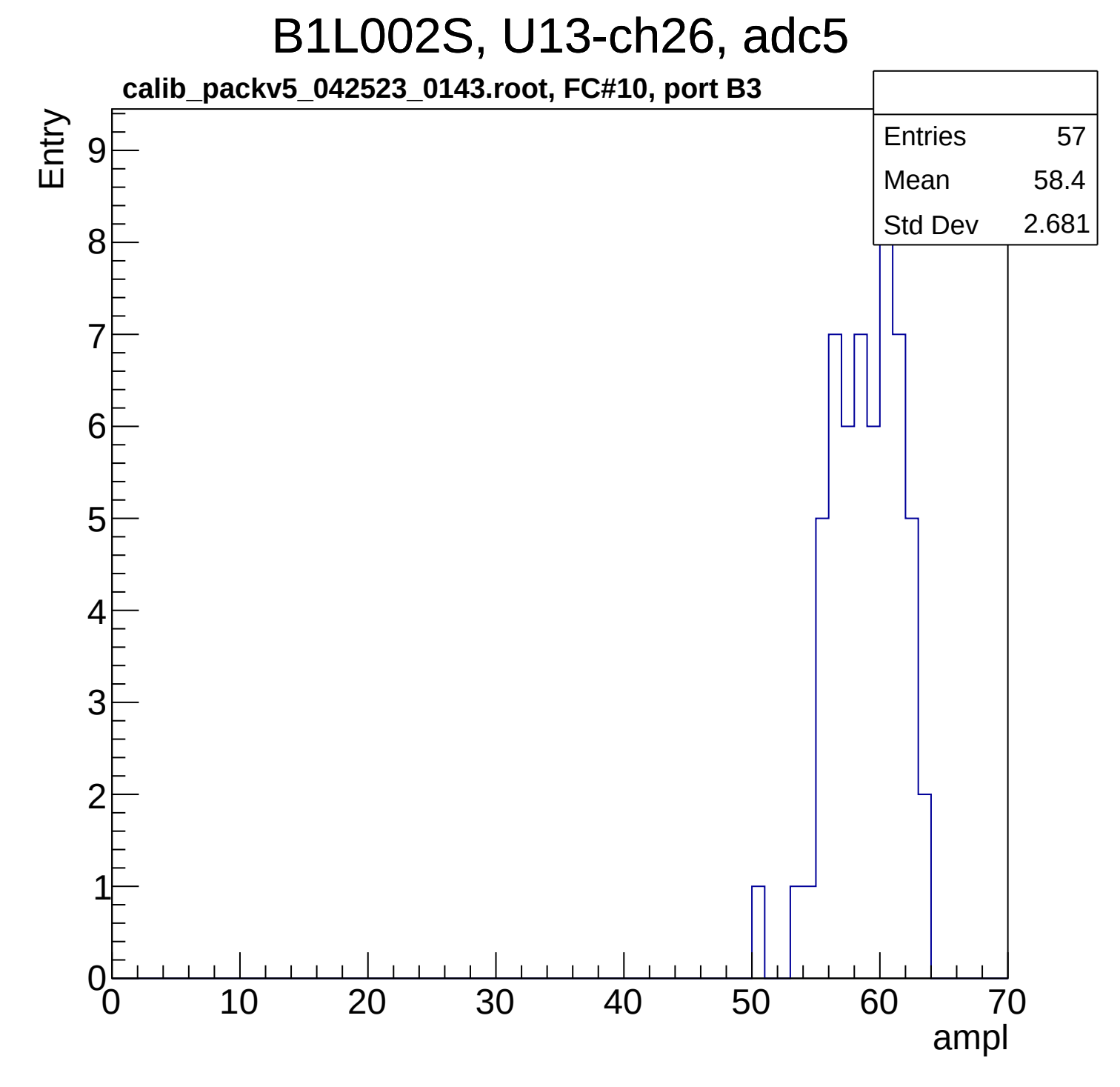
B1L002S, U13-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.4
Std Dev 2.681
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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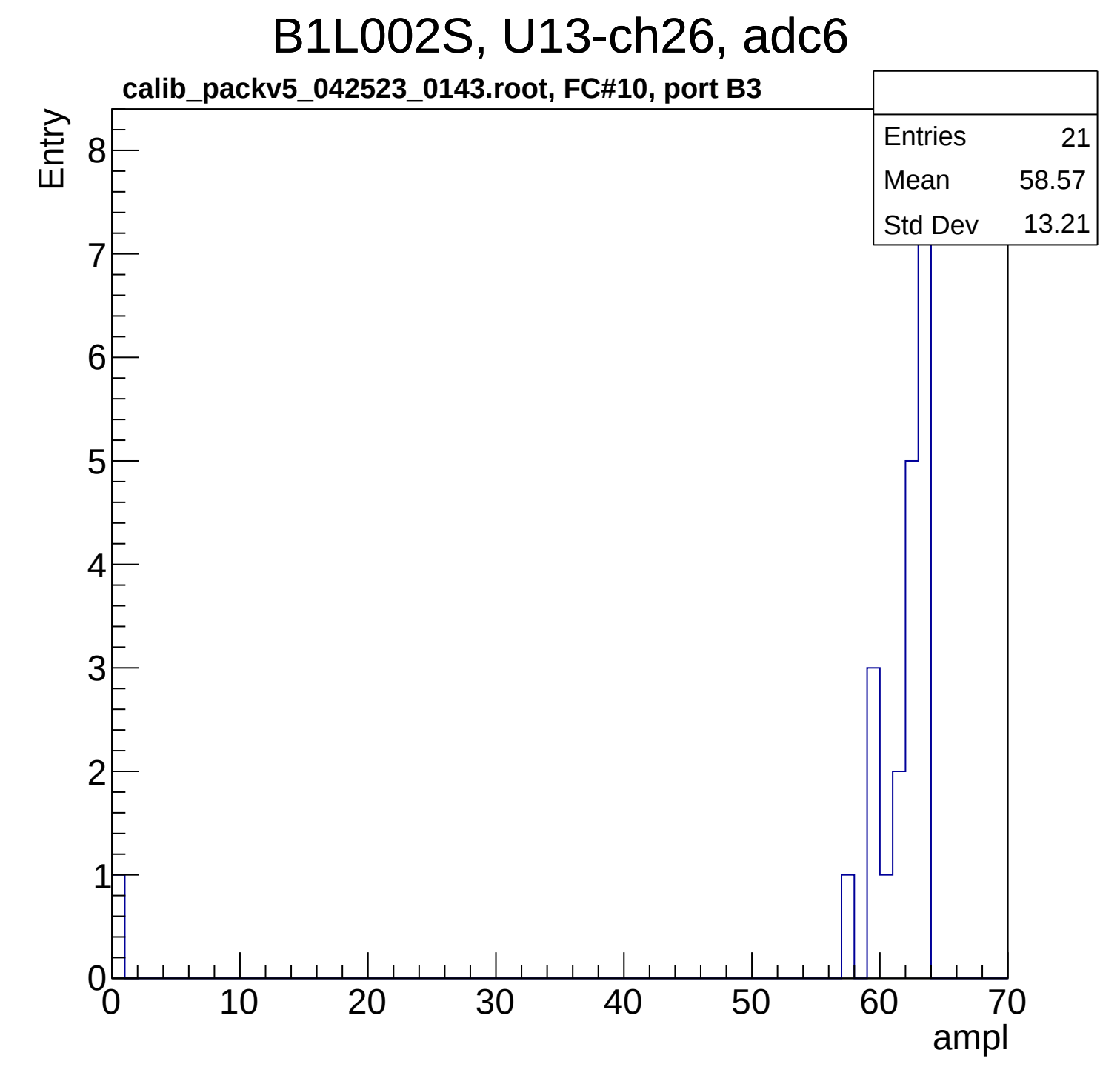
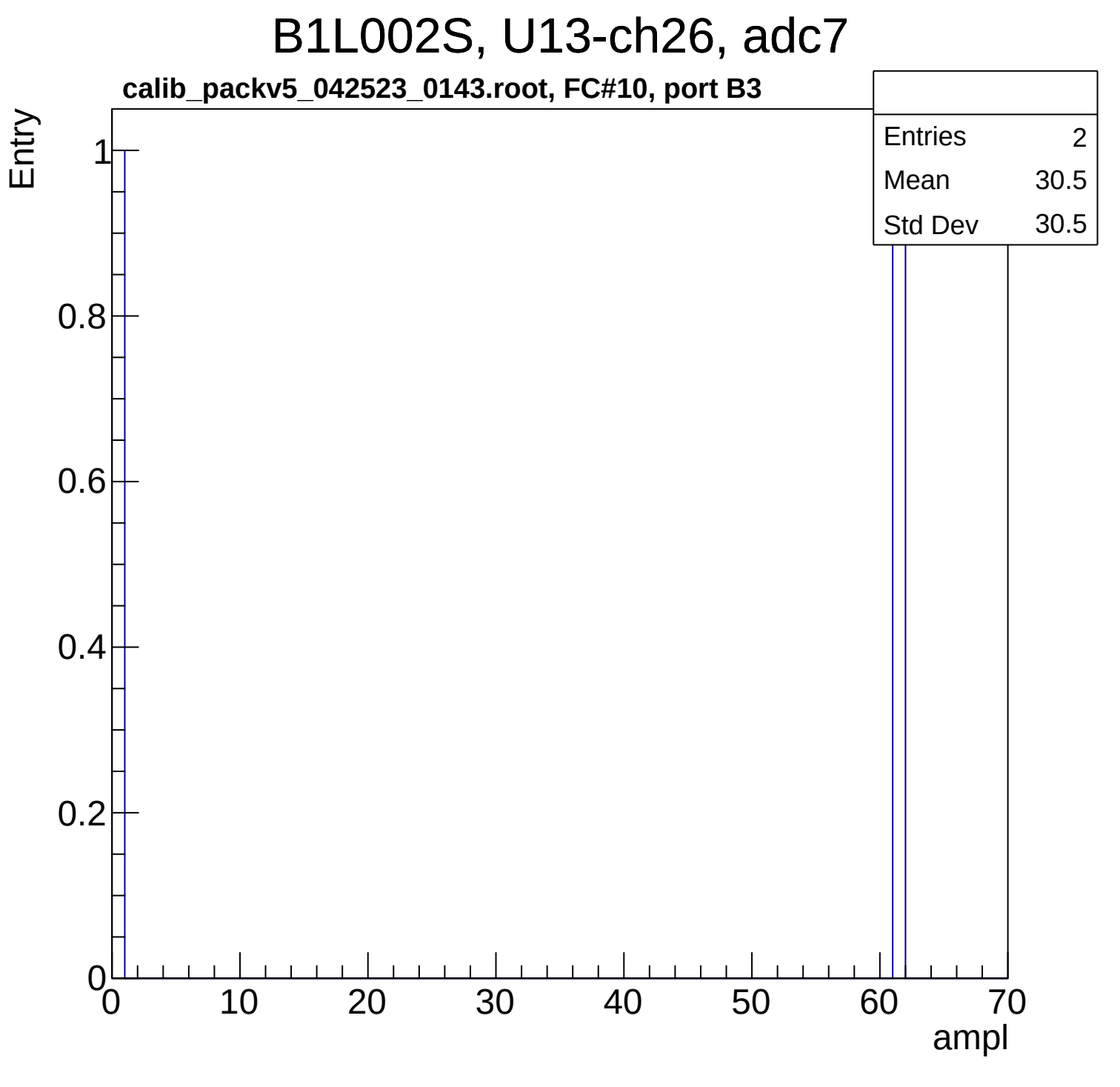
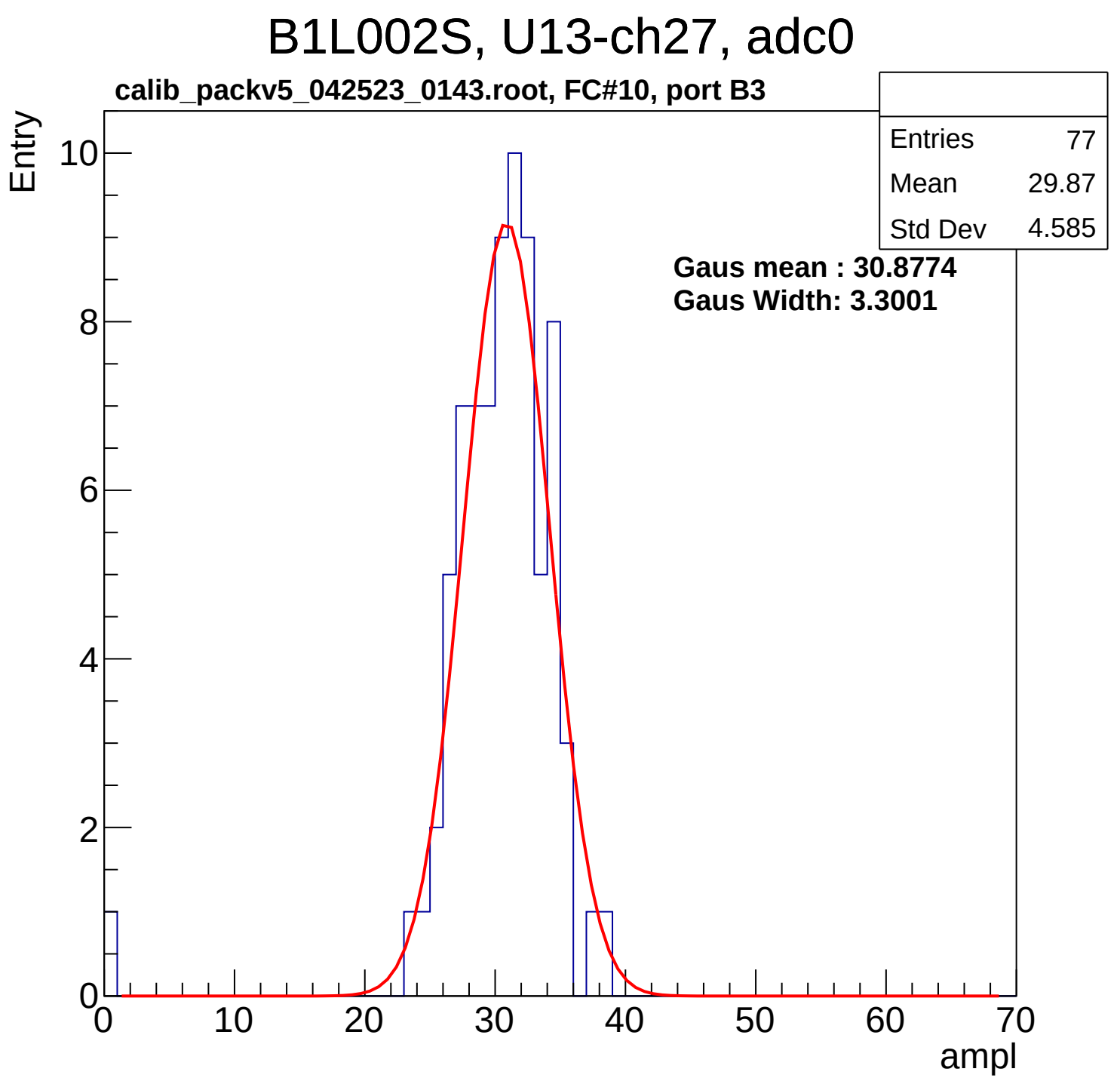
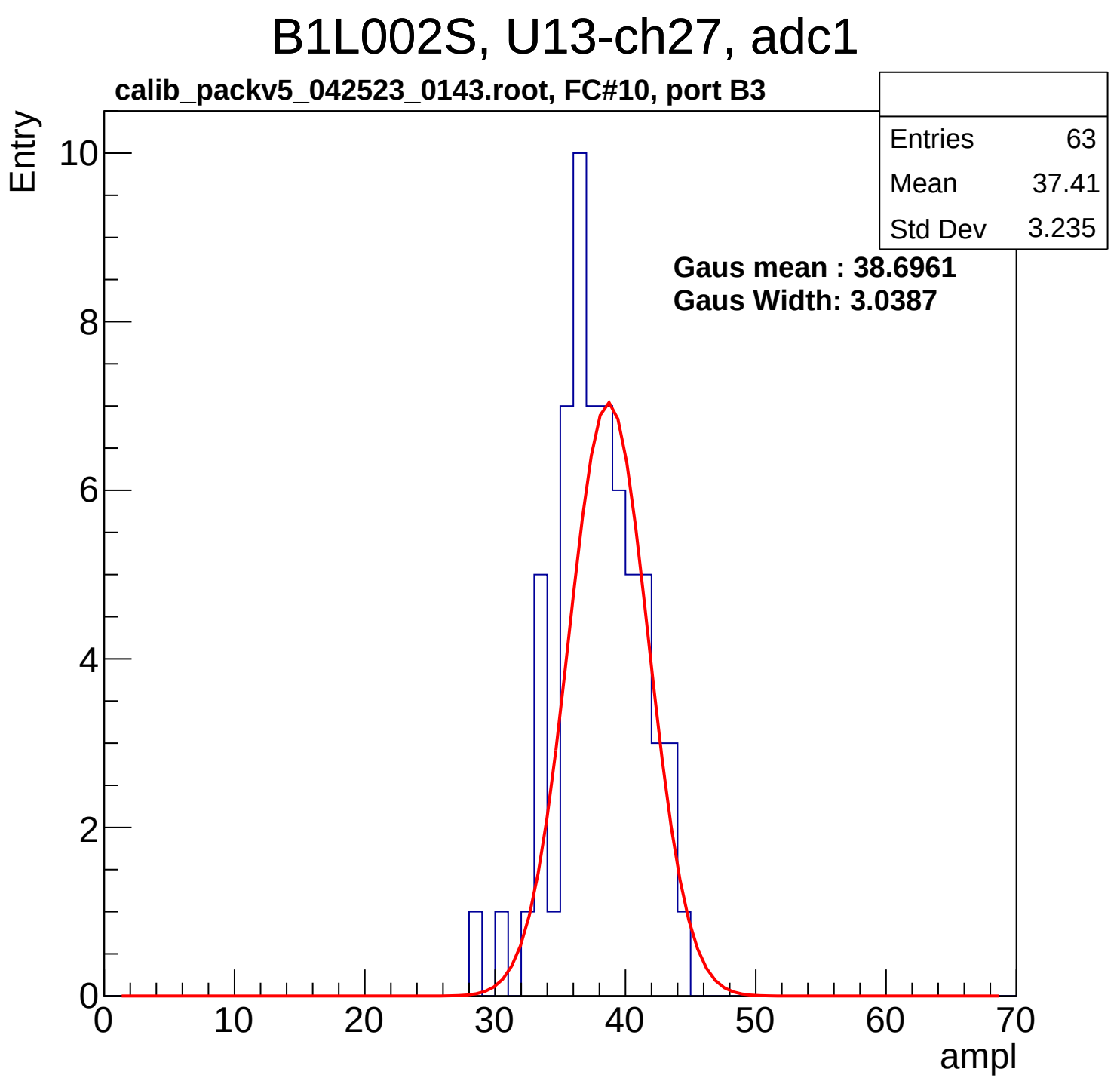

B1L002S, U13-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.57
Std Dev 13.21
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.87
Std Dev 4.585
Gaus mean : 30.8774
Gaus Width: 3.3001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 37.41
Std Dev 3.235
Gaus mean : 38.6961
Gaus Width: 3.0387
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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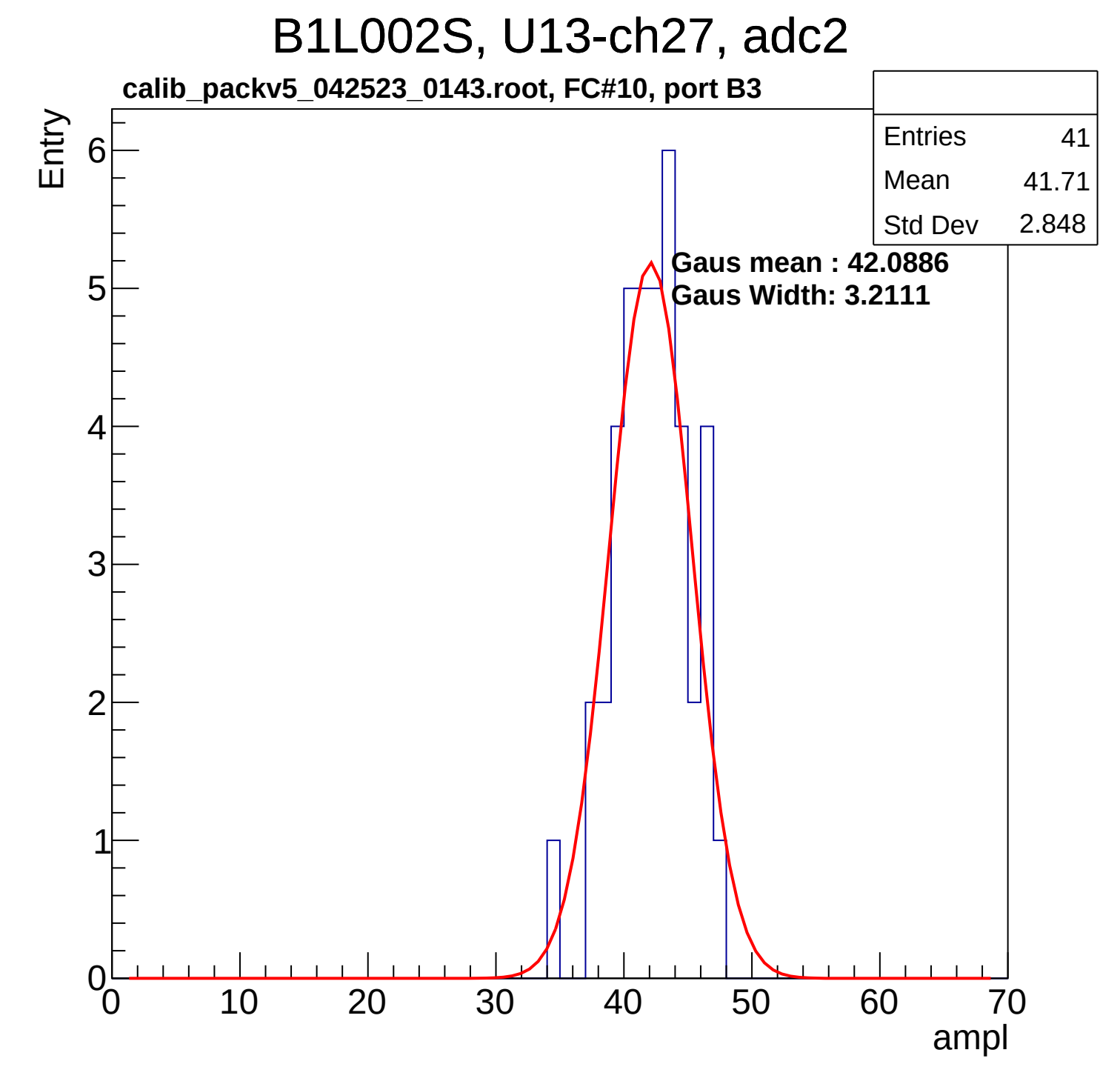
B1L002S, U13-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 41.71
Std Dev 2.848
Gaus mean : 42.0886
Gaus Width: 3.2111
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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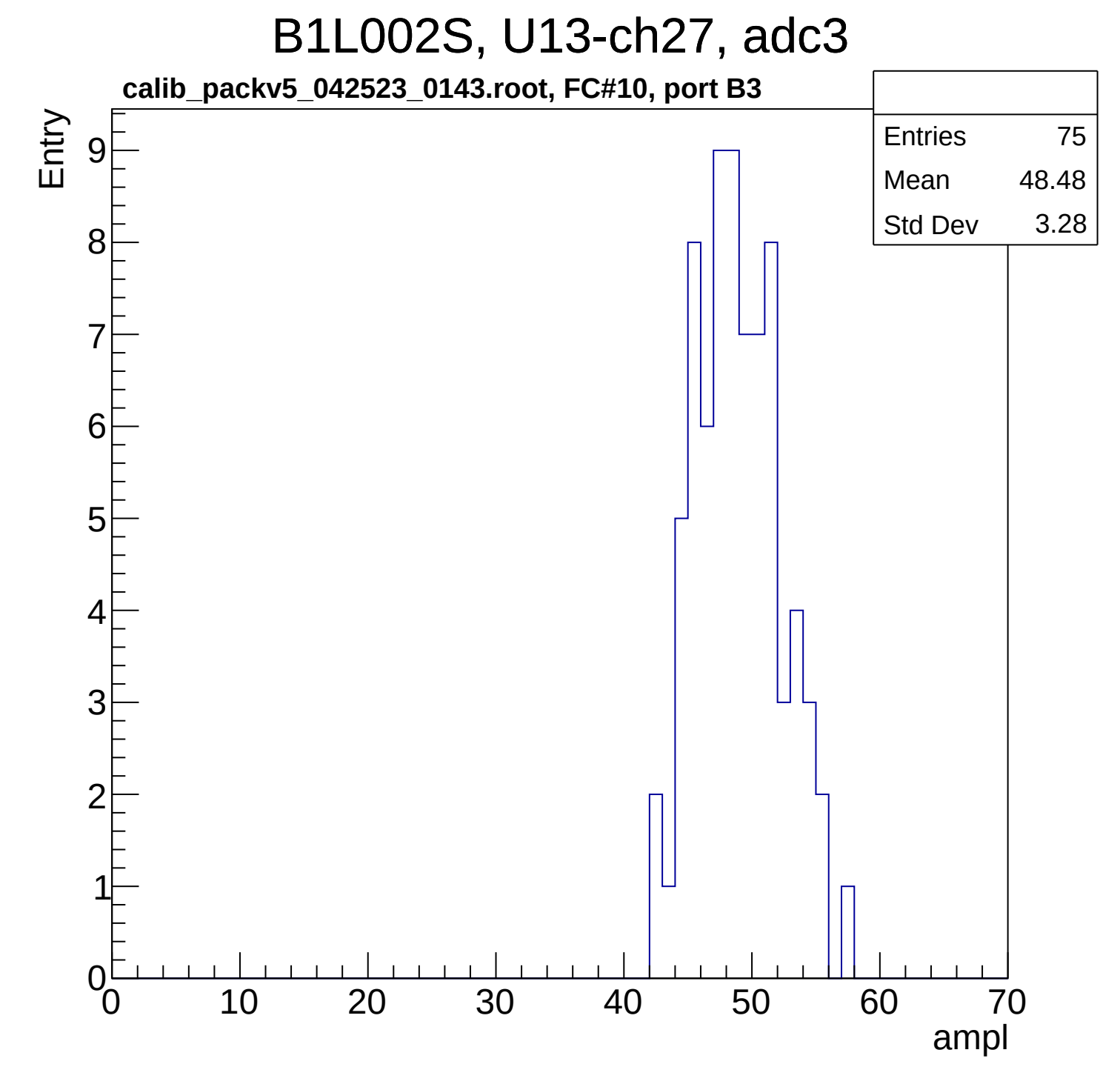
B1L002S, U13-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 48.48
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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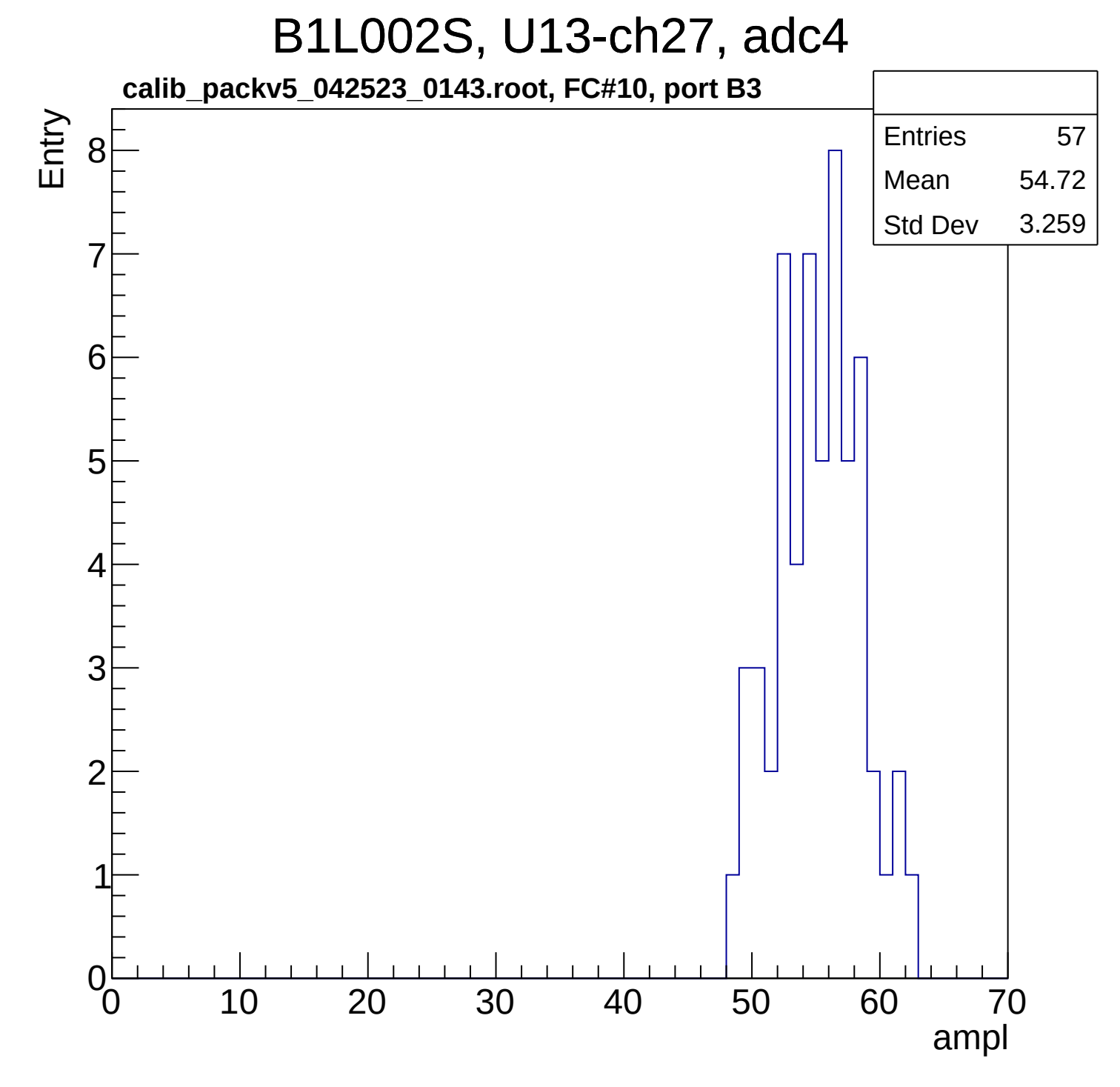
B1L002S, U13-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.72
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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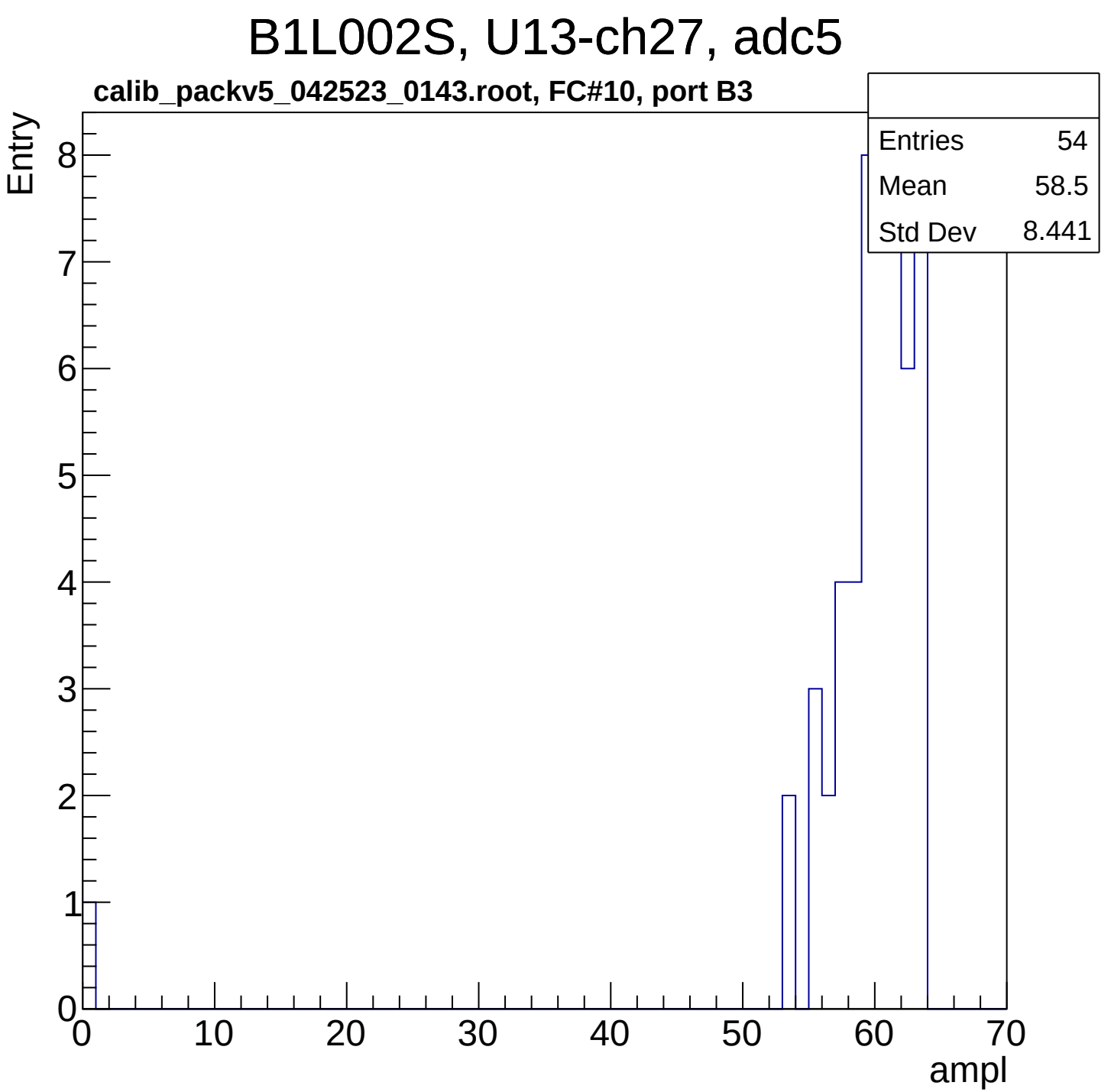
B1L002S, U13-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.5
Std Dev 8.441
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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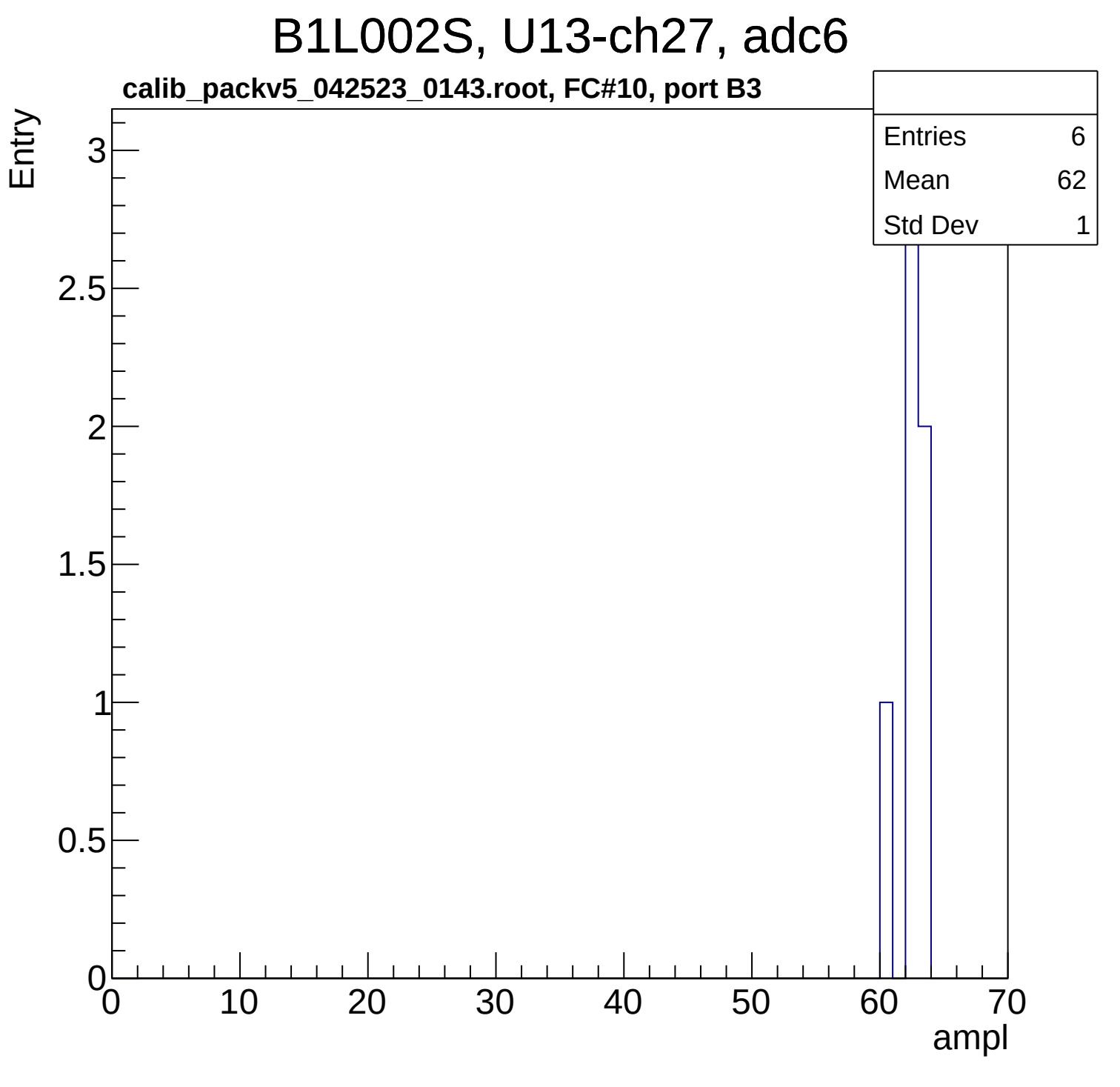
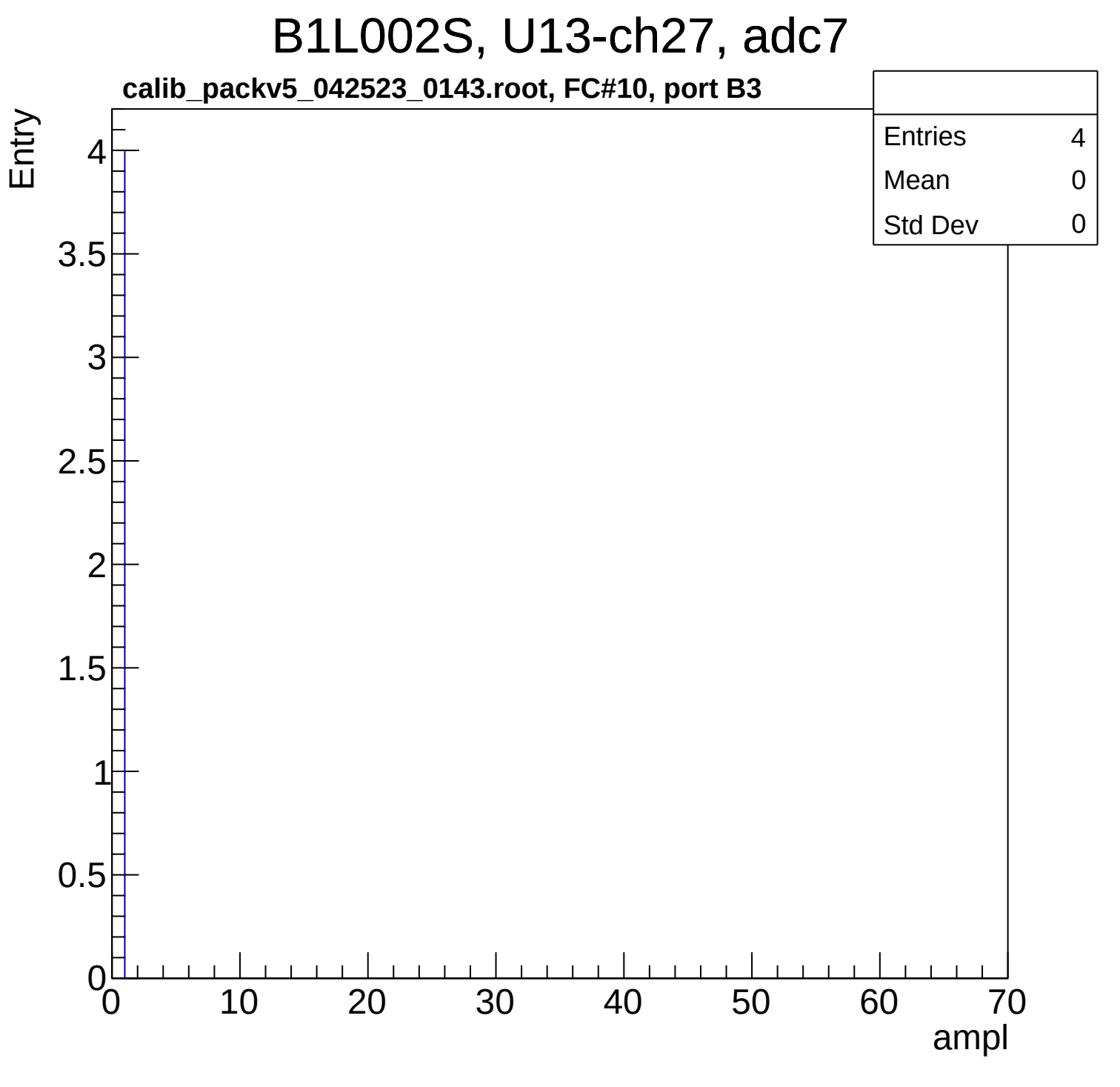
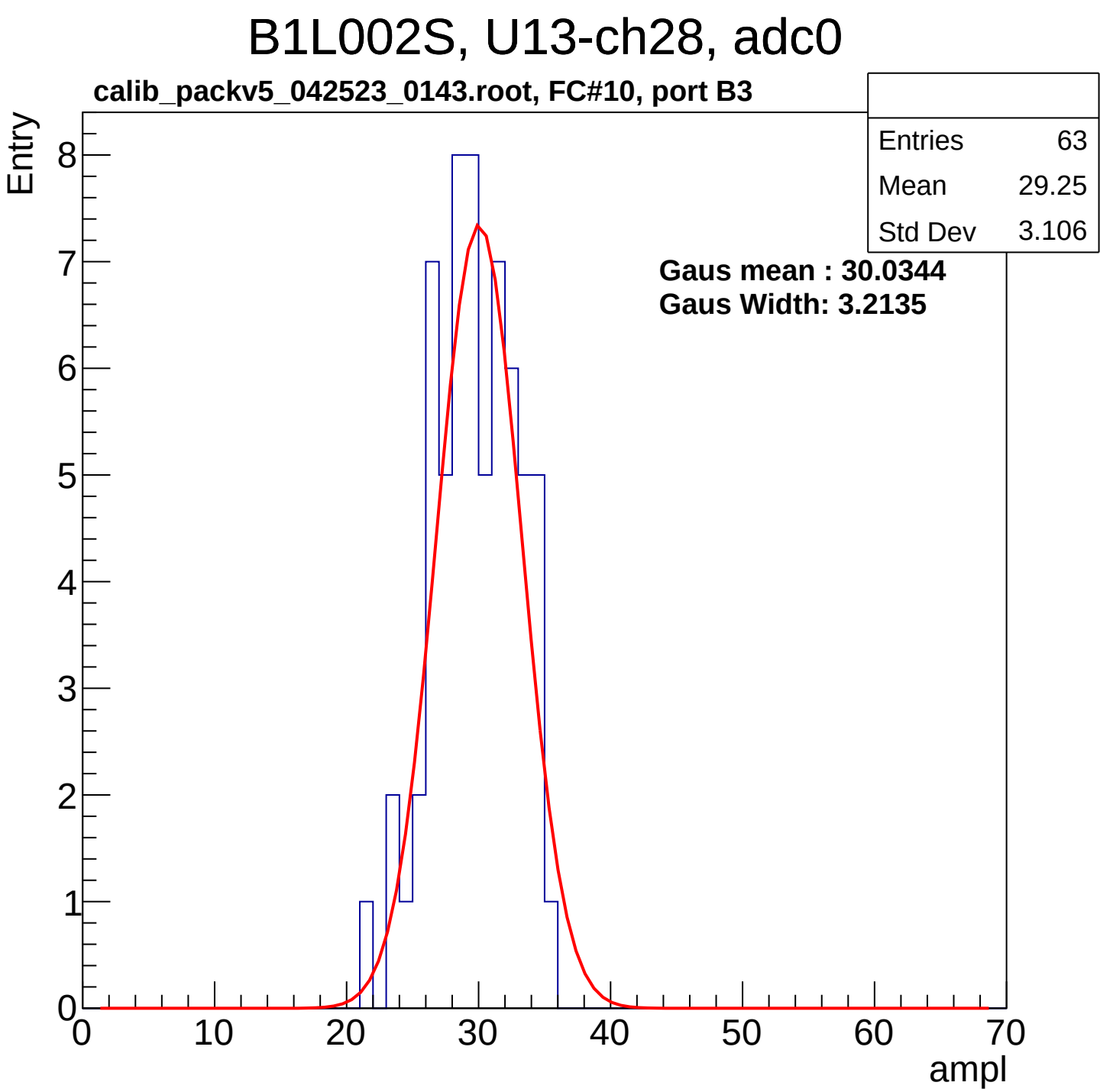
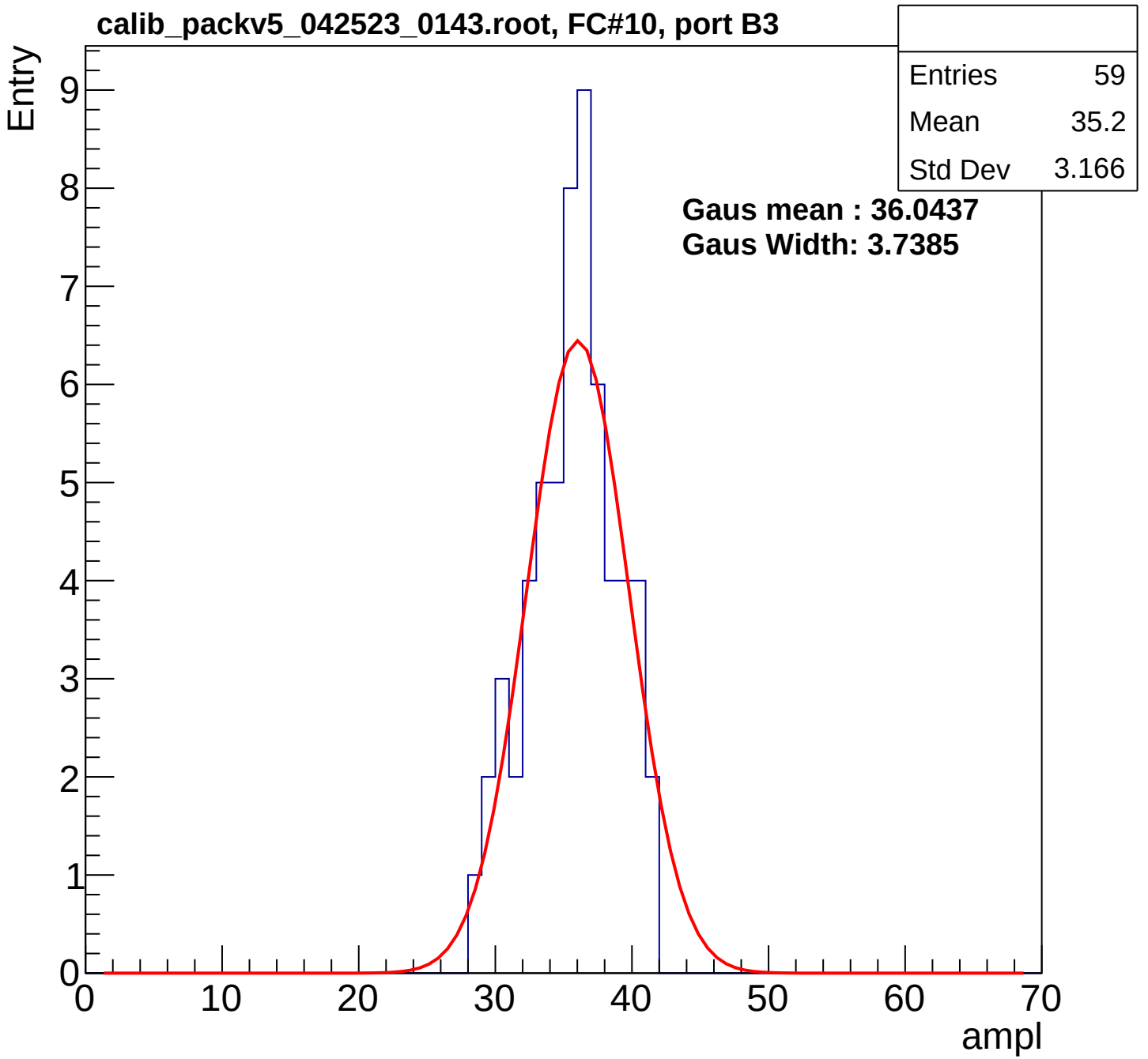

B1L002S, U13-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 29.25
Std Dev 3.106
Gaus mean : 30.0344
Gaus Width: 3.2135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.2
Std Dev 3.166
Gaus mean : 36.0437
Gaus Width: 3.7385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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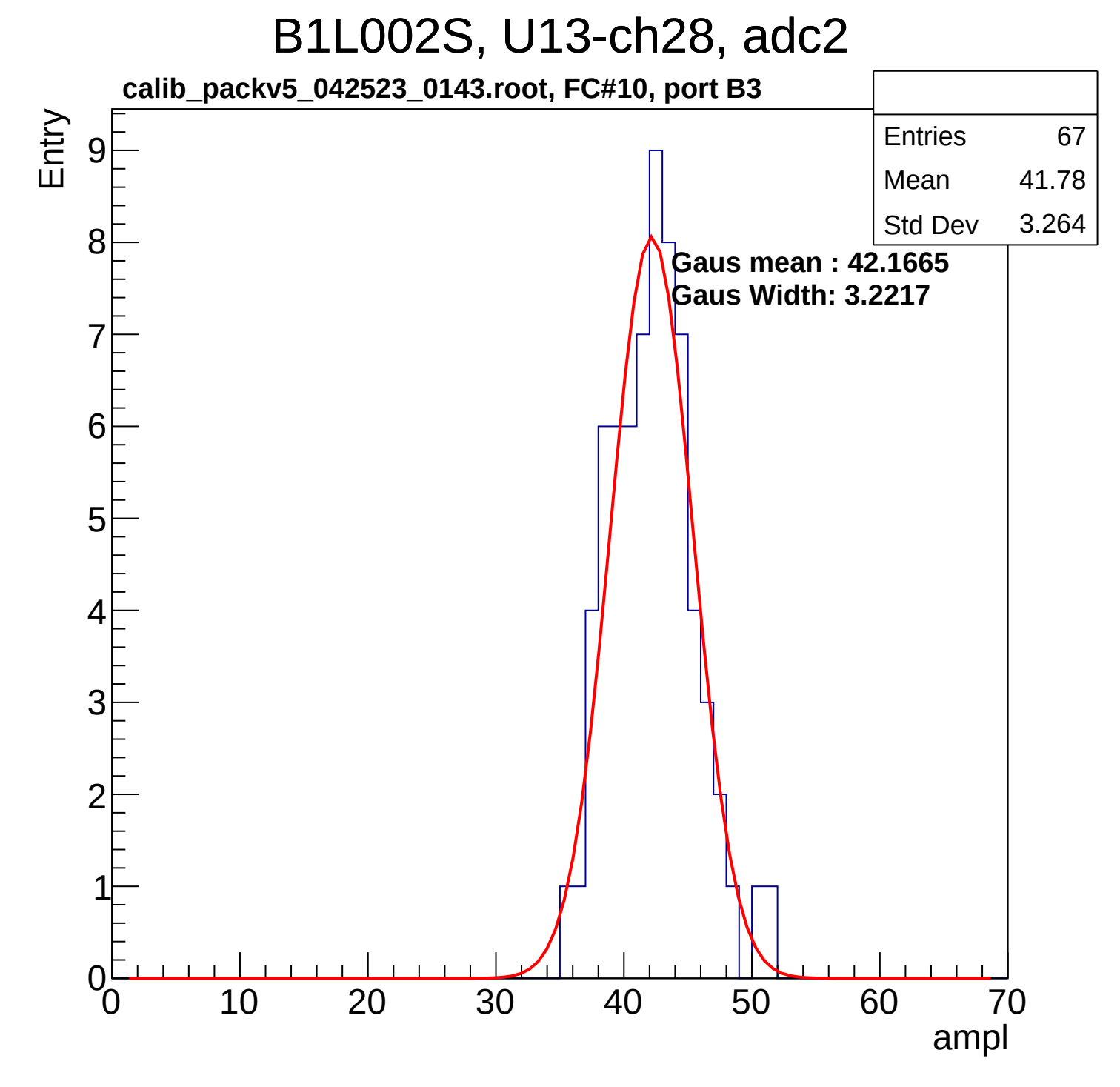
B1L002S, U13-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.78
Std Dev 3.264
Gaus mean : 42.1665
Gaus Width: 3.2217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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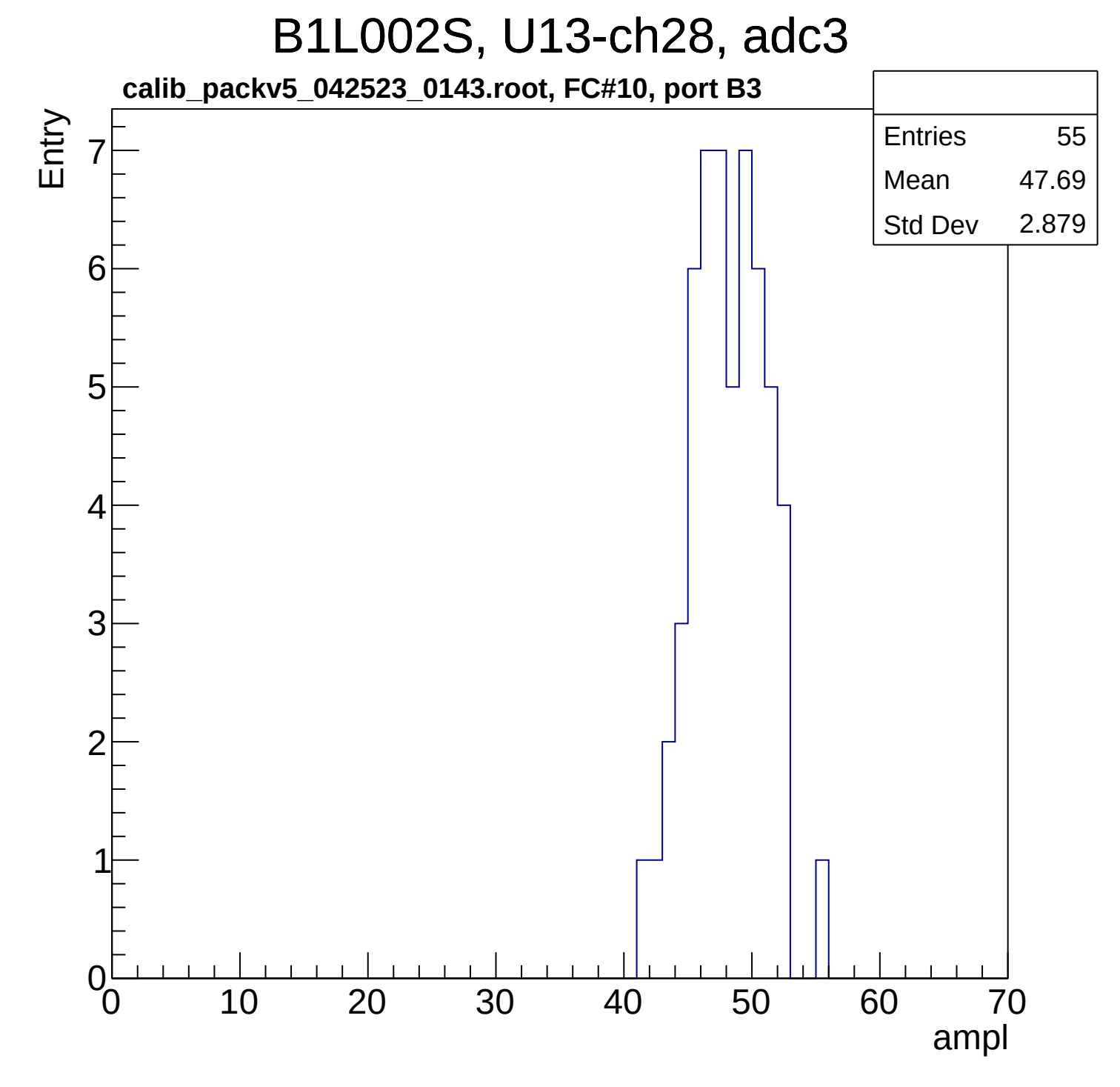
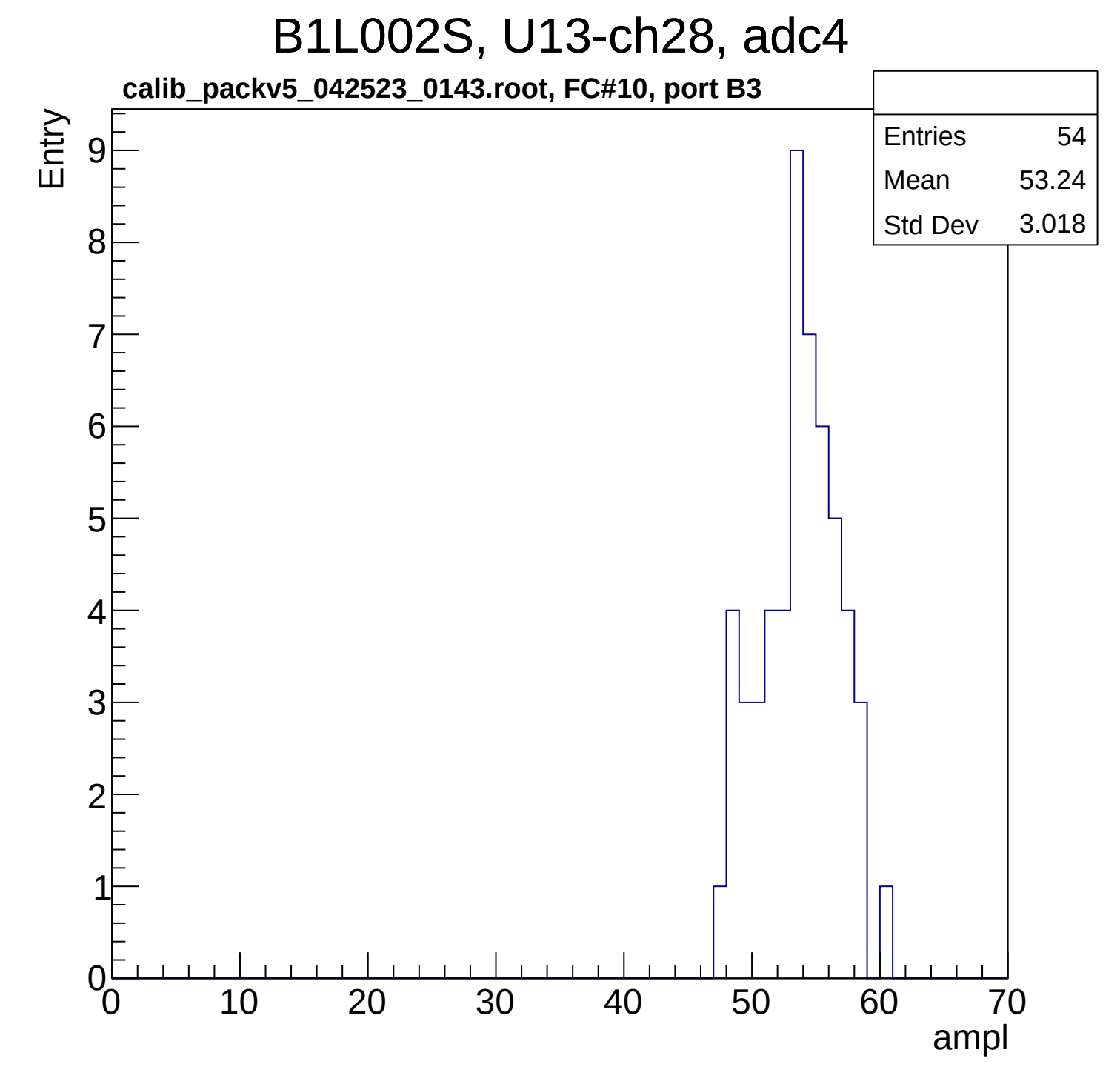
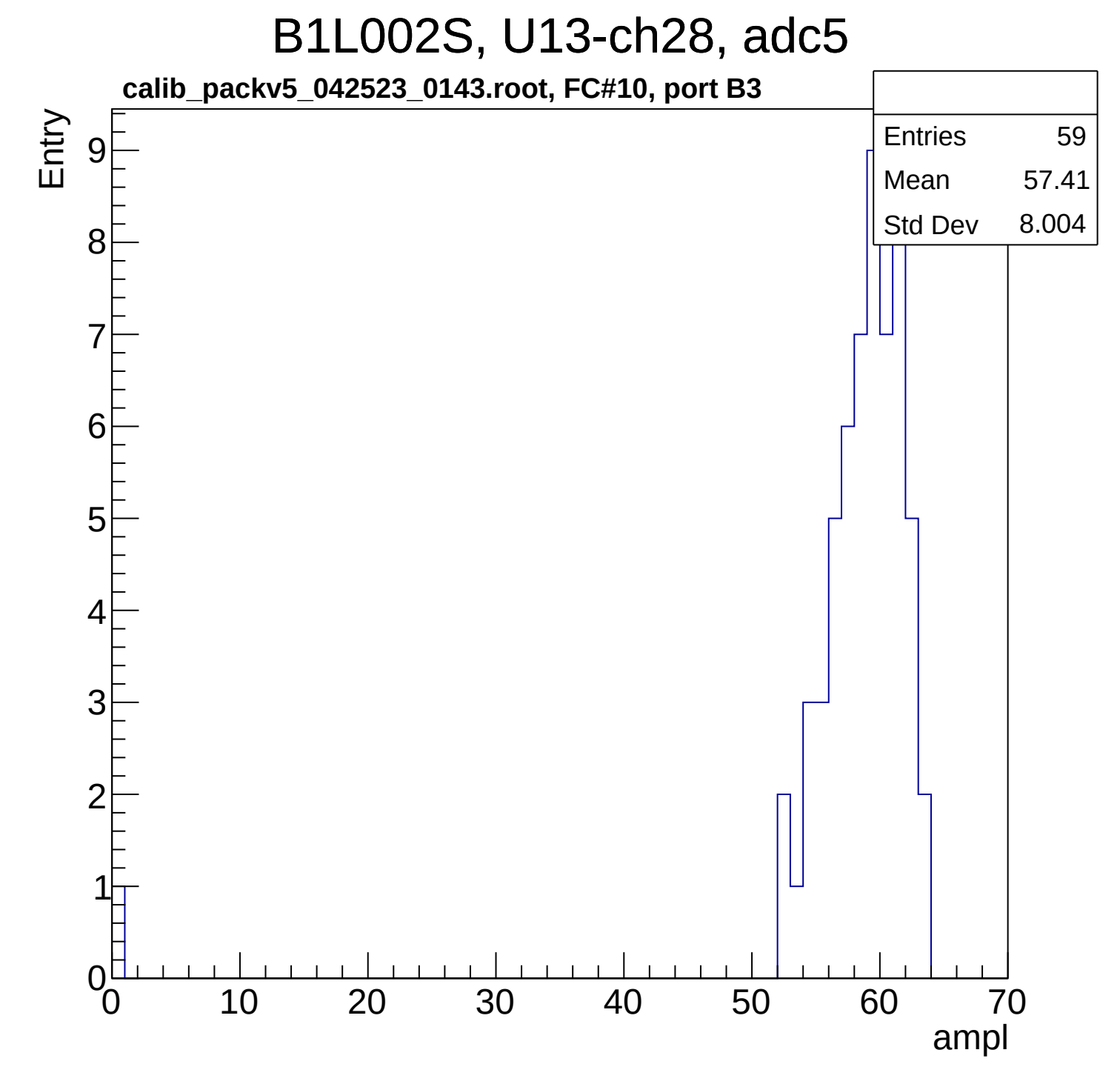

B1L002S, U13-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.69
Std Dev 2.879
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.24
Std Dev 3.018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.41
Std Dev 8.004
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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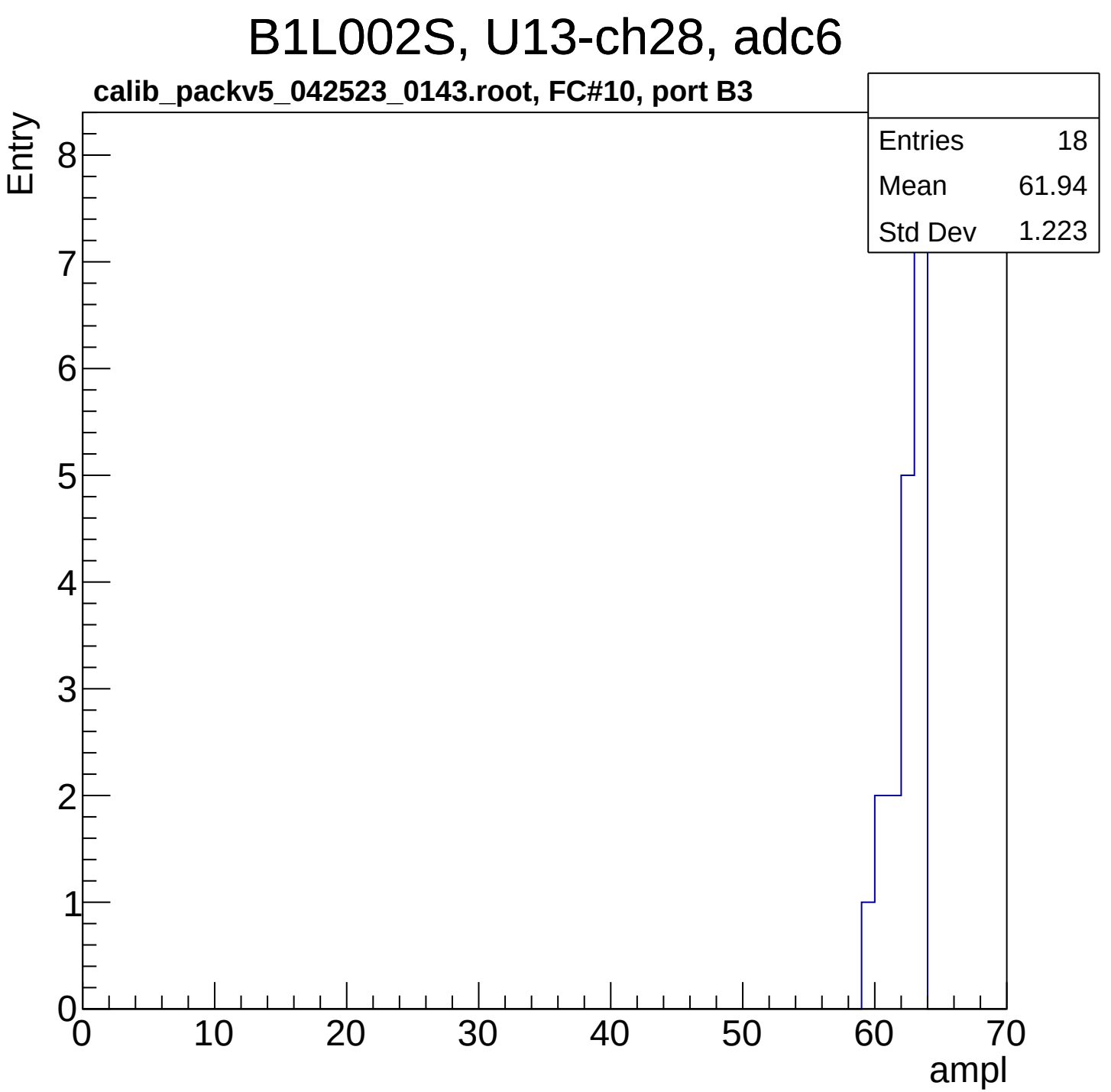
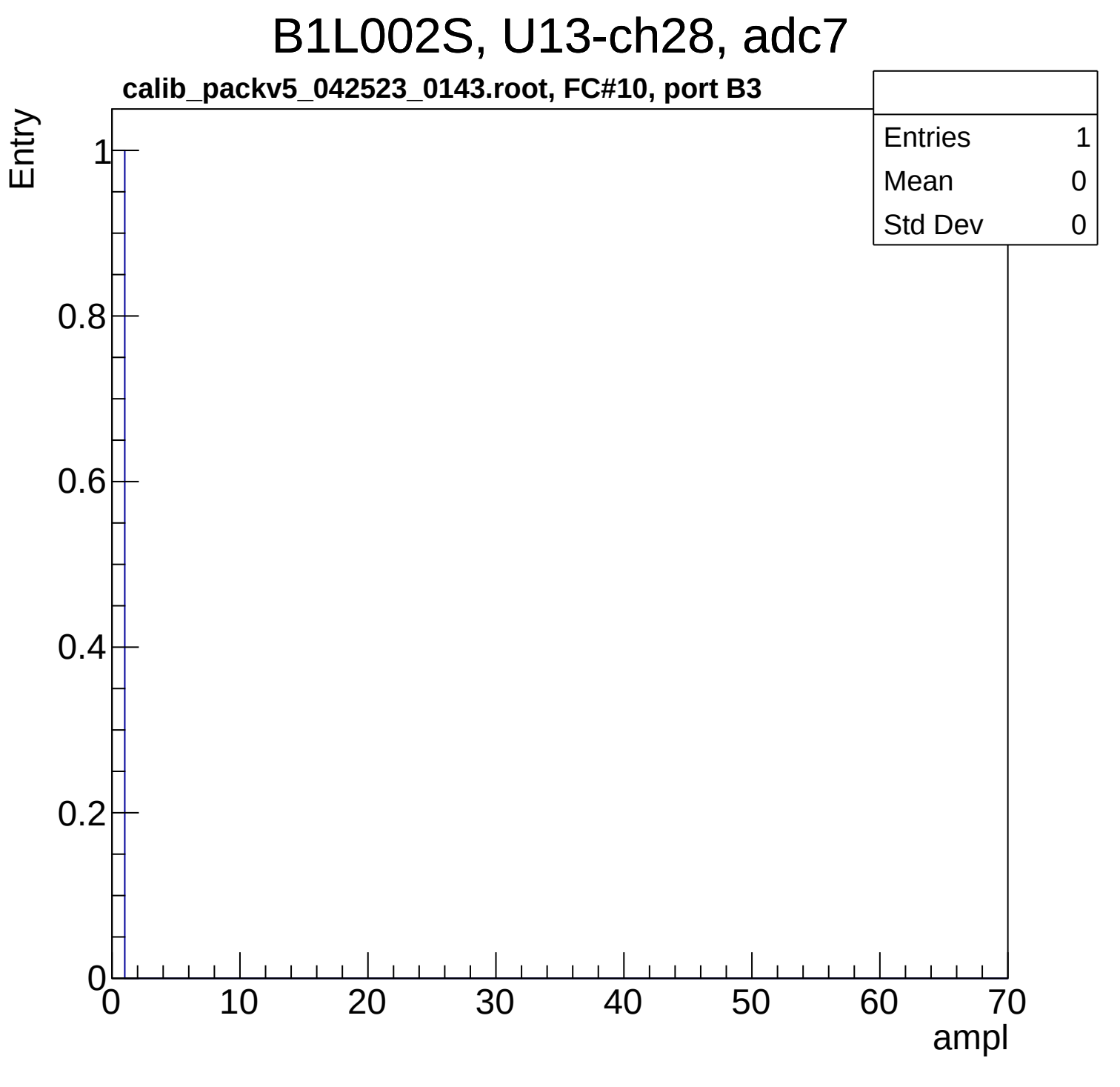
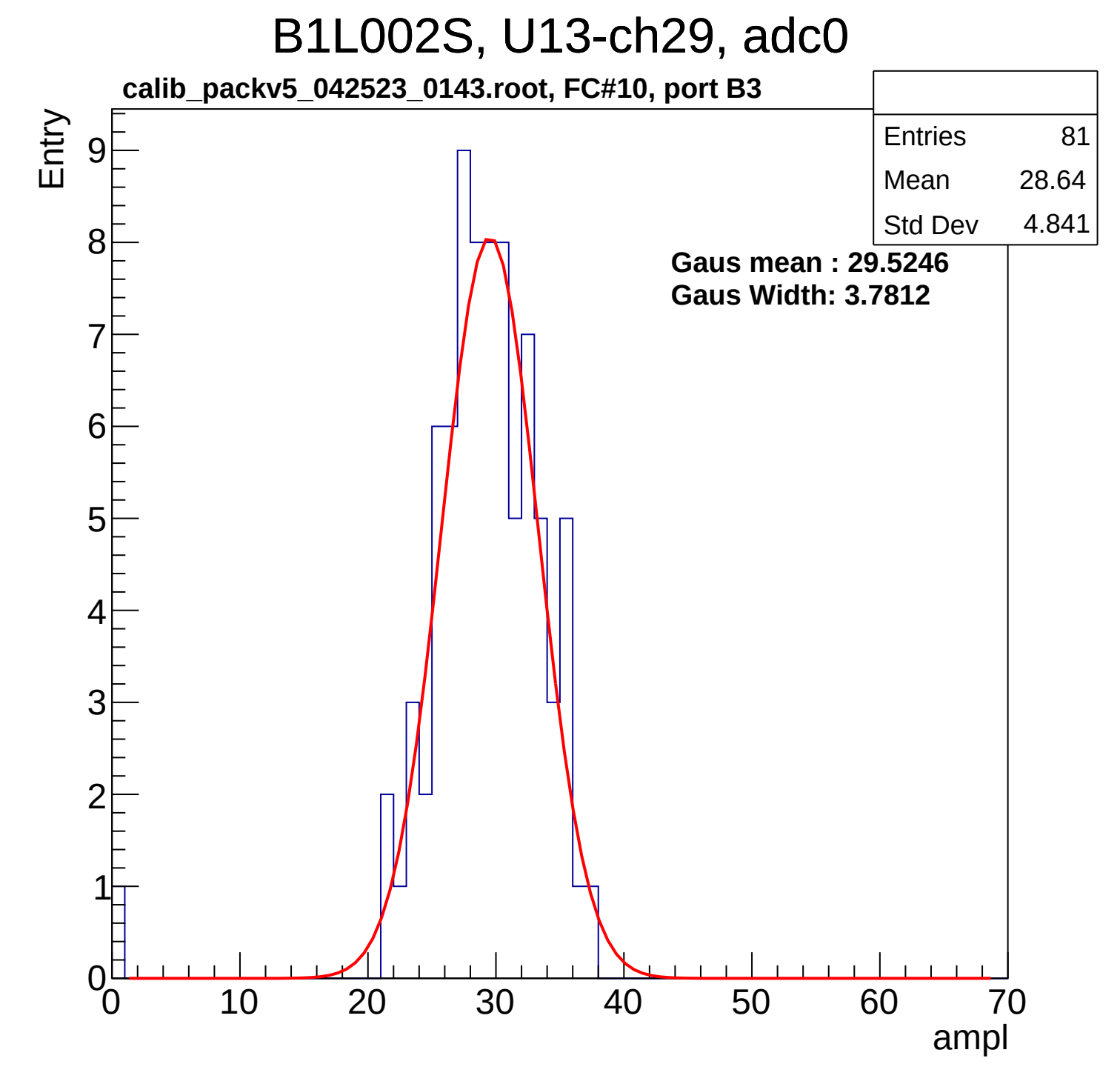

B1L002S, U13-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.94
Std Dev 1.223
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 28.64
Std Dev 4.841
Gaus mean : 29.5246
Gaus Width: 3.7812
Entry
ampl
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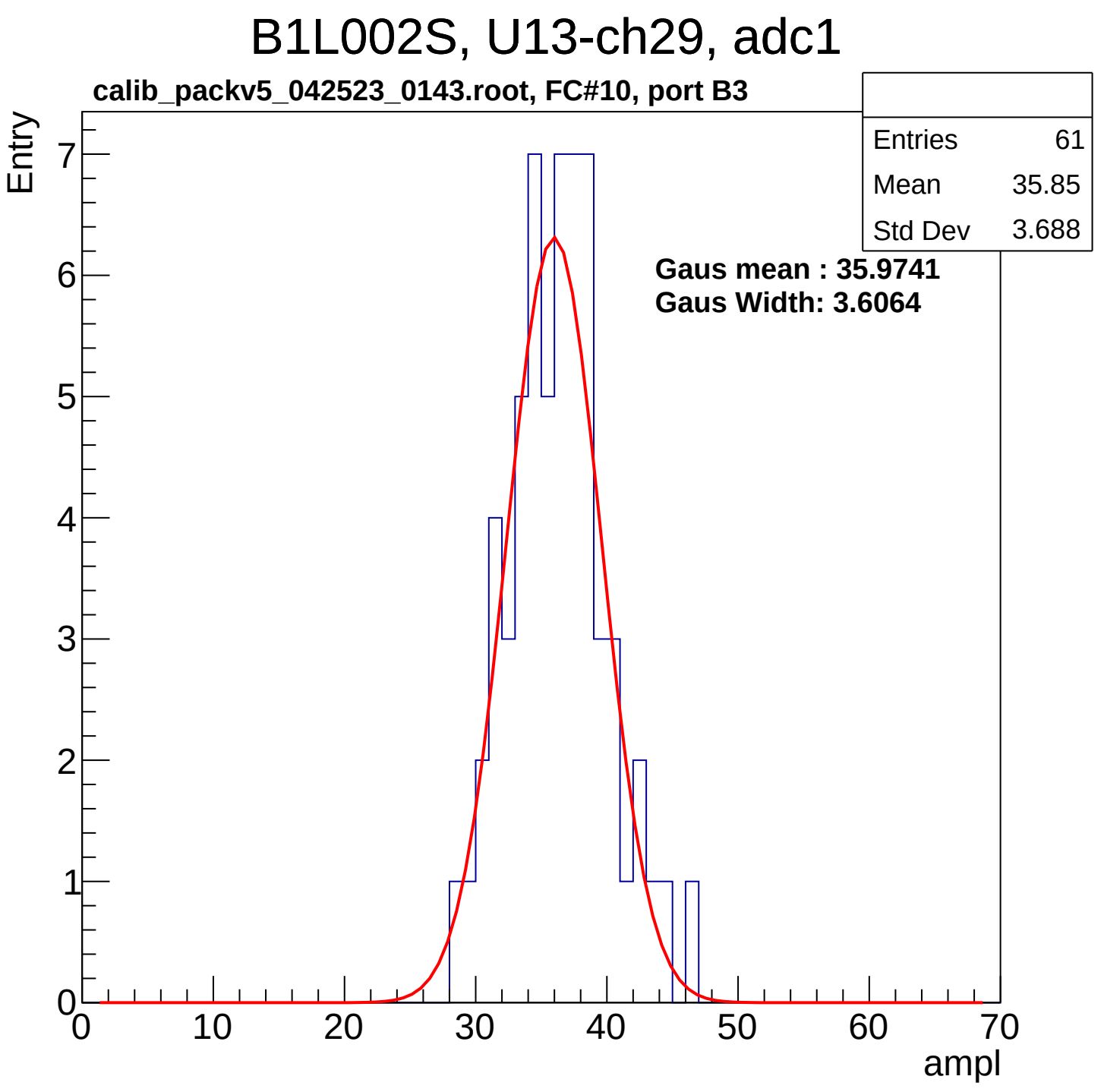
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.85
Std Dev 3.688
Gaus mean : 35.9741
Gaus Width: 3.6064
Entry
ampl
0
1
2
3
4
5
6
7
0
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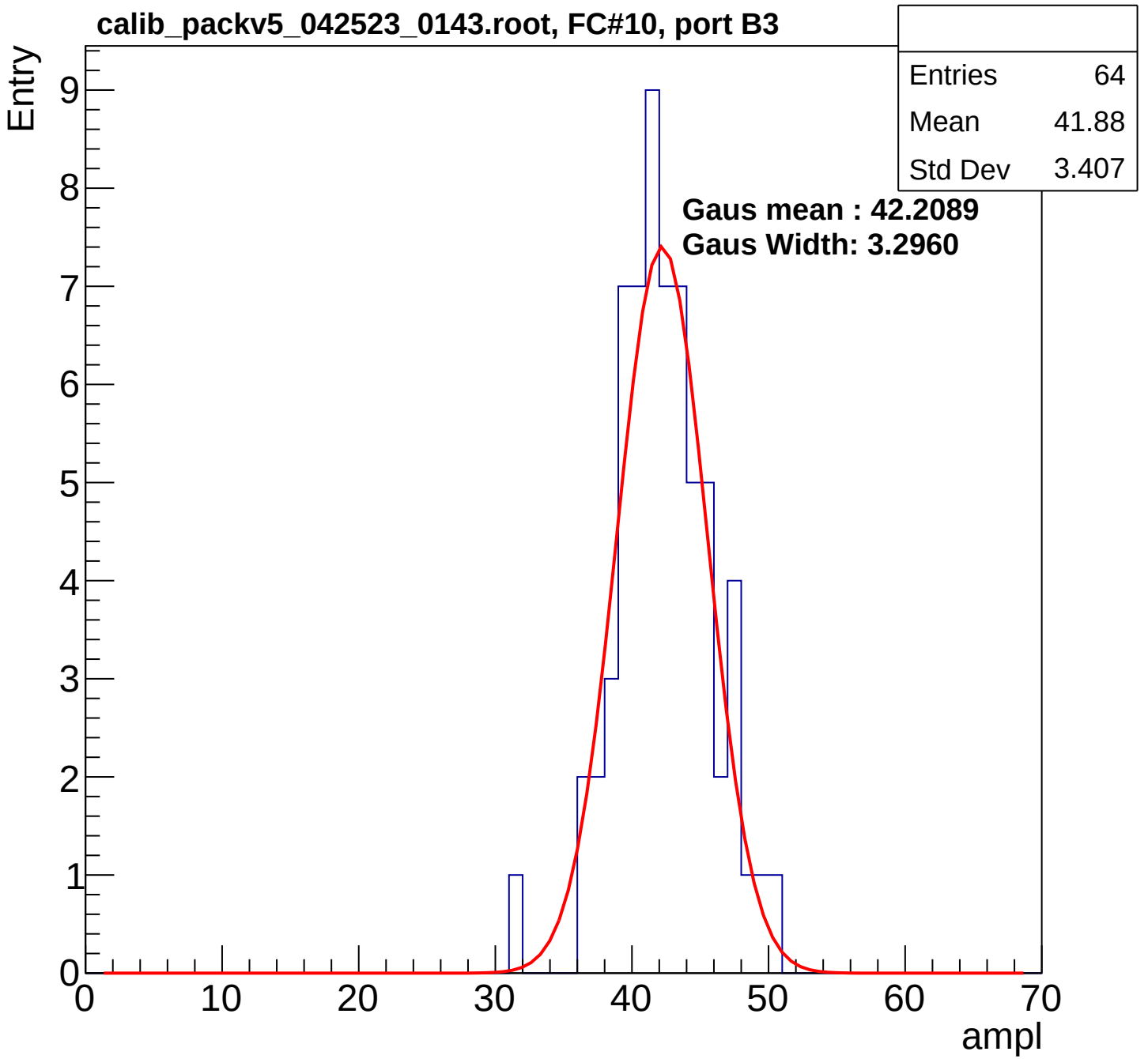
20
30
40
50
60
70


B1L002S, U13-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.88
Std Dev 3.407
Gaus mean : 42.2089
Gaus Width: 3.2960
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
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60
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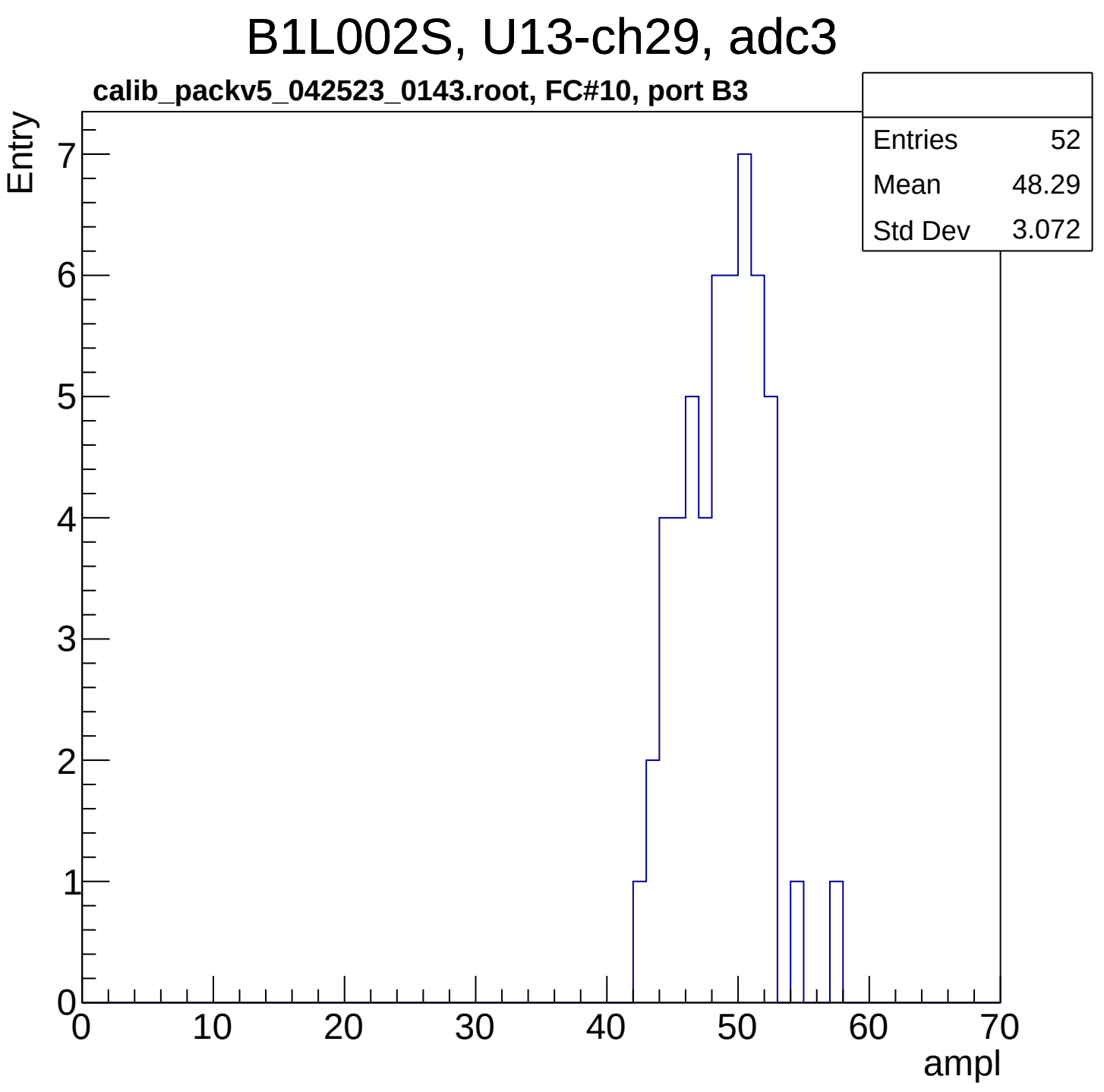
B1L002S, U13-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 48.29
Std Dev 3.072
Entry
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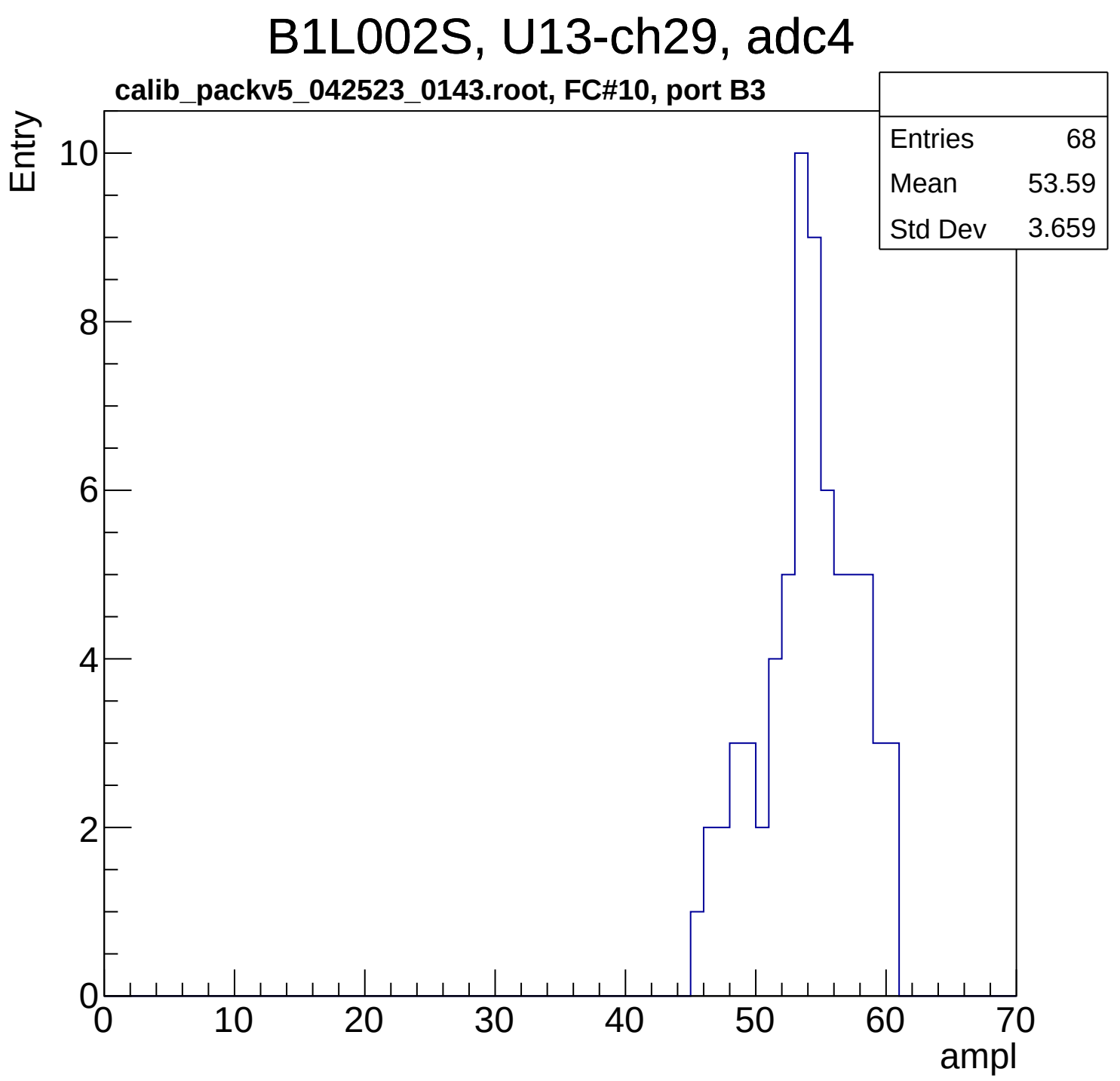
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.59
Std Dev 3.659
Entry
ampl
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2
4
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8
10
0
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20
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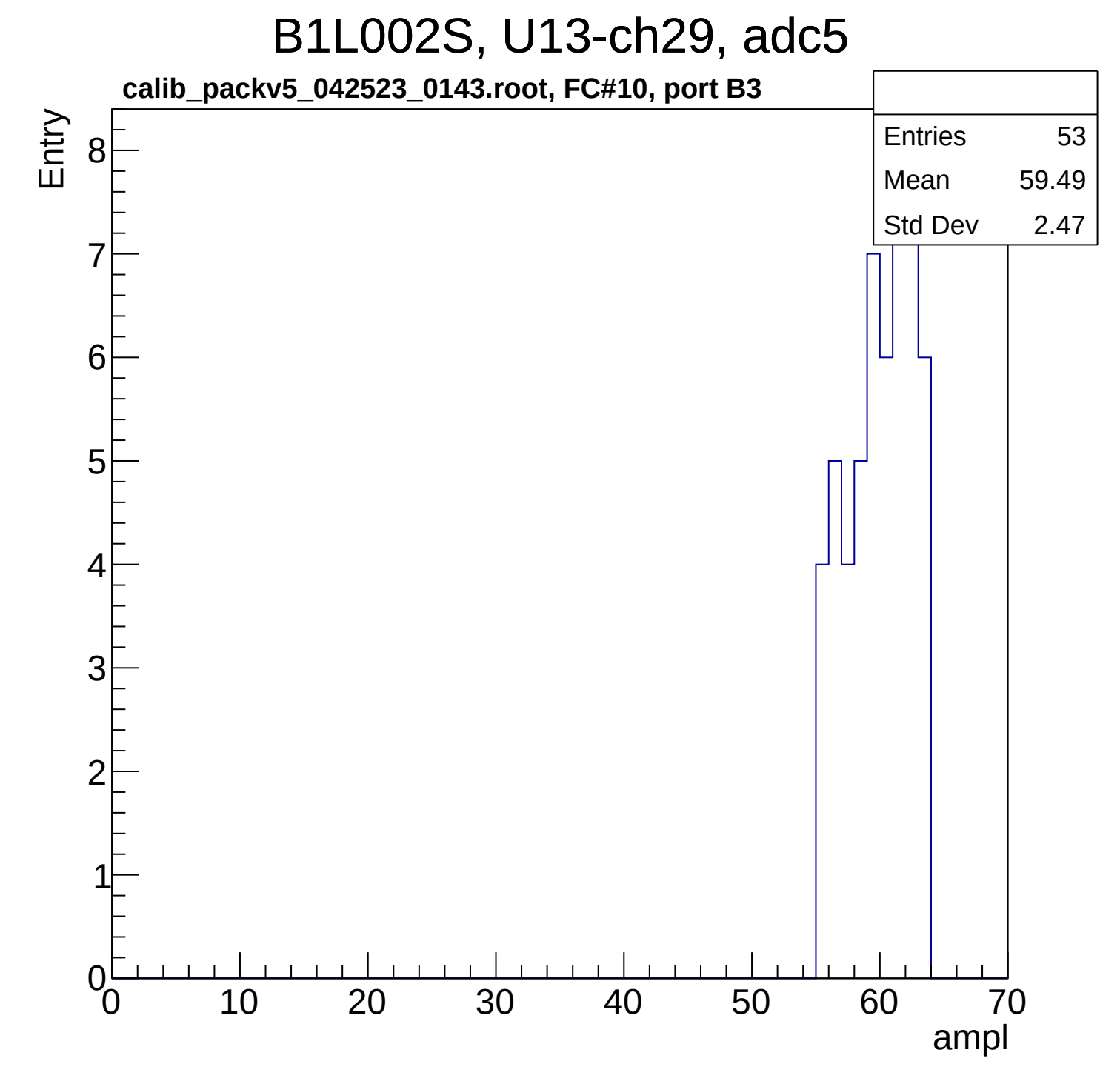
40
50
60
70


B1L002S, U13-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.49
Std Dev 2.47
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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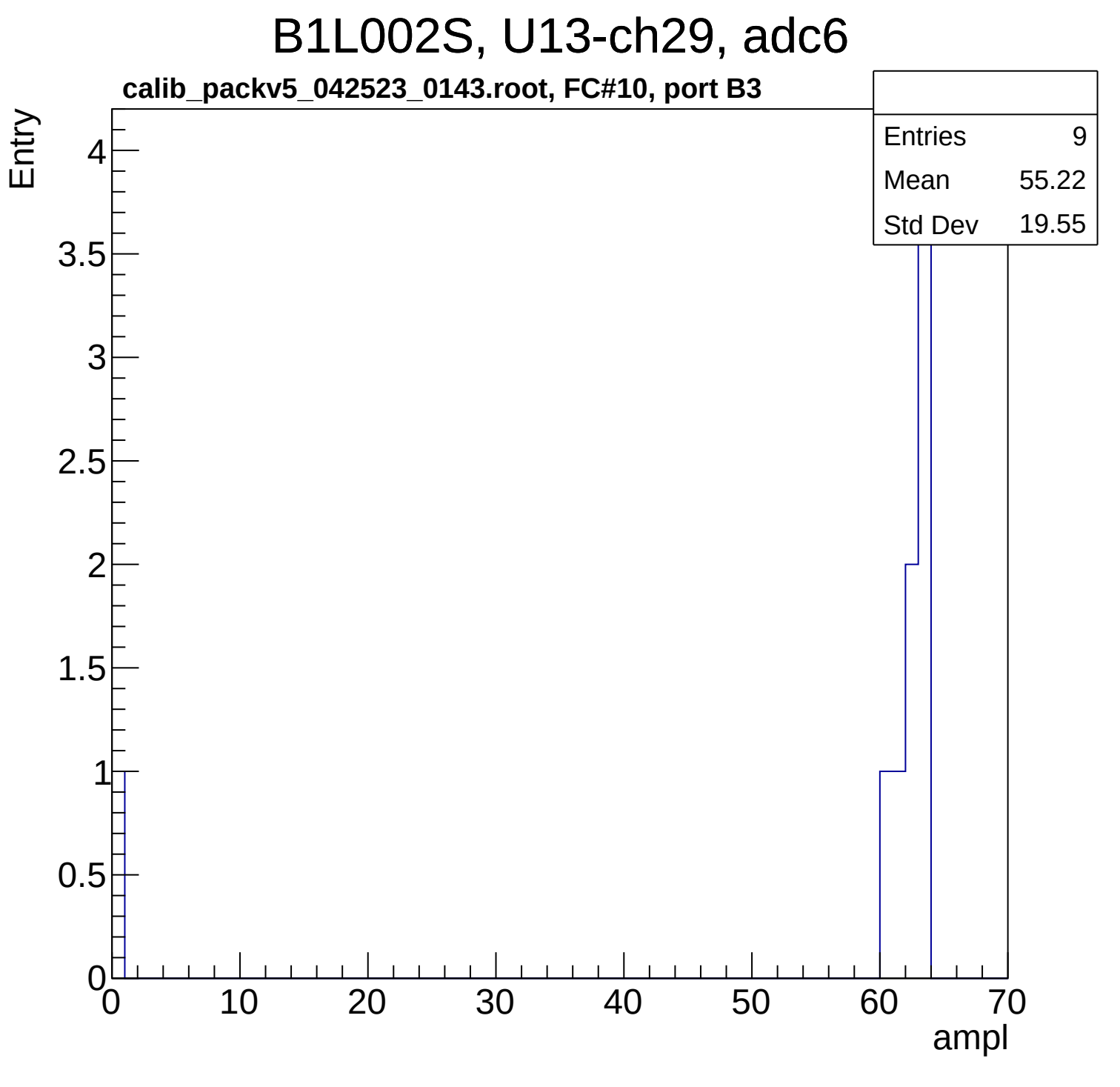
B1L002S, U13-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
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60
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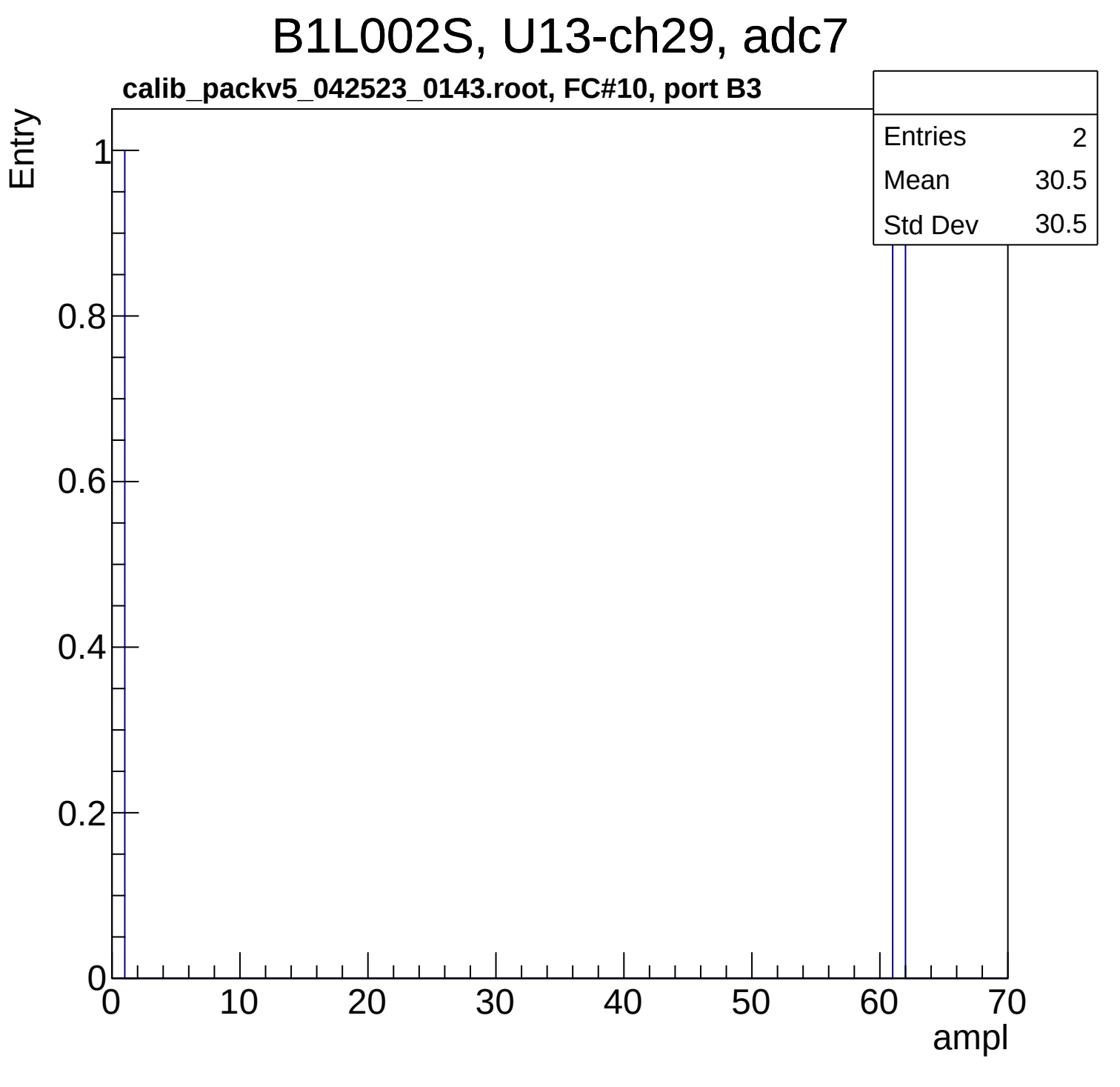
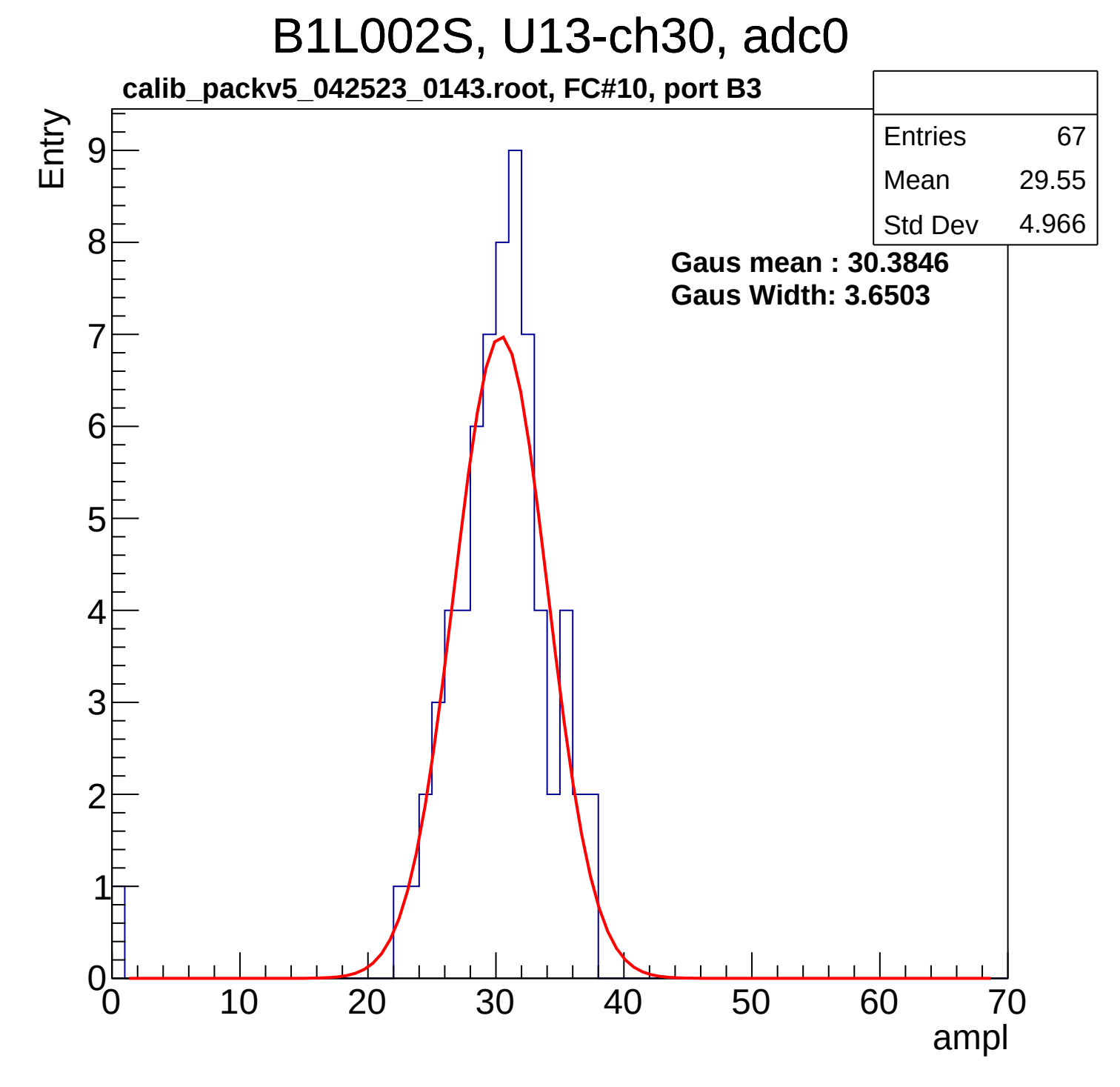
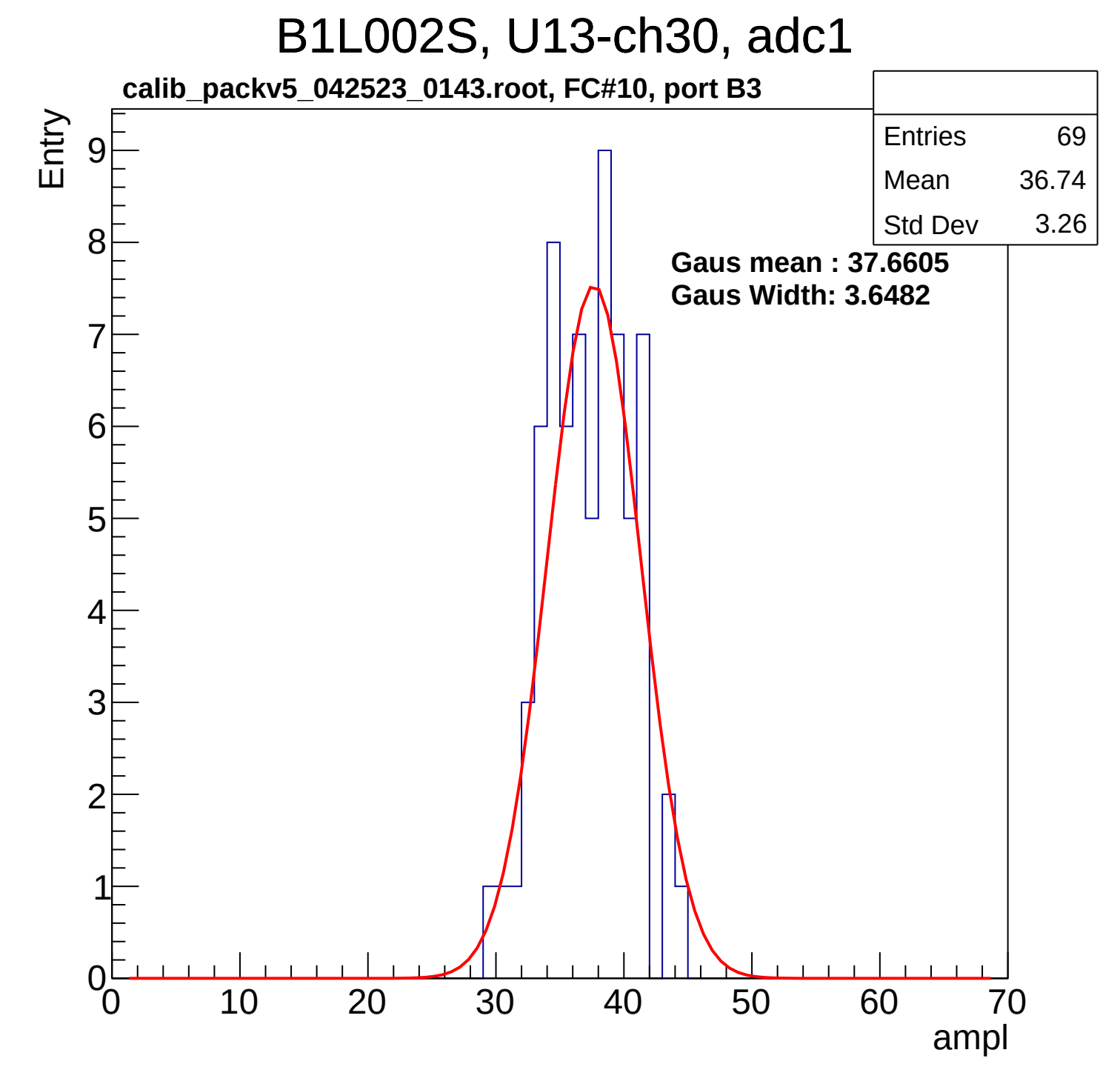

B1L002S, U13-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.55
Std Dev 4.966
Gaus mean : 30.3846
Gaus Width: 3.6503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 36.74
Std Dev 3.26
Gaus mean : 37.6605
Gaus Width: 3.6482
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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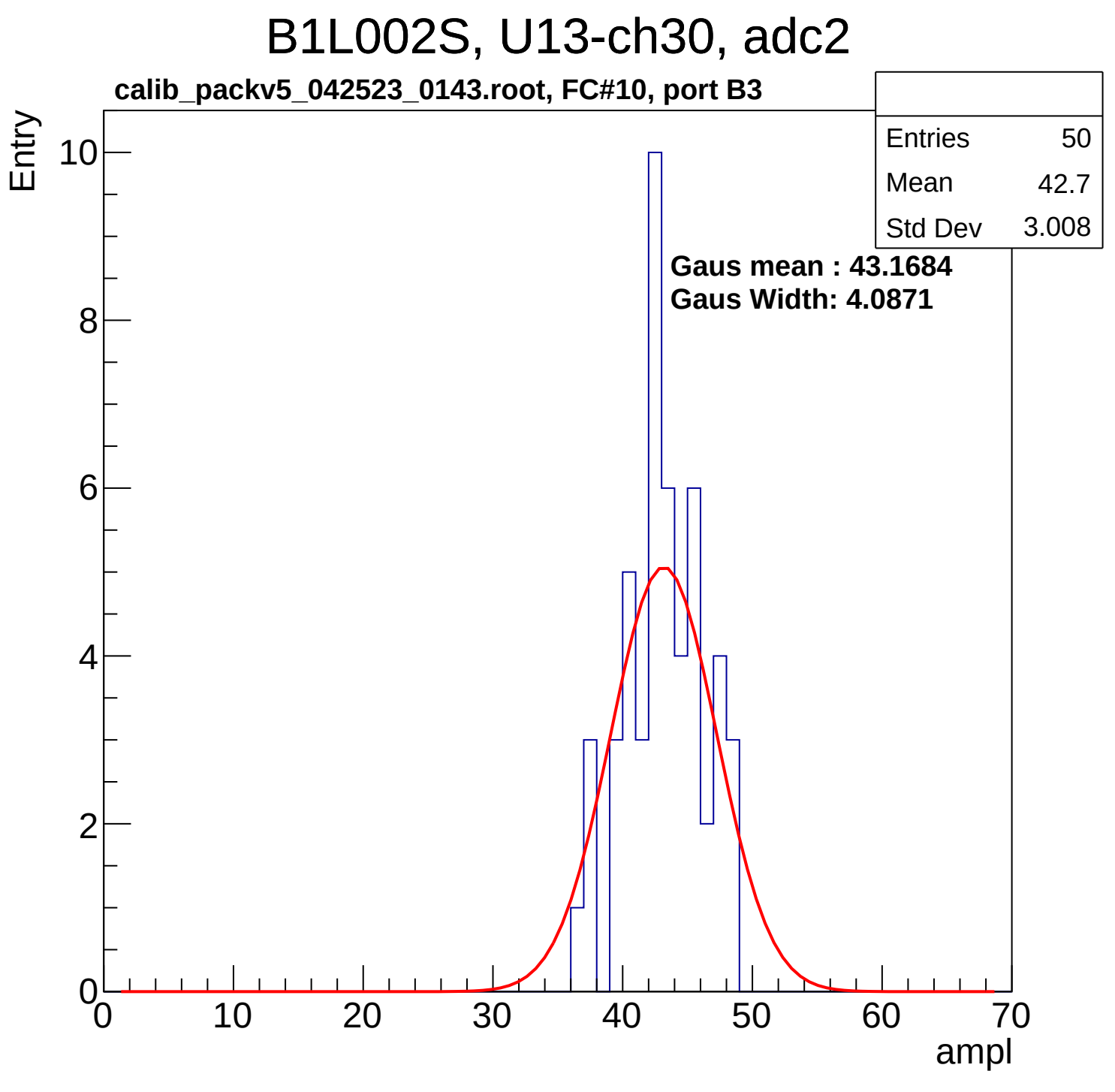
B1L002S, U13-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.7
Std Dev 3.008
Gaus mean : 43.1684
Gaus Width: 4.0871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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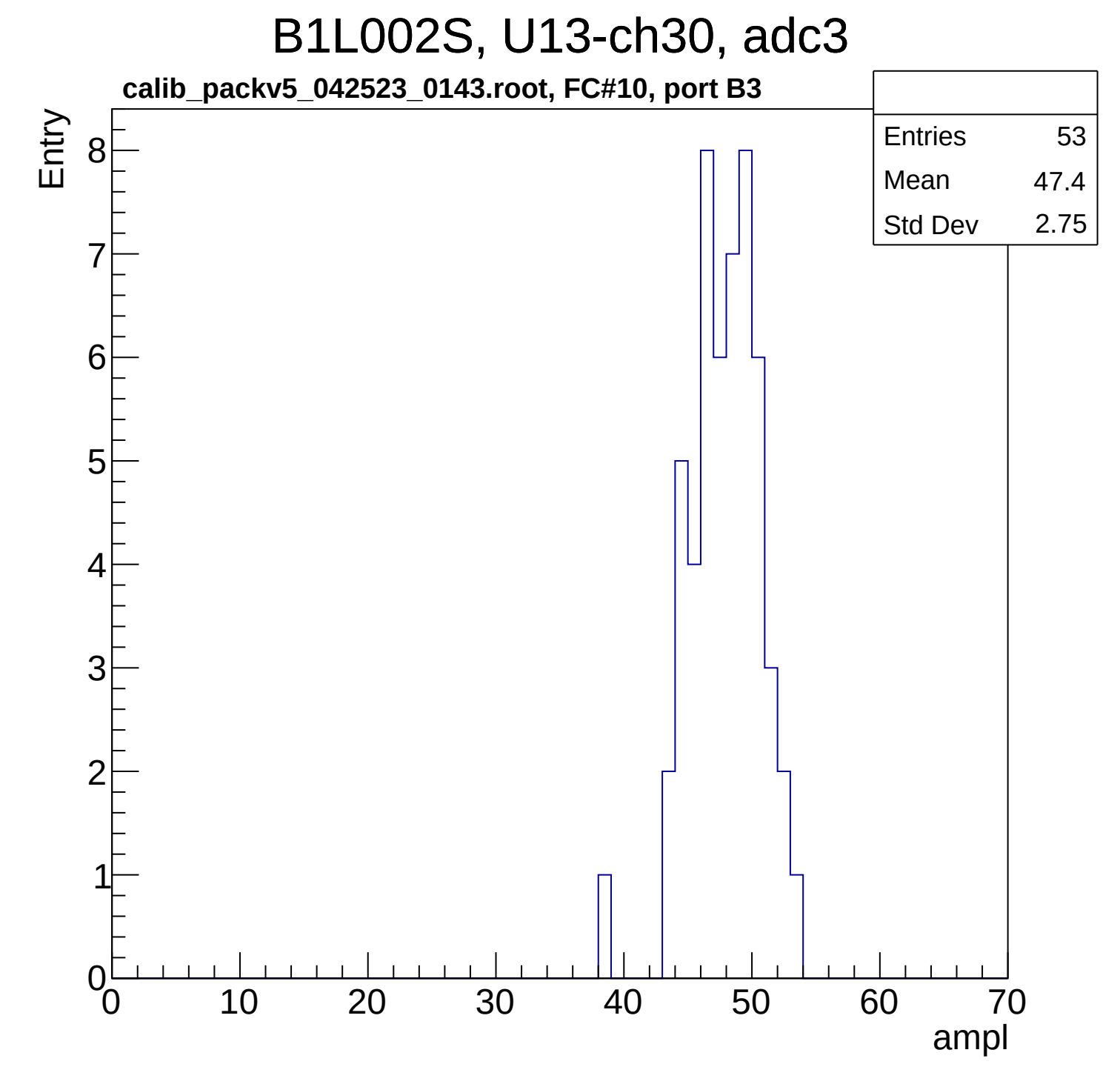
B1L002S, U13-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.4
Std Dev 2.75
Entry
ampl
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1
2
3
4
5
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20
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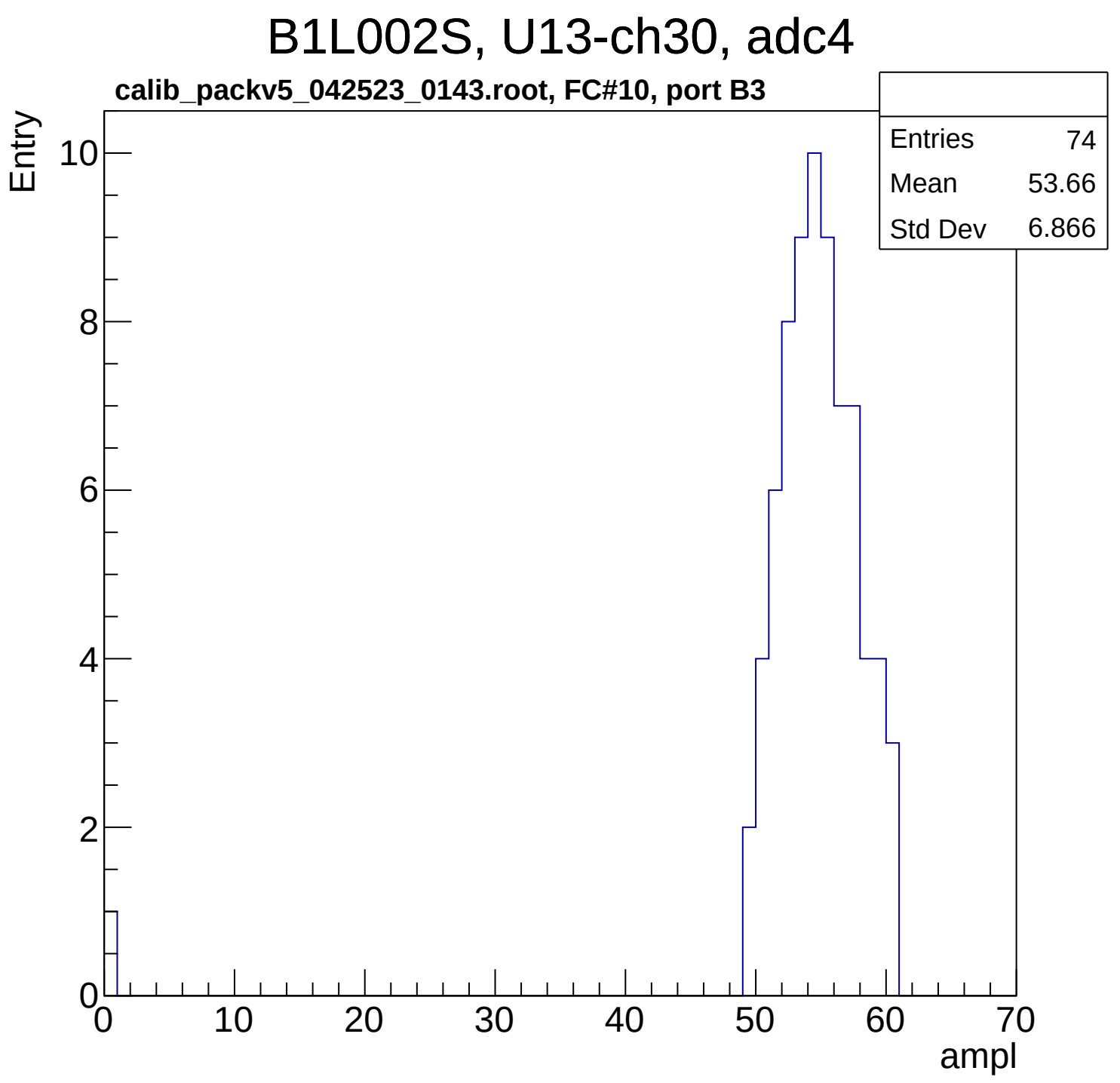
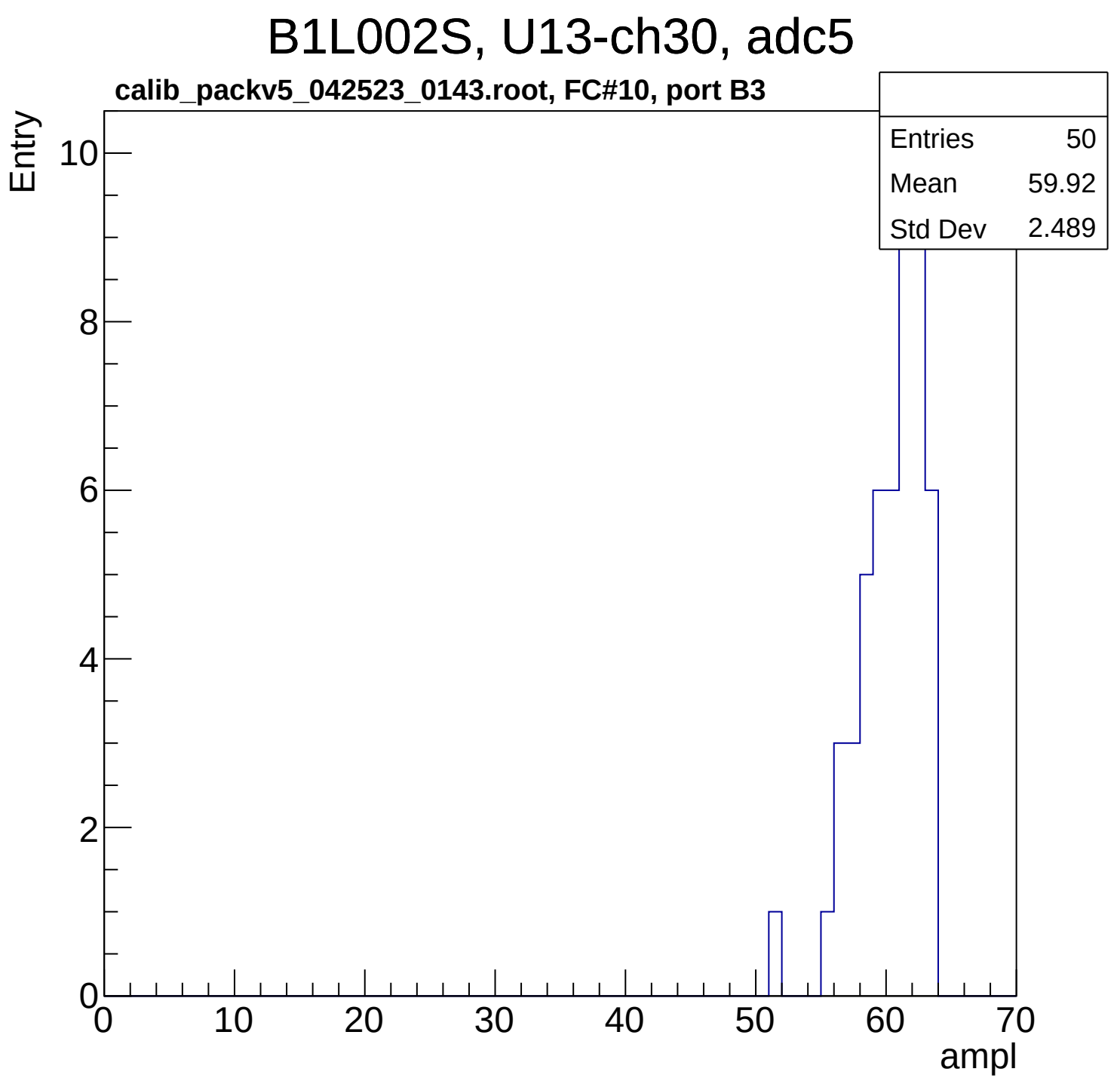
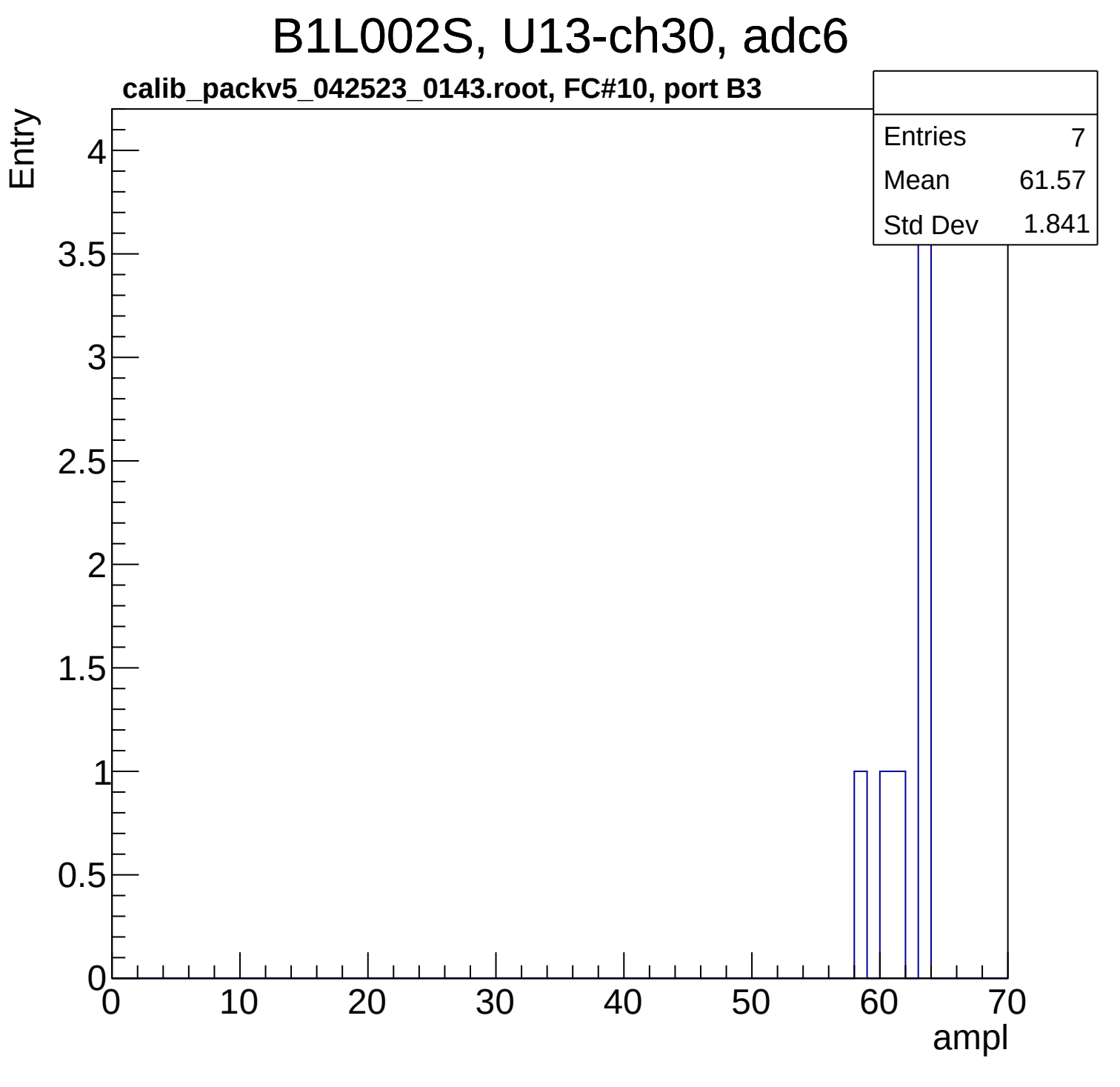

B1L002S, U13-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 53.66
Std Dev 6.866
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.92
Std Dev 2.489
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.57
Std Dev 1.841
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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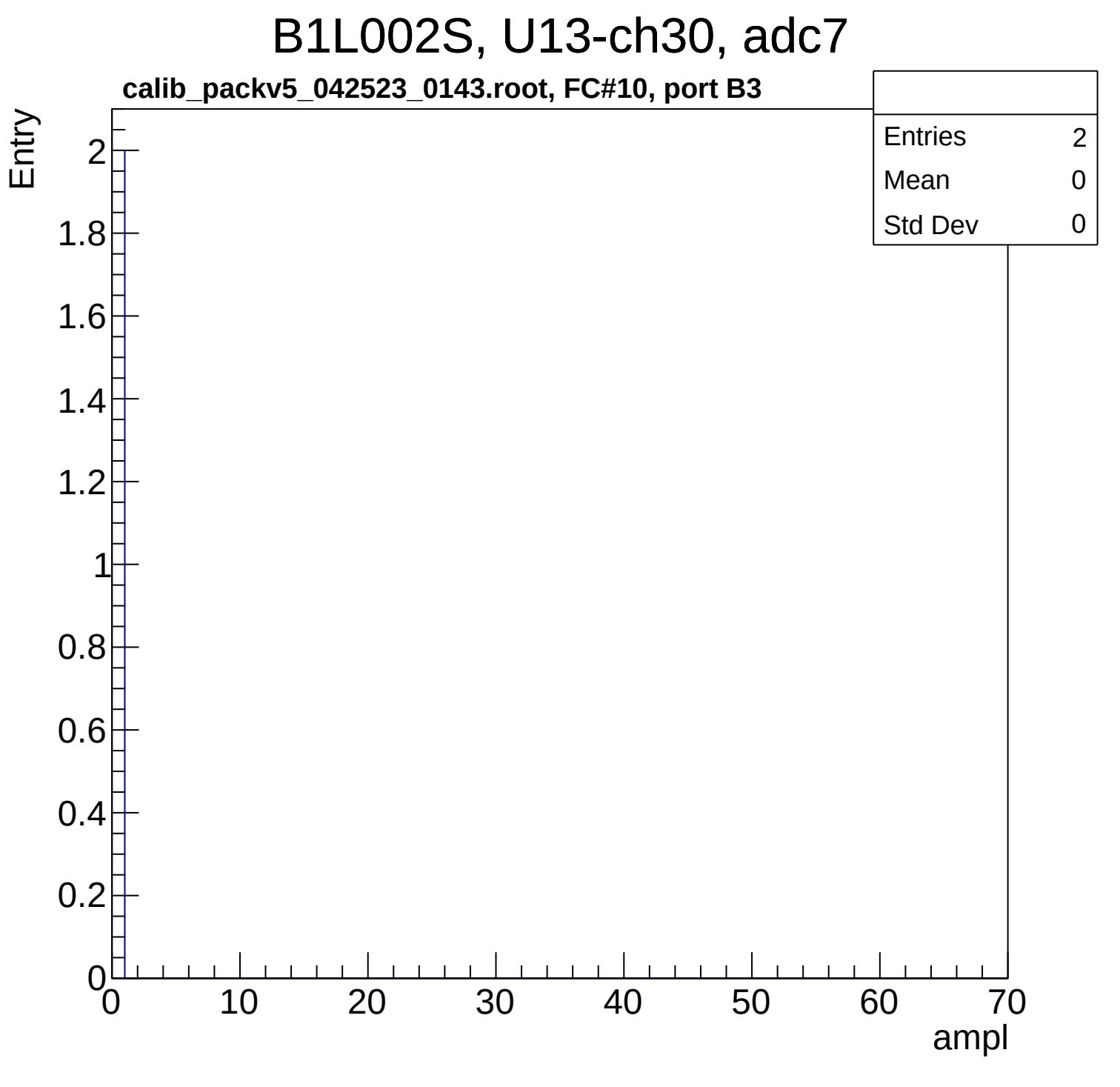
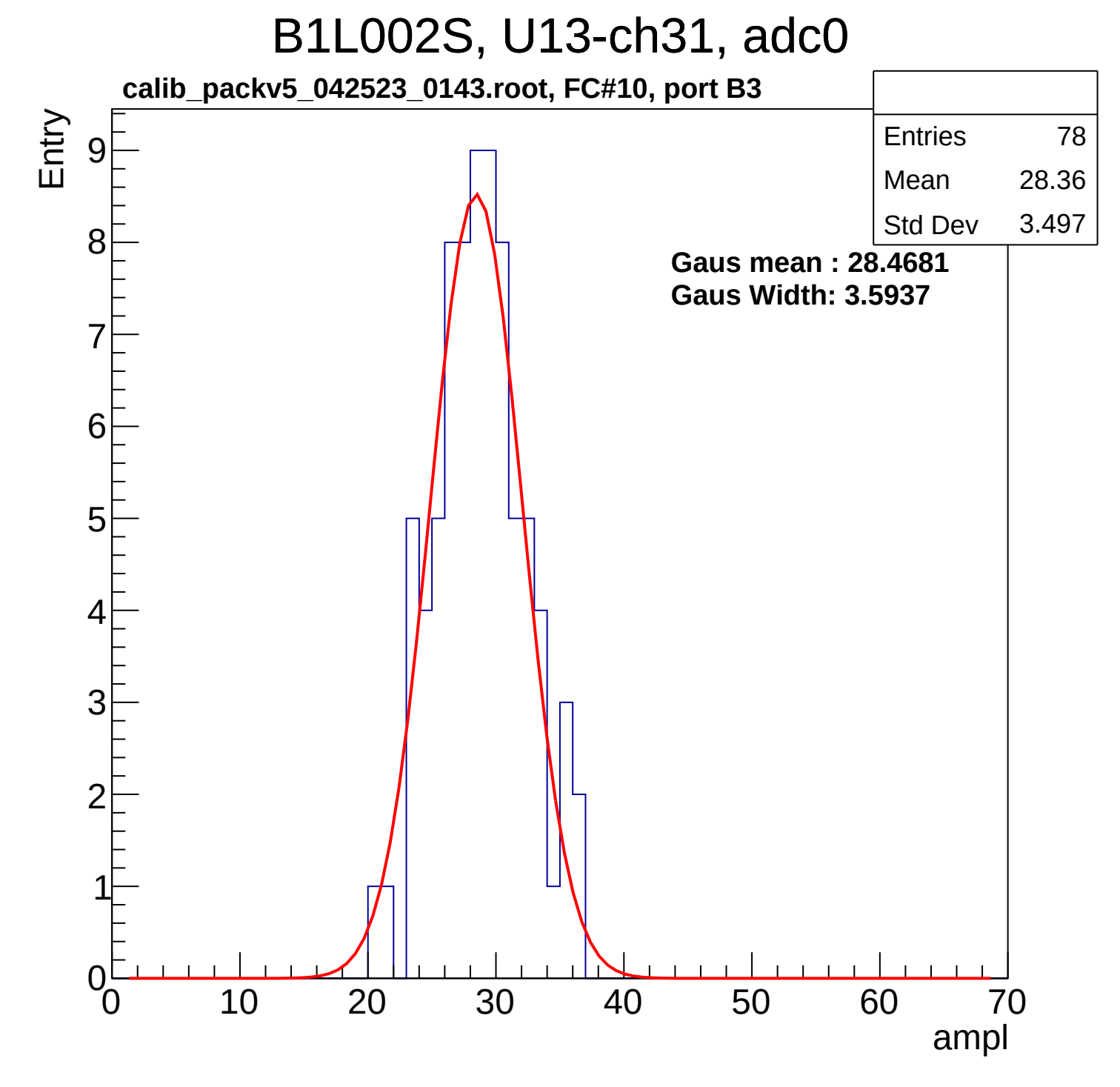

B1L002S, U13-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.36
Std Dev 3.497
Gaus mean : 28.4681
Gaus Width: 3.5937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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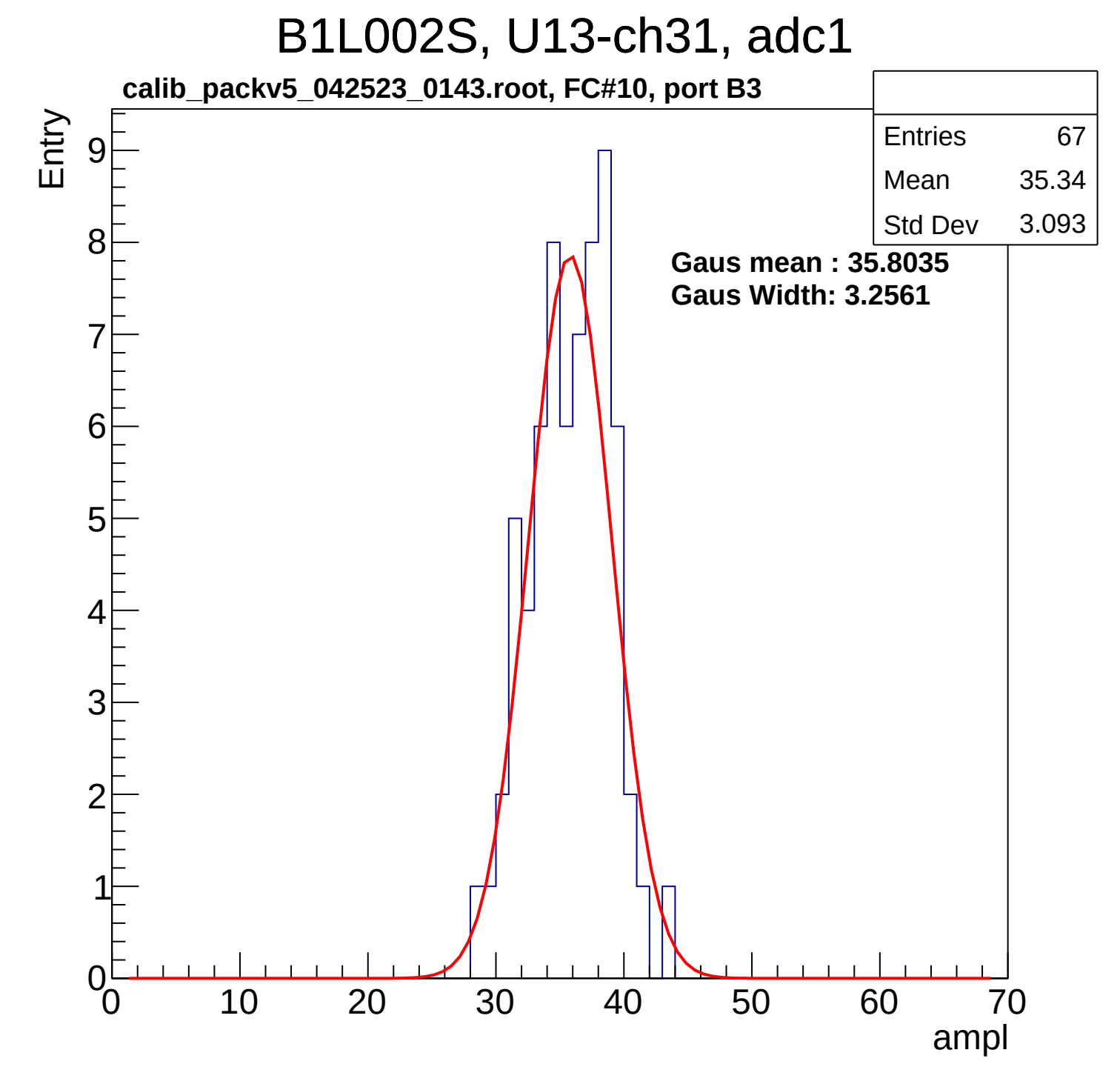
B1L002S, U13-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.34
Std Dev 3.093
Gaus mean : 35.8035
Gaus Width: 3.2561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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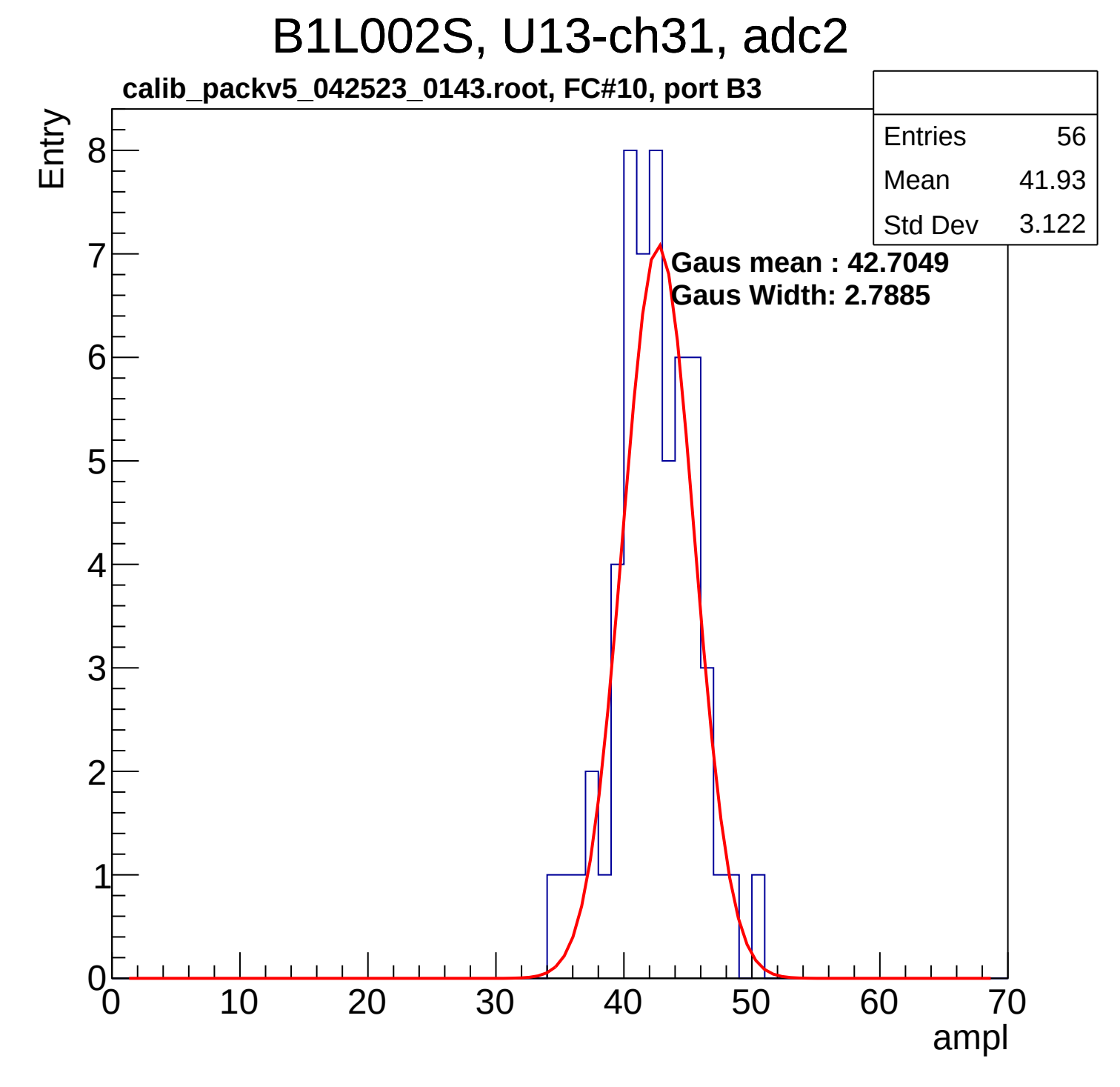
B1L002S, U13-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.93
Std Dev 3.122
Gaus mean : 42.7049
Gaus Width: 2.7885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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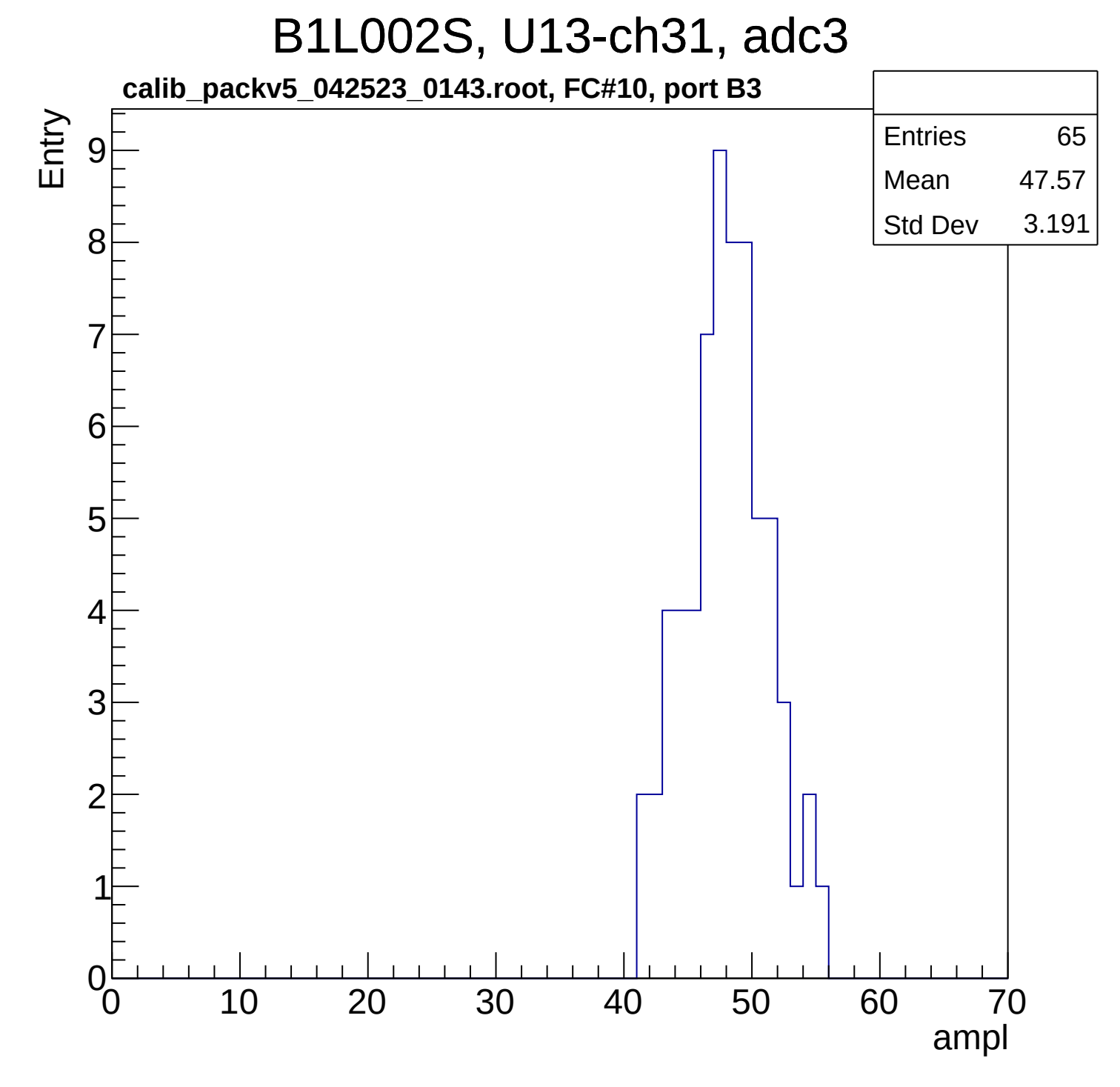
B1L002S, U13-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.57
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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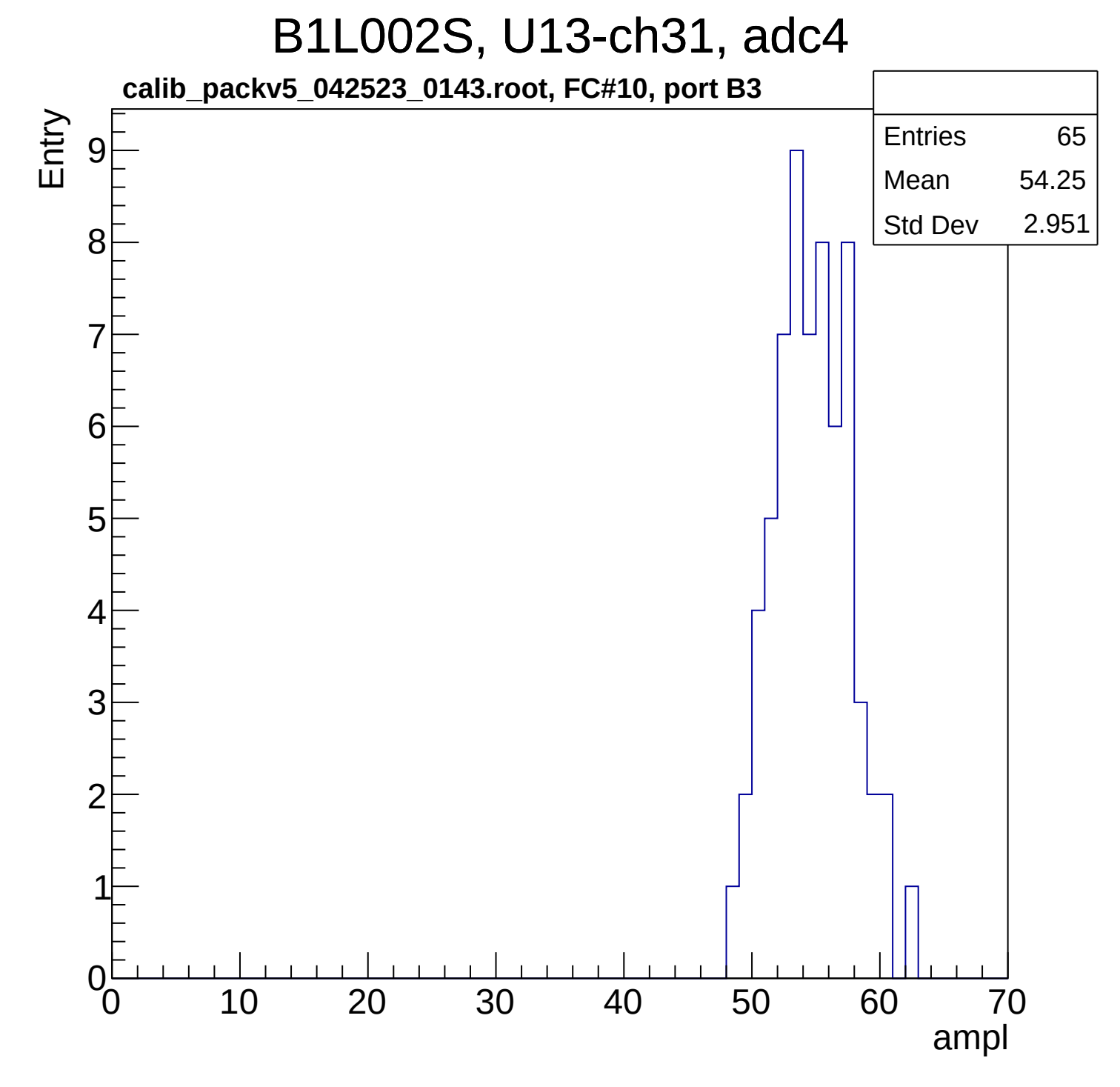
B1L002S, U13-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 54.25
Std Dev 2.951
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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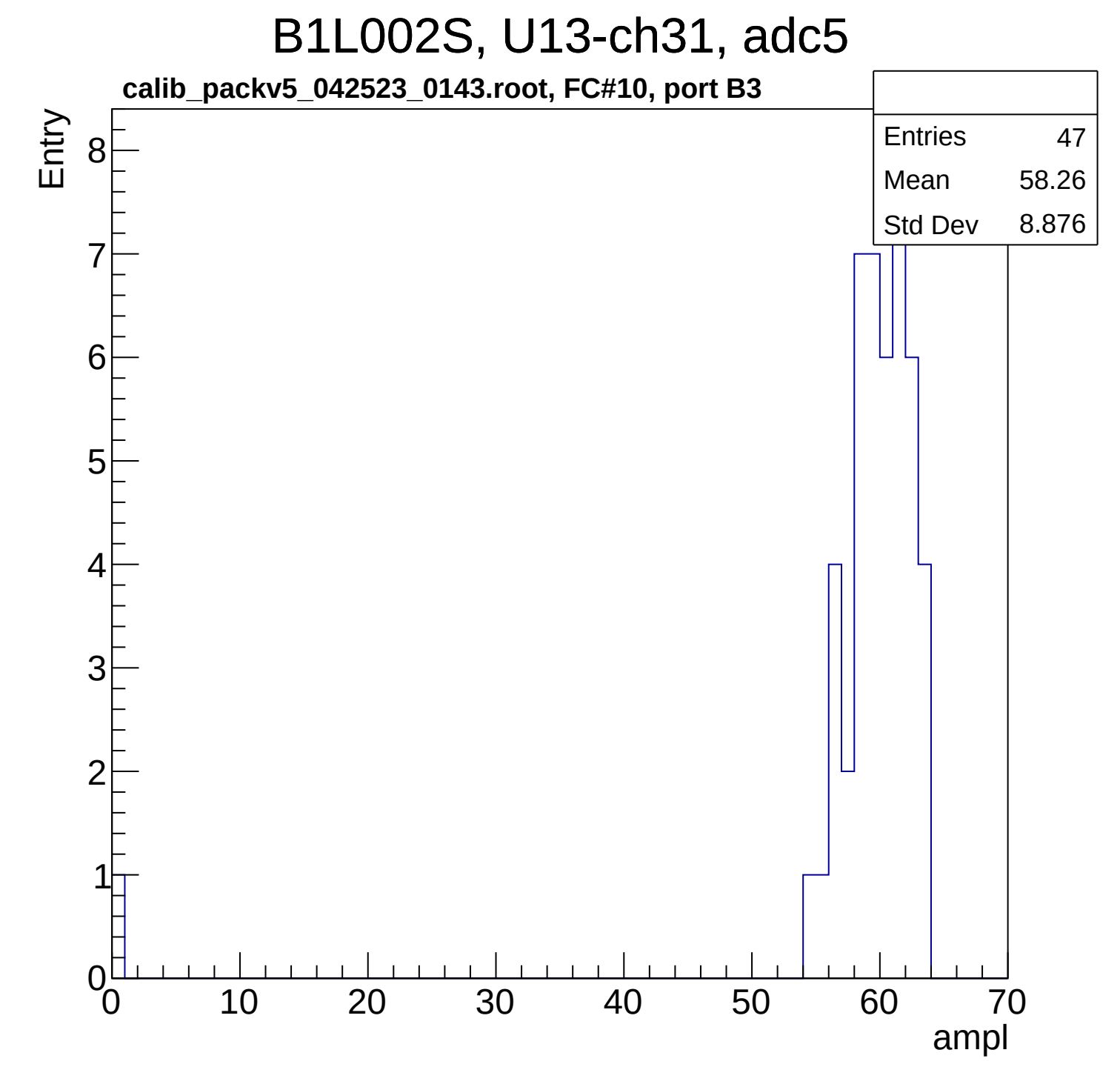
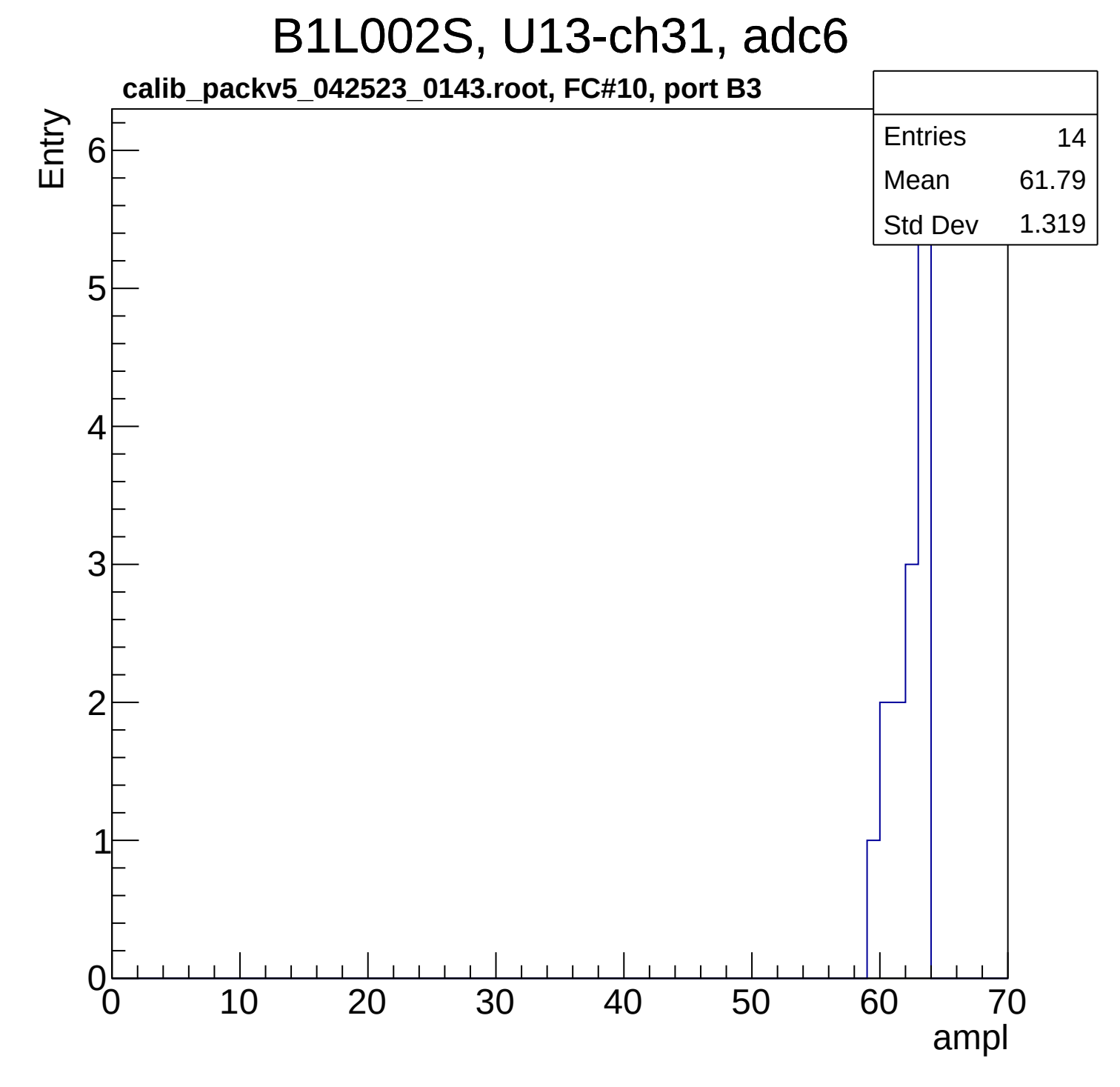
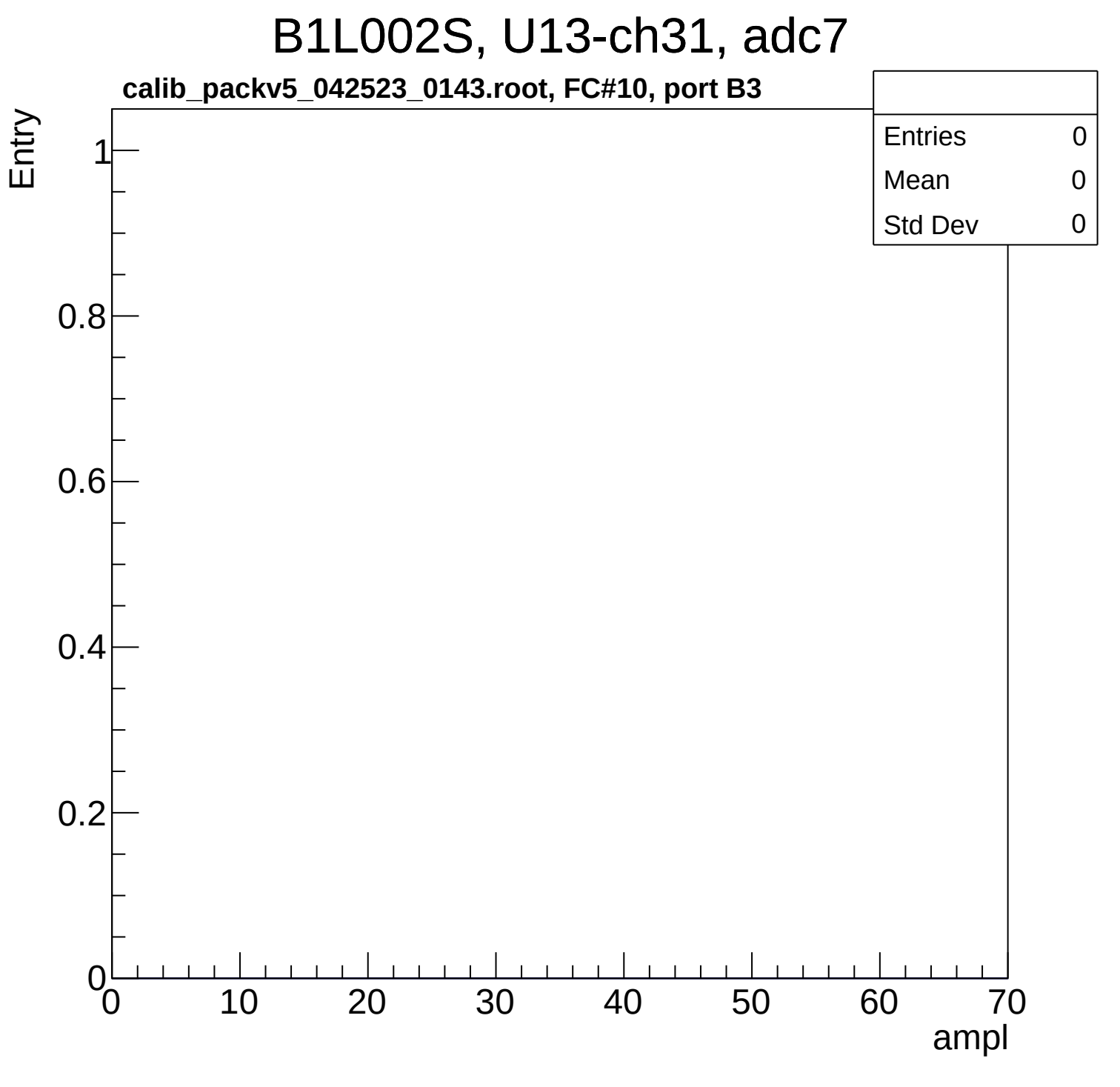
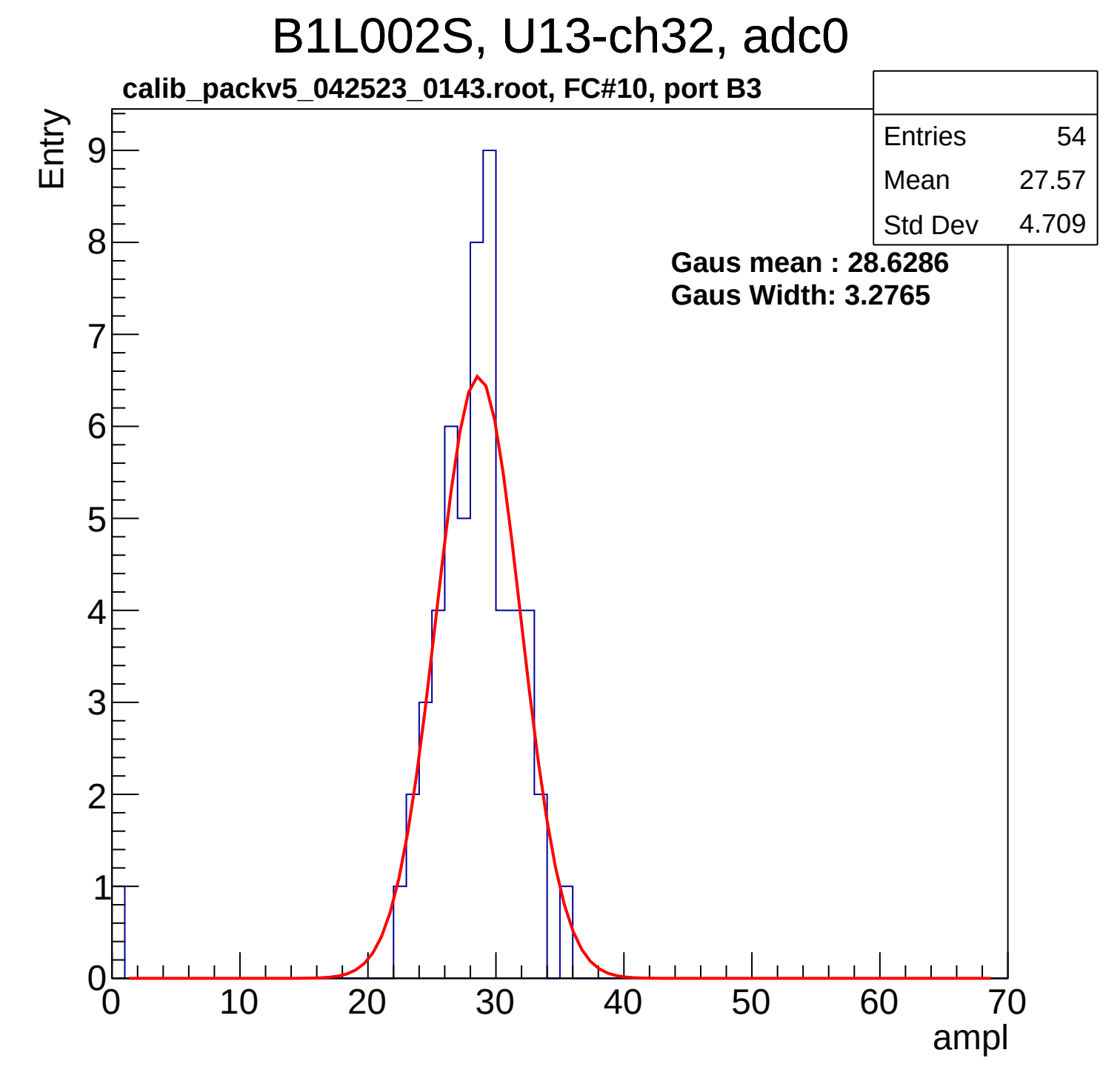

B1L002S, U13-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.26
Std Dev 8.876
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.79
Std Dev 1.319
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.57
Std Dev 4.709
Gaus mean : 28.6286
Gaus Width: 3.2765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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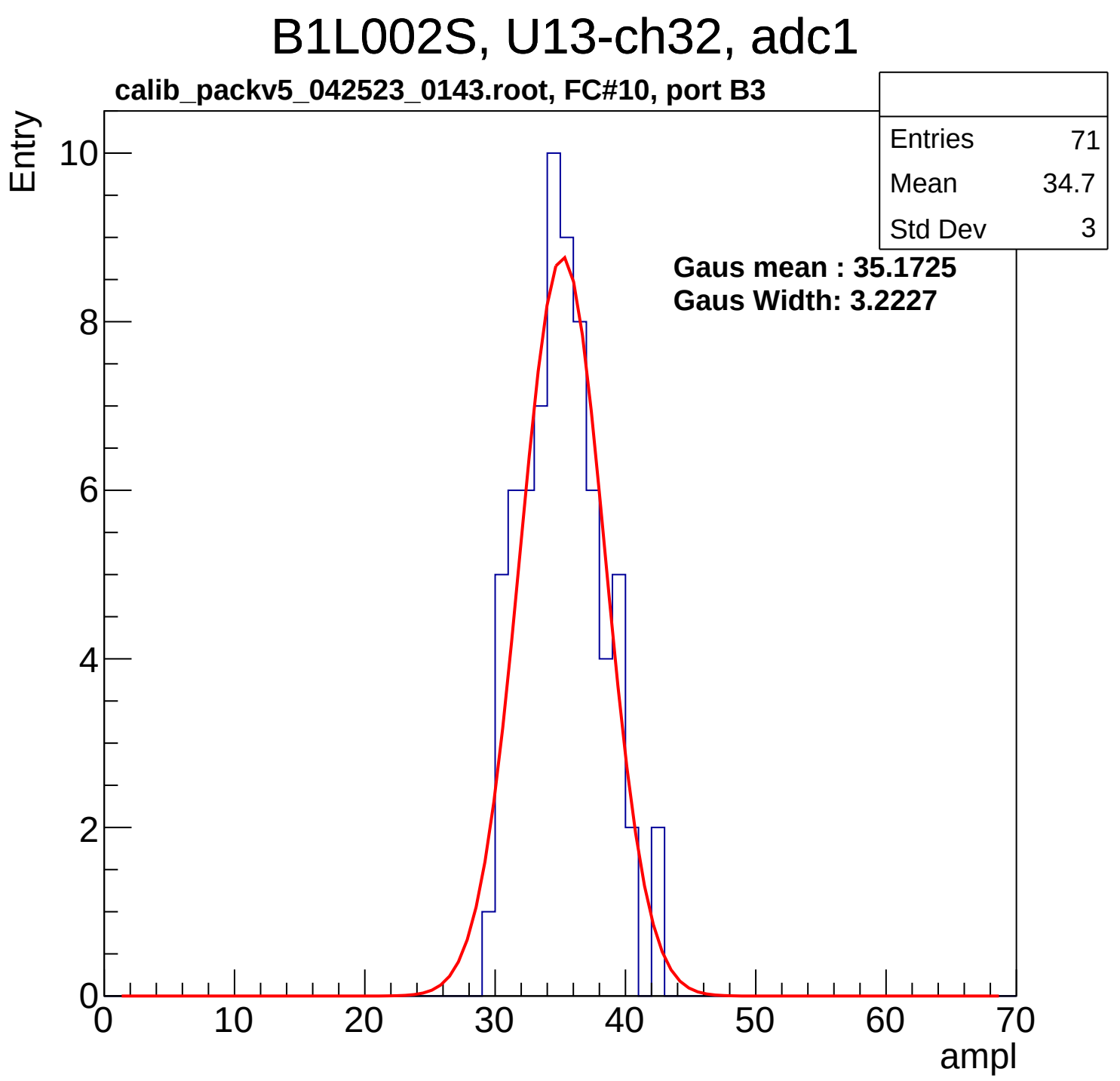
B1L002S, U13-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.7
Std Dev 3
Gaus mean : 35.1725
Gaus Width: 3.2227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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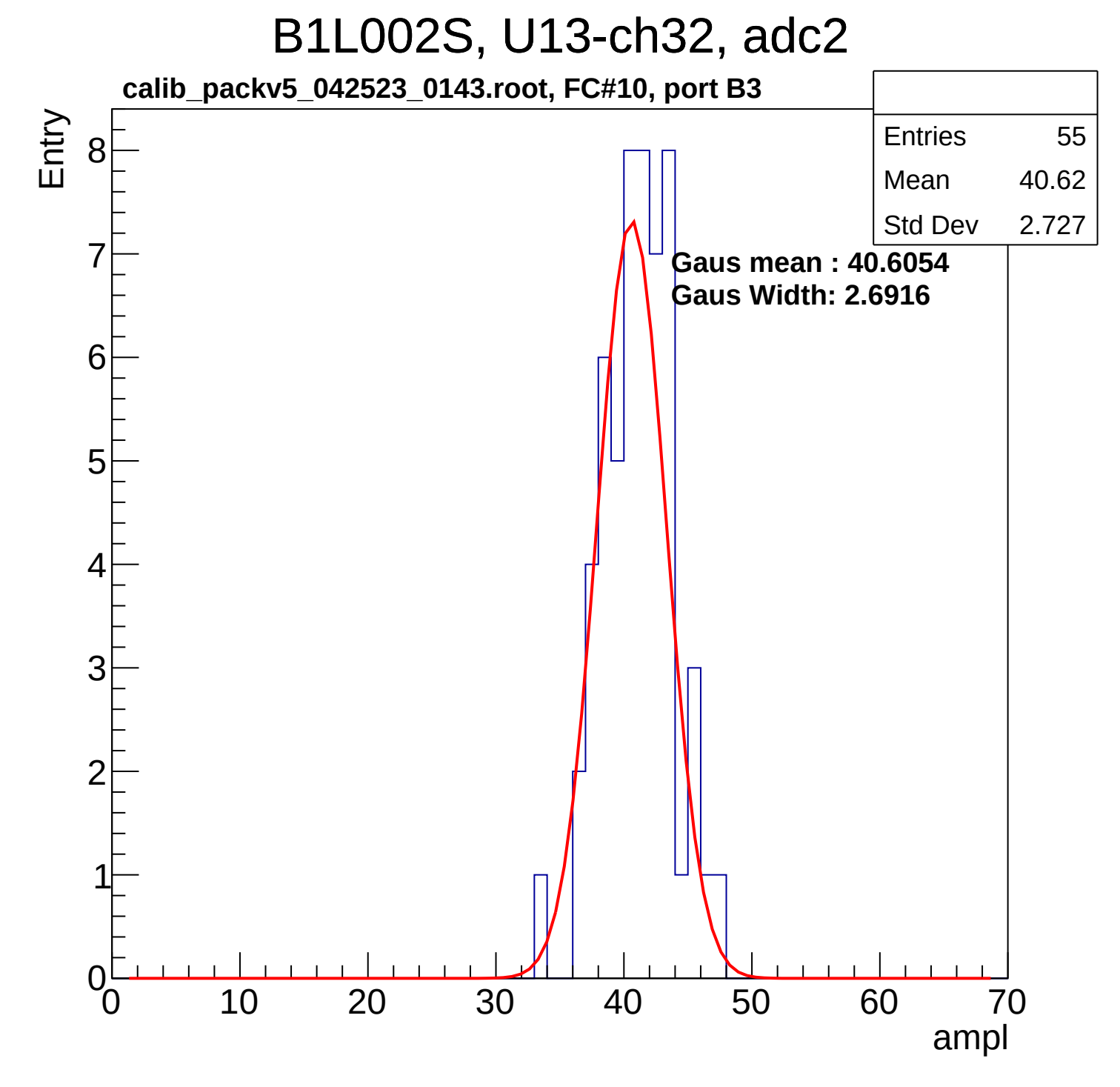
B1L002S, U13-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.62
Std Dev 2.727
Gaus mean : 40.6054
Gaus Width: 2.6916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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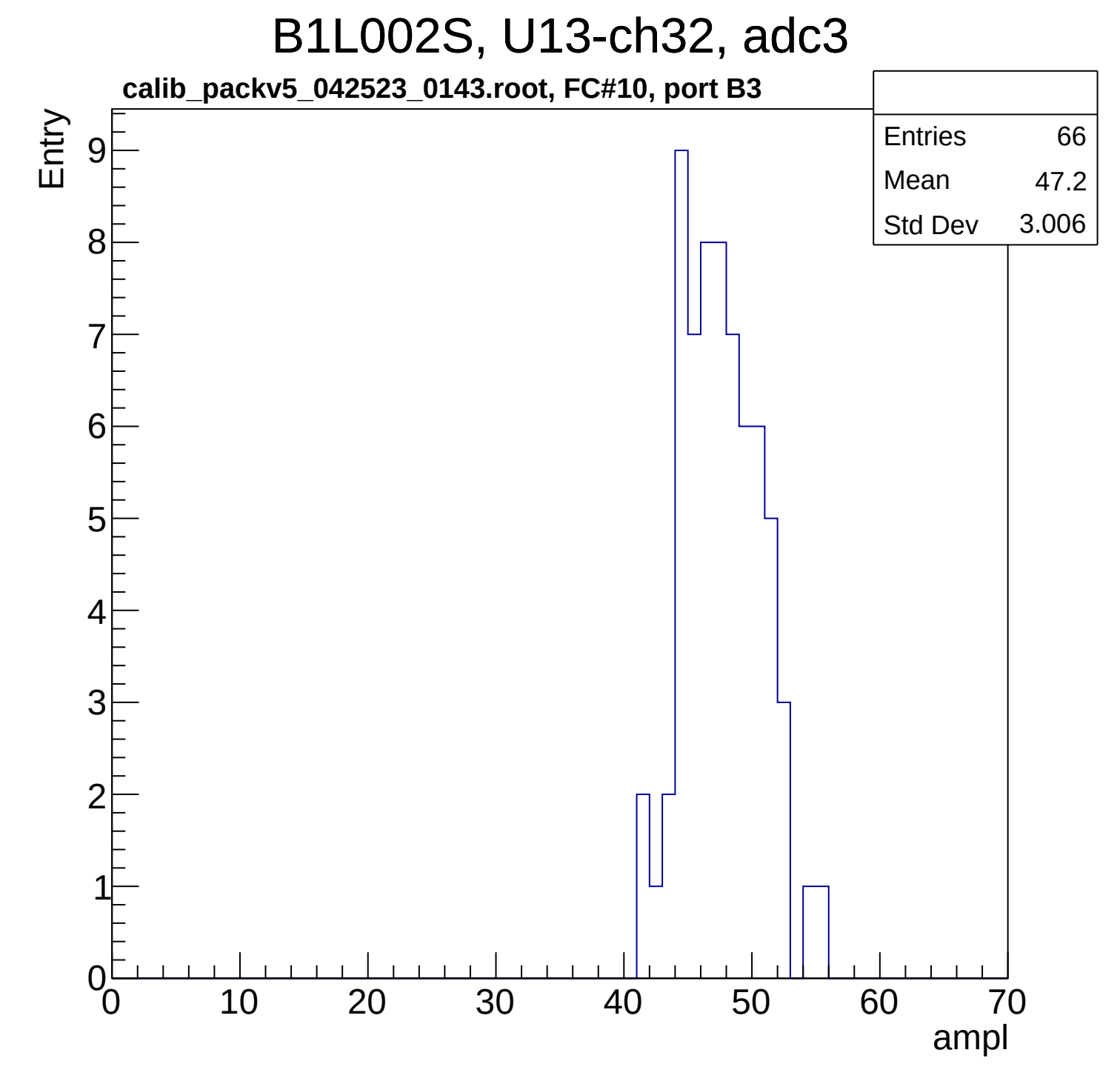
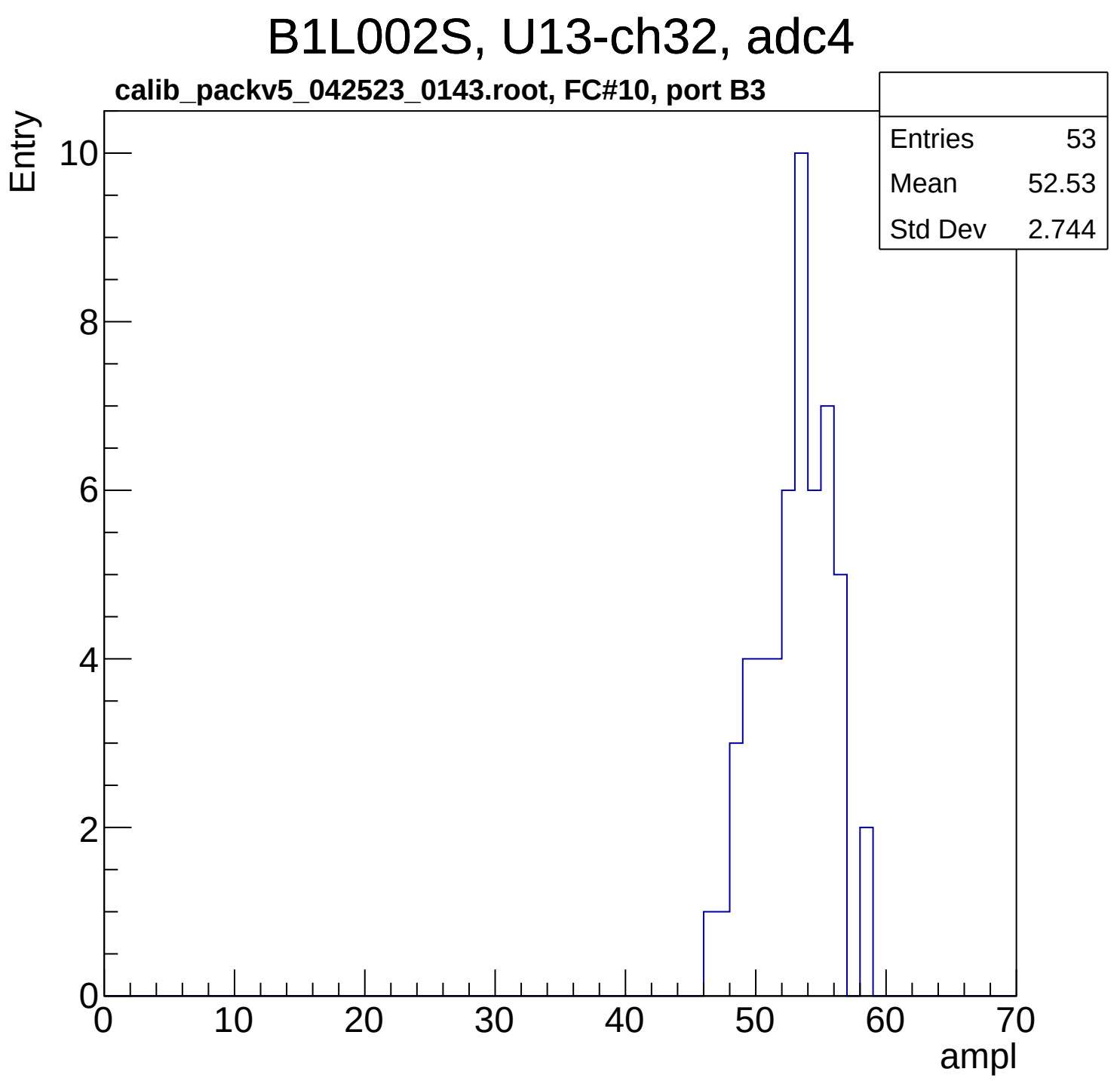
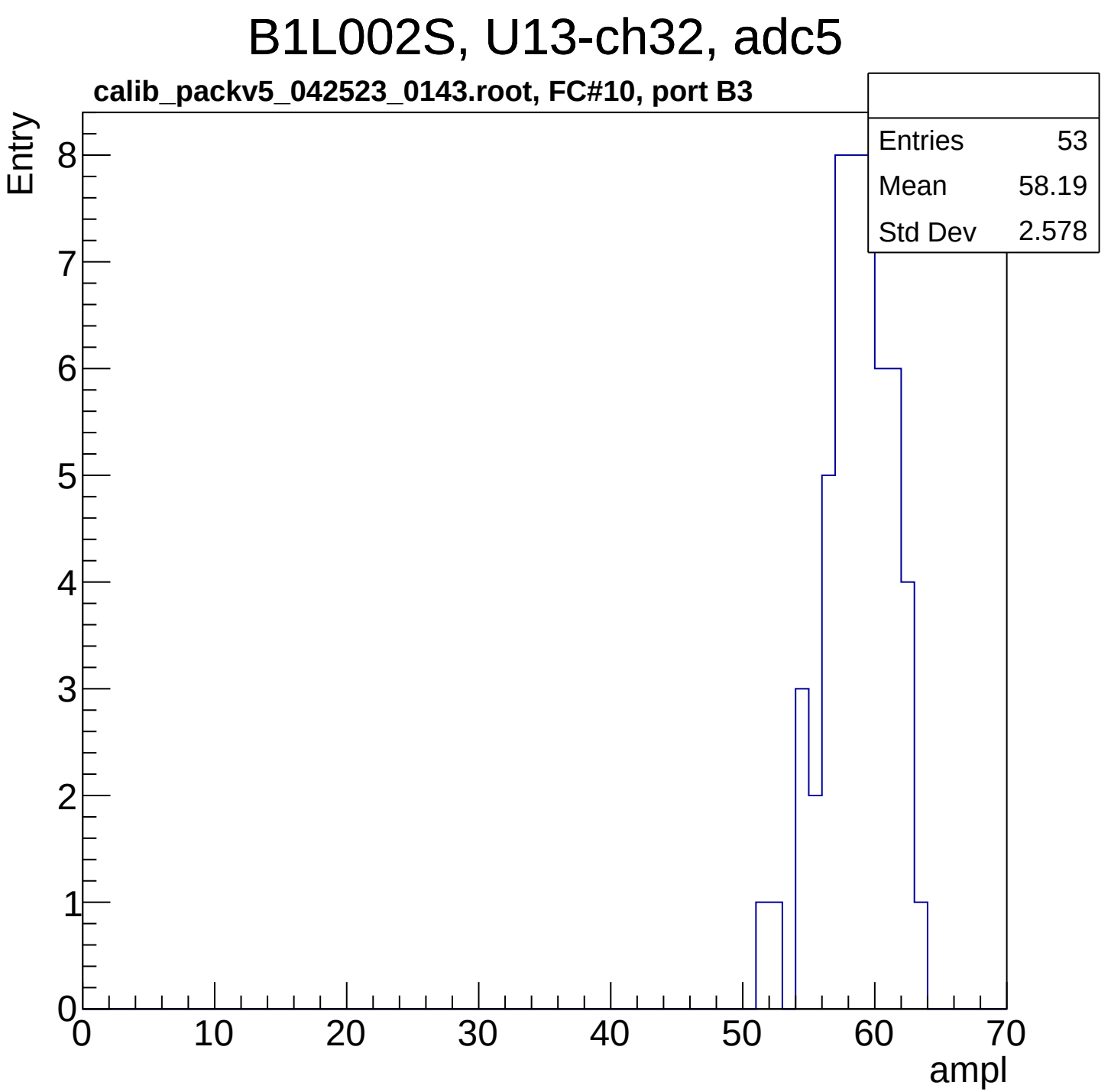

B1L002S, U13-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.2
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.53
Std Dev 2.744
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.19
Std Dev 2.578
Entry
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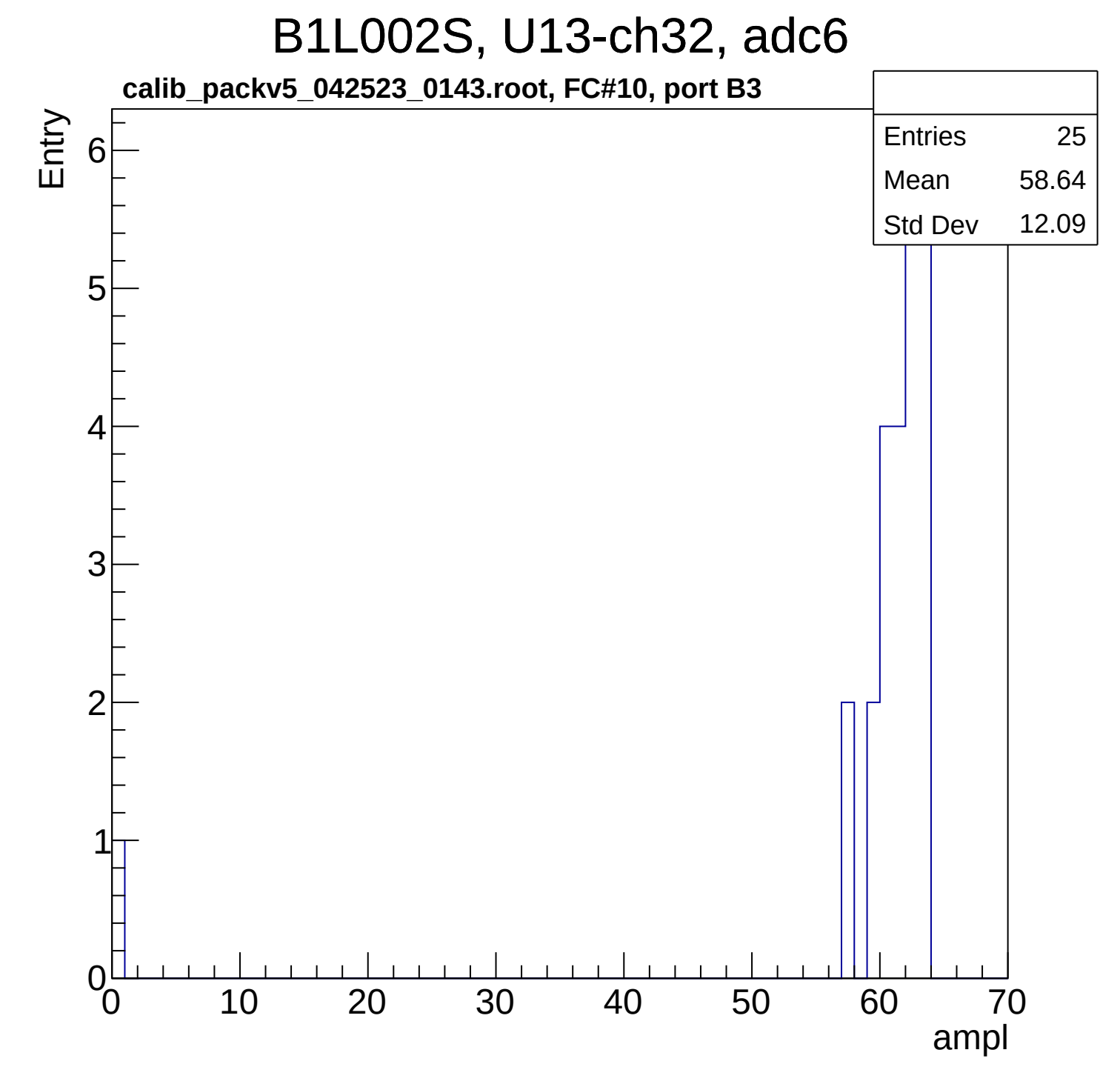
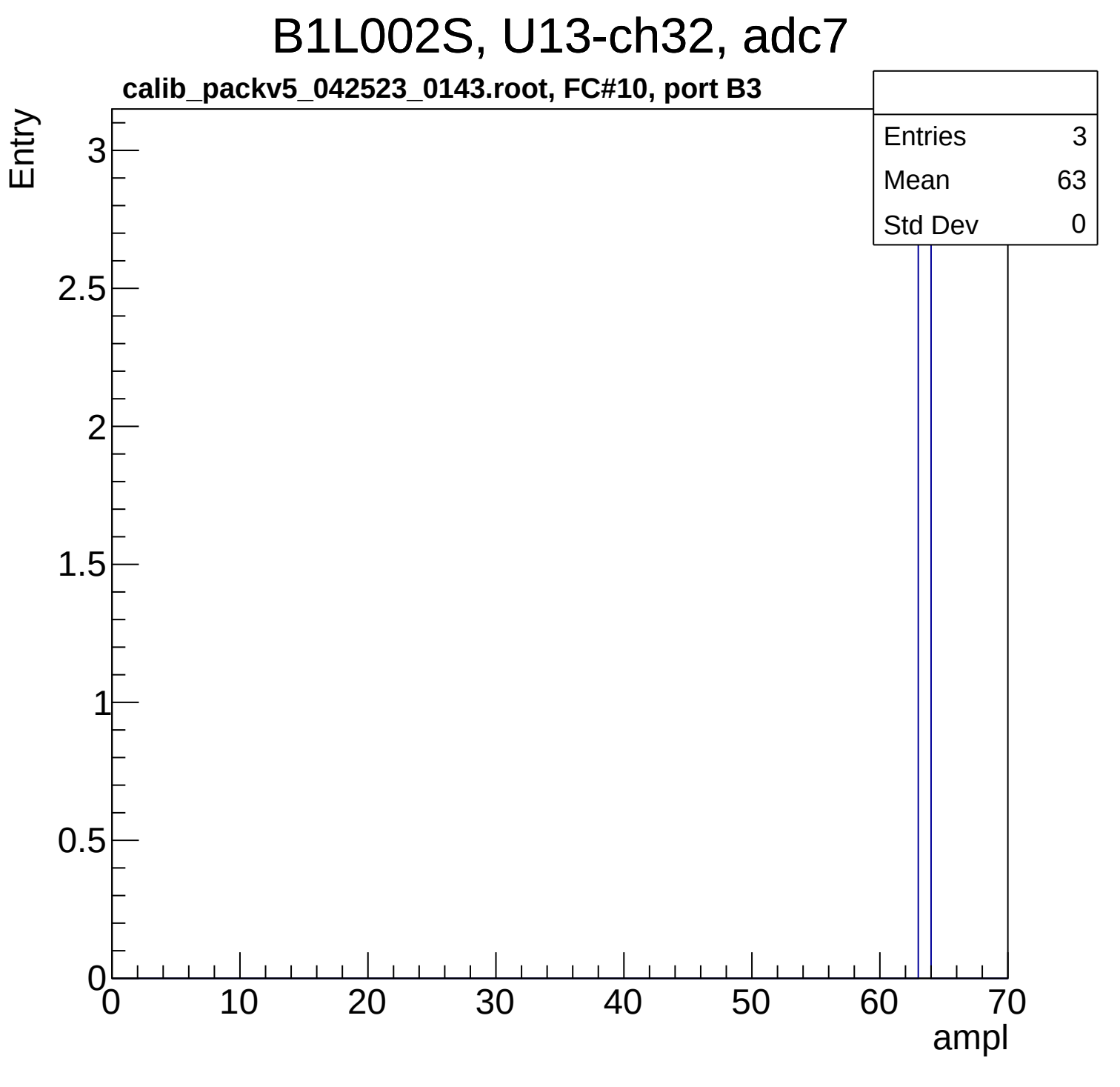
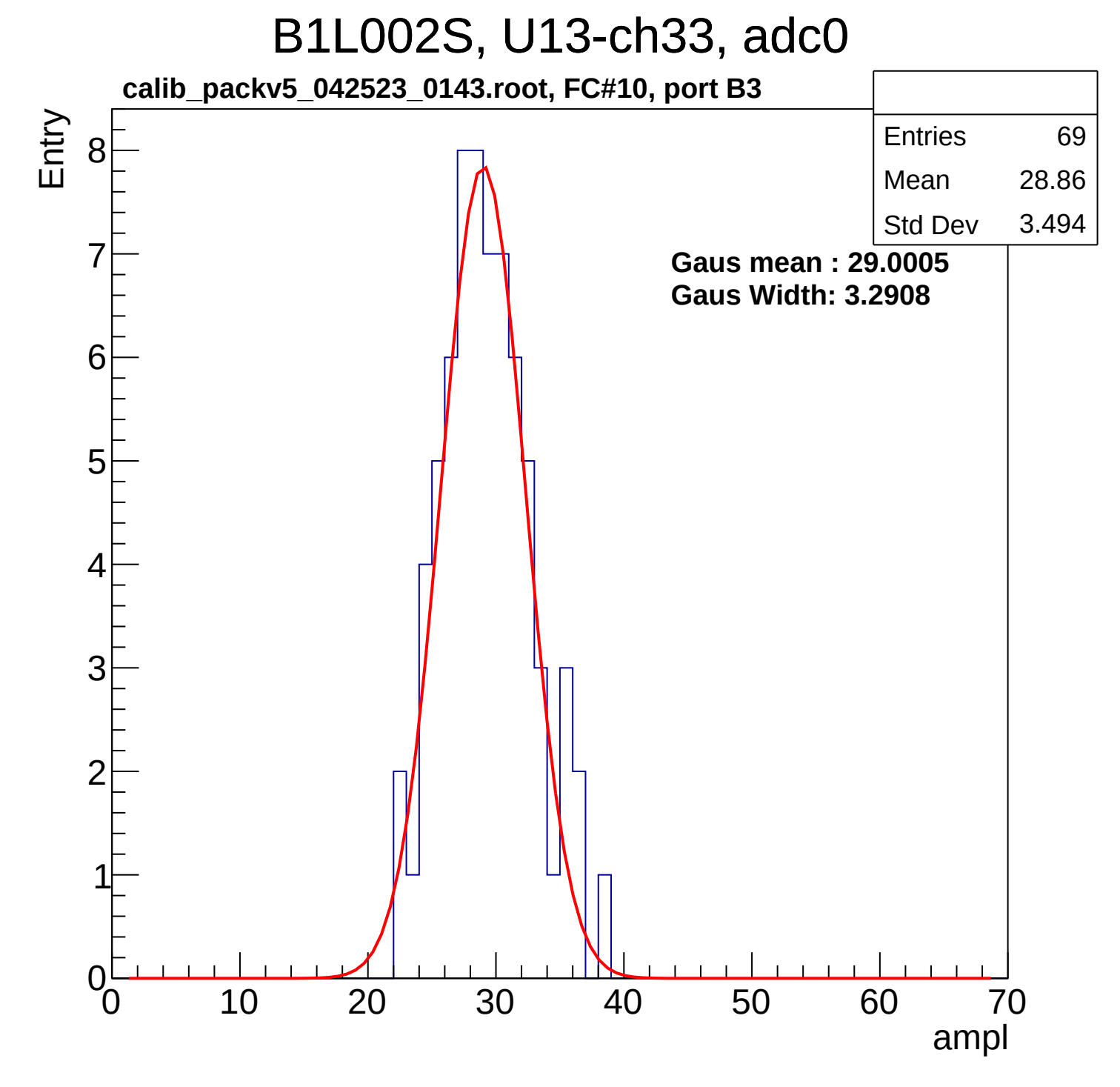
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.64
Std Dev 12.09
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.86
Std Dev 3.494
Gaus mean : 29.0005
Gaus Width: 3.2908
Entry
ampl
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1
2
3
4
5
6
7
8
0
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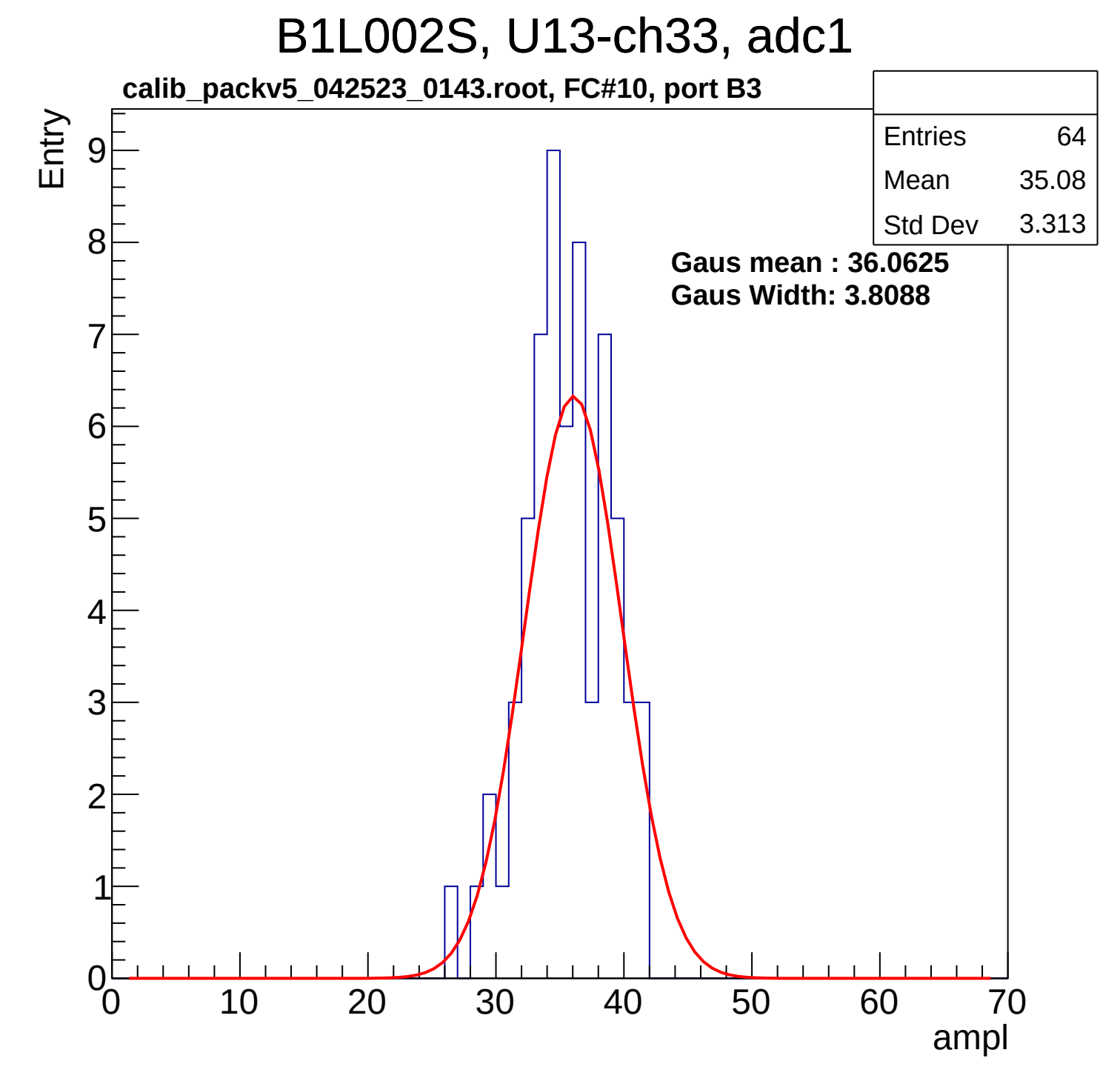
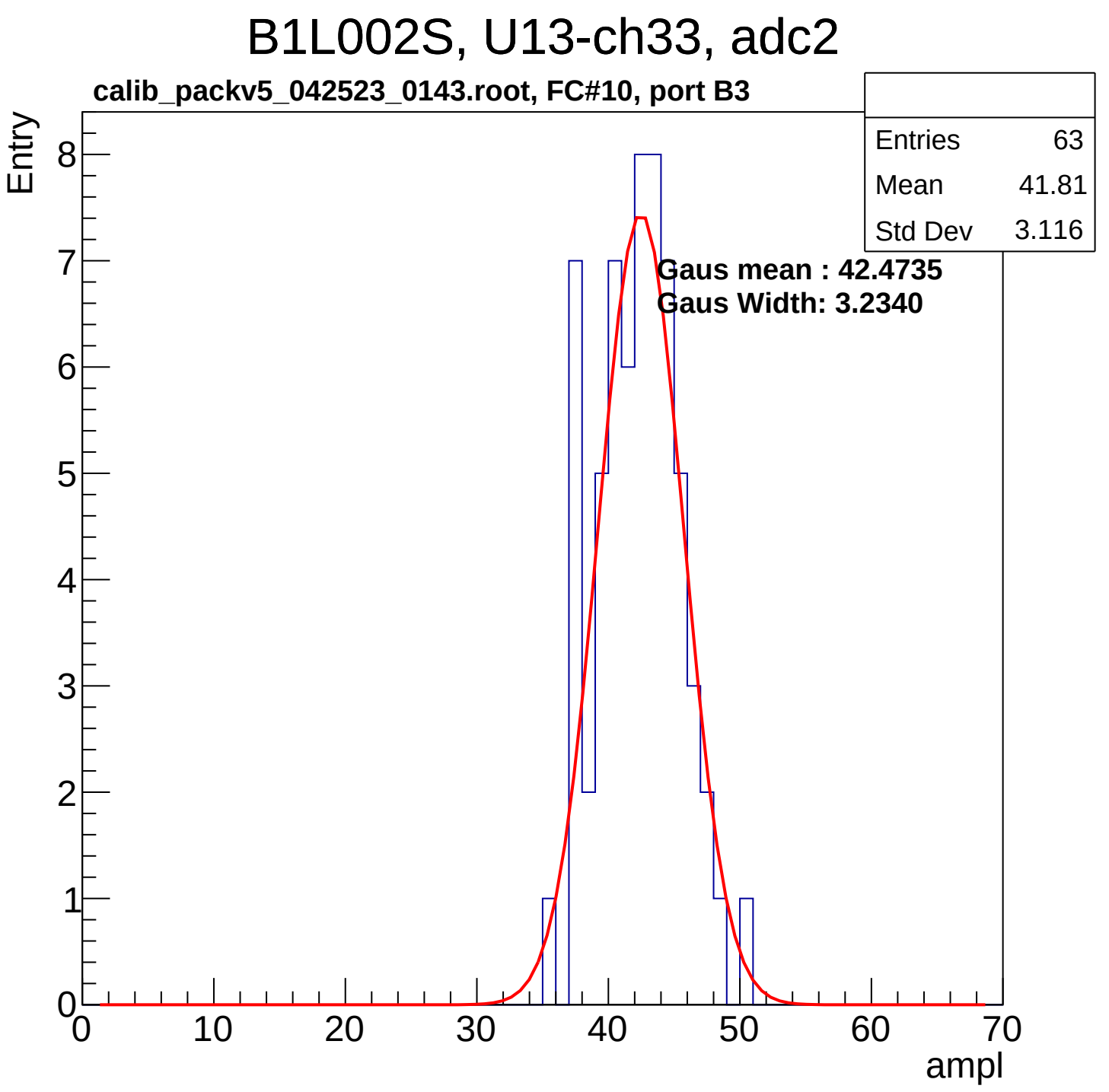
20
30
40
50
60
70


B1L002S, U13-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.08
Std Dev 3.313
Gaus mean : 36.0625
Gaus Width: 3.8088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.81
Std Dev 3.116
Gaus mean : 42.4735
Gaus Width: 3.2340
Entry
ampl
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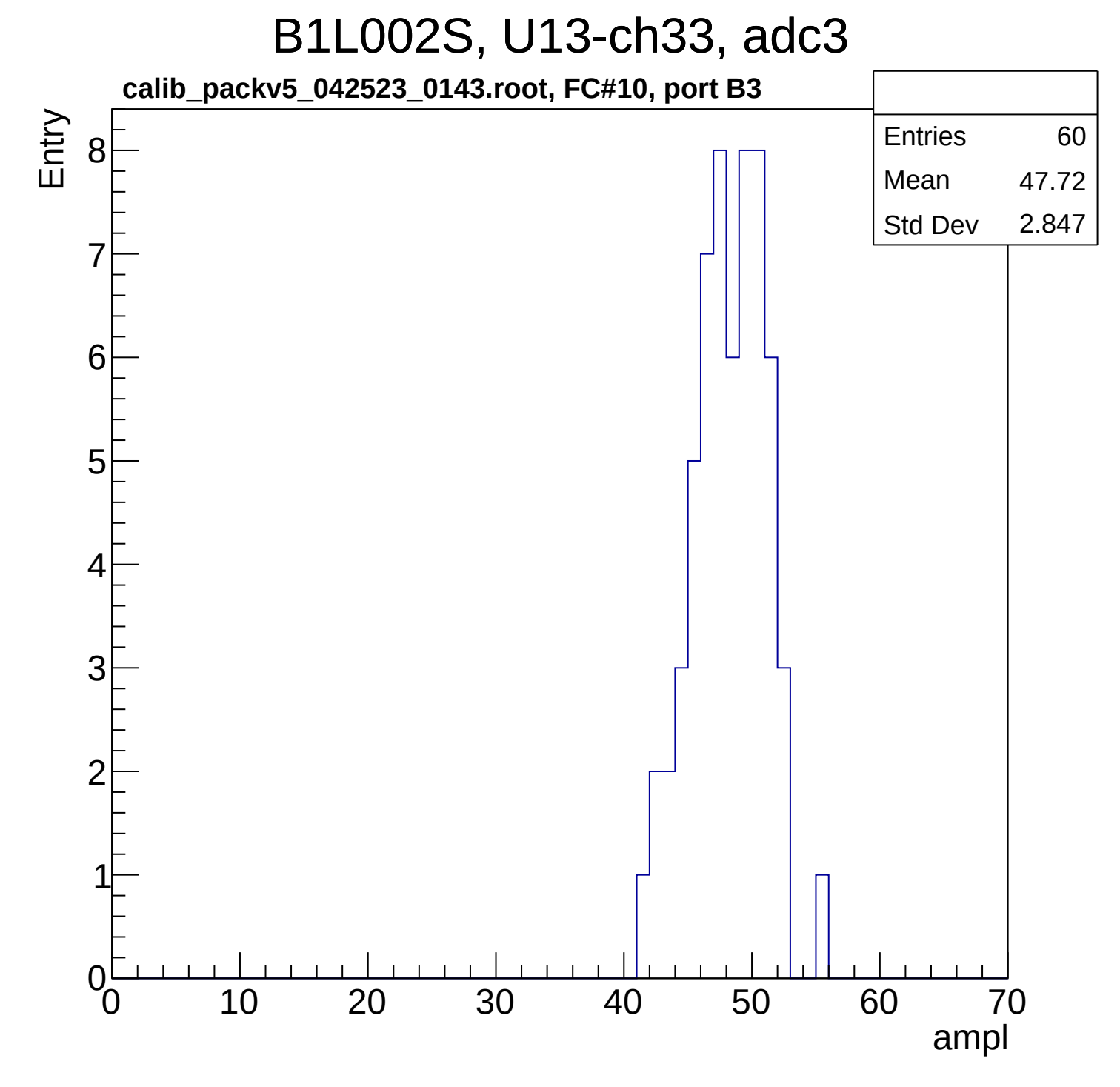
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.72
Std Dev 2.847
Entry
ampl
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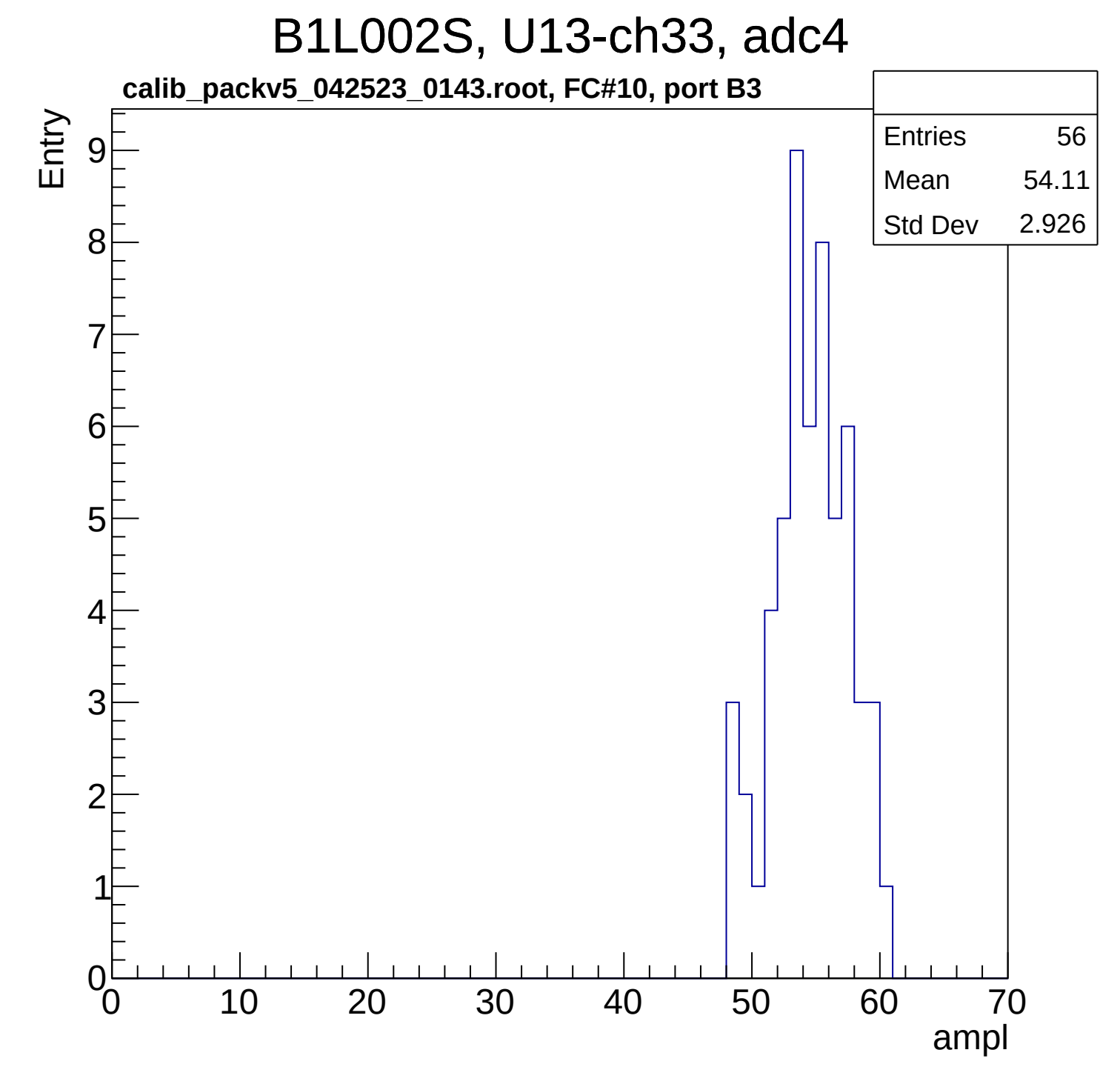
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.11
Std Dev 2.926
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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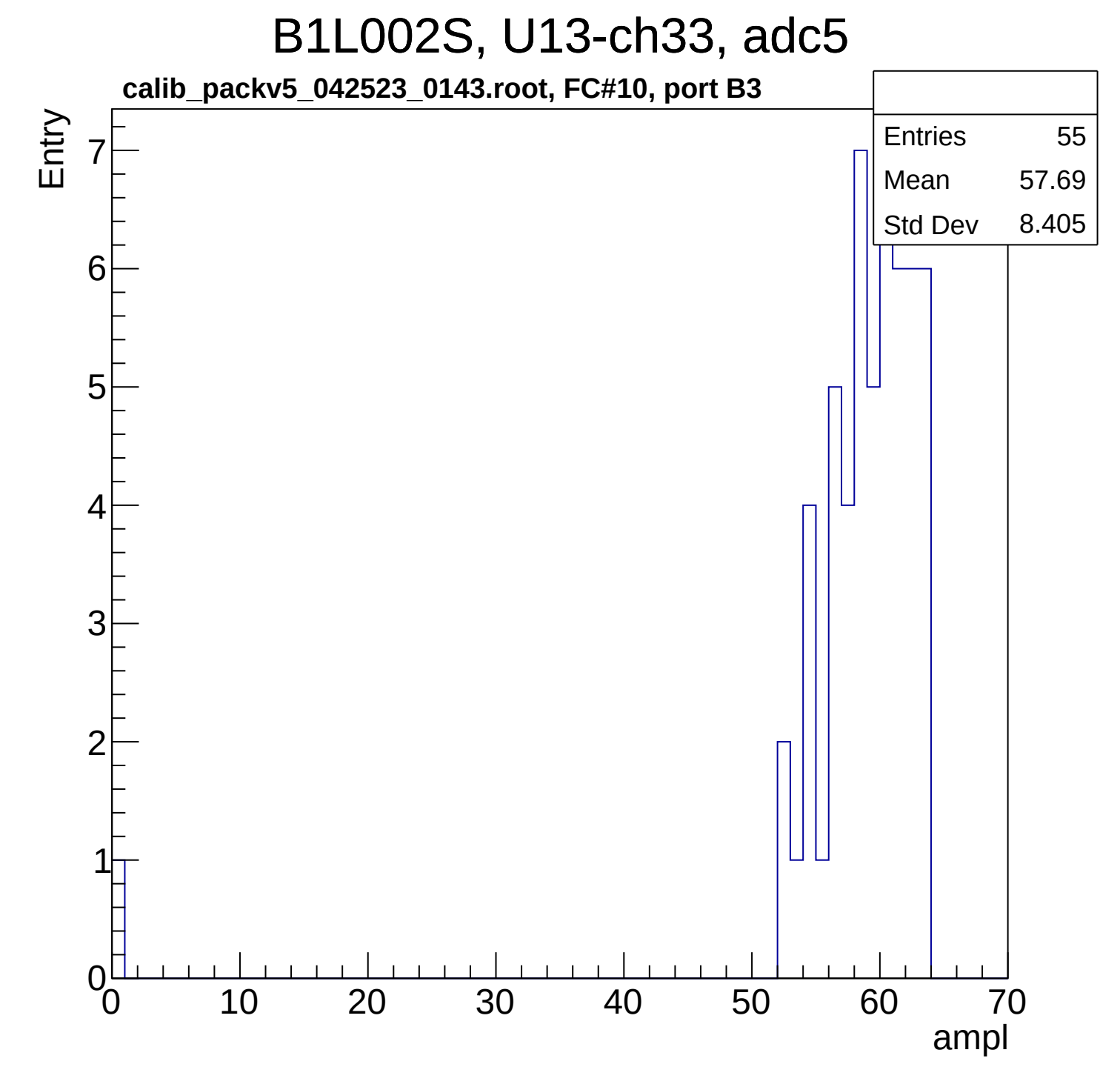
B1L002S, U13-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.69
Std Dev 8.405
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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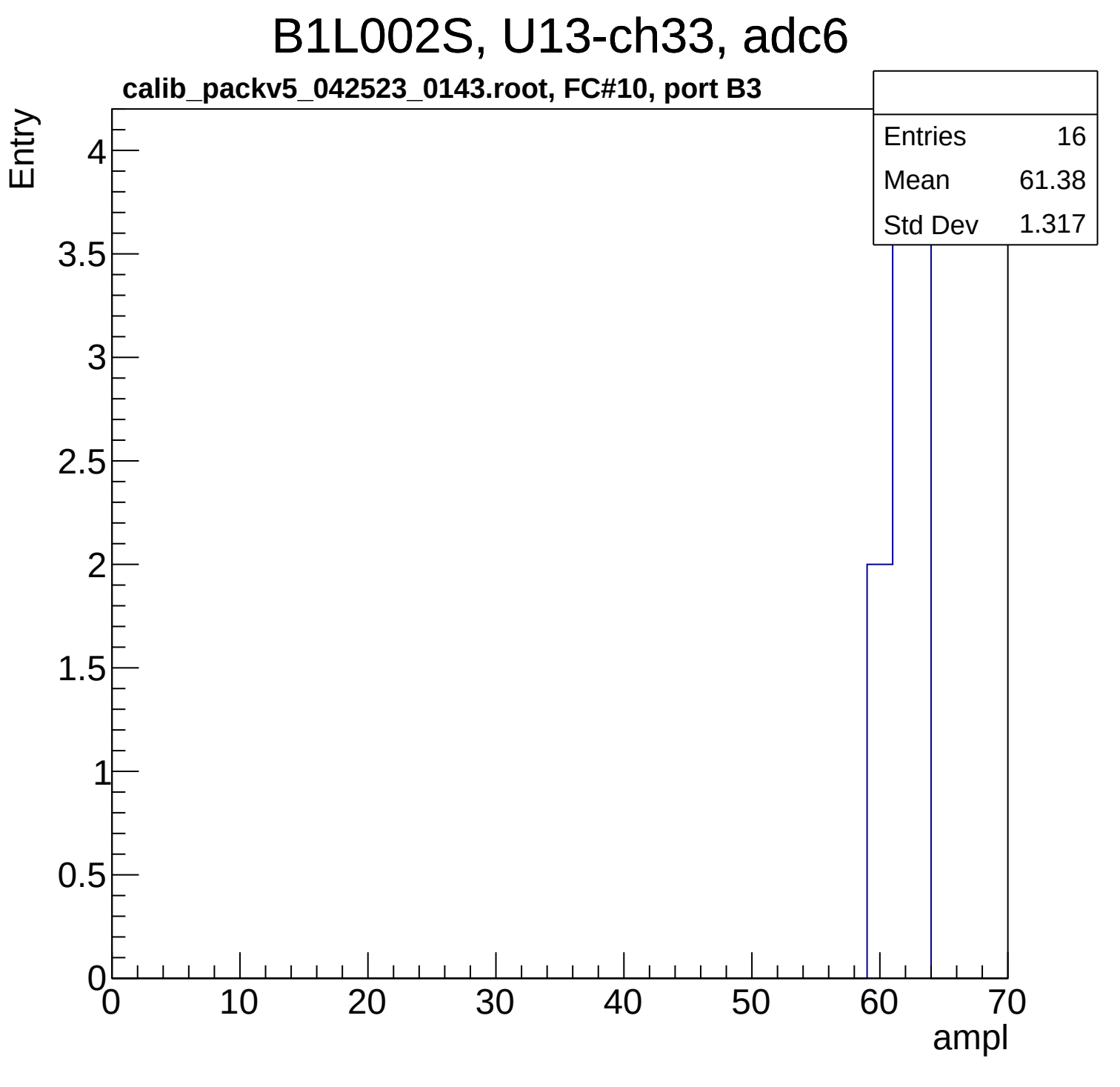
B1L002S, U13-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.38
Std Dev 1.317
Entry
ampl
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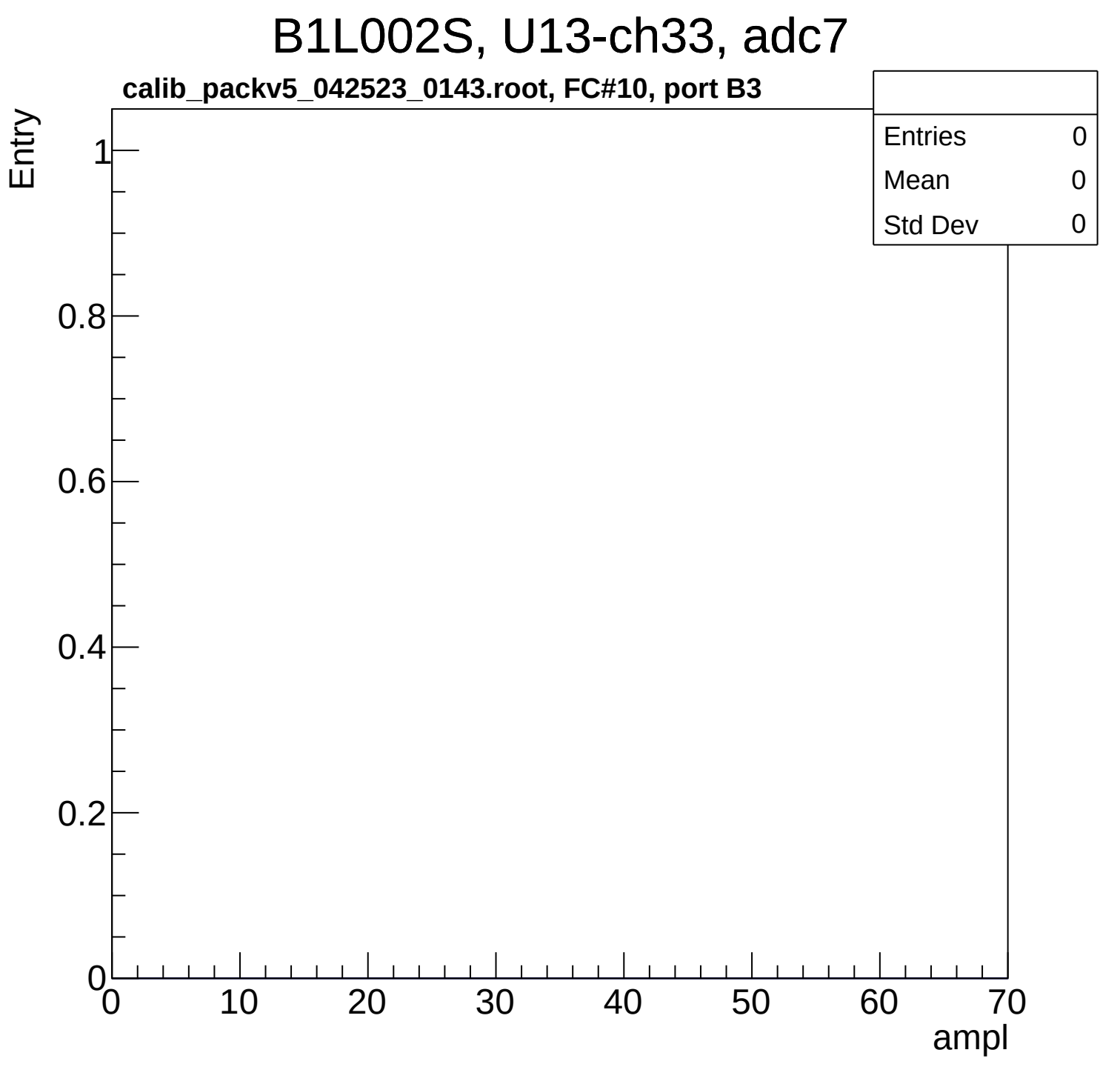
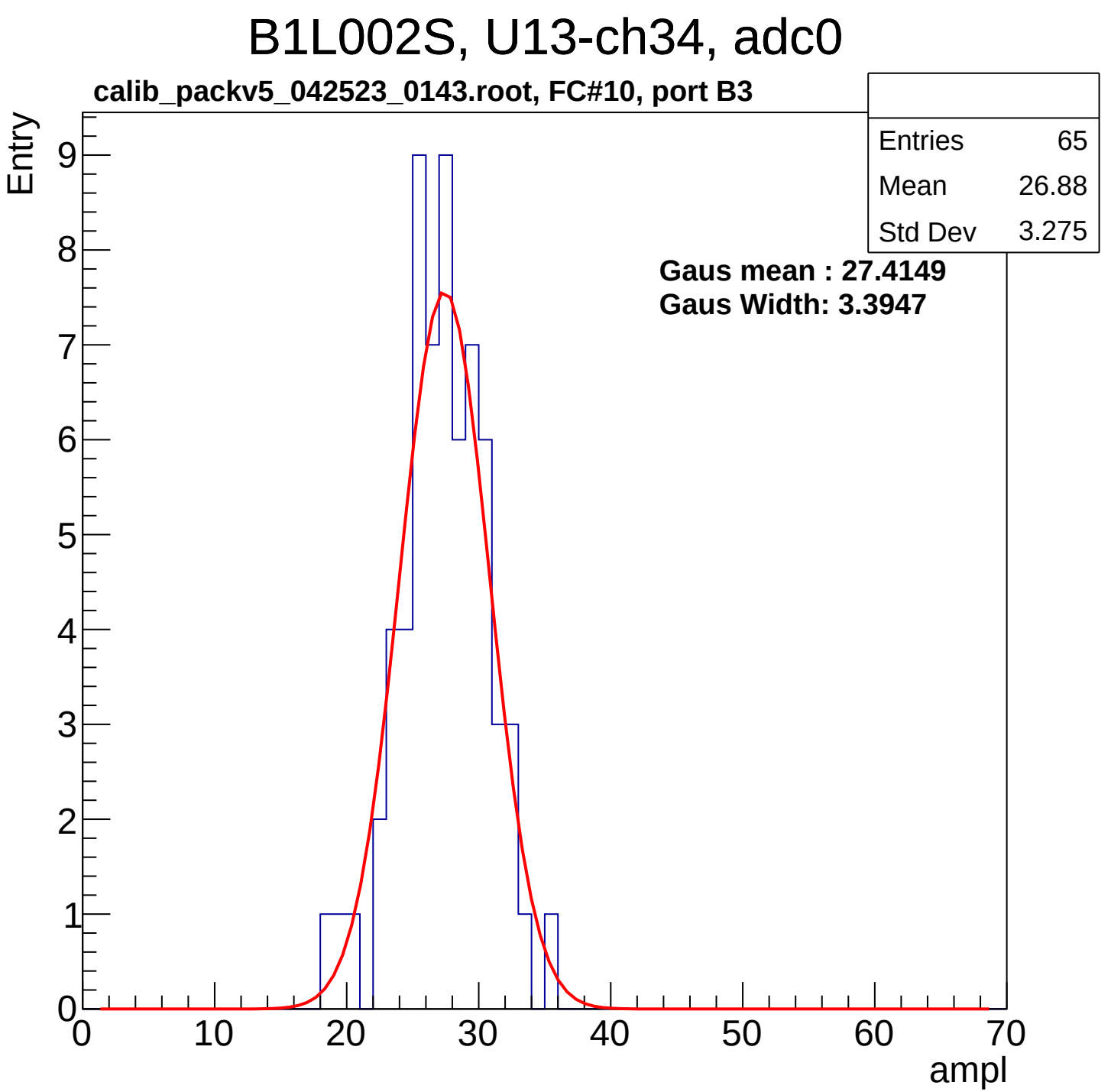
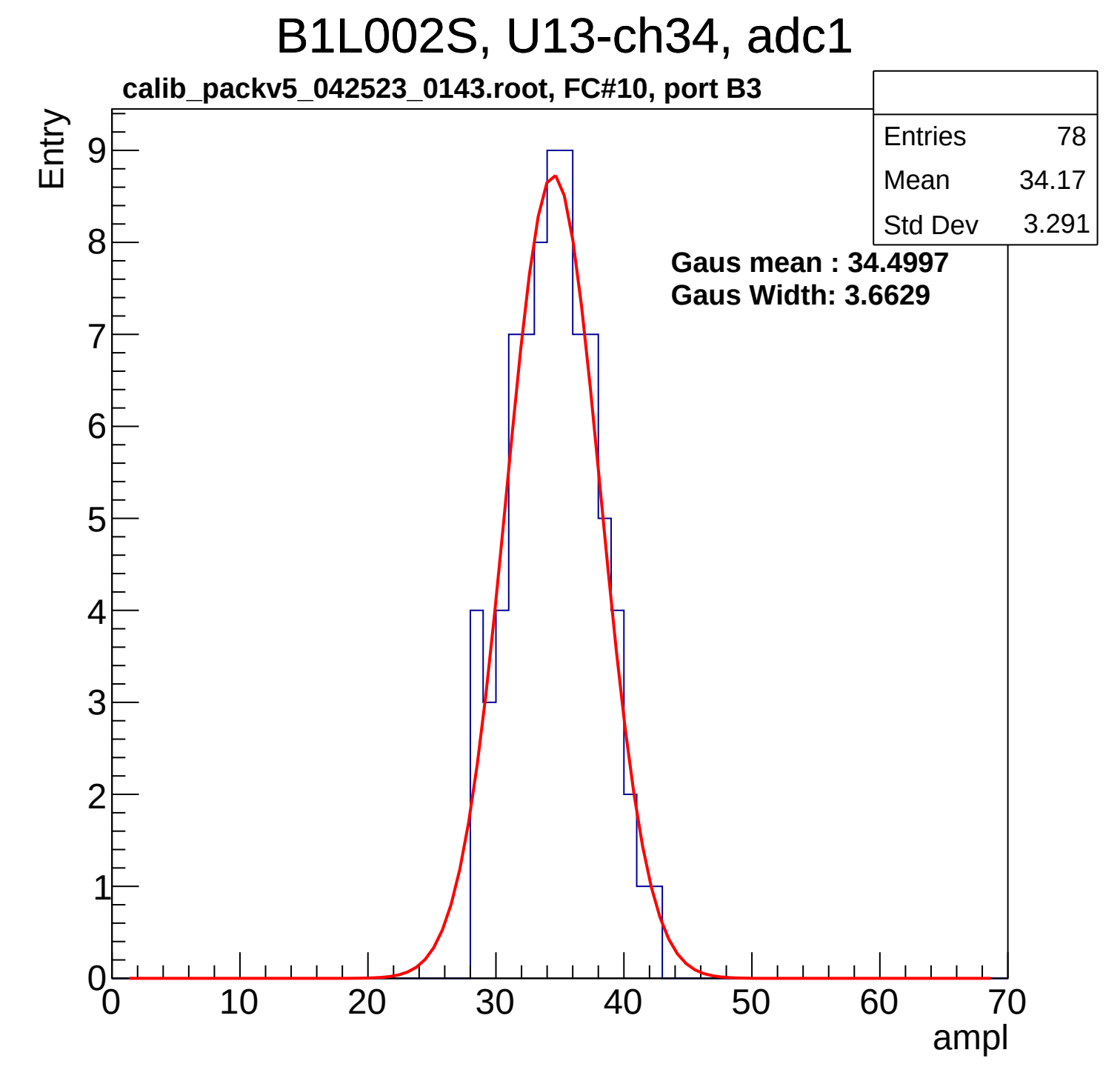
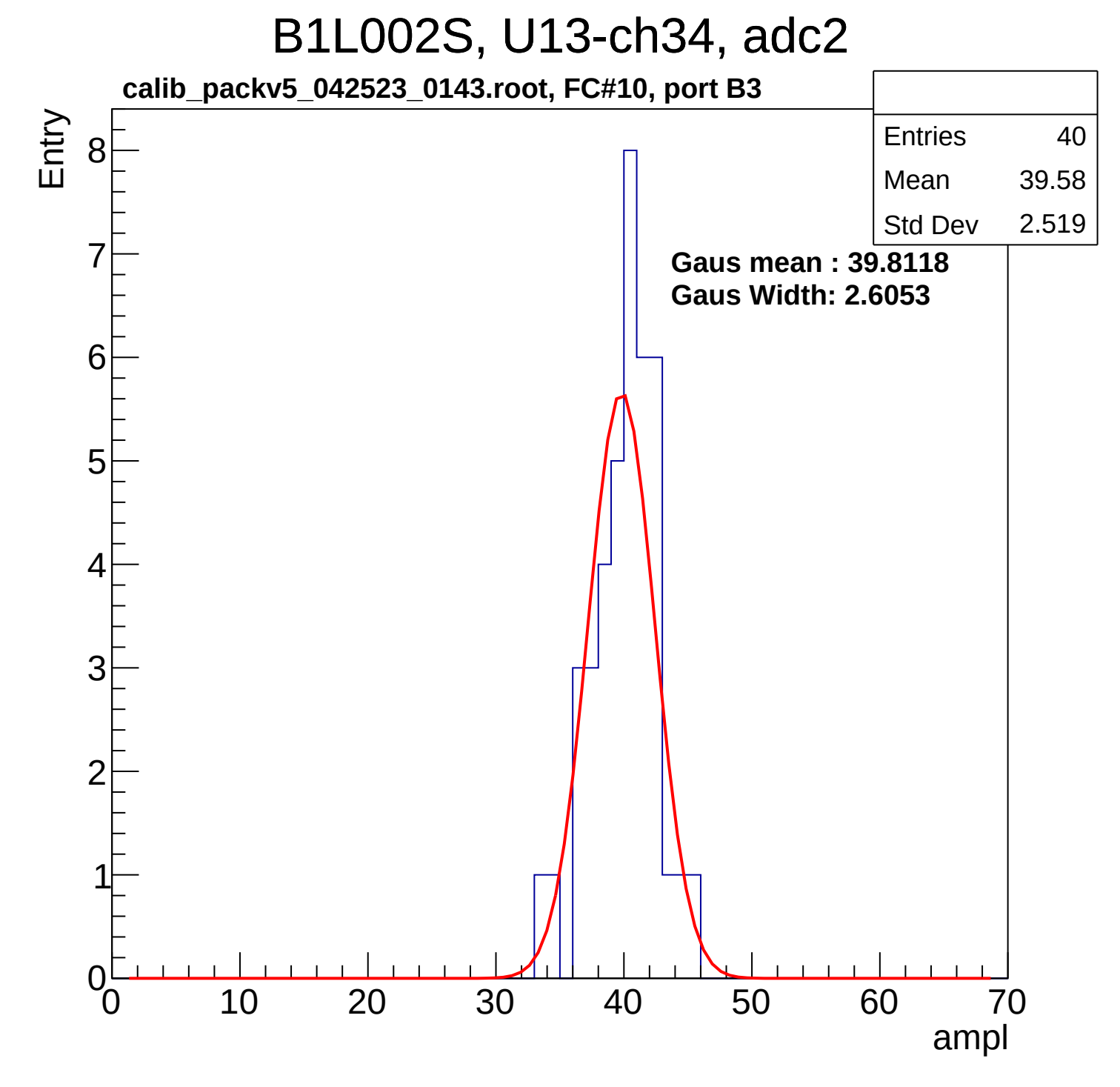
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.88
Std Dev 3.275
Gaus mean : 27.4149
Gaus Width: 3.3947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 34.17
Std Dev 3.291
Gaus mean : 34.4997
Gaus Width: 3.6629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 39.58
Std Dev 2.519
Gaus mean : 39.8118
Gaus Width: 2.6053
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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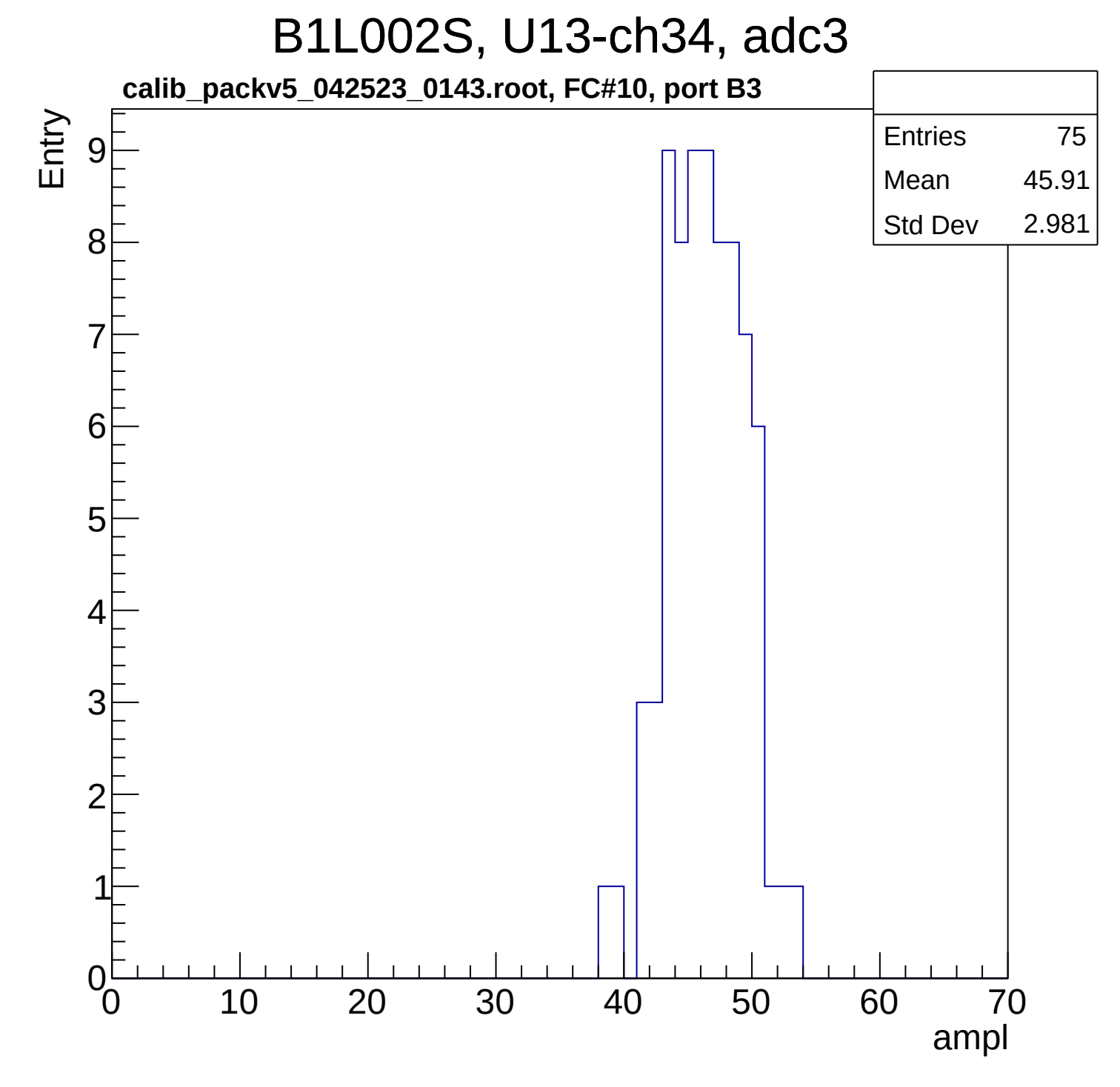
B1L002S, U13-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 45.91
Std Dev 2.981
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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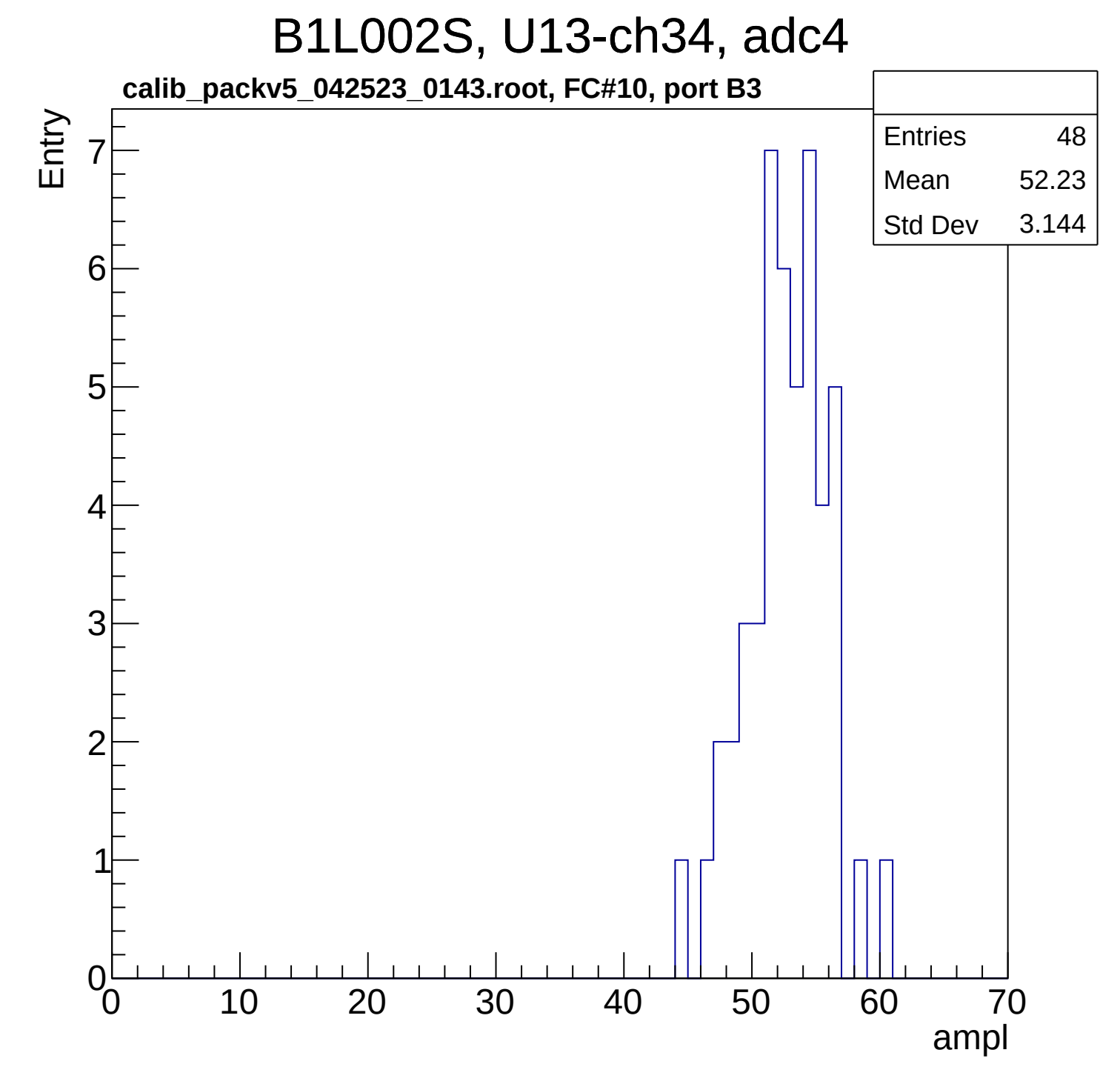
B1L002S, U13-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.23
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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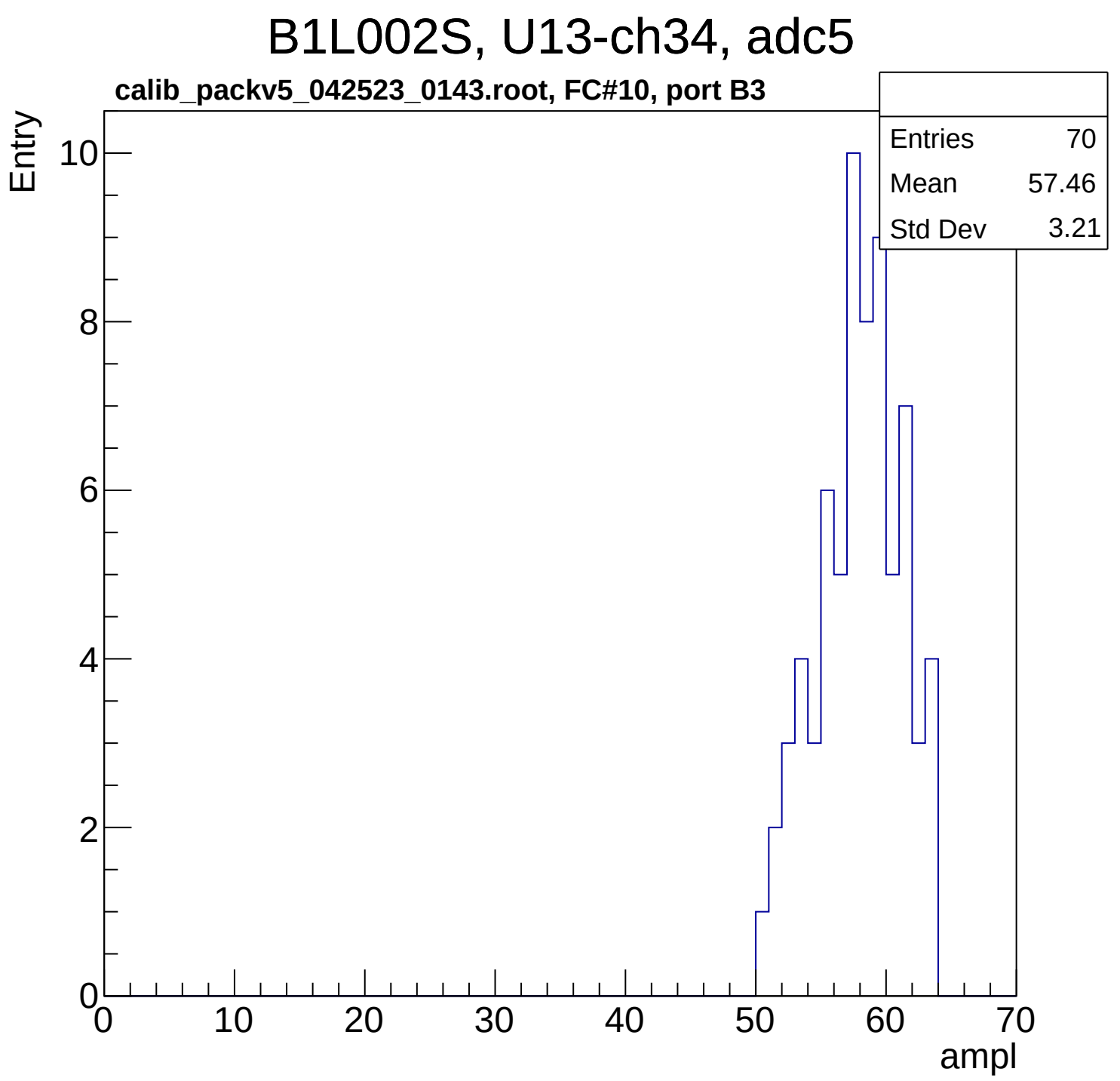
B1L002S, U13-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 57.46
Std Dev 3.21
Entry
0
2
4
6
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10
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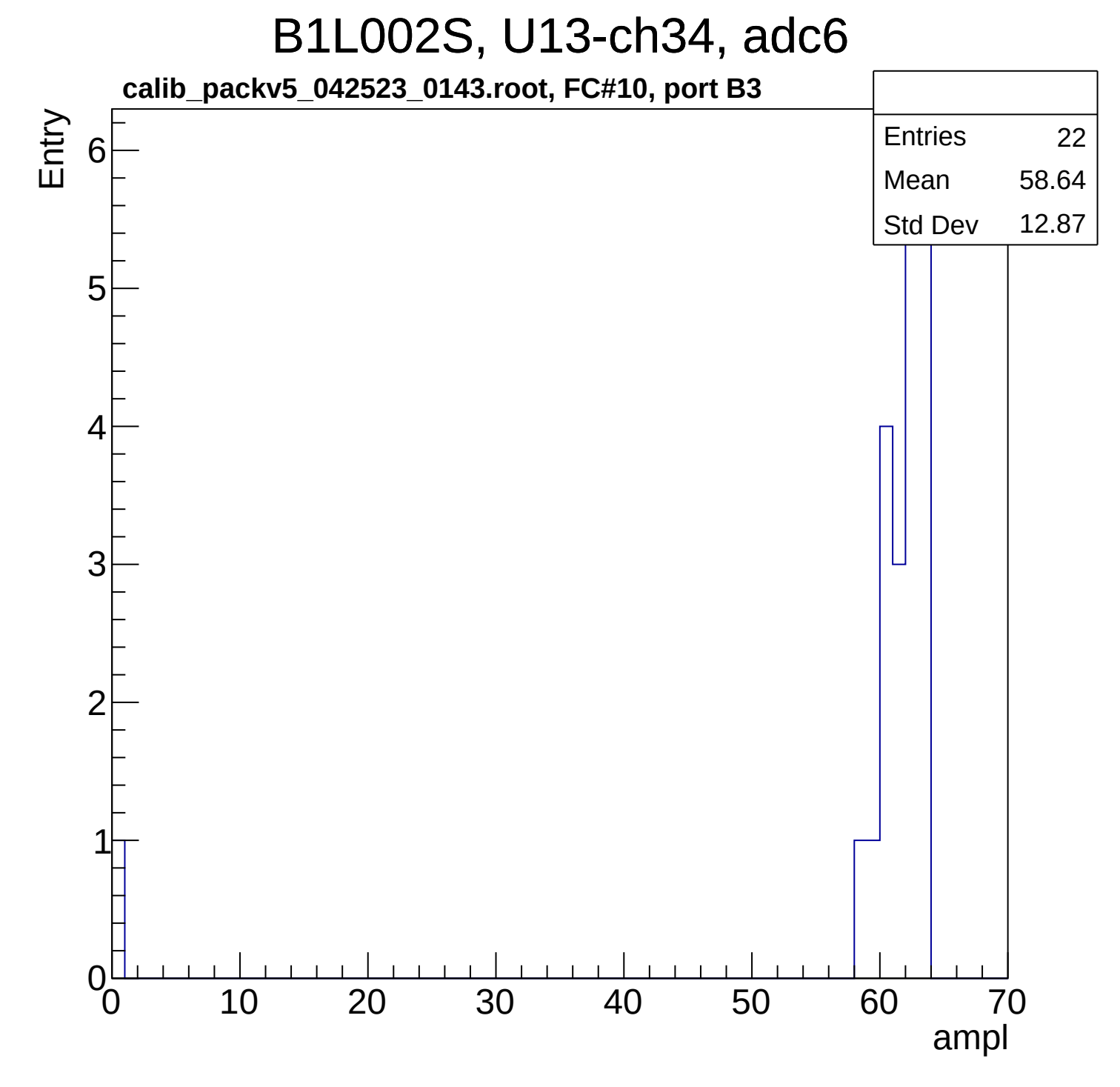
70
ampl


B1L002S, U13-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.64
Std Dev 12.87
Entry
0
1
2
3
4
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6
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10
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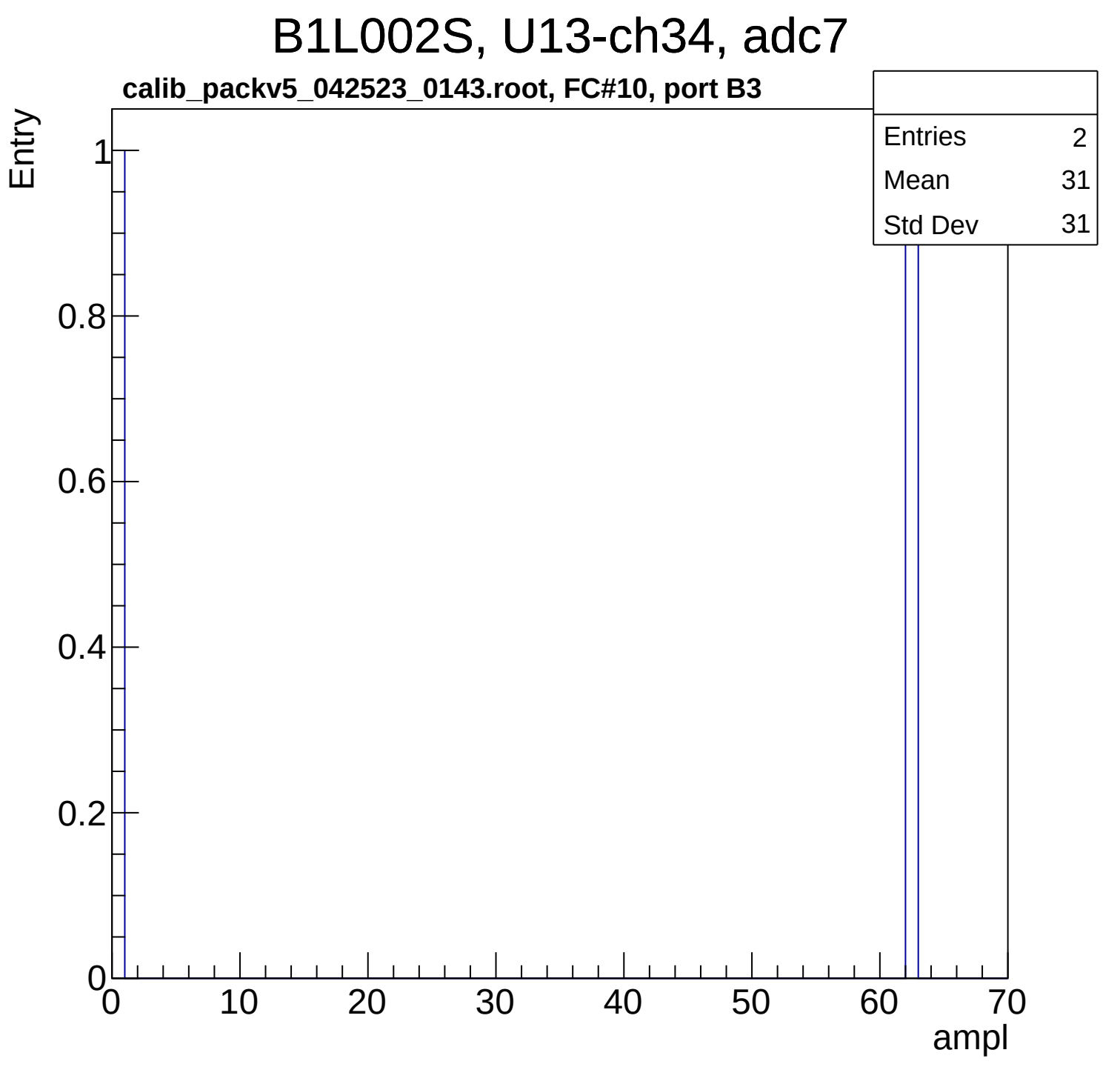
70
ampl


B1L002S, U13-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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0.8
0.6
0.4
0.2
0
0
10
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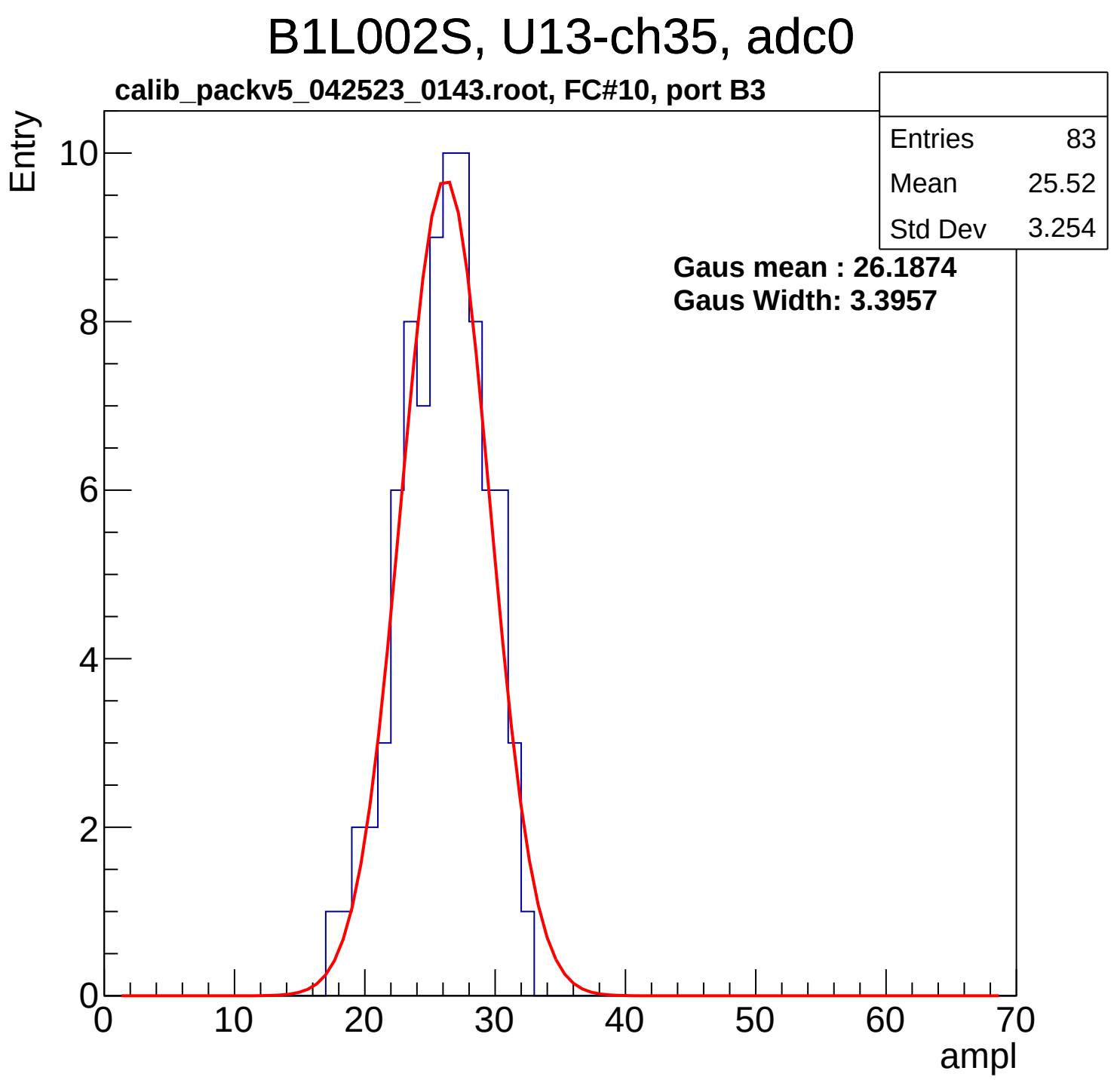
50
60
70
ampl


B1L002S, U13-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 25.52
Std Dev 3.254
Gaus mean : 26.1874
Gaus Width: 3.3957
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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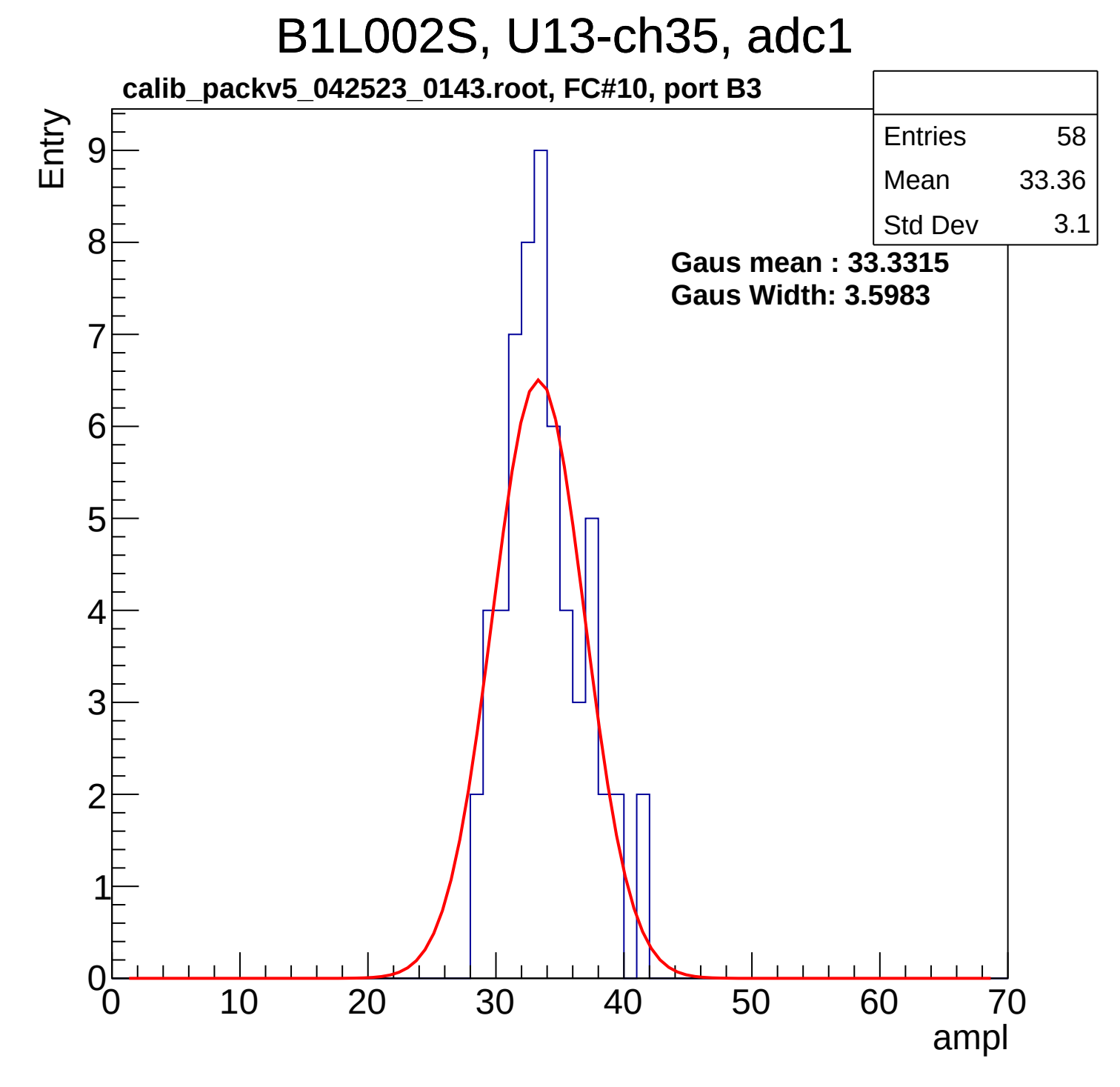
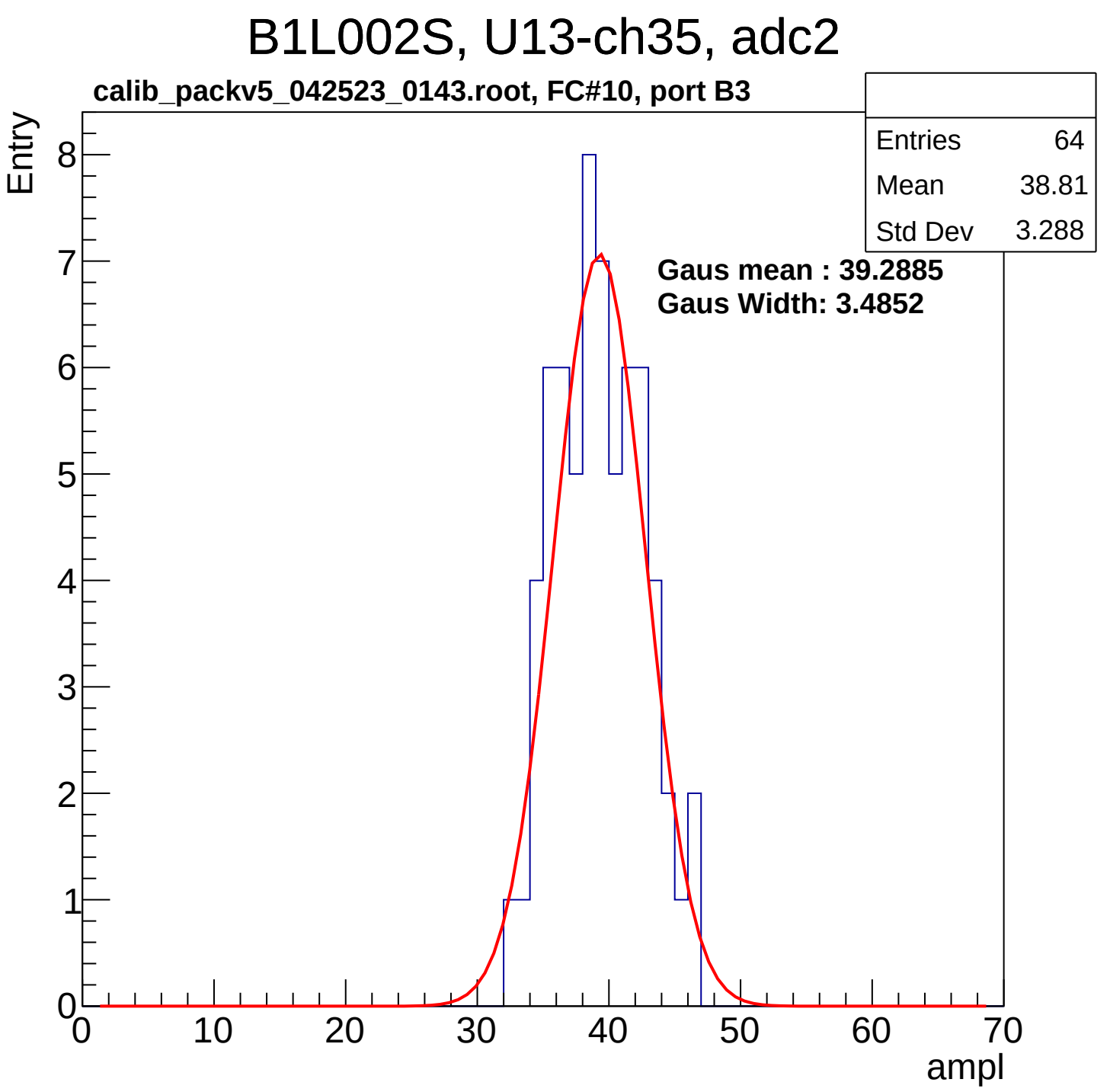

B1L002S, U13-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.36
Std Dev 3.1
Gaus mean : 33.3315
Gaus Width: 3.5983
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 38.81
Std Dev 3.288
Gaus mean : 39.2885
Gaus Width: 3.4852
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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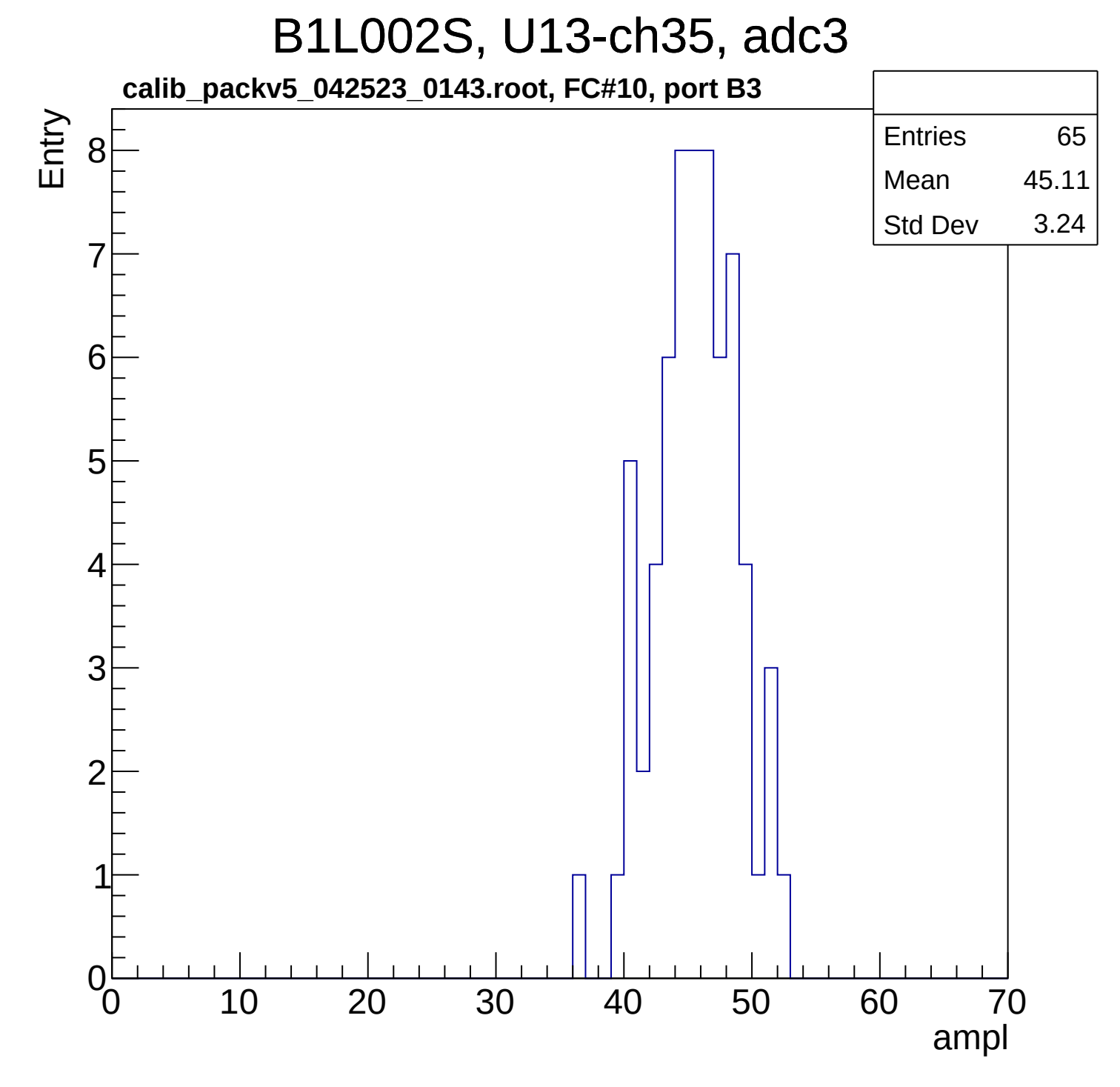
B1L002S, U13-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.11
Std Dev 3.24
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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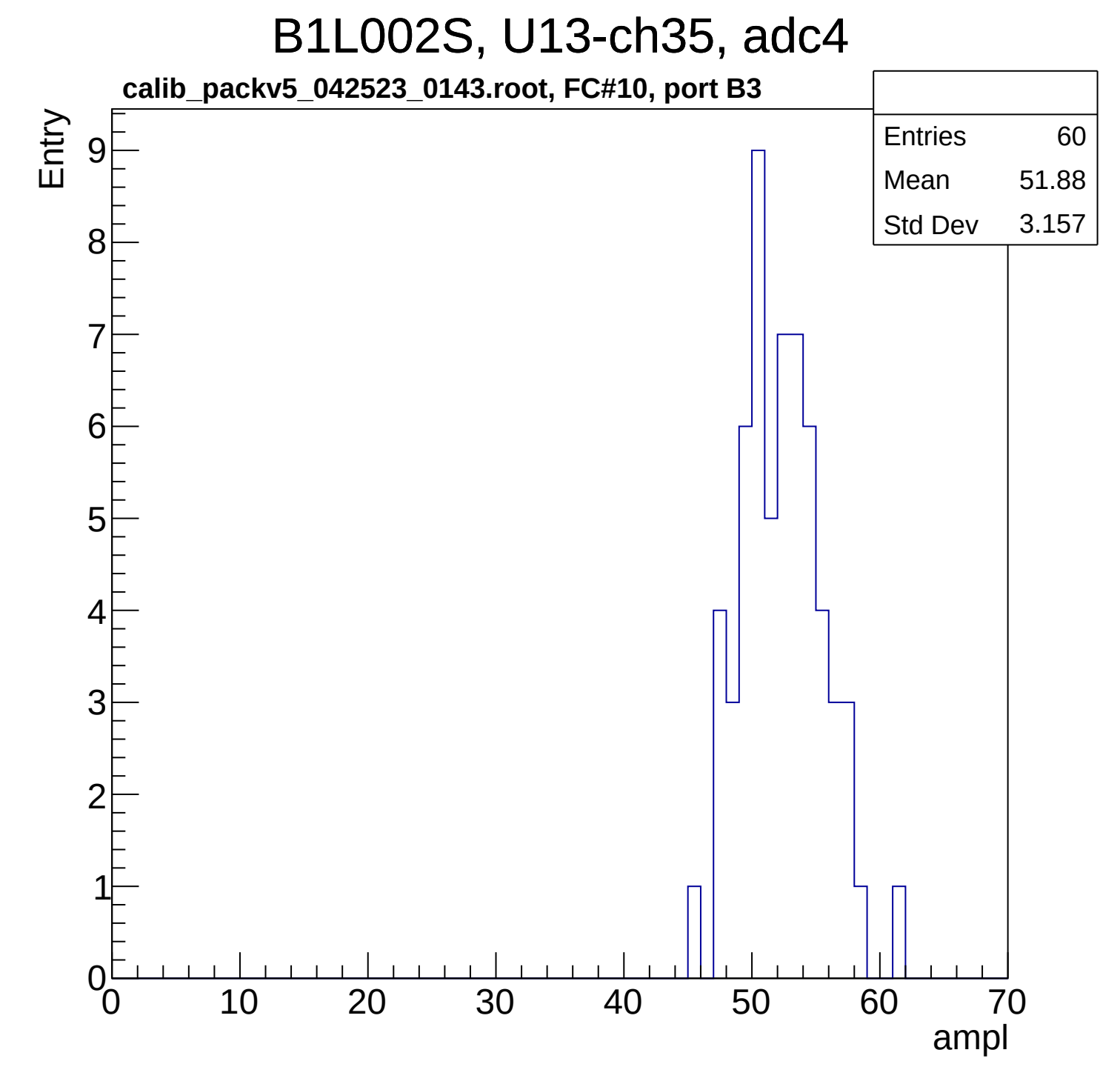
B1L002S, U13-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.88
Std Dev 3.157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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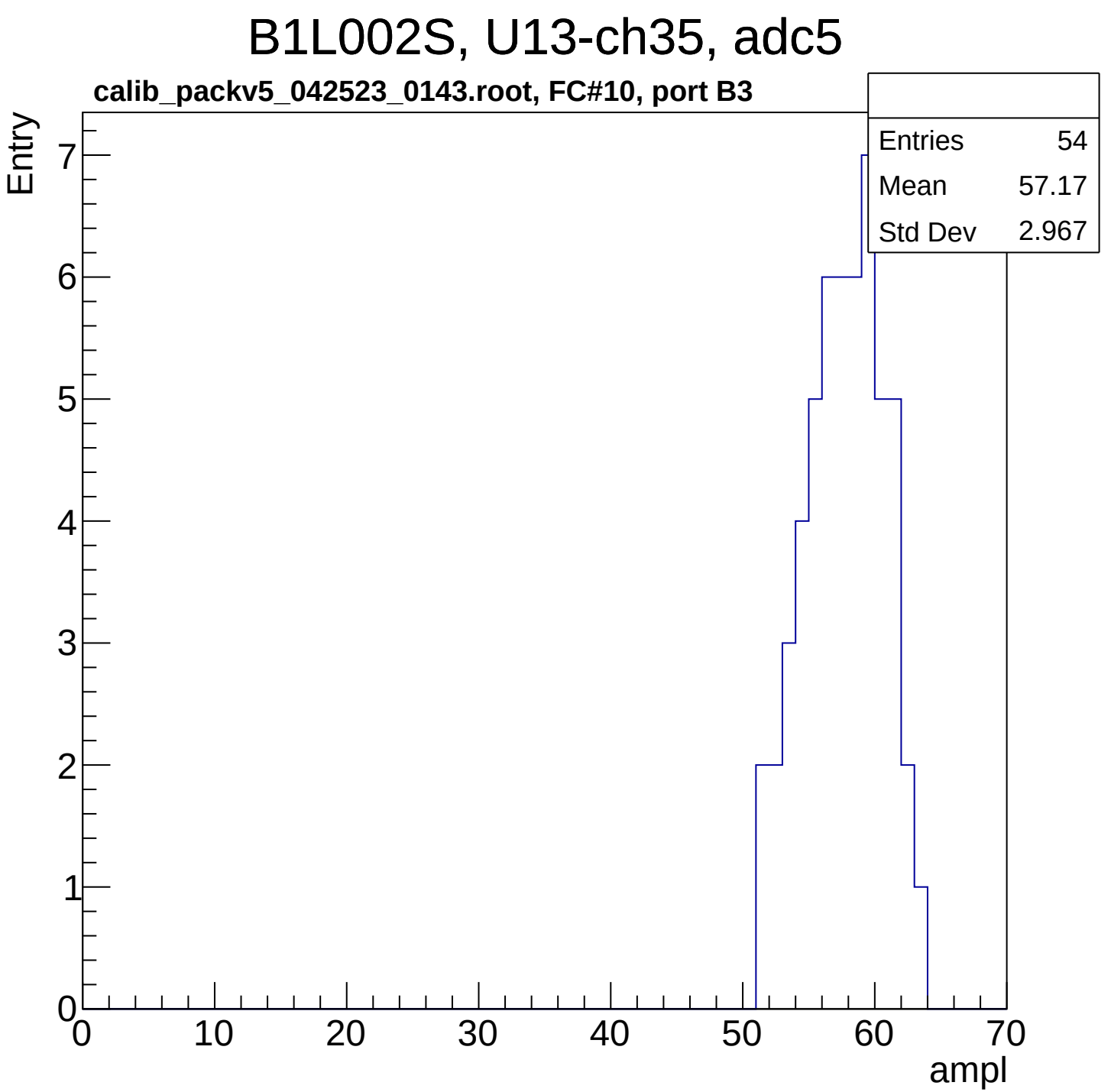
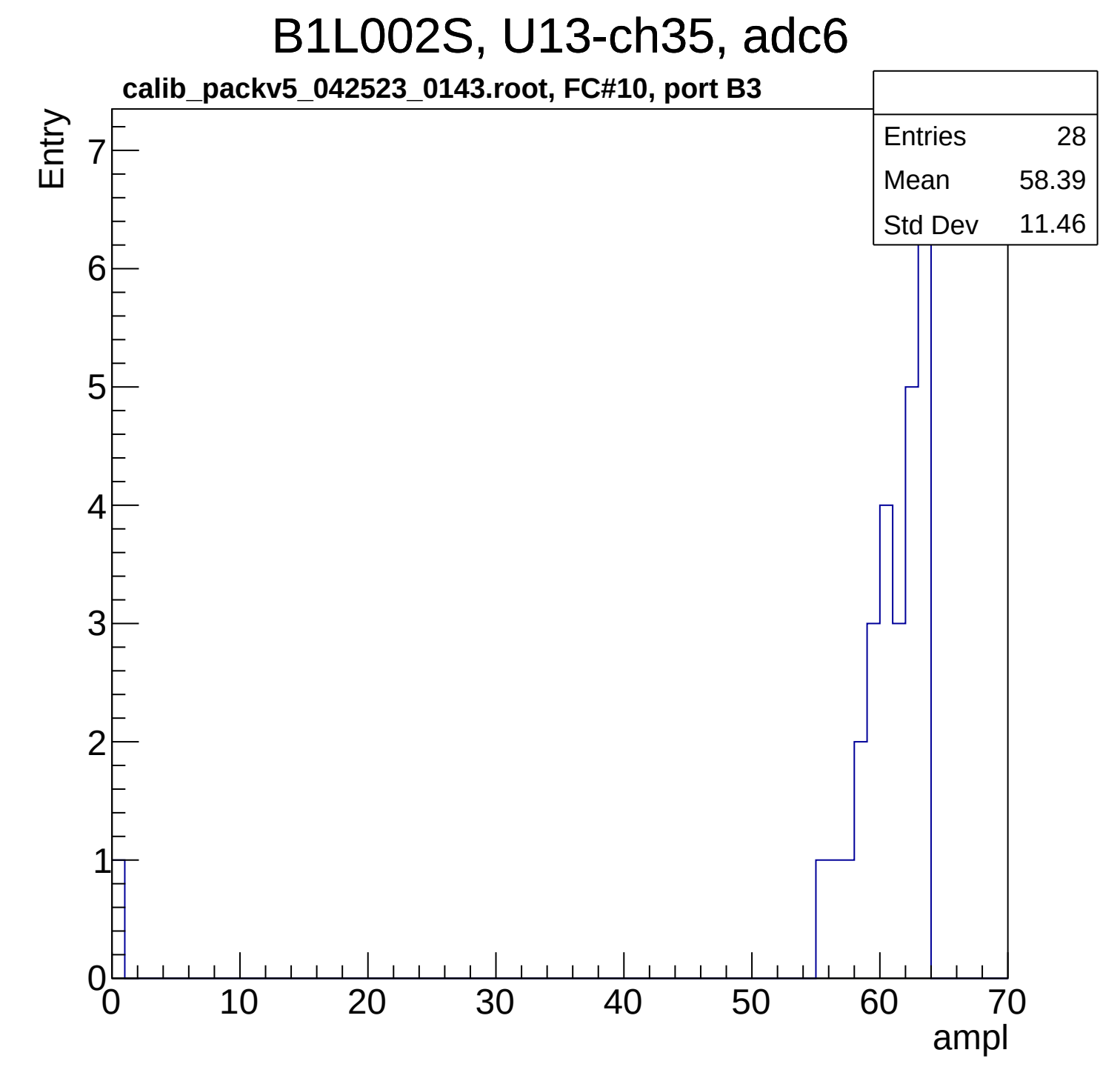
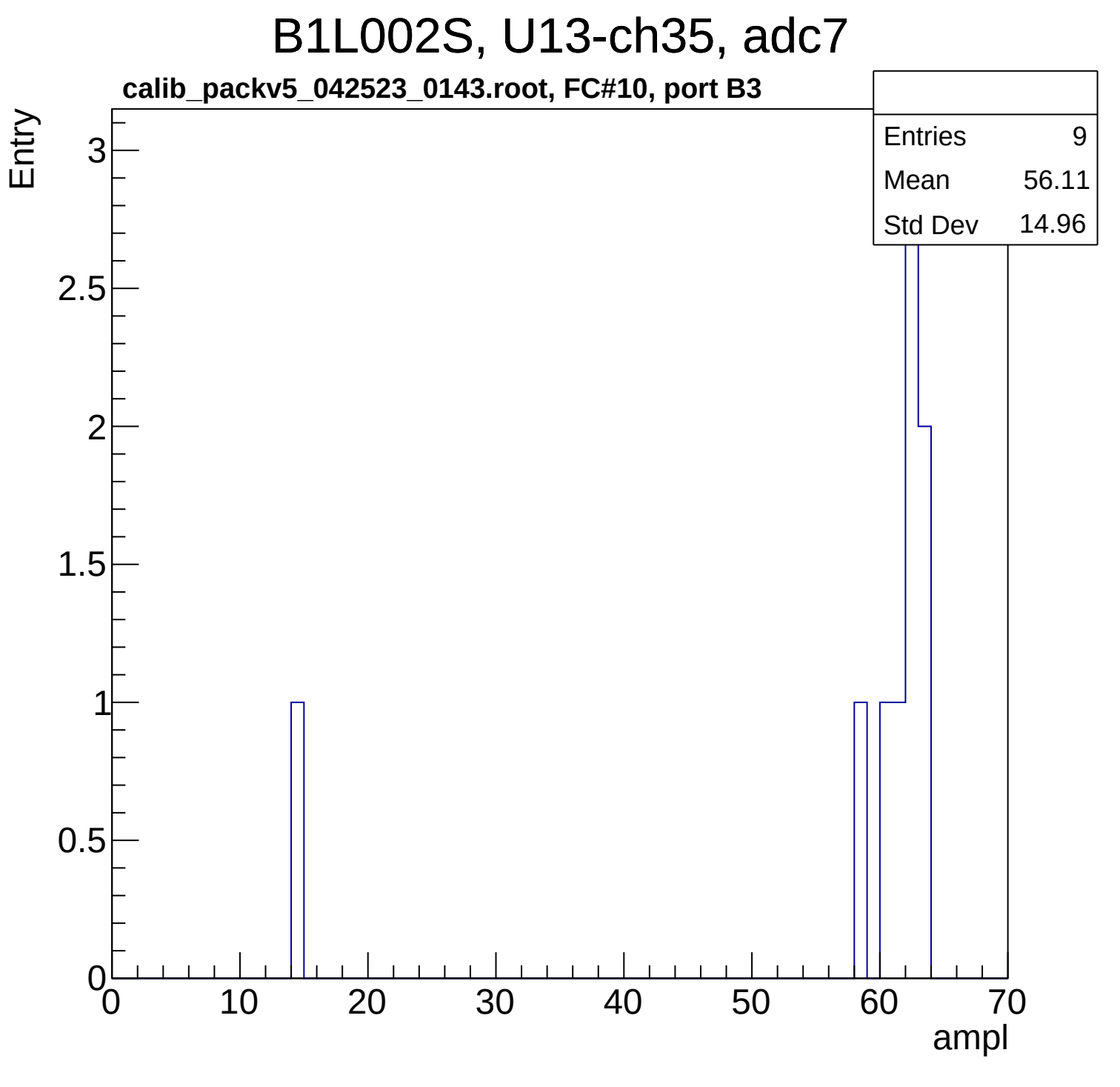
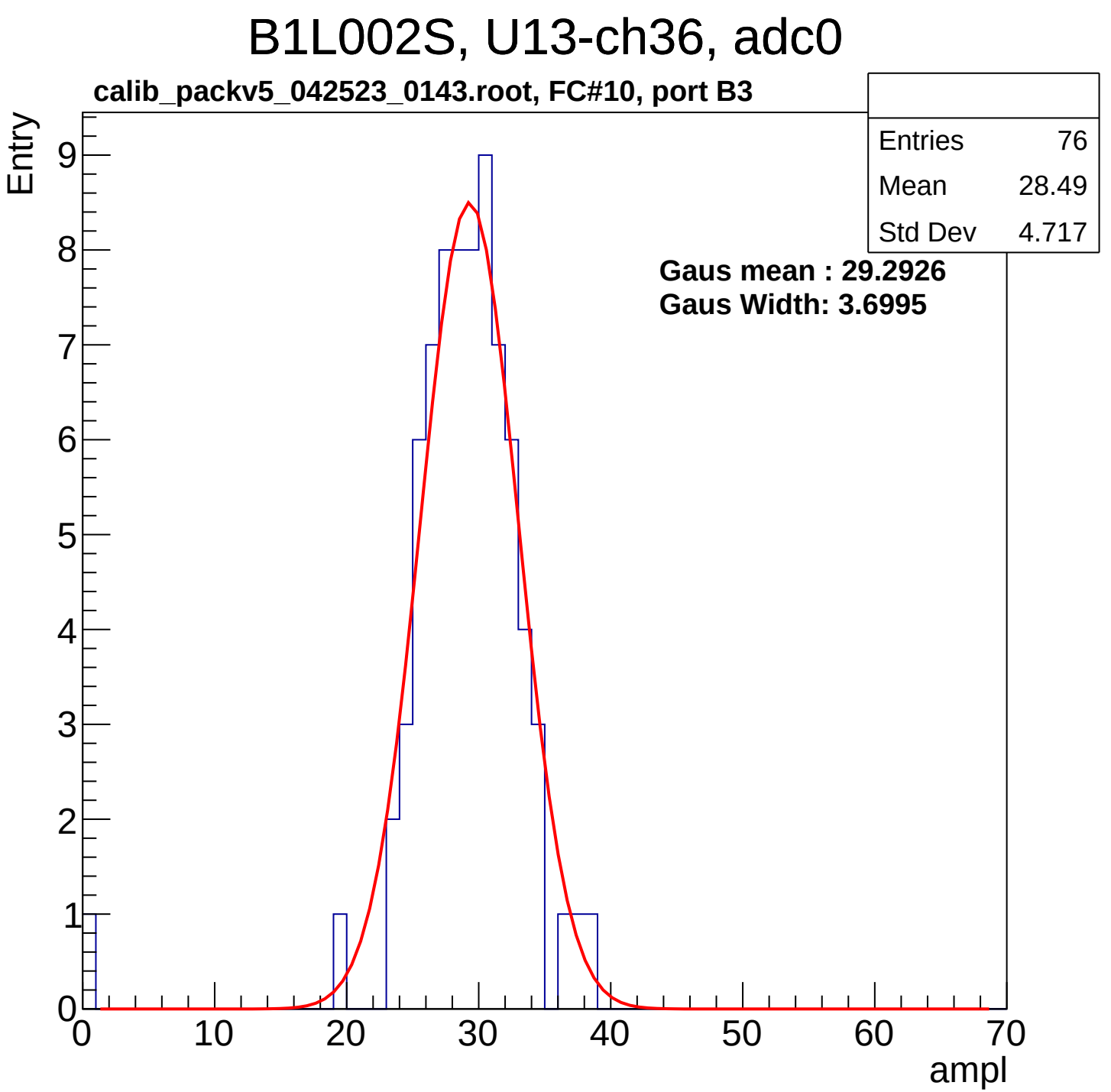

B1L002S, U13-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.17
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.39
Std Dev 11.46
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 56.11
Std Dev 14.96
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.49
Std Dev 4.717
Gaus mean : 29.2926
Gaus Width: 3.6995
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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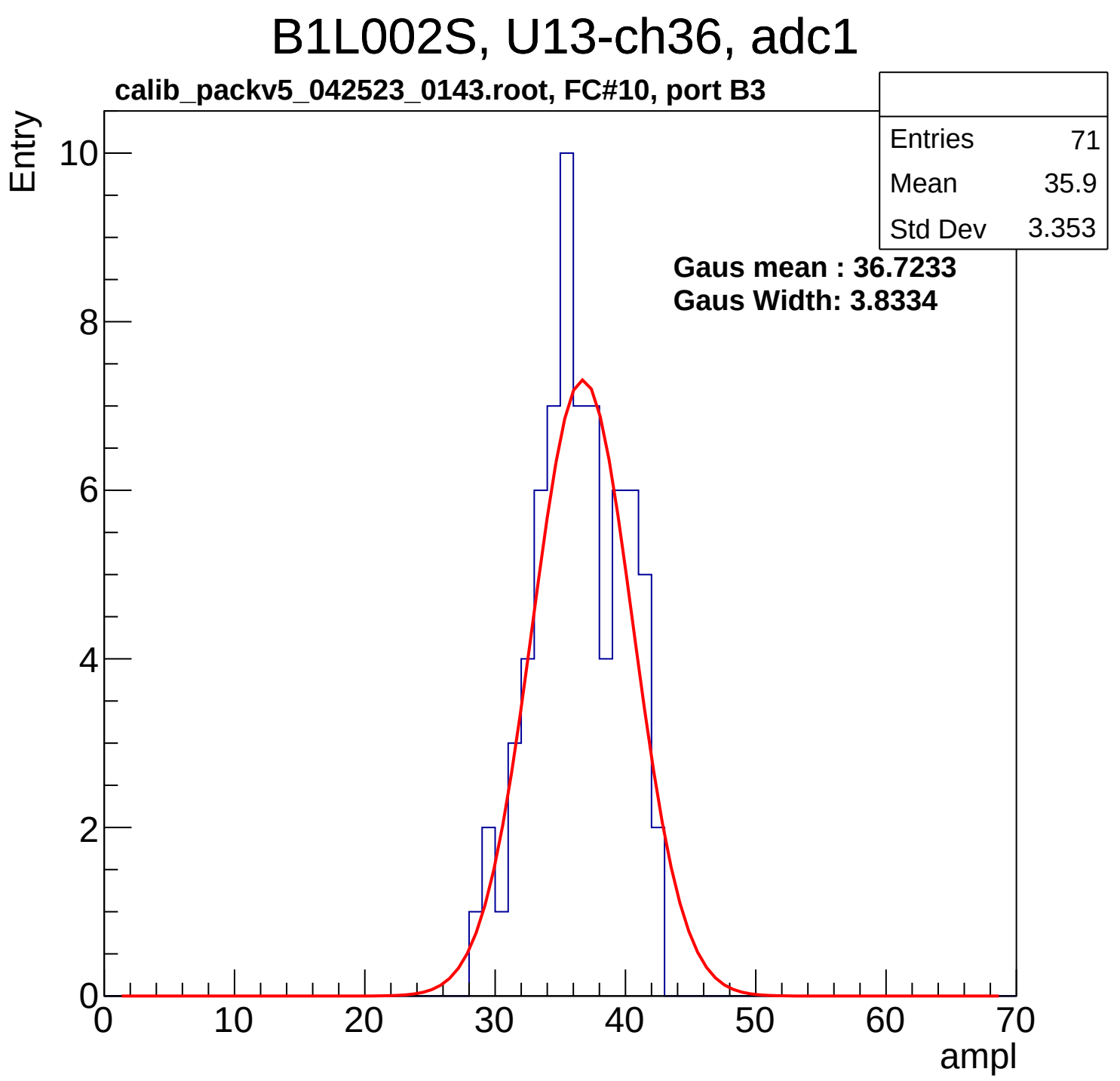
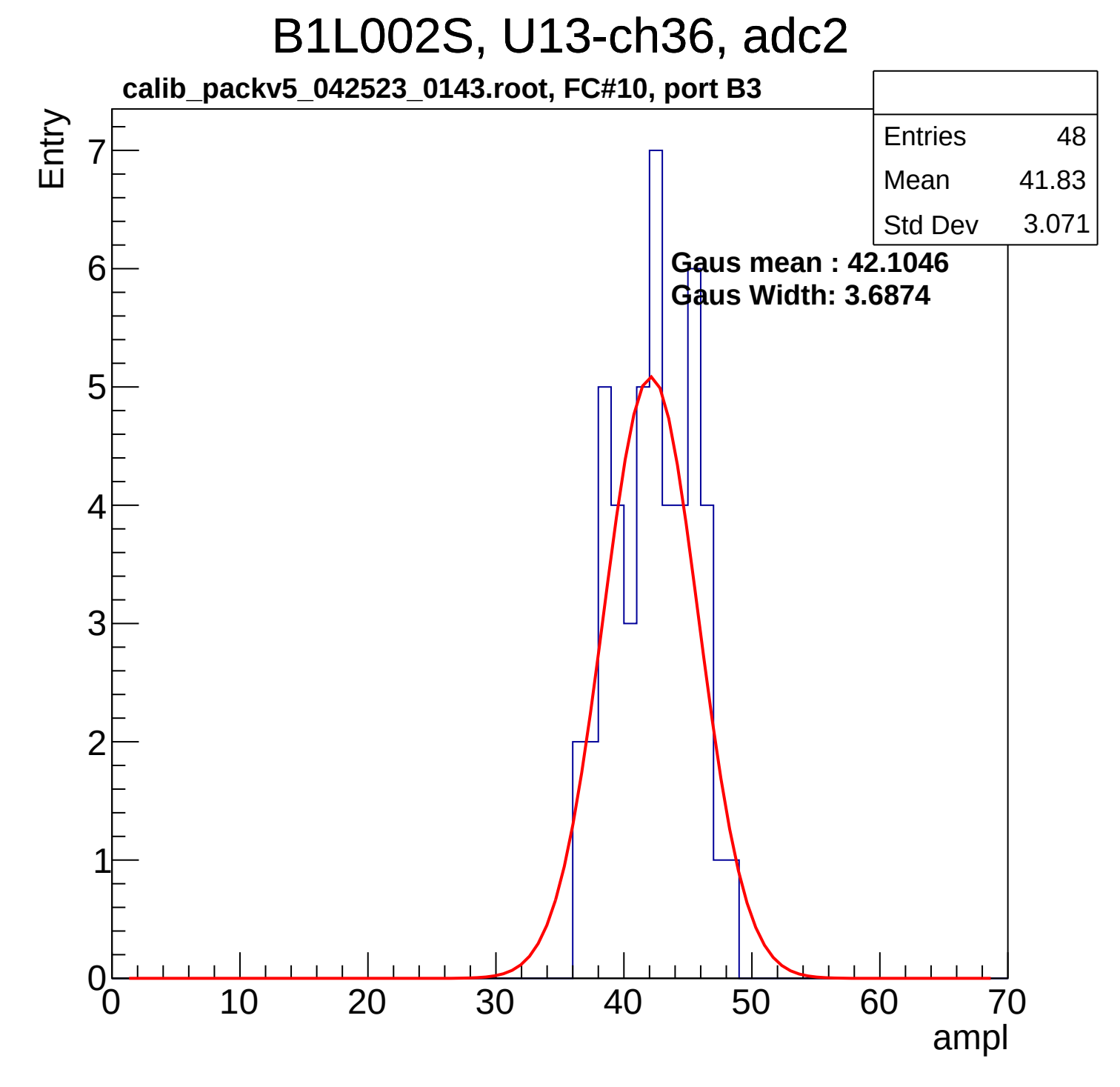
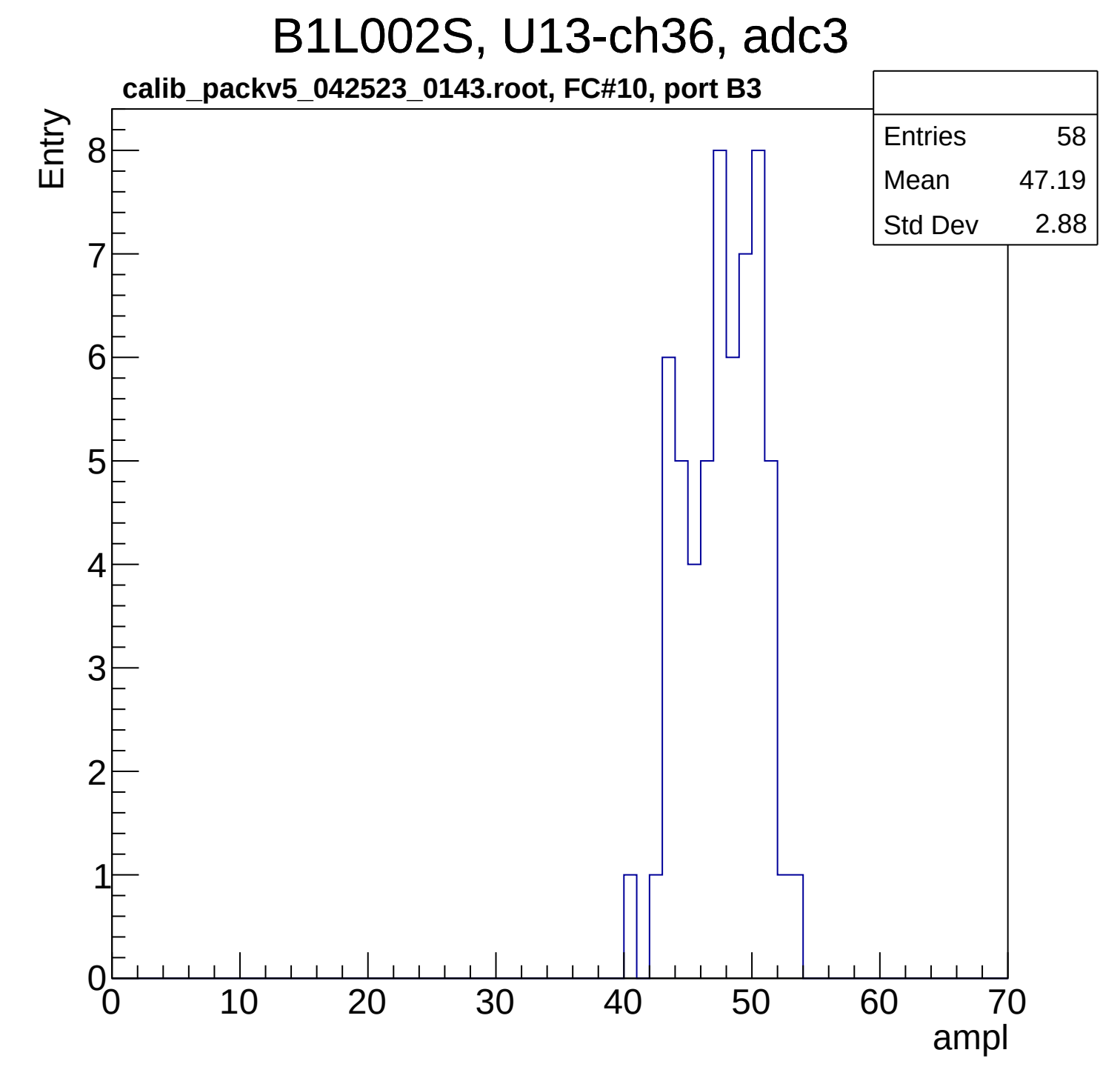
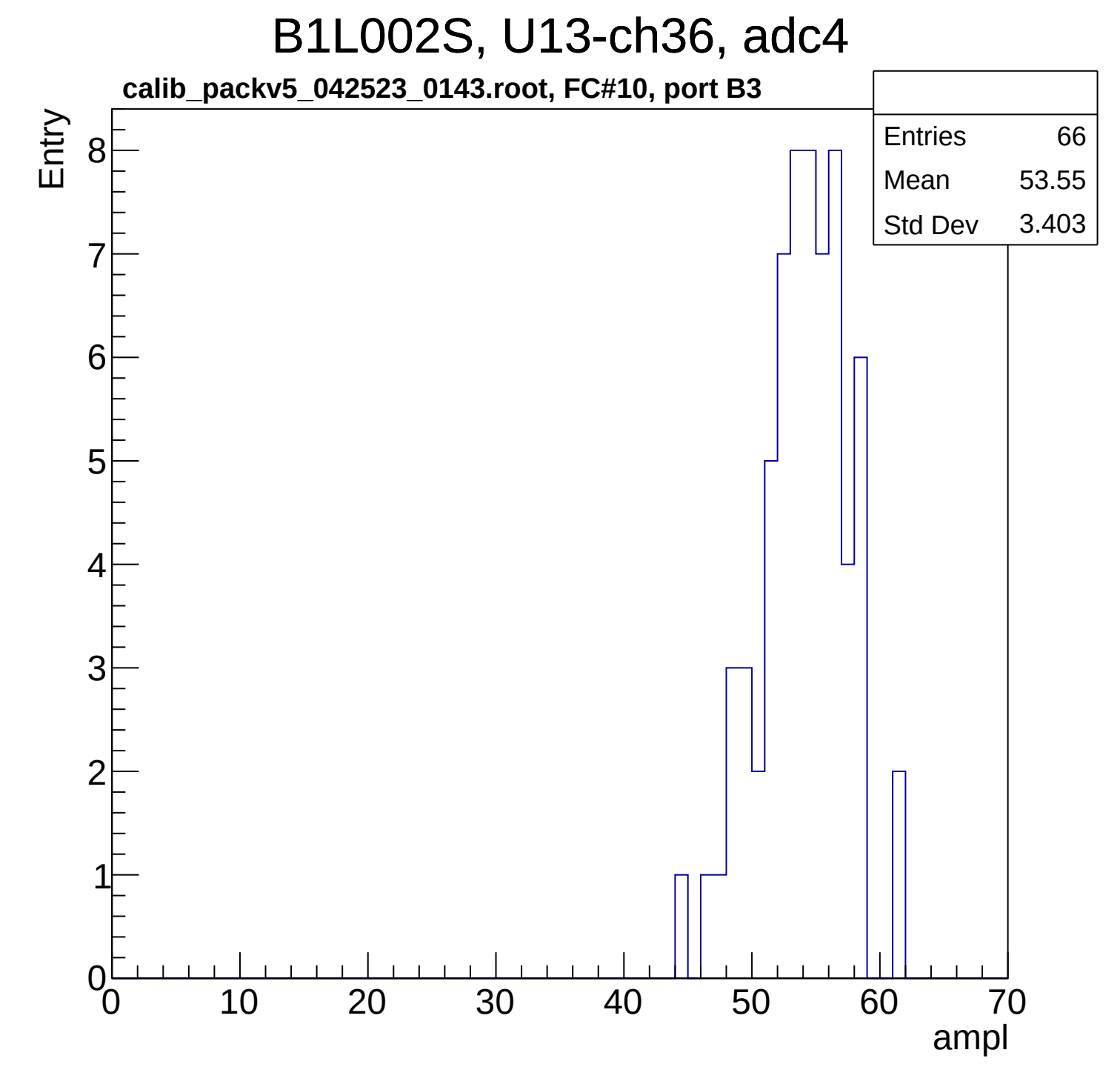

B1L002S, U13-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.9
Std Dev 3.353
Gaus mean : 36.7233
Gaus Width: 3.8334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.83
Std Dev 3.071
Gaus mean : 42.1046
Gaus Width: 3.6874
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.19
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.55
Std Dev 3.403
Entry
ampl
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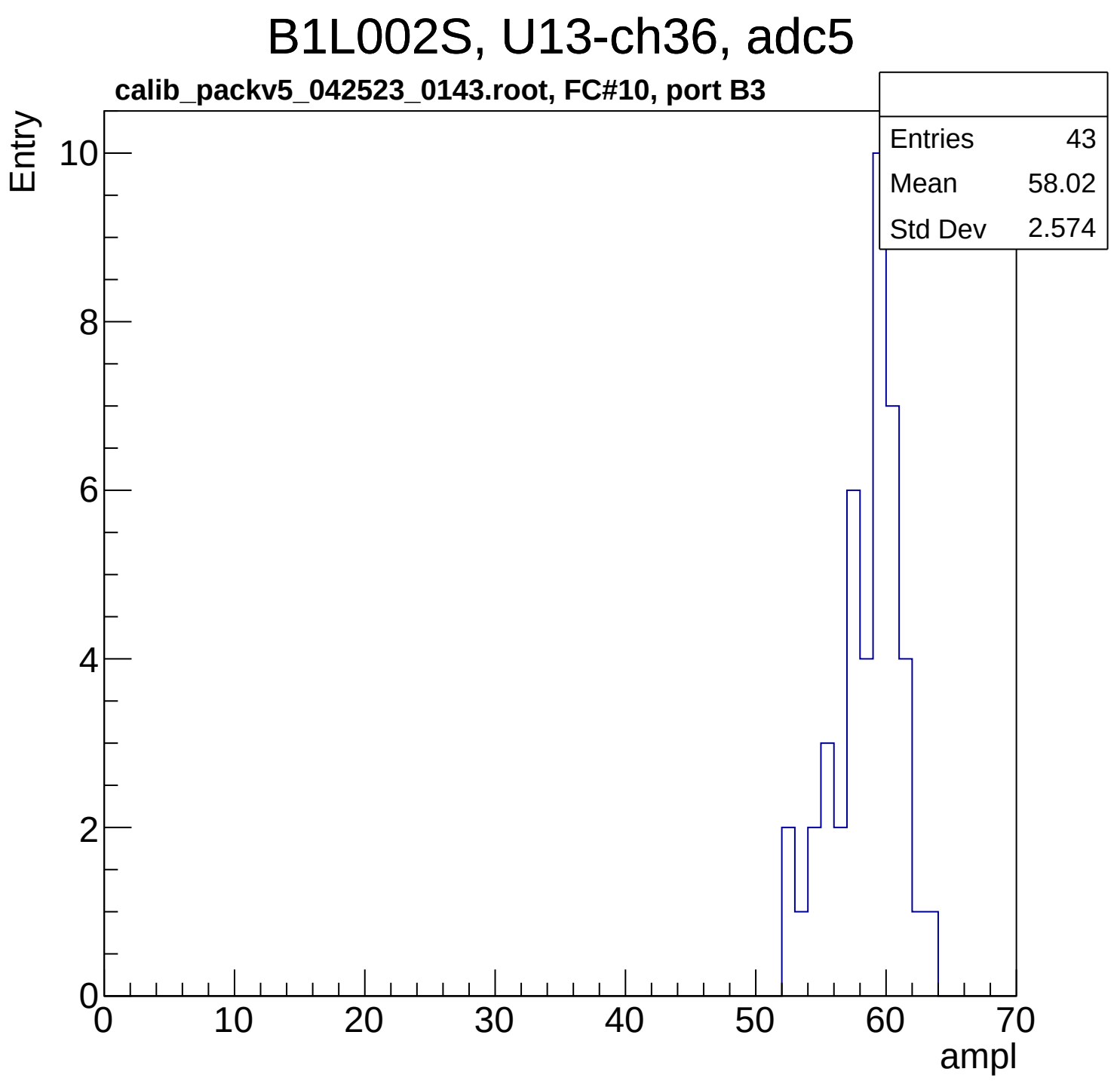
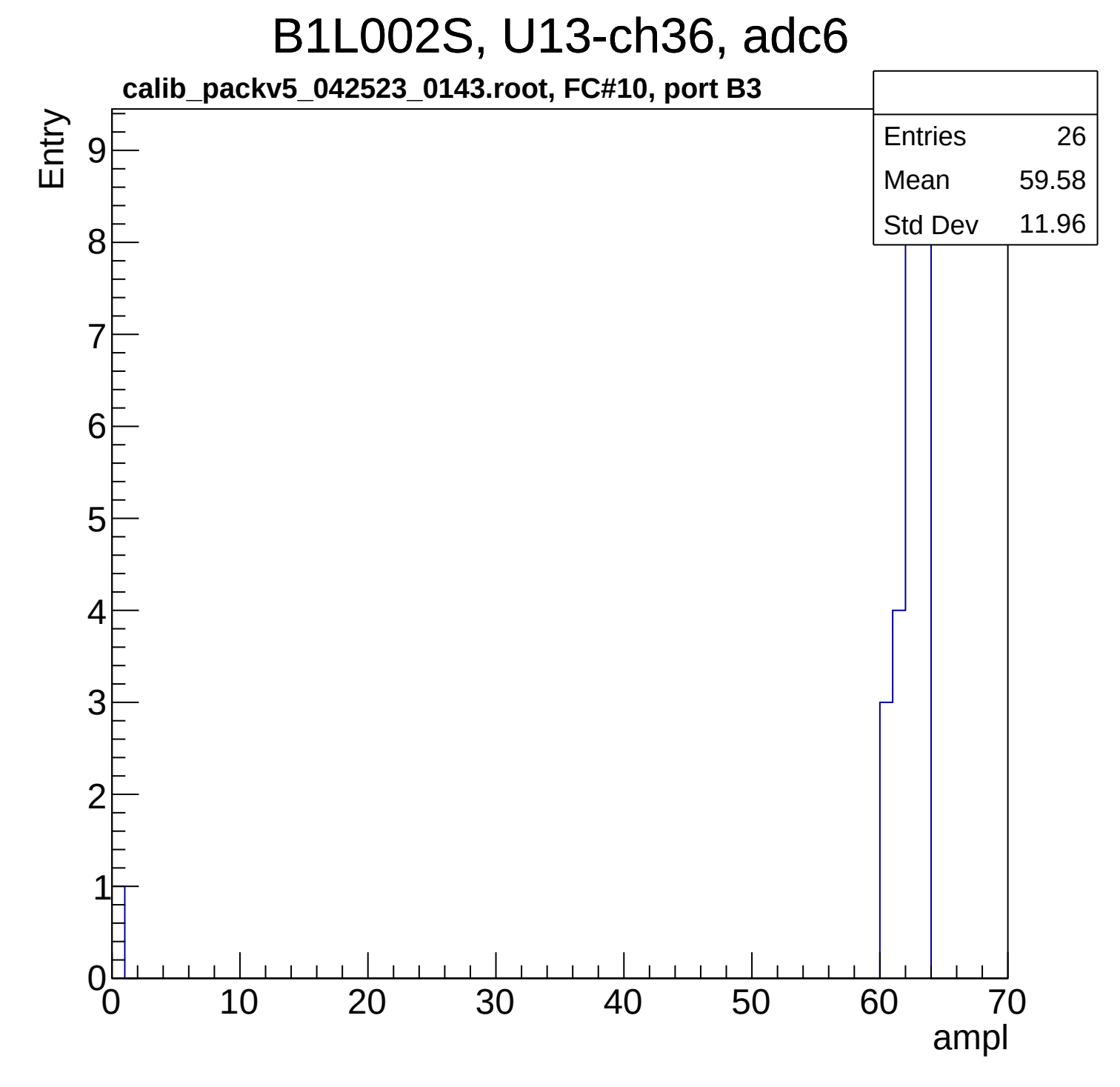
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.02
Std Dev 2.574
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.58
Std Dev 11.96
Entry
ampl
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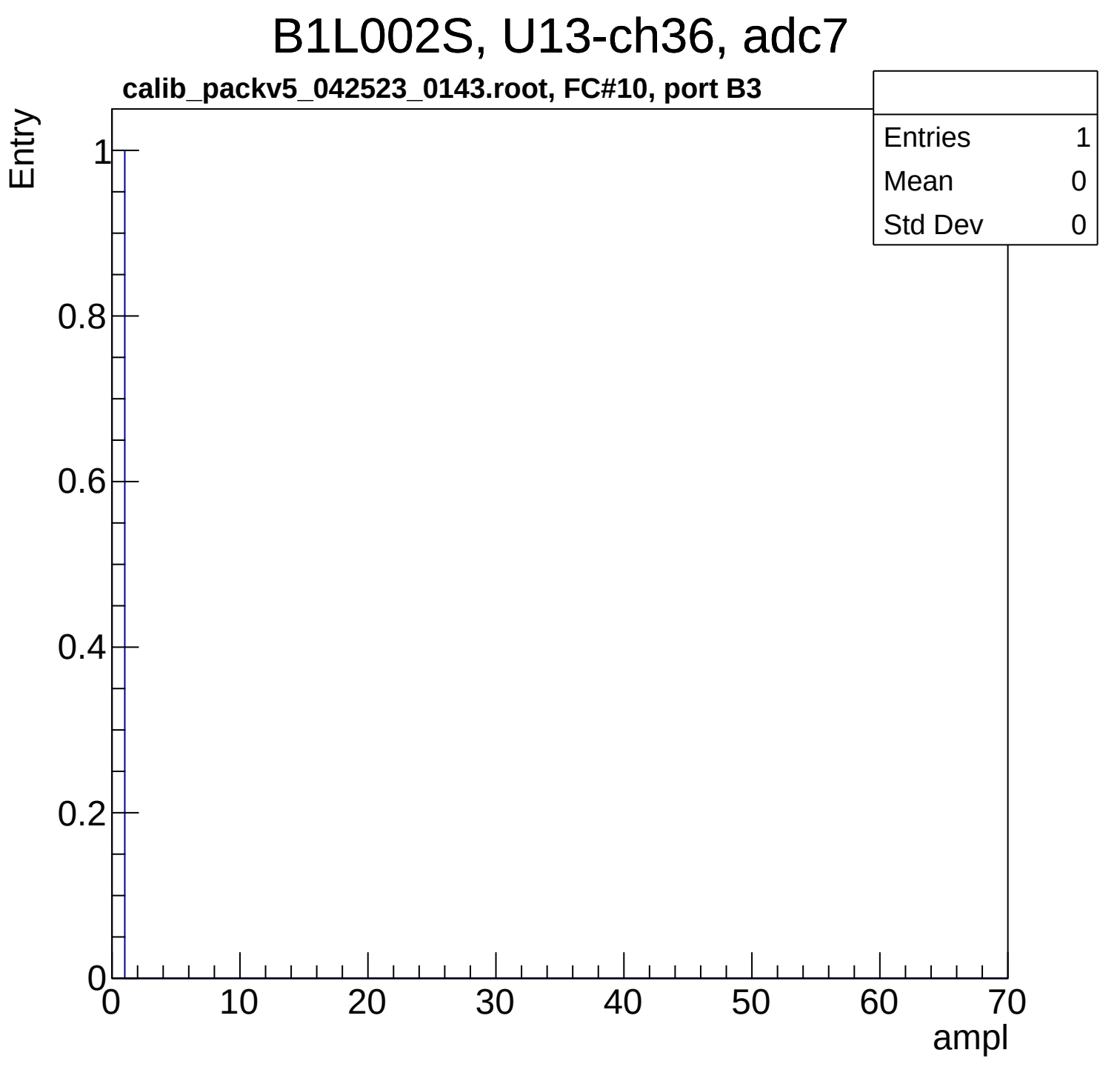
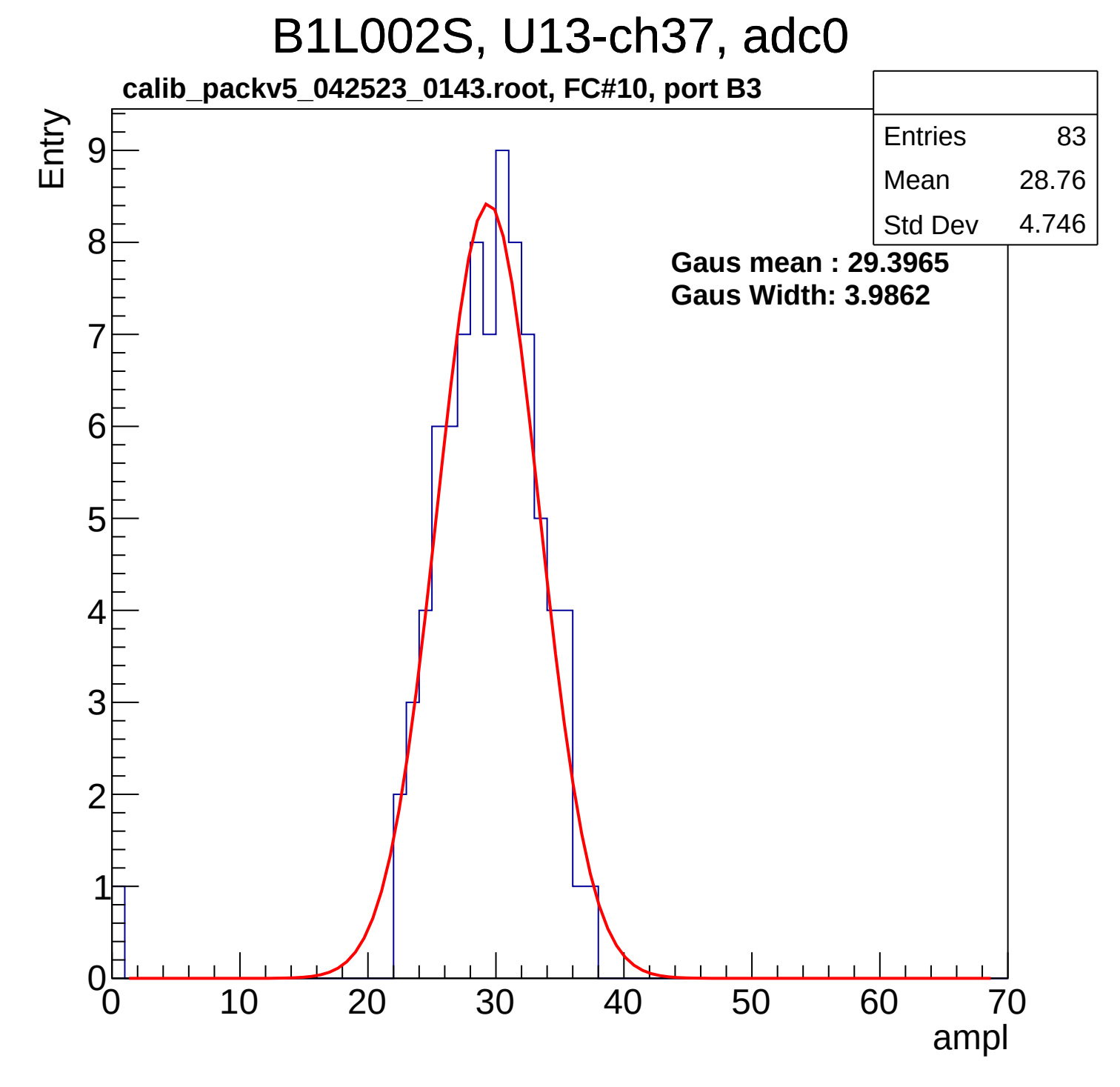
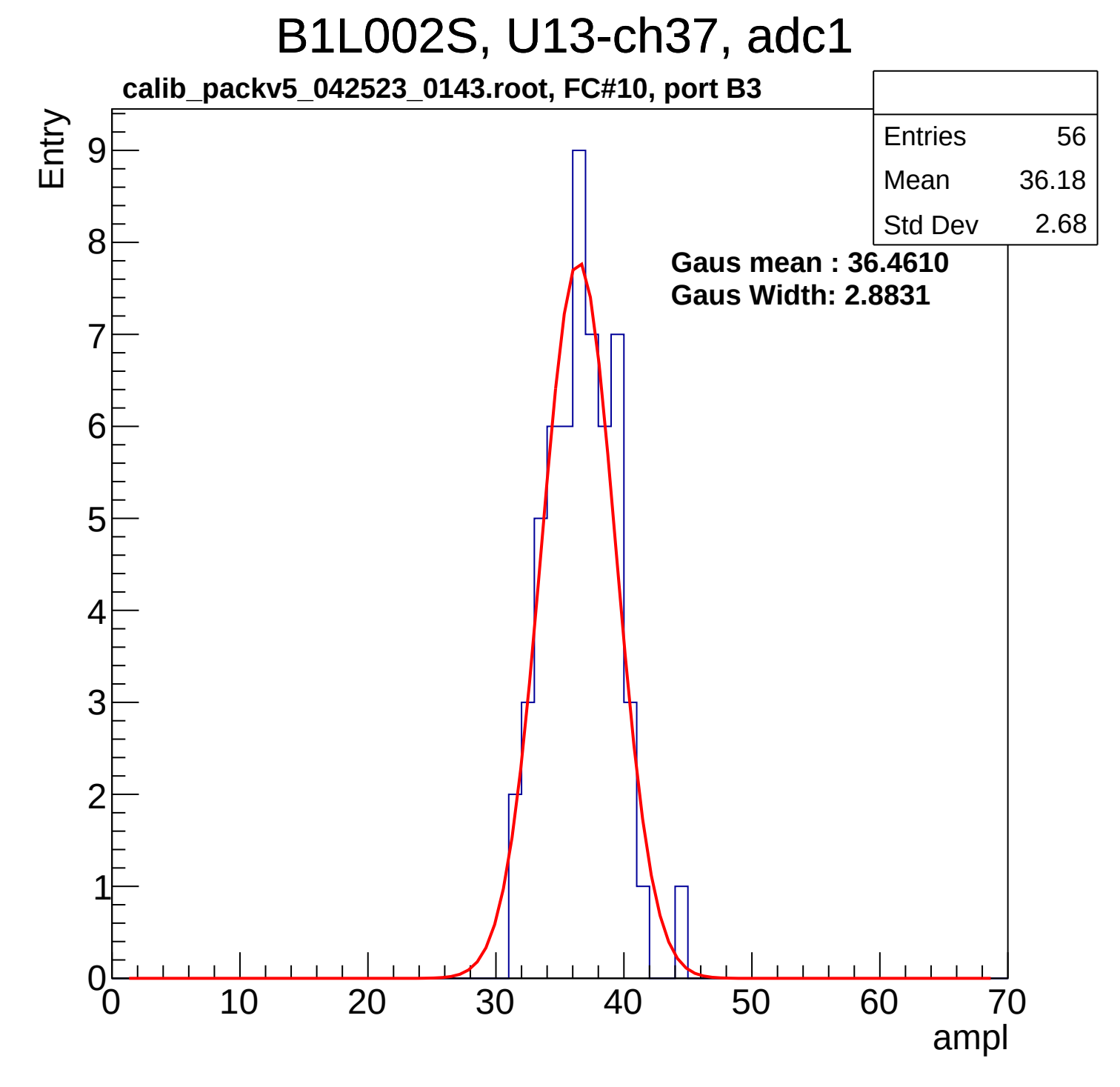
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 28.76
Std Dev 4.746
Gaus mean : 29.3965
Gaus Width: 3.9862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 36.18
Std Dev 2.68
Gaus mean : 36.4610
Gaus Width: 2.8831
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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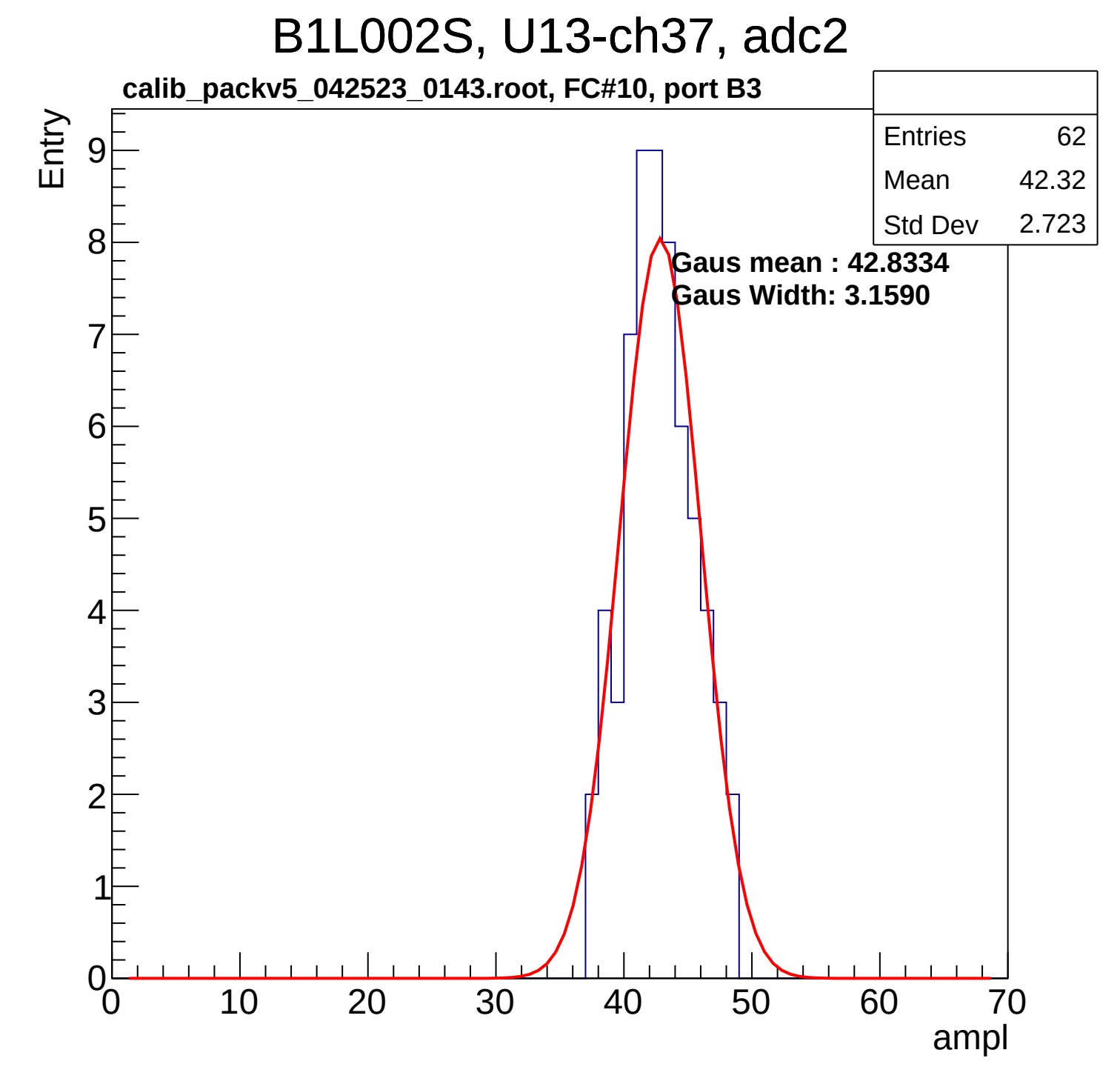
B1L002S, U13-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.32
Std Dev 2.723
Gaus mean : 42.8334
Gaus Width: 3.1590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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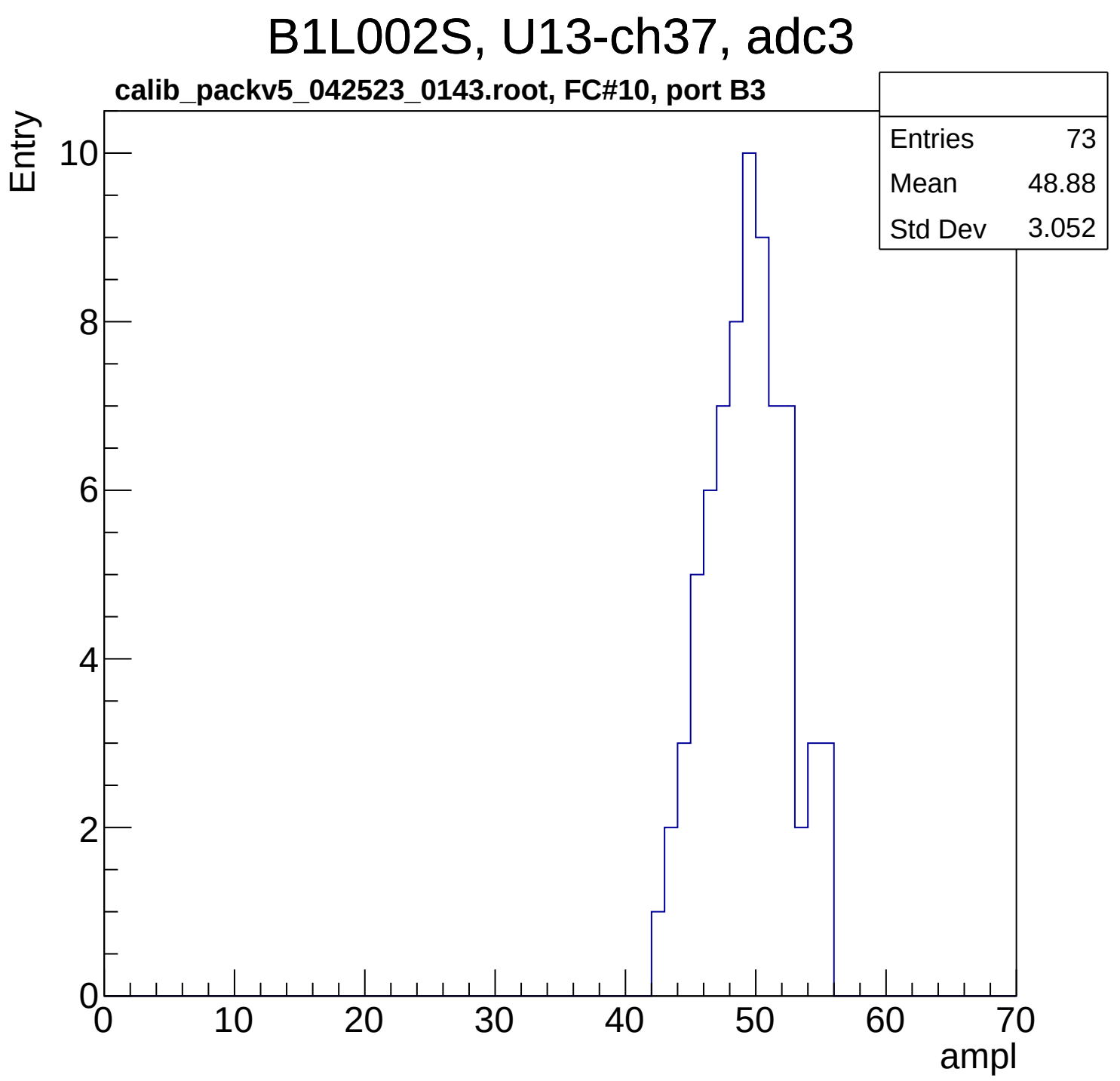
60
70


B1L002S, U13-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.88
Std Dev 3.052
Entry
0
2
4
6
8
10
0
10
20
30
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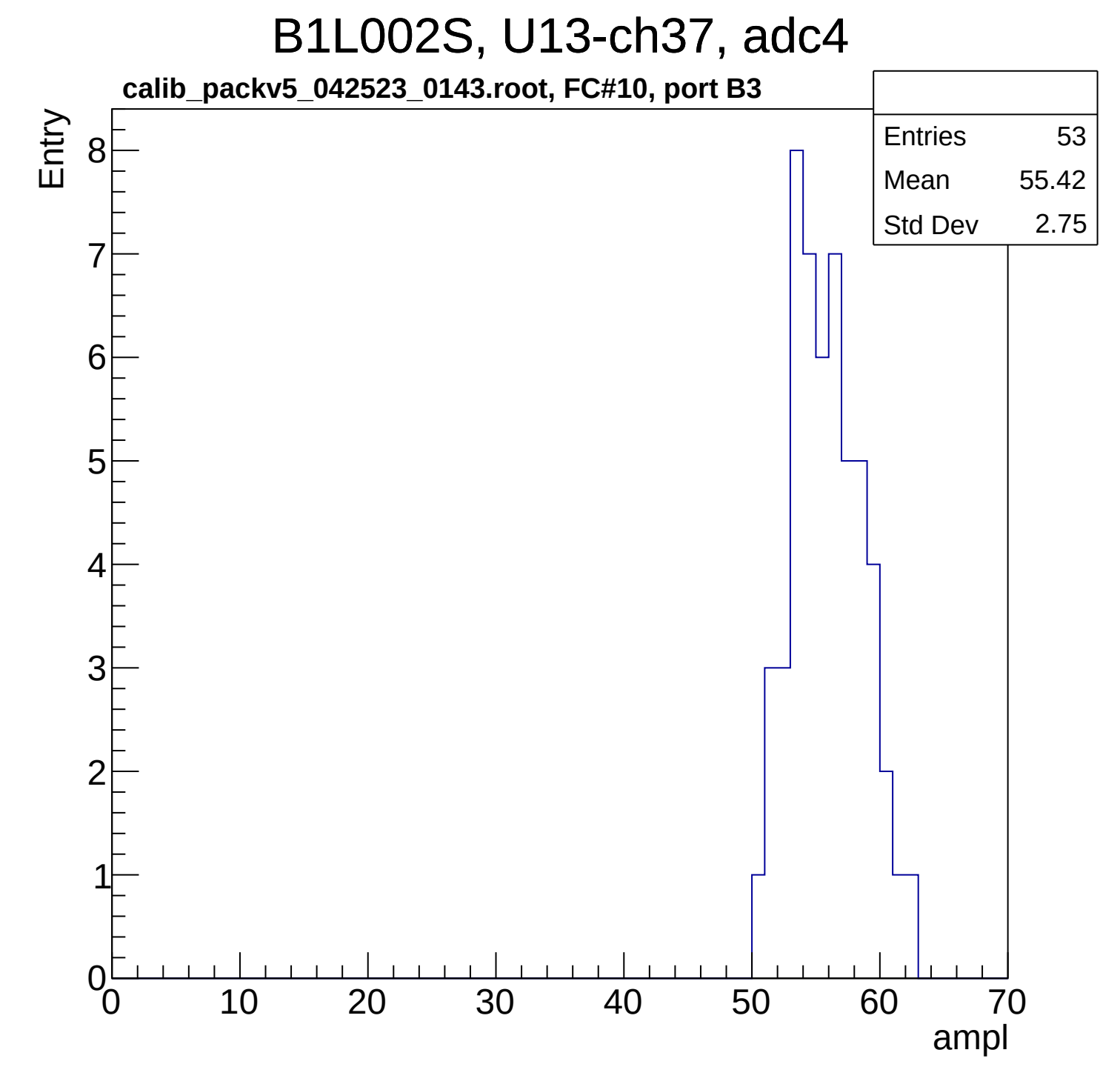
50
60
70
ampl


B1L002S, U13-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 55.42
Std Dev 2.75
Entry
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4
5
6
7
8
0
10
20
30
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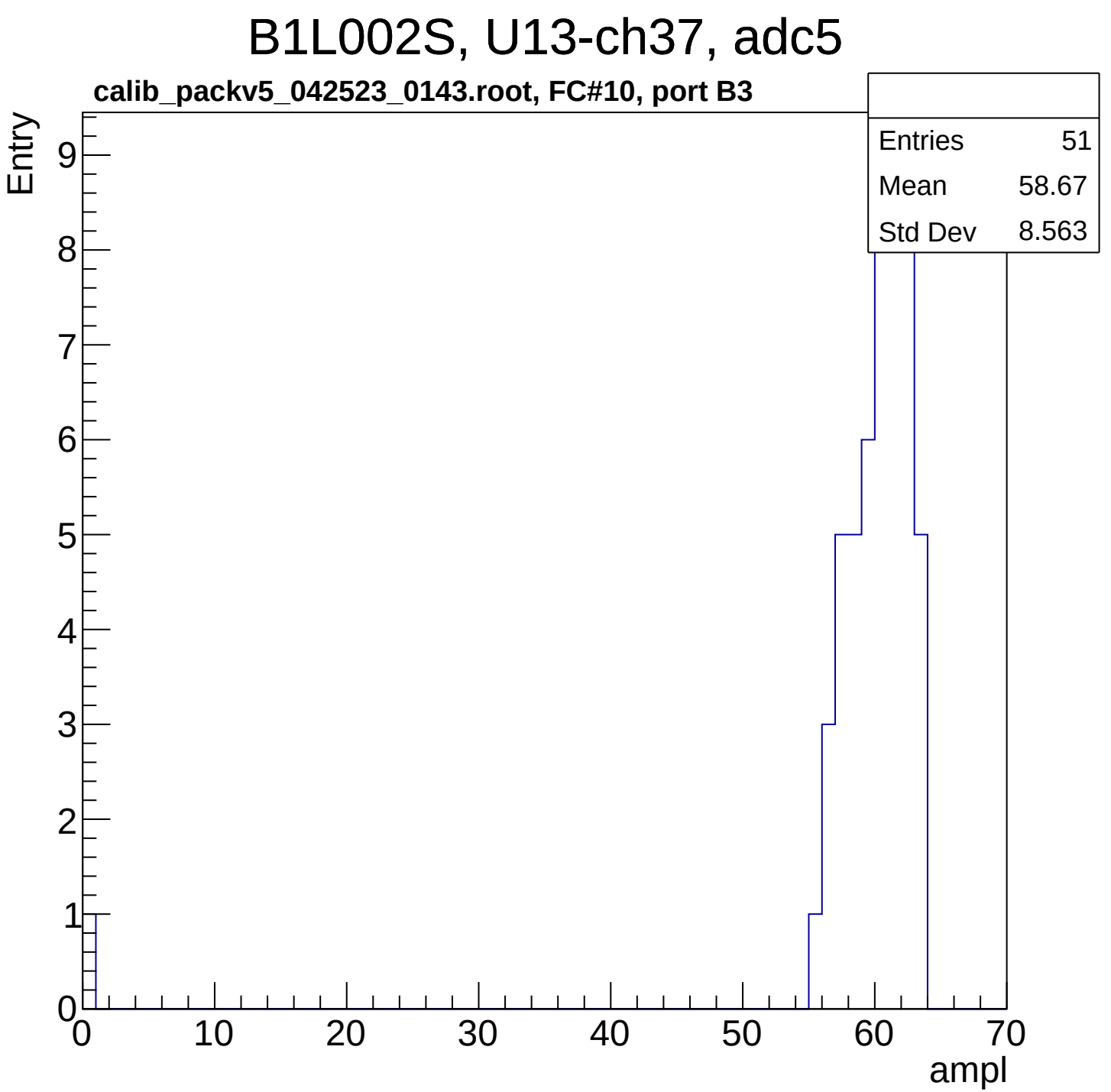
50
60
70
ampl


B1L002S, U13-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.67
Std Dev 8.563
Entry
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5
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7
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9
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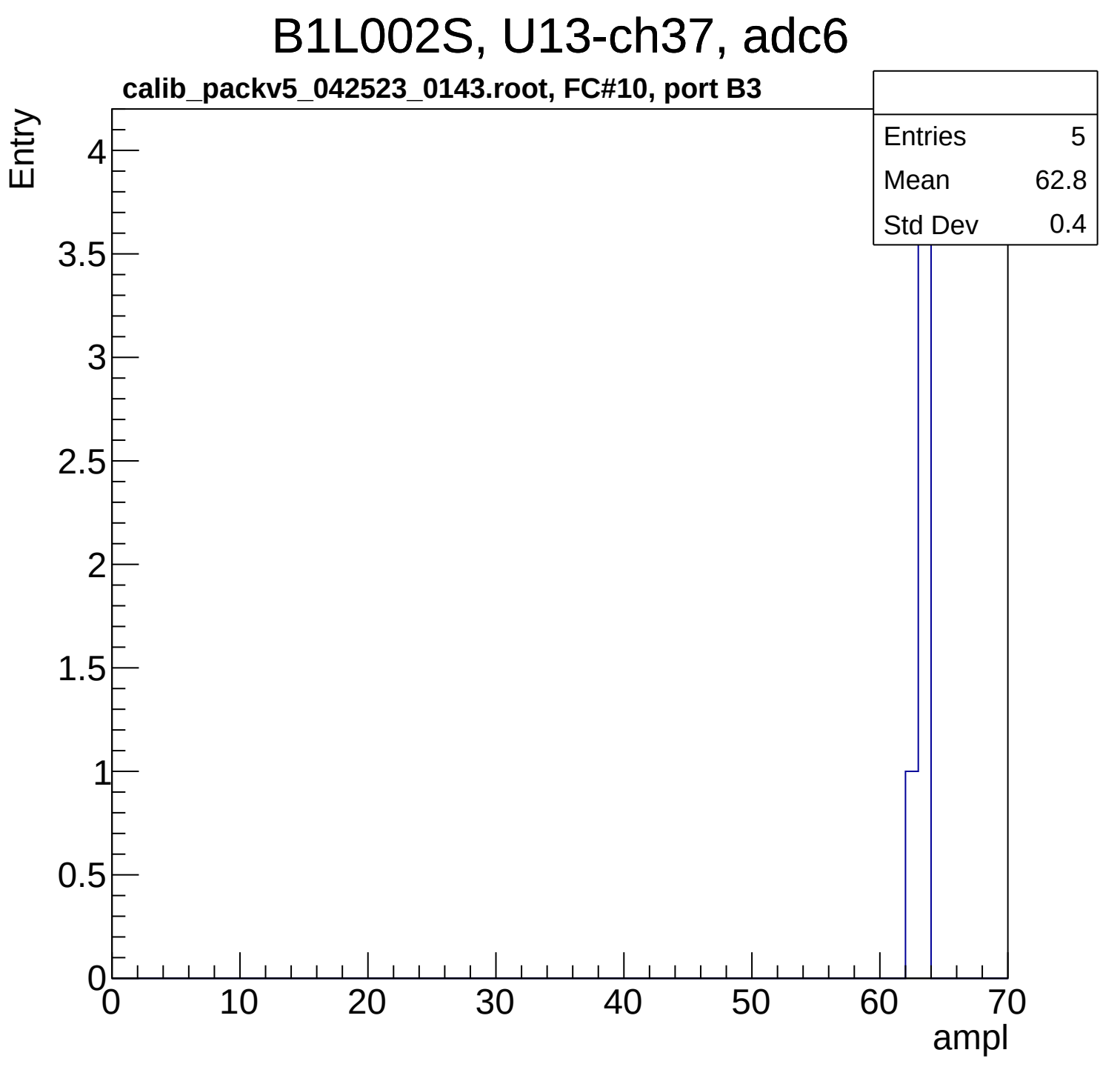
50
60
70
ampl


B1L002S, U13-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.8
Std Dev 0.4
Entry
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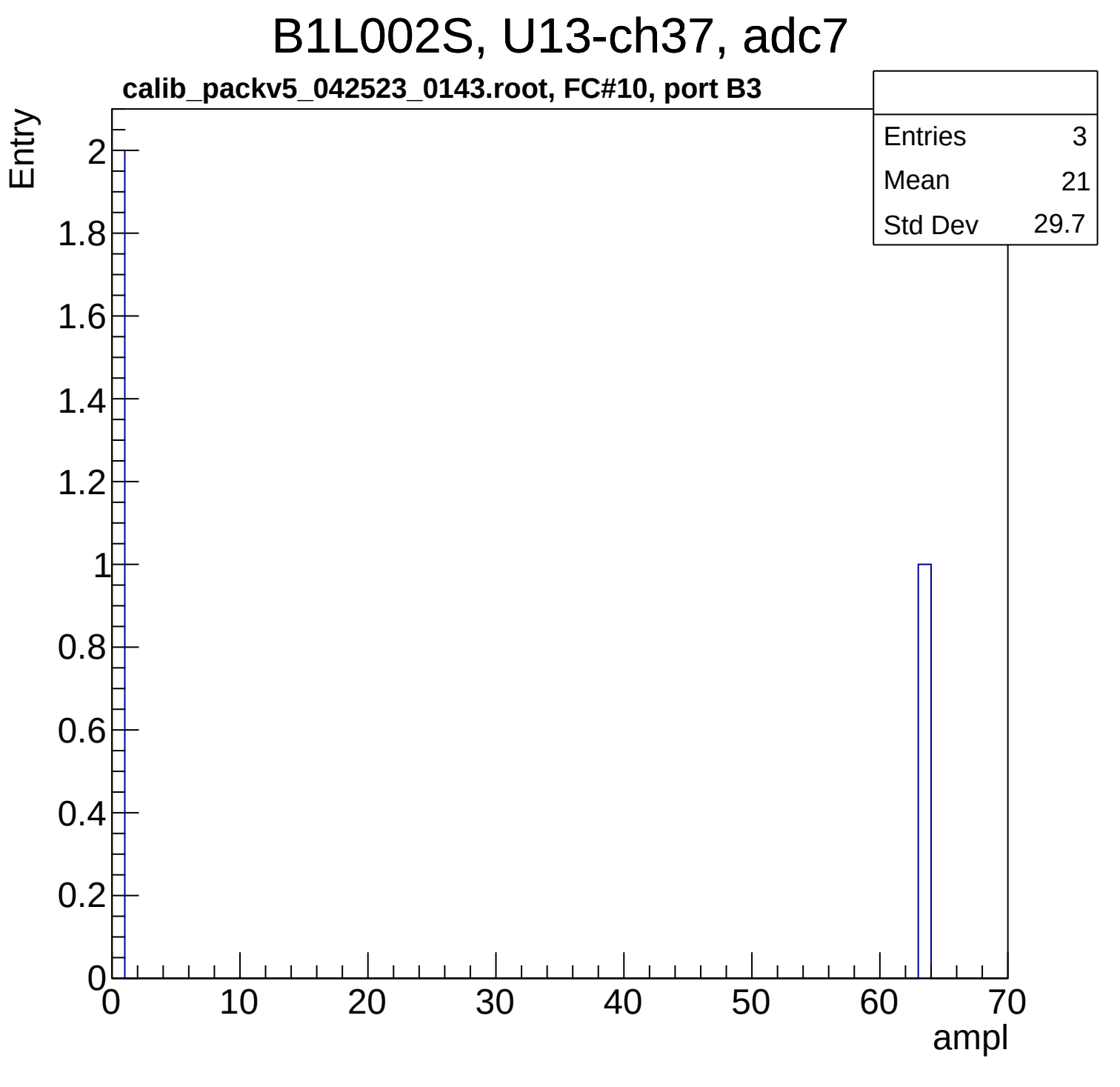
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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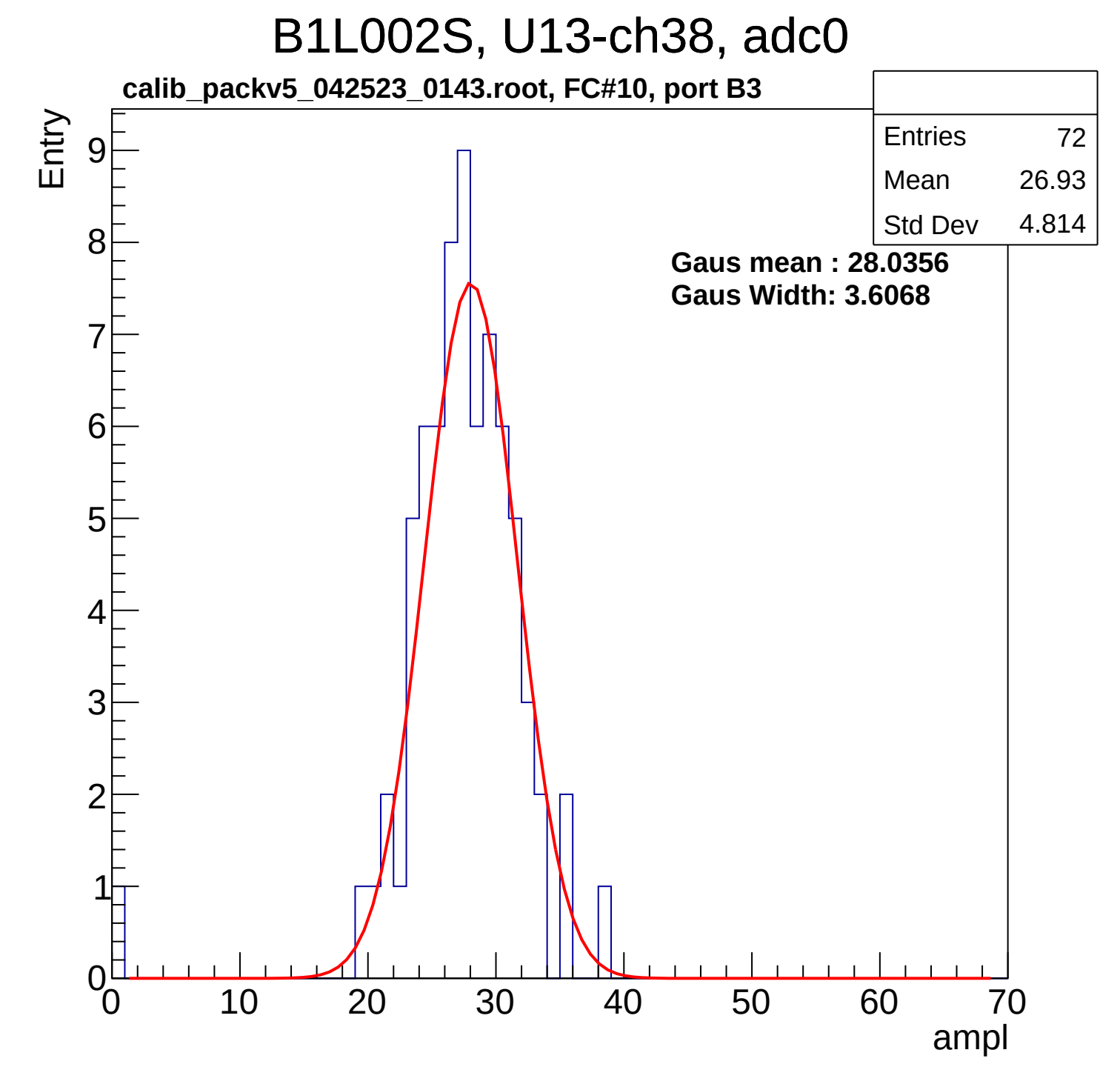
B1L002S, U13-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.93
Std Dev 4.814
Gaus mean : 28.0356
Gaus Width: 3.6068
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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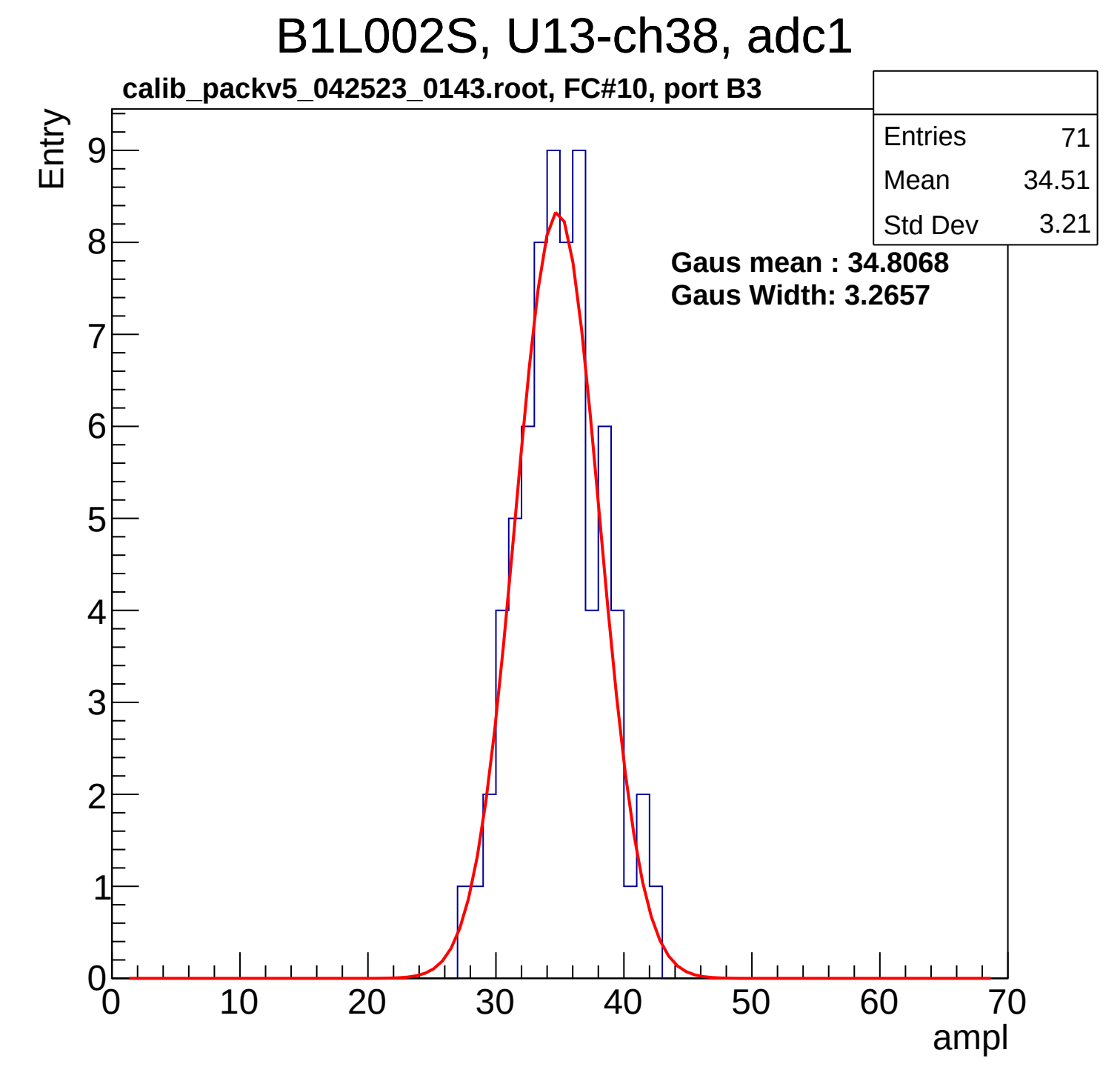
B1L002S, U13-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.51
Std Dev 3.21
Gaus mean : 34.8068
Gaus Width: 3.2657
Entry
ampl
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2
3
4
5
6
7
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0
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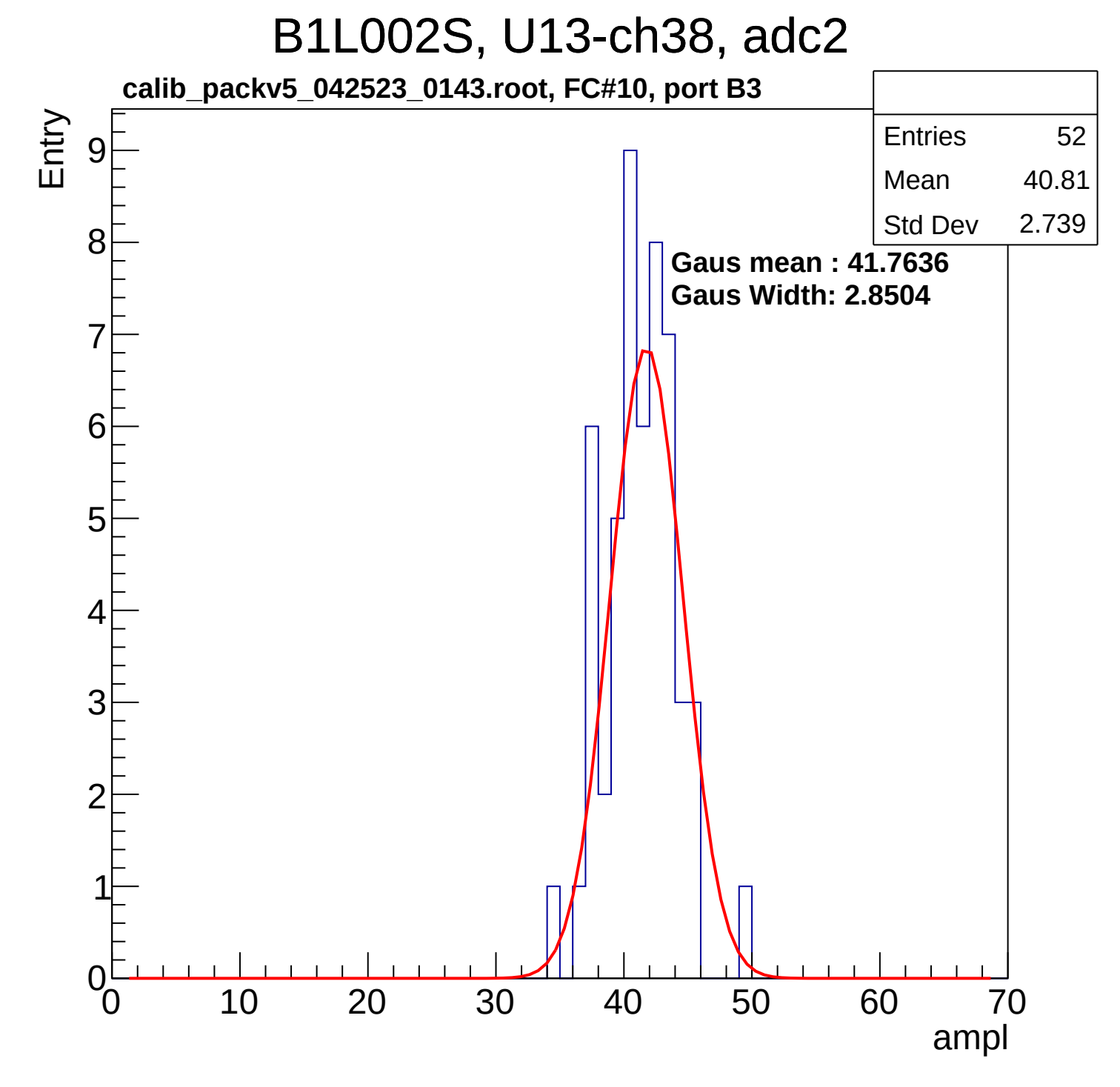
B1L002S, U13-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.81
Std Dev 2.739
Gaus mean : 41.7636
Gaus Width: 2.8504
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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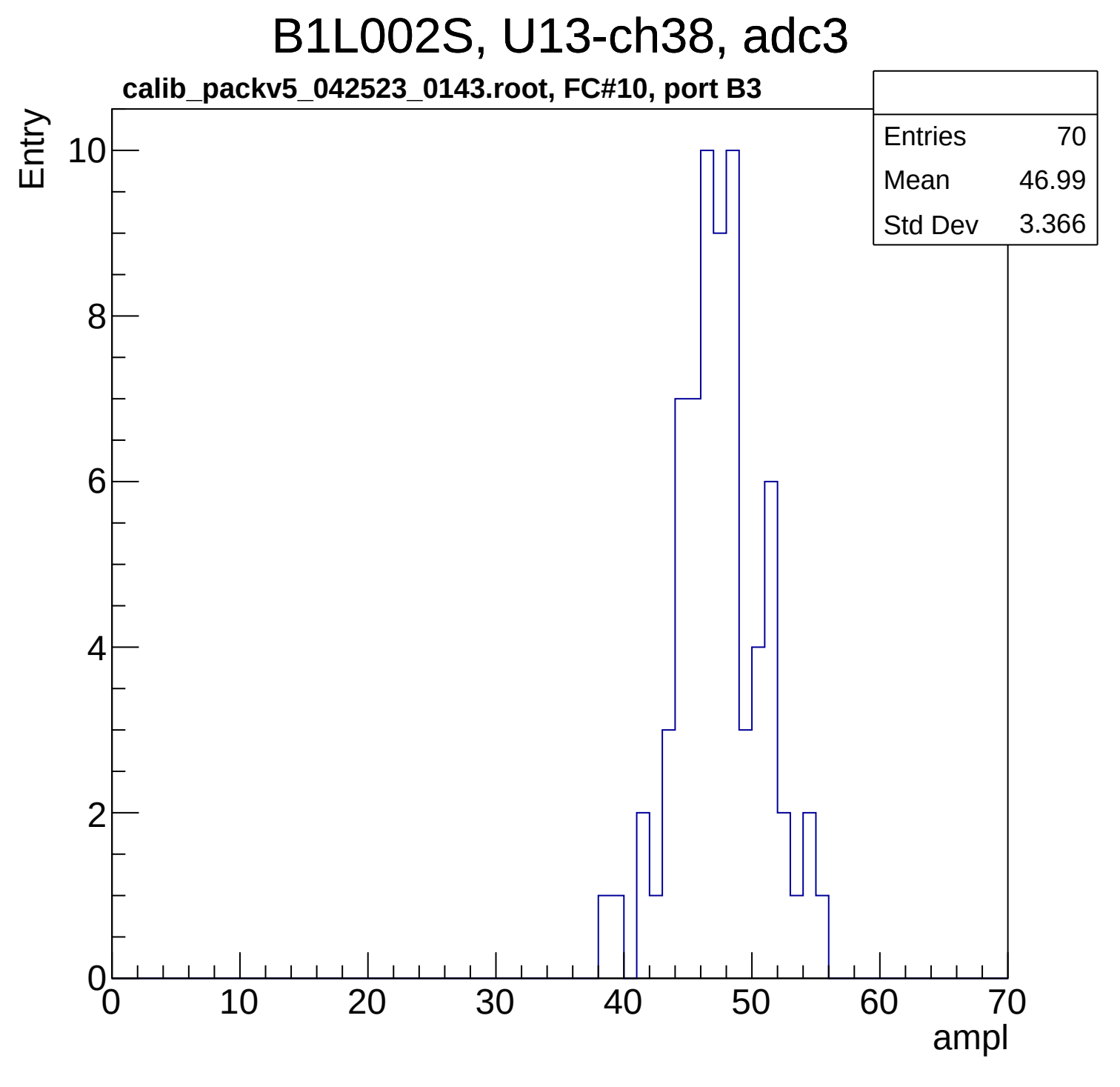
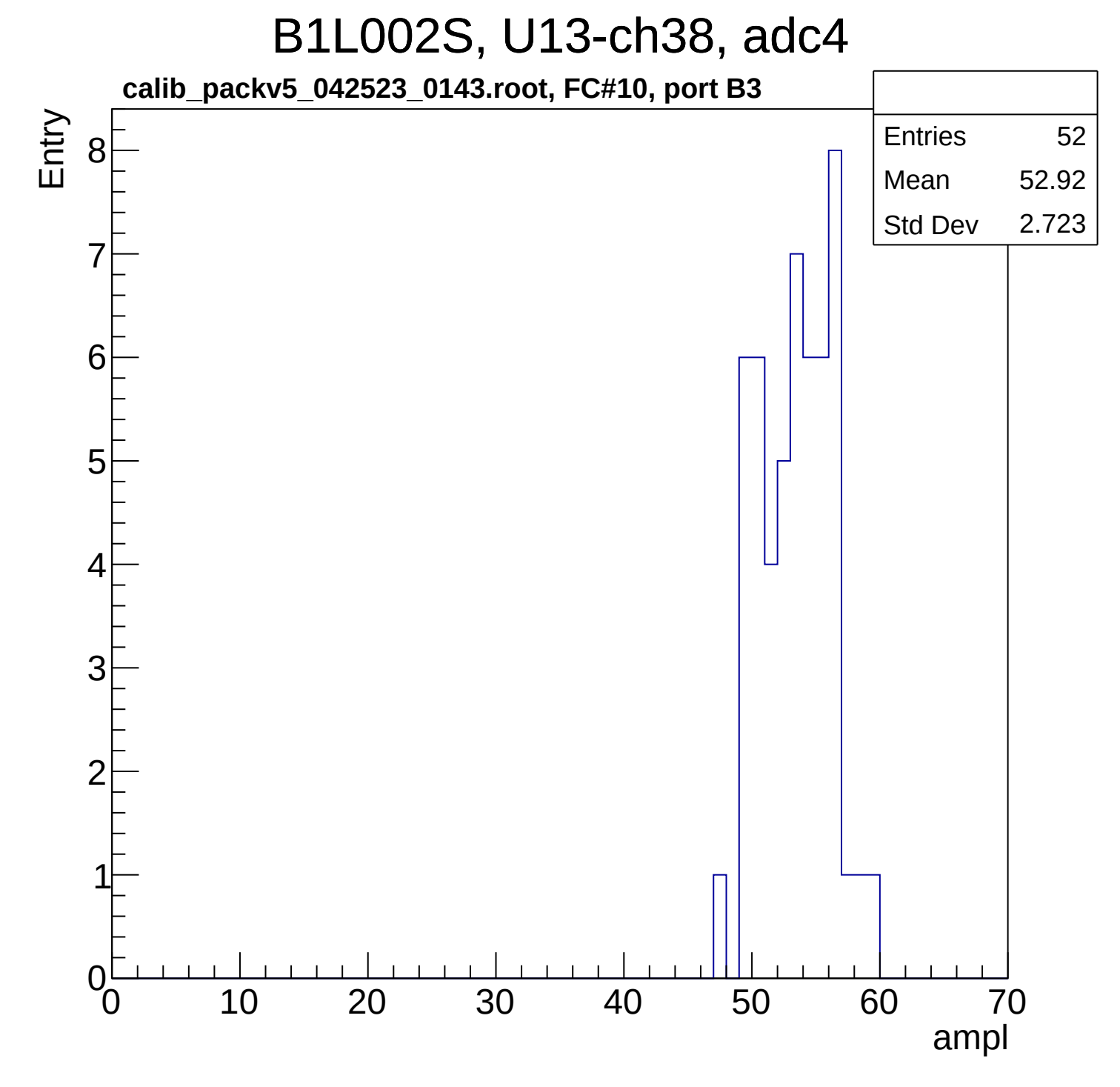

B1L002S, U13-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.99
Std Dev 3.366
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.92
Std Dev 2.723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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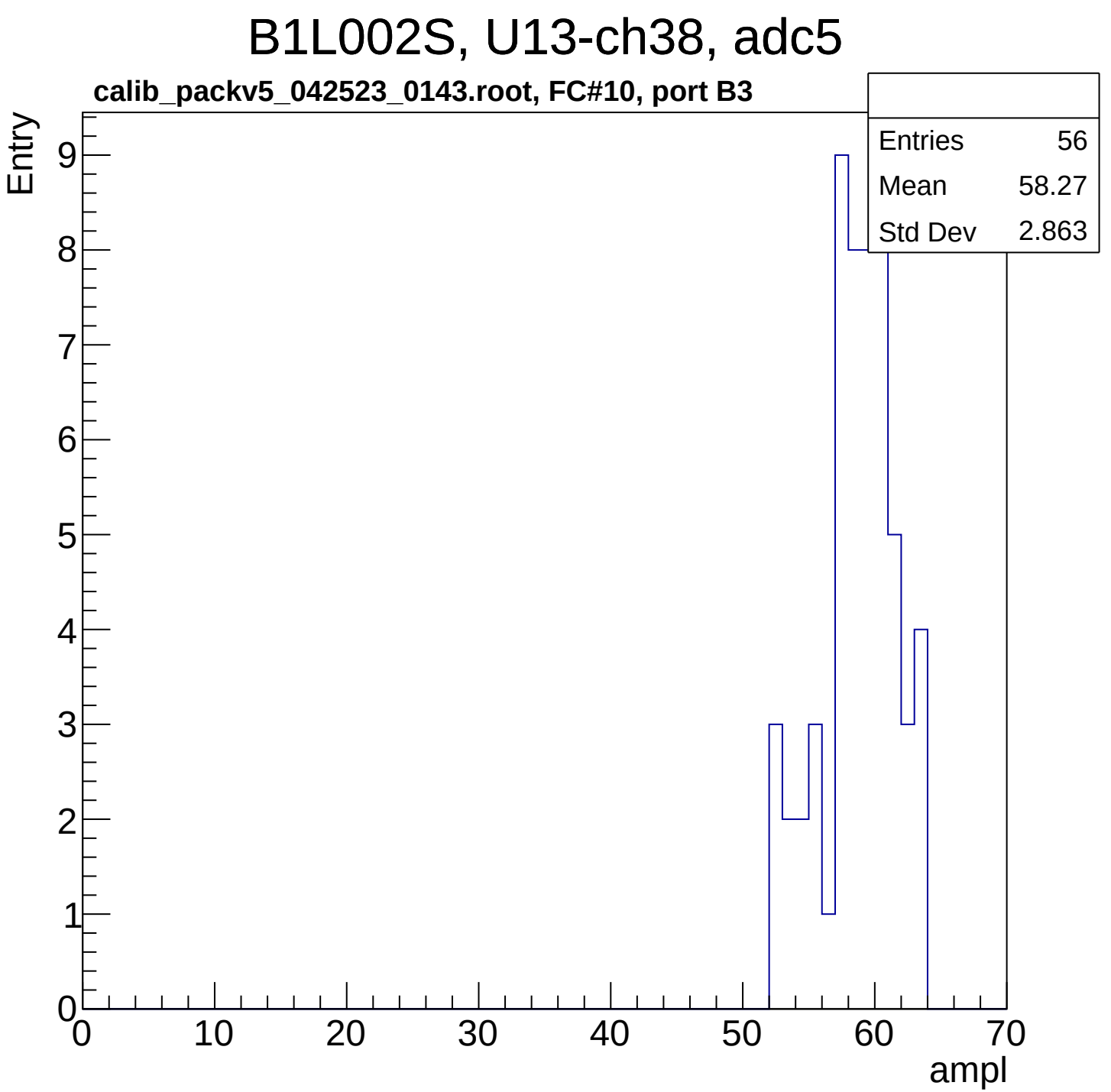
60
70


B1L002S, U13-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.27
Std Dev 2.863
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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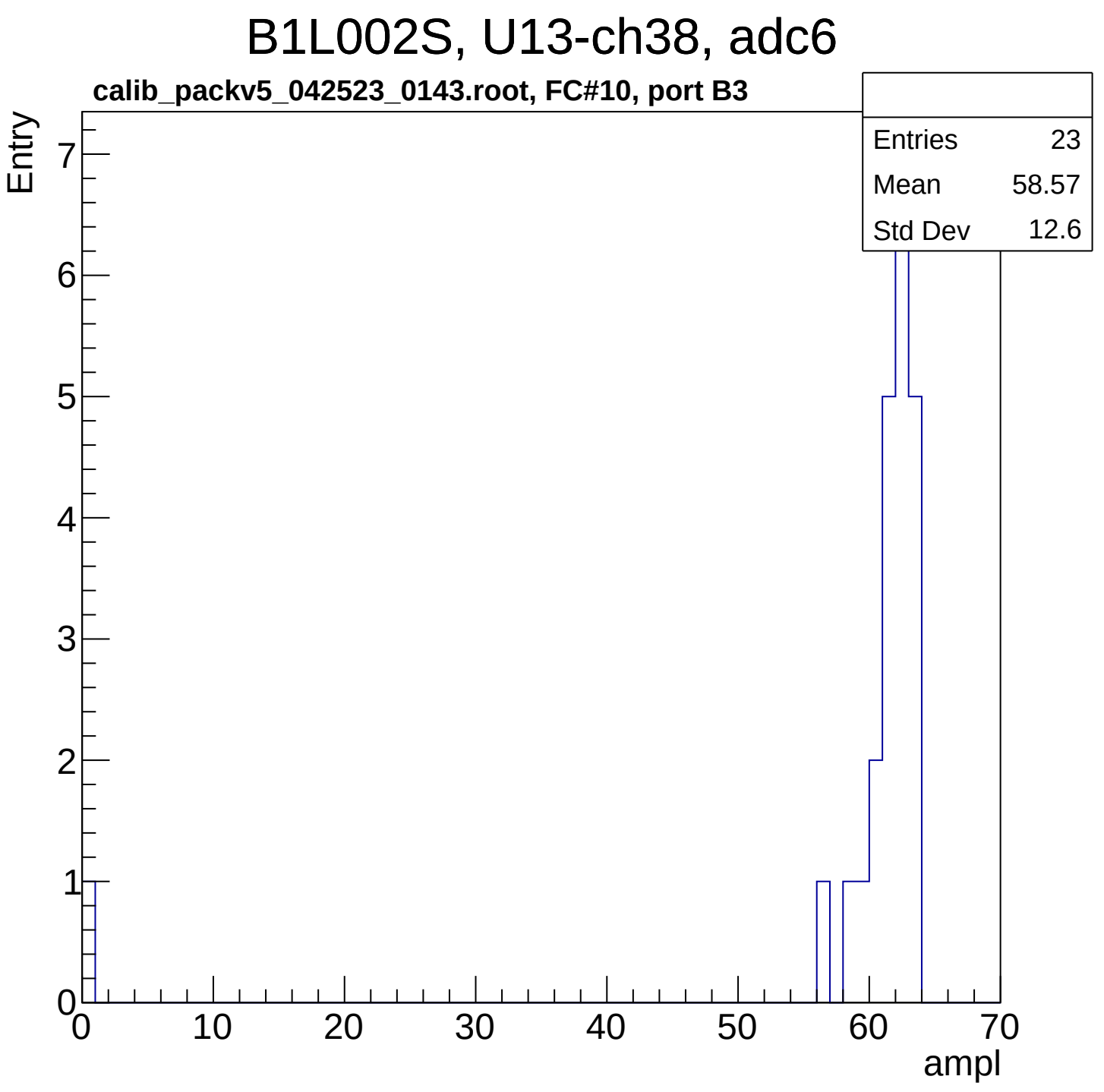
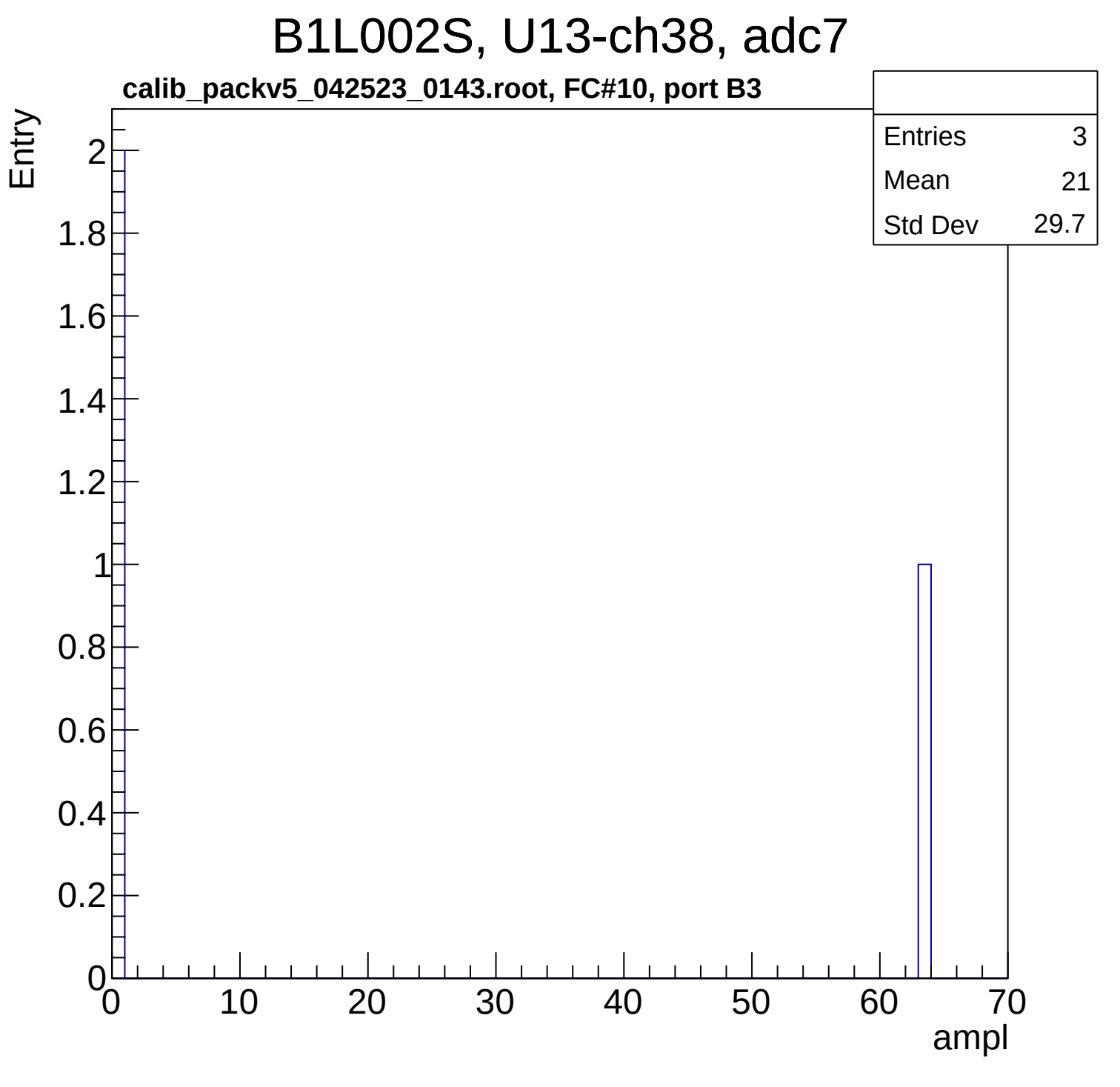

B1L002S, U13-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.57
Std Dev 12.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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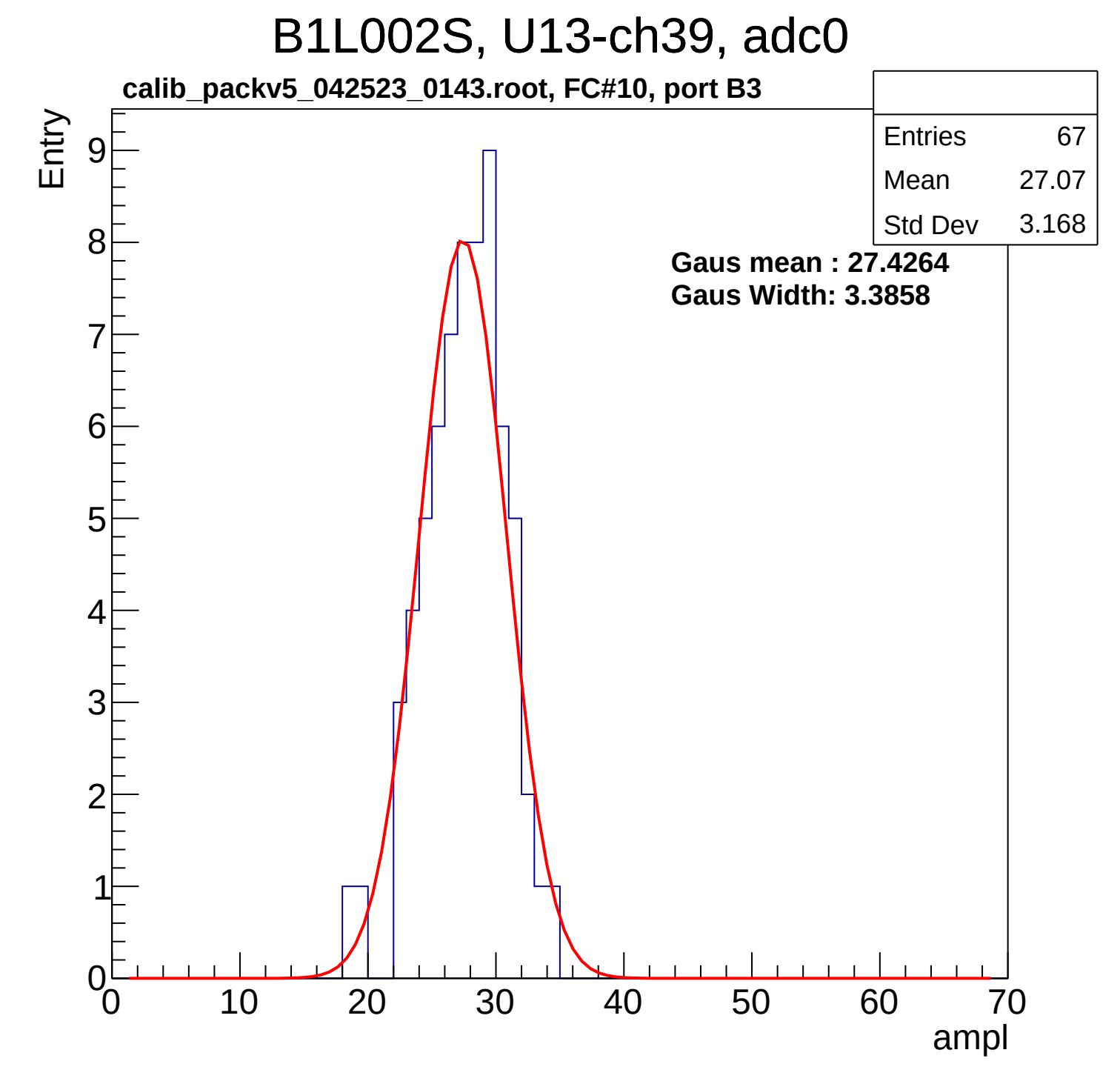
B1L002S, U13-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.07
Std Dev 3.168
Gaus mean : 27.4264
Gaus Width: 3.3858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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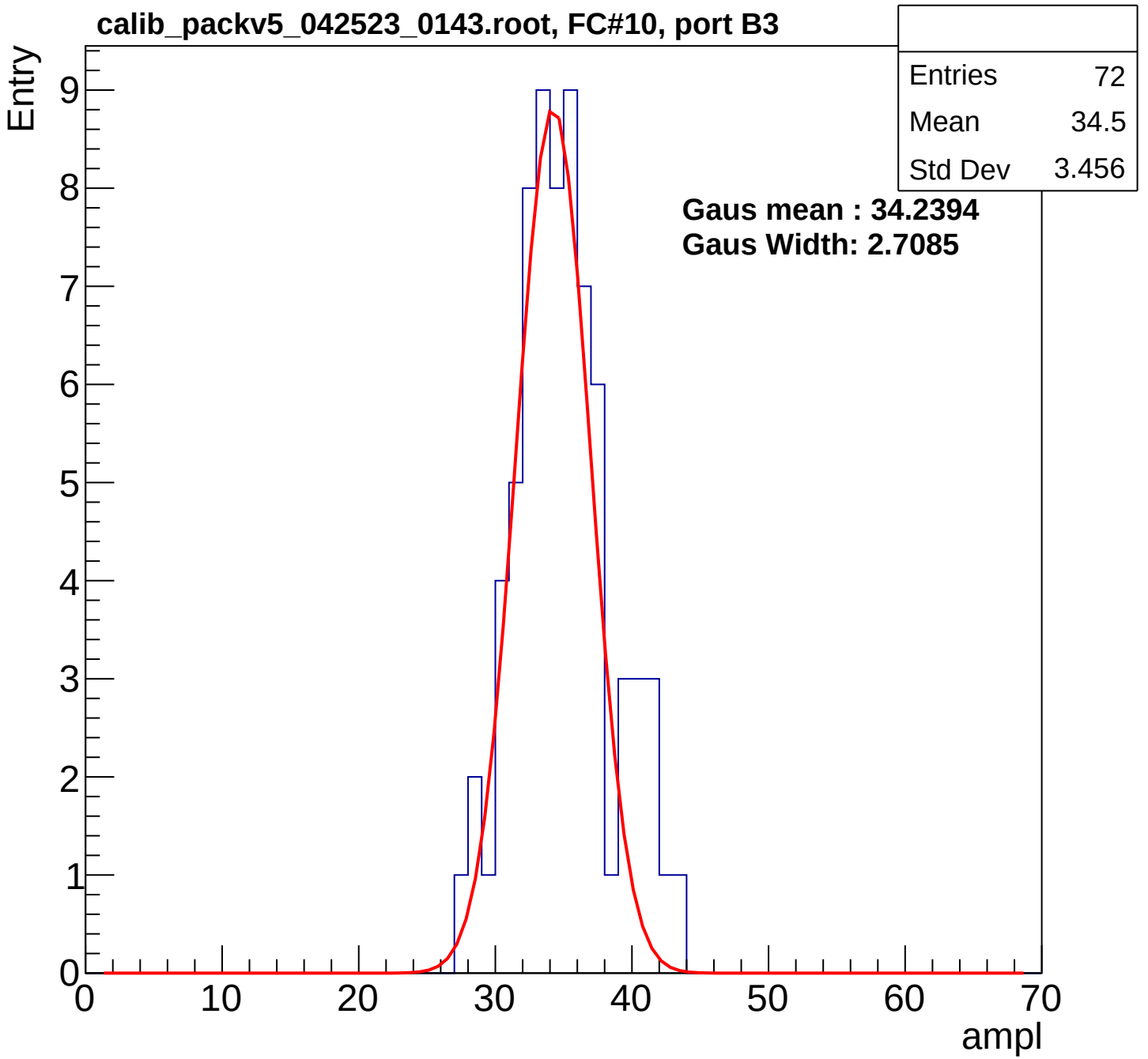
B1L002S, U13-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.5
Std Dev 3.456
Gaus mean : 34.2394
Gaus Width: 2.7085
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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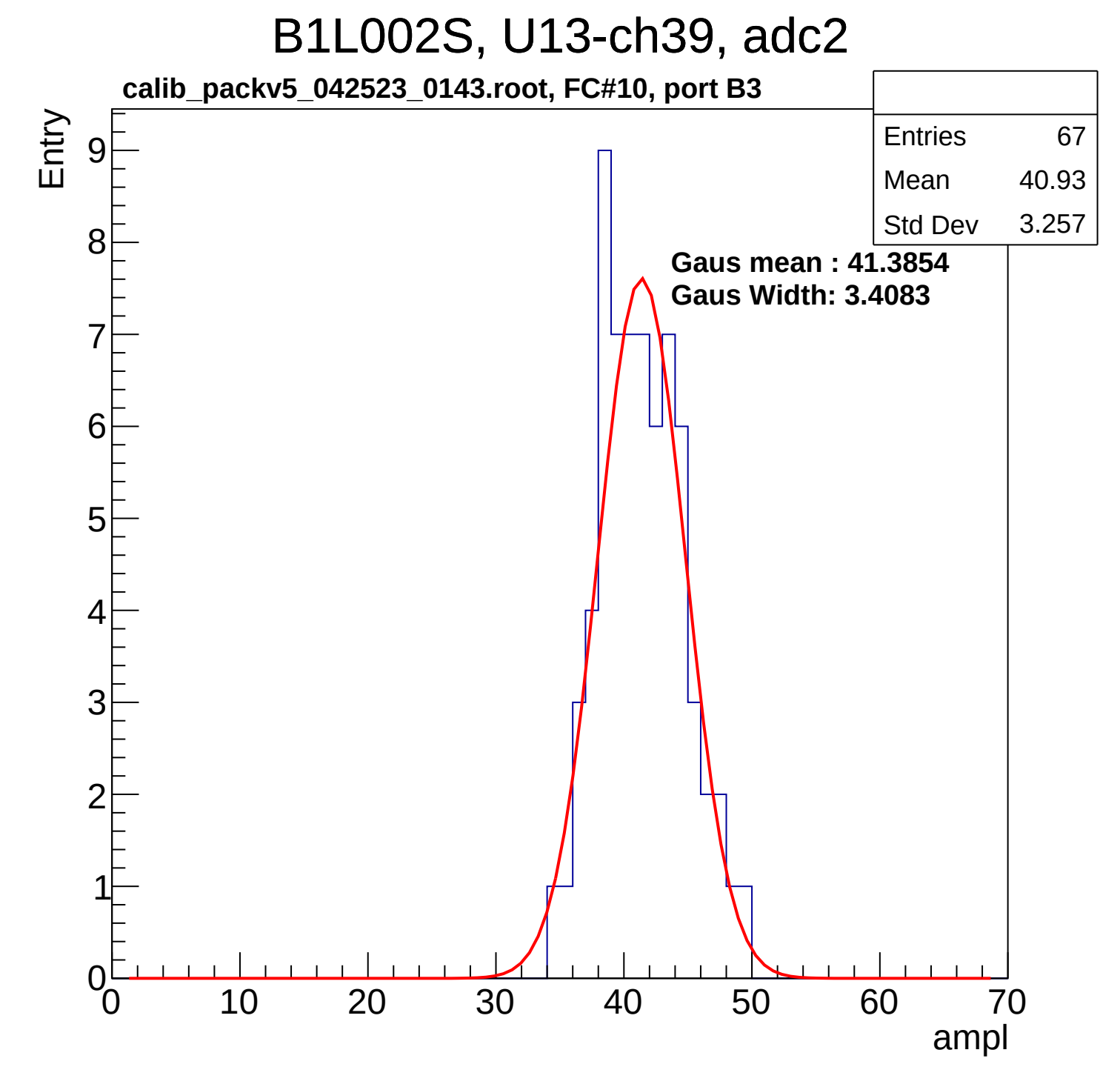
B1L002S, U13-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.93
Std Dev 3.257
Gaus mean : 41.3854
Gaus Width: 3.4083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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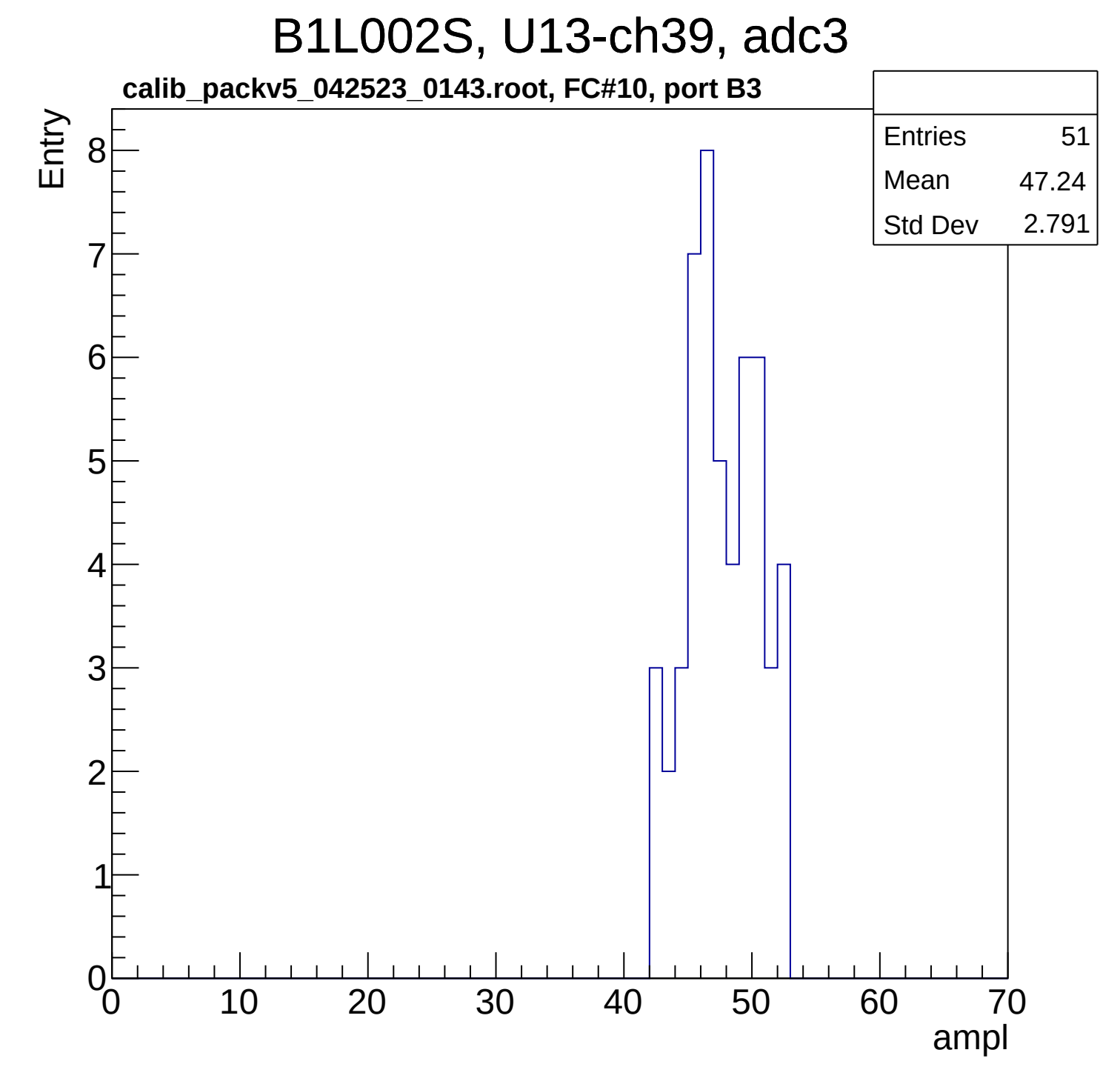
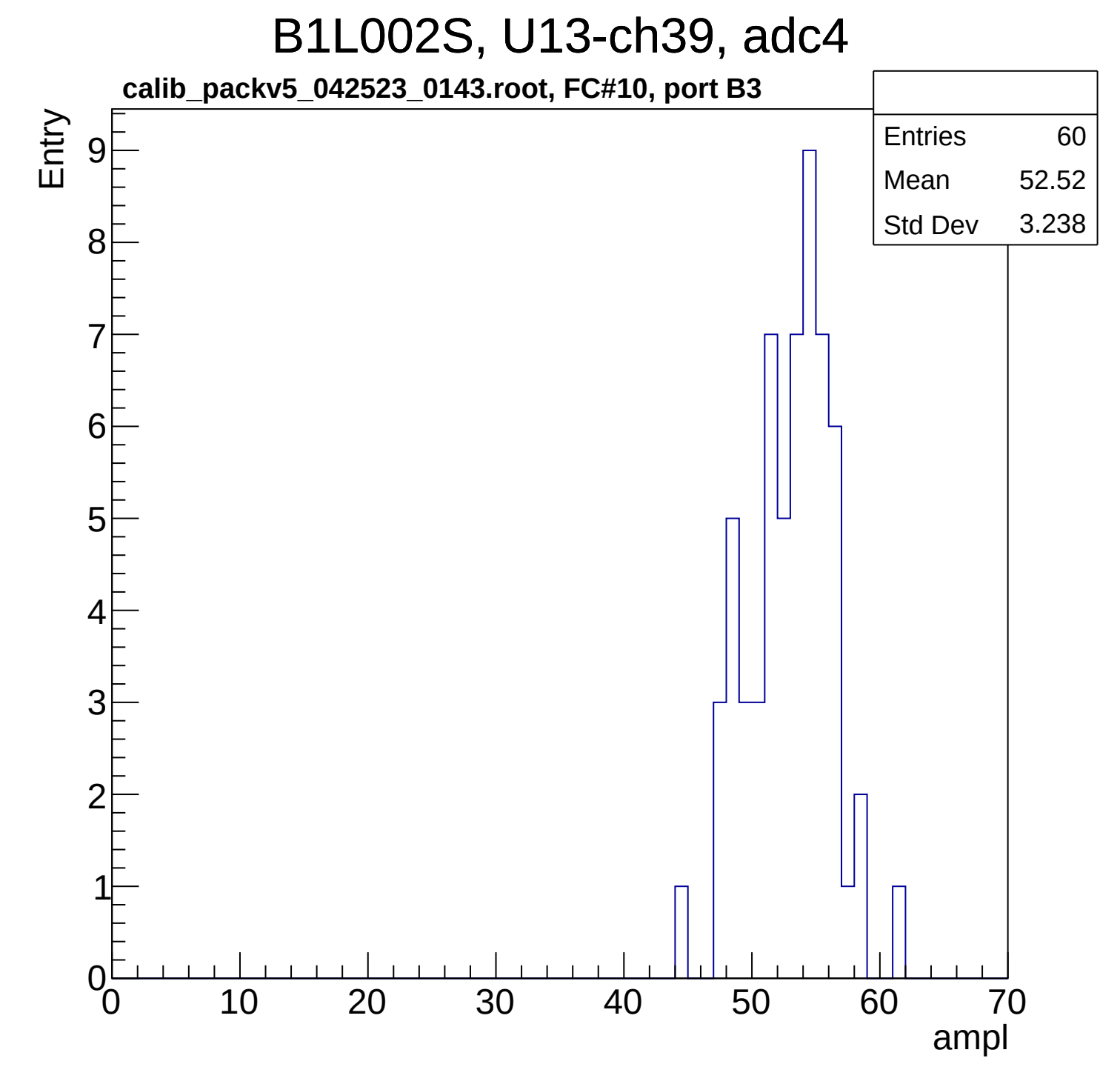
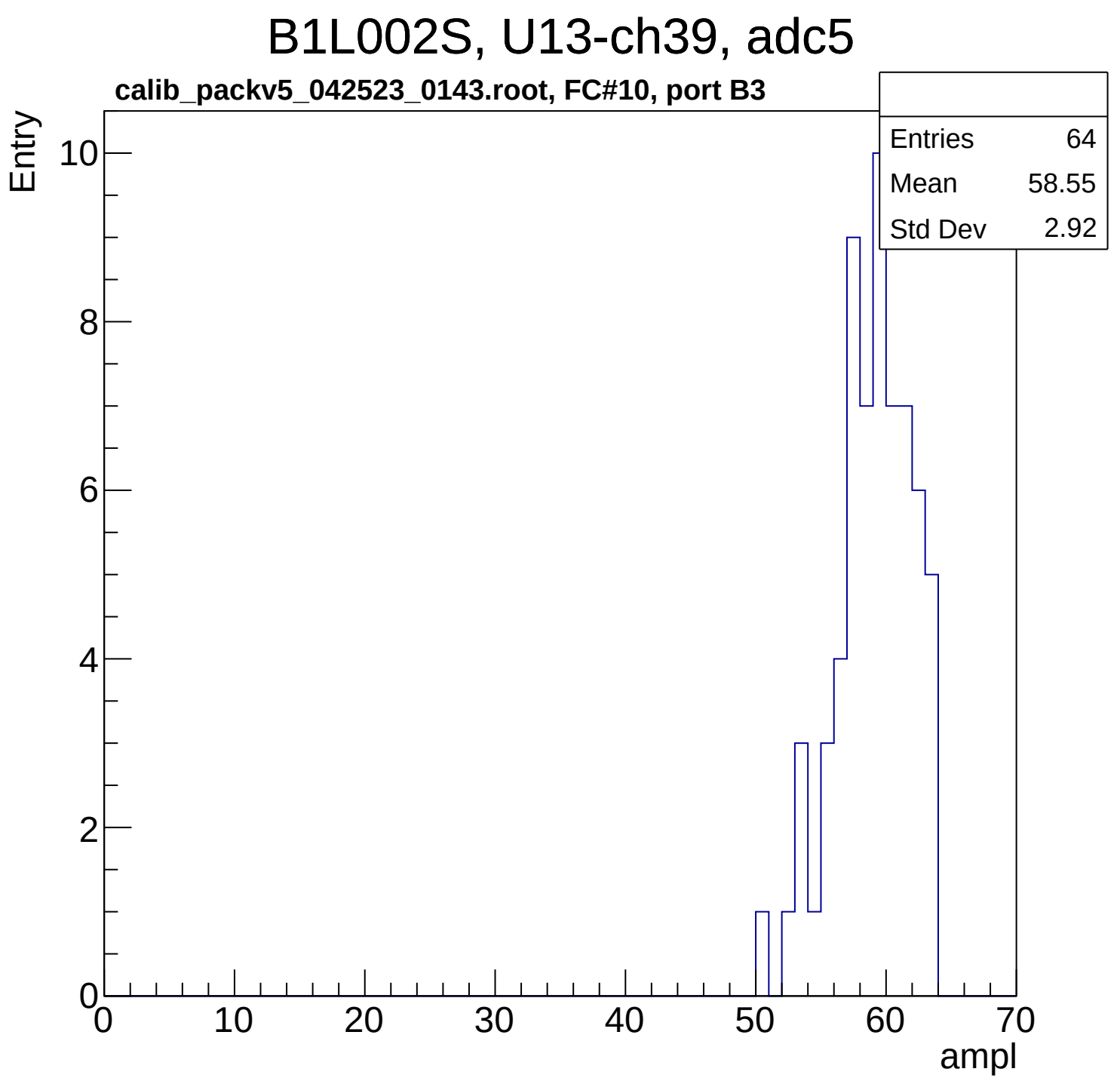
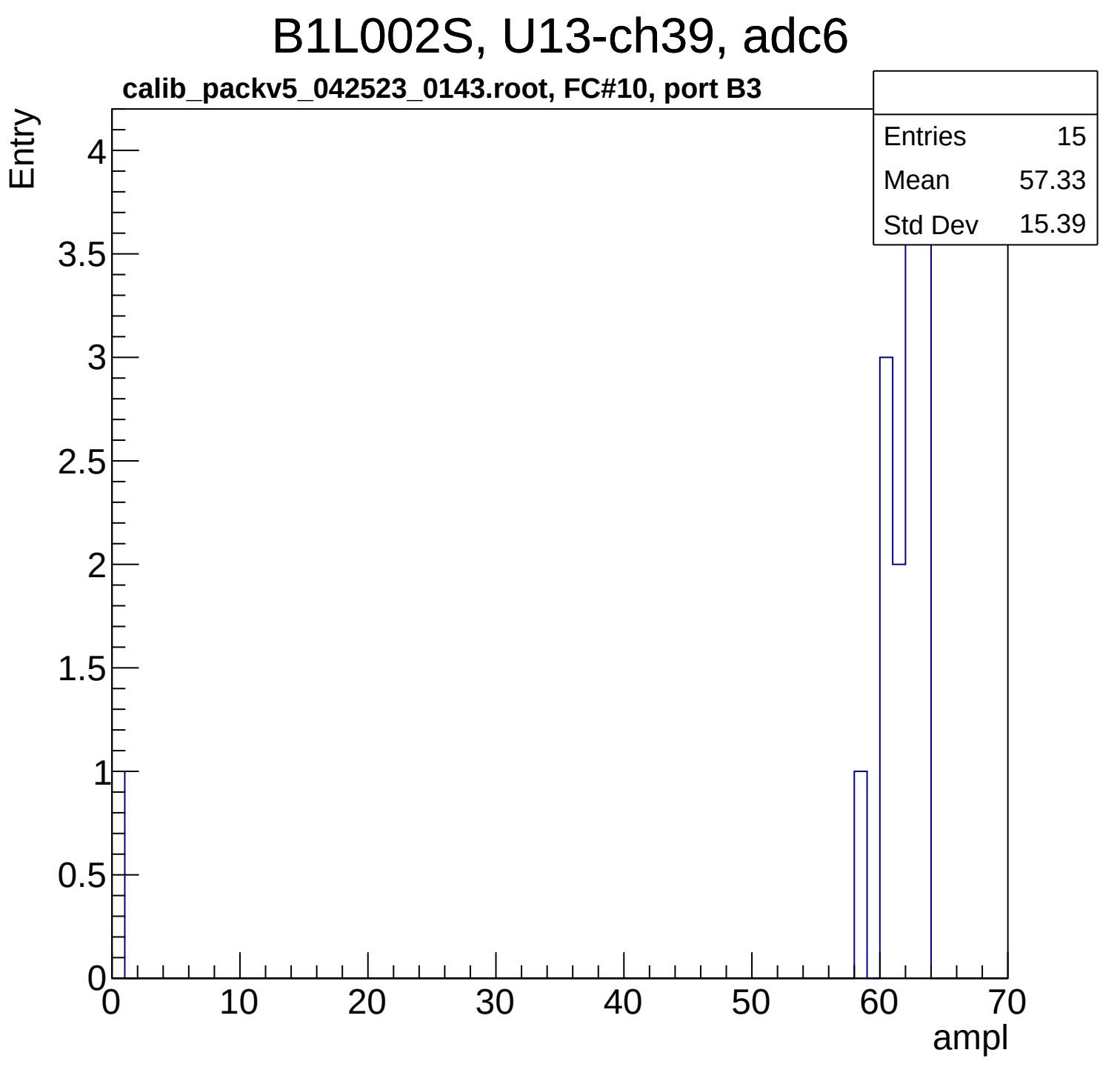
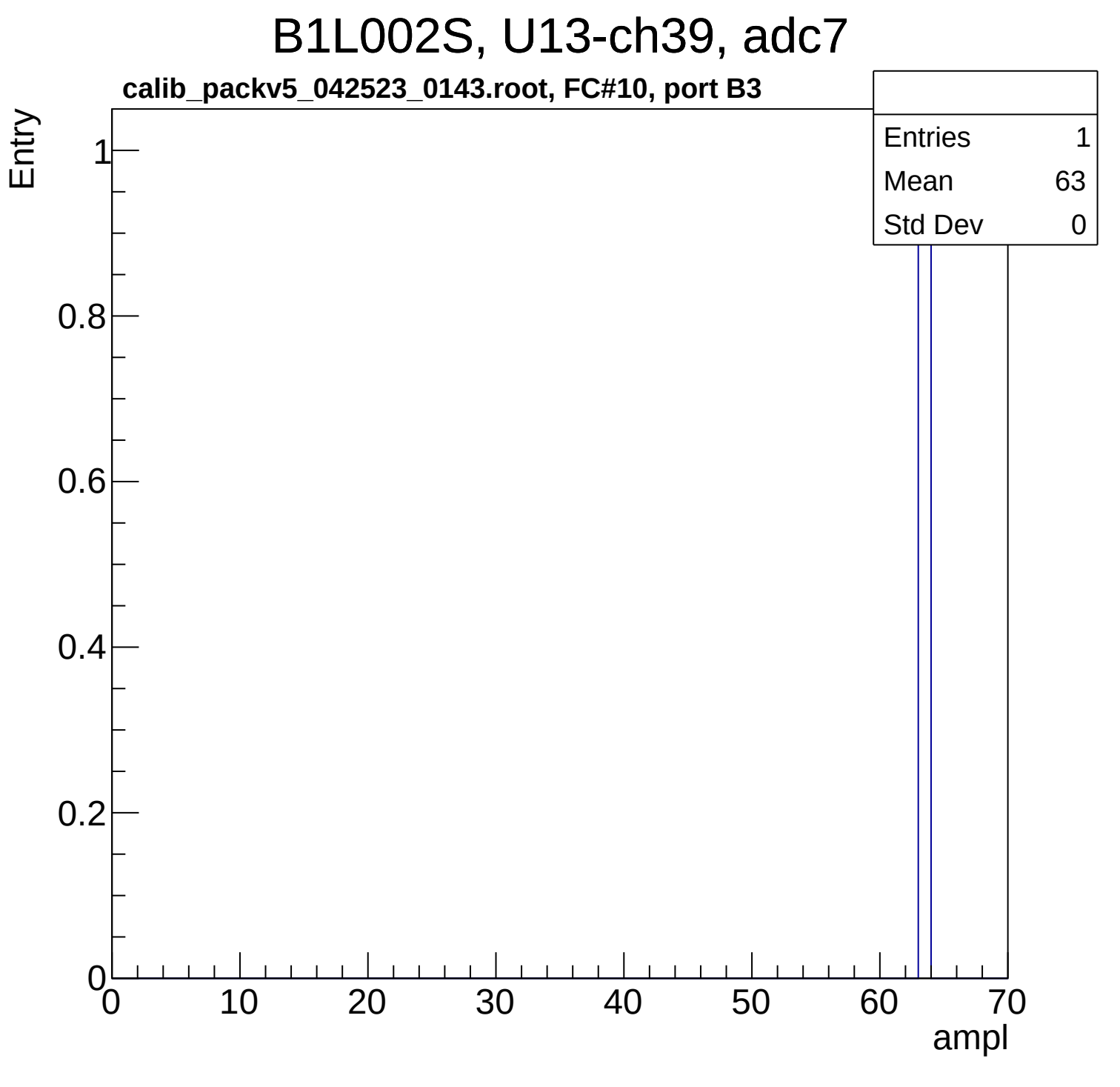
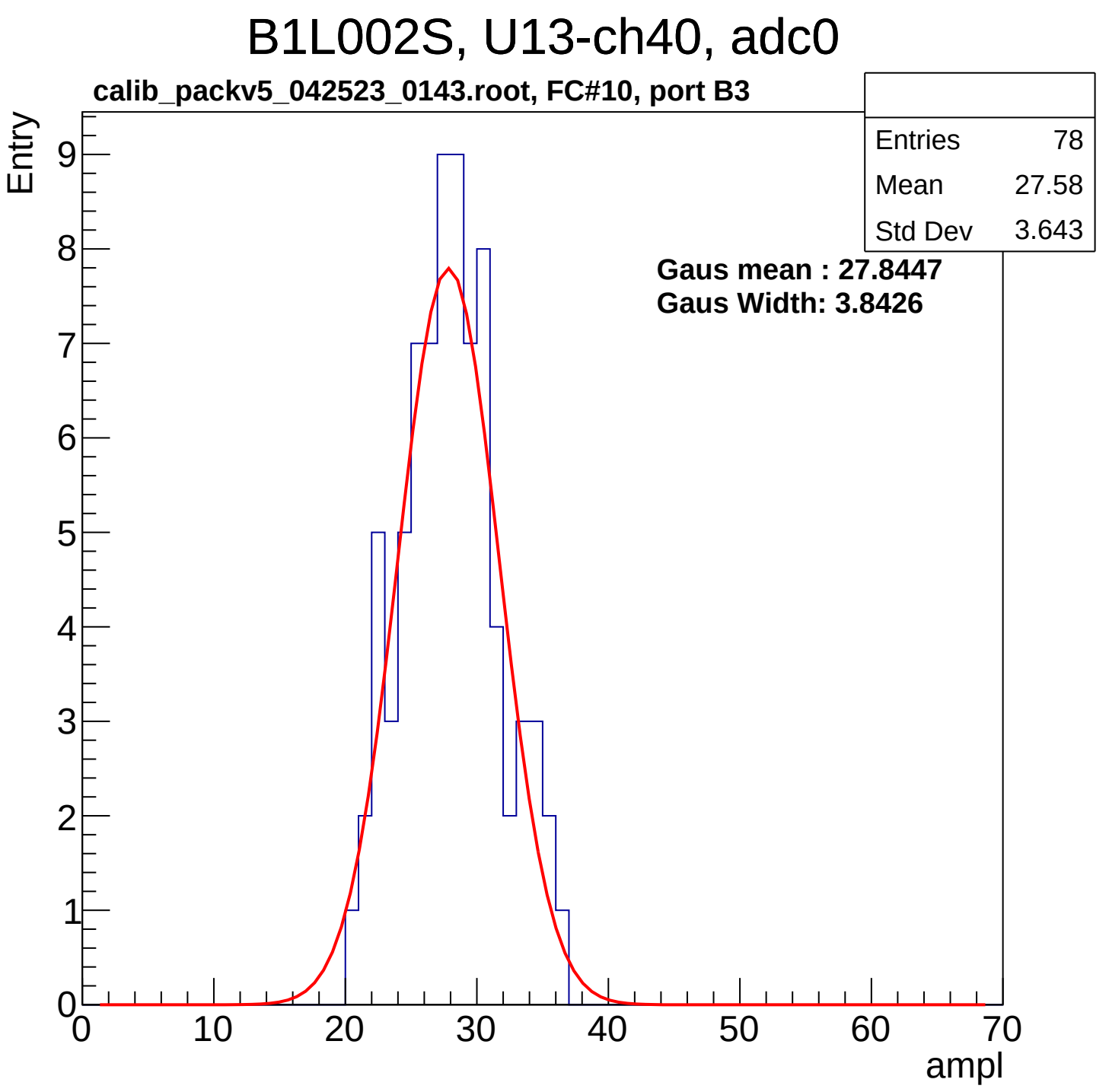

B1L002S, U13-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.24
Std Dev 2.791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

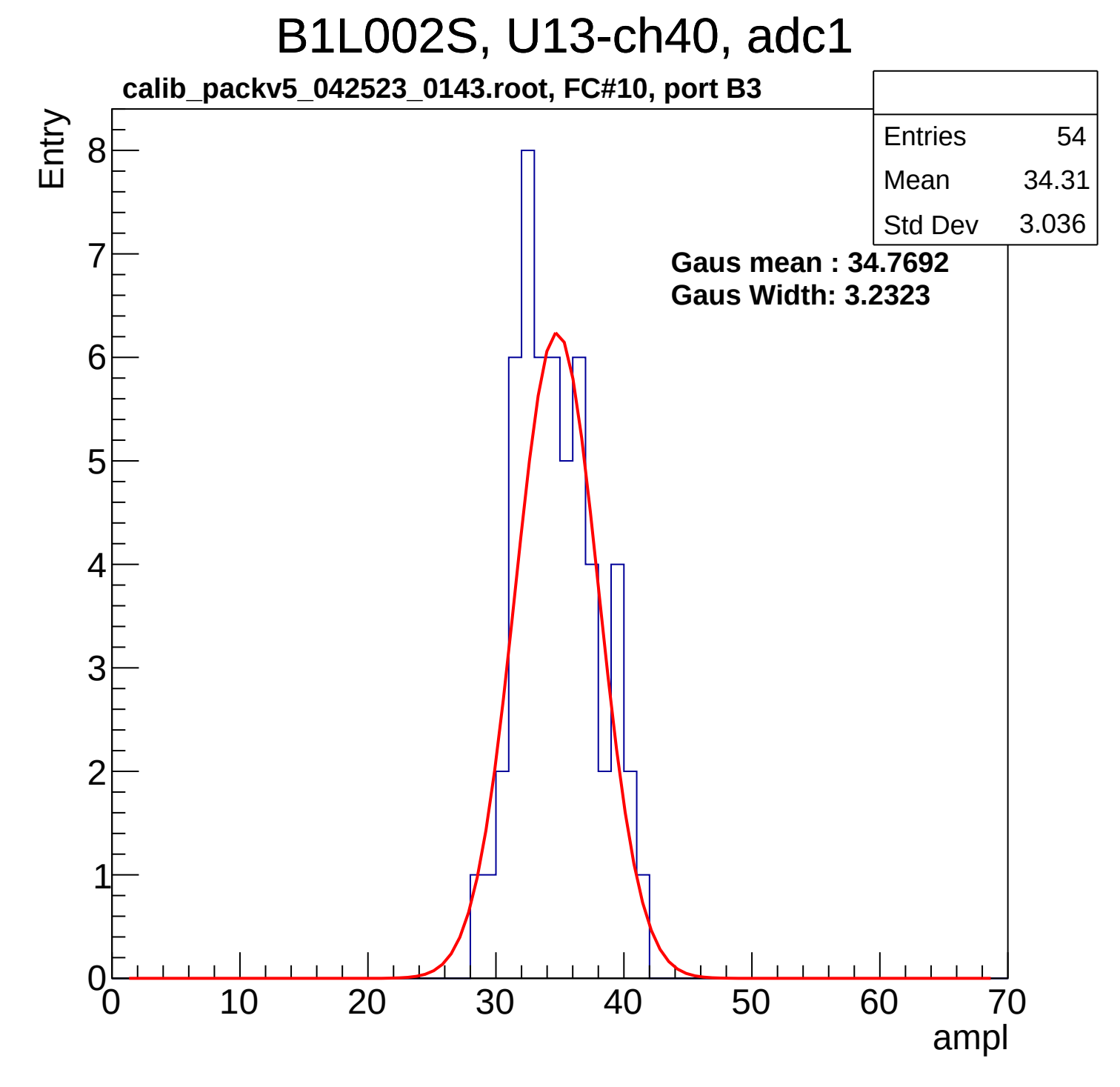

B1L002S, U13-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.52
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

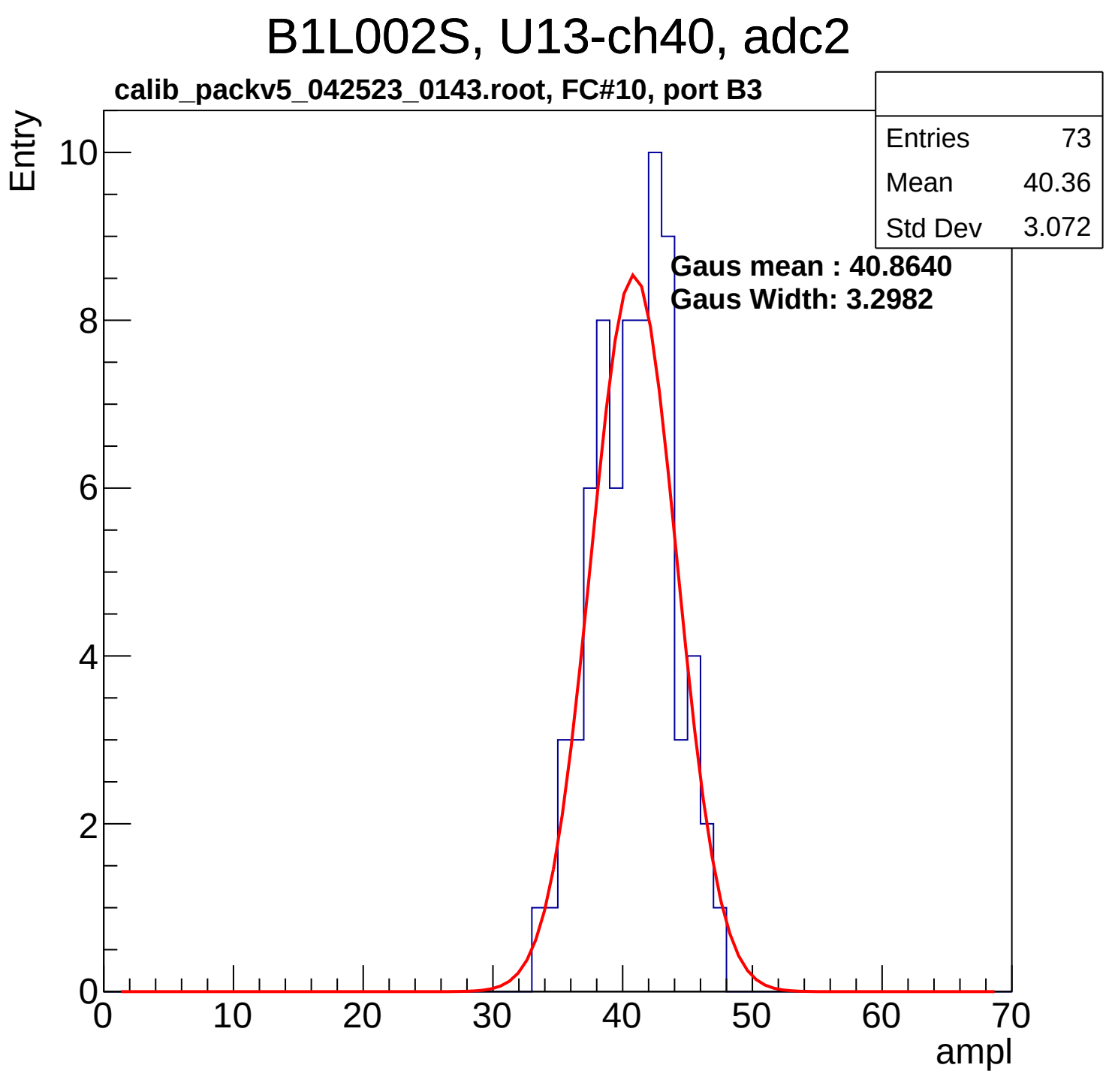

B1L002S, U13-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.55
Std Dev 2.92
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

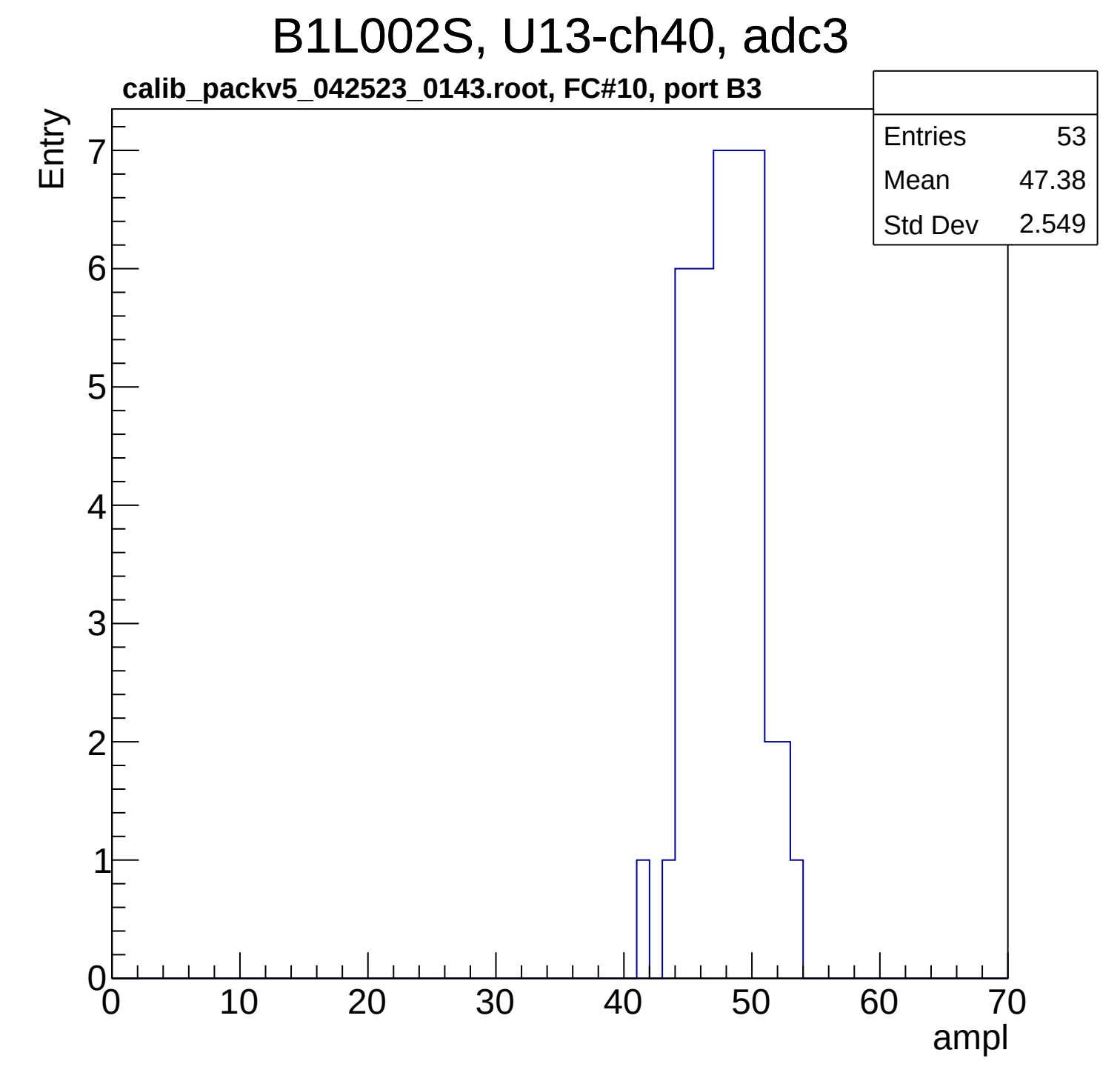

B1L002S, U13-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.33
Std Dev 15.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

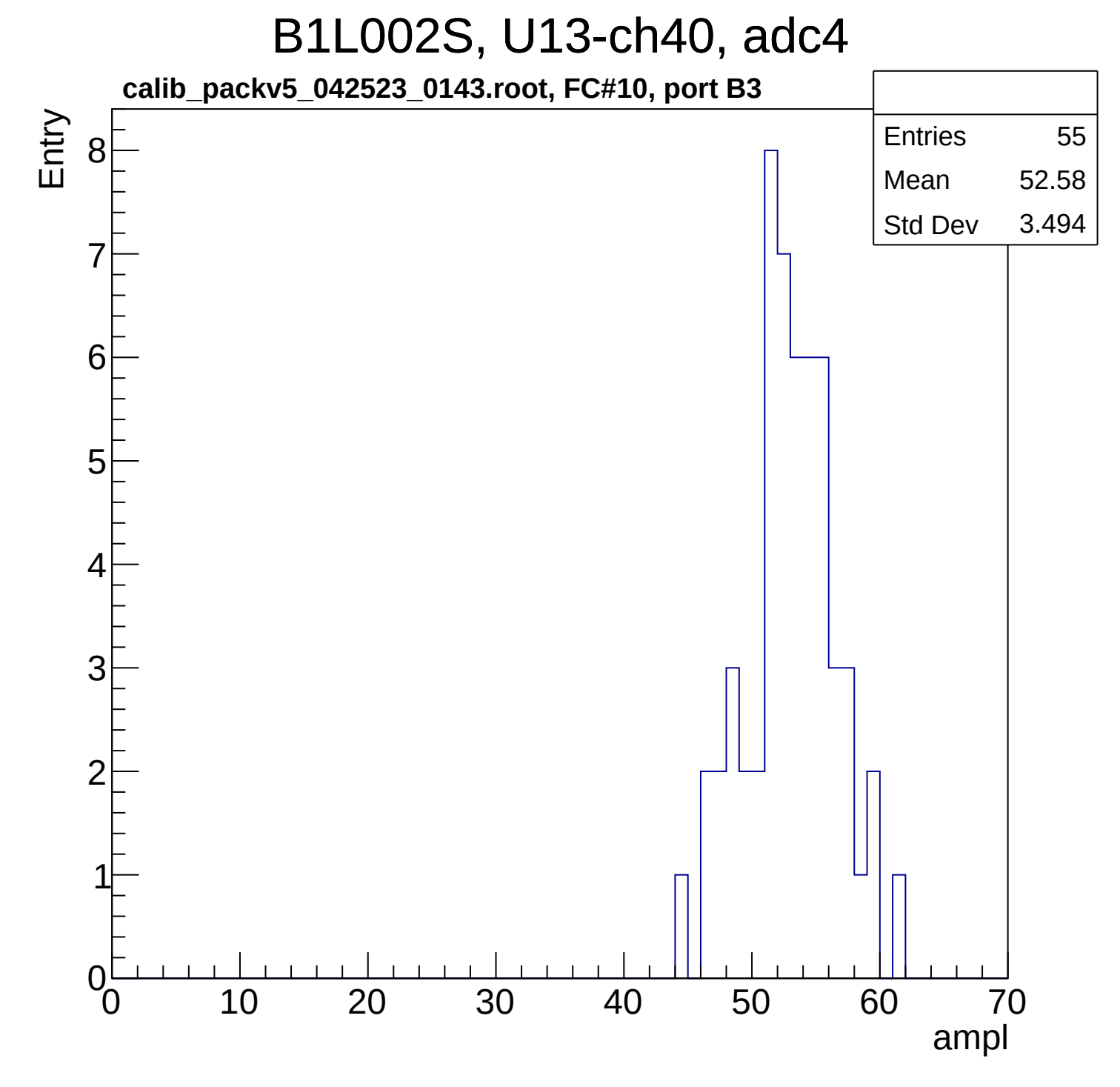

B1L002S, U13-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.58
Std Dev 3.643
Gaus mean : 27.8447
Gaus Width: 3.8426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 34.31
Std Dev 3.036
Gaus mean : 34.7692
Gaus Width: 3.2323
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.36
Std Dev 3.072
Gaus mean : 40.8640
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.38
Std Dev 2.549
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.58
Std Dev 3.494
Entry
ampl
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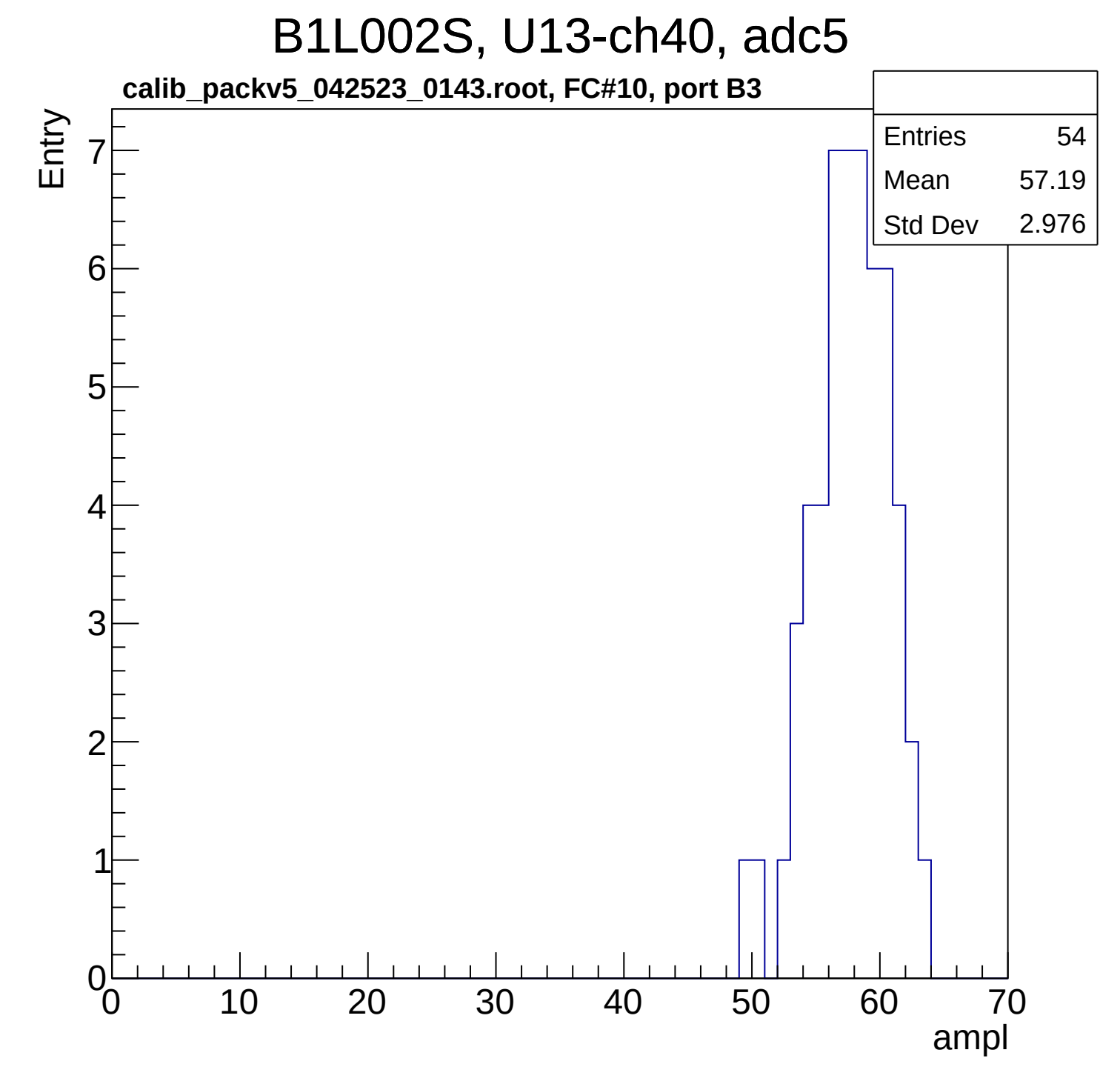
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.19
Std Dev 2.976
Entry
ampl
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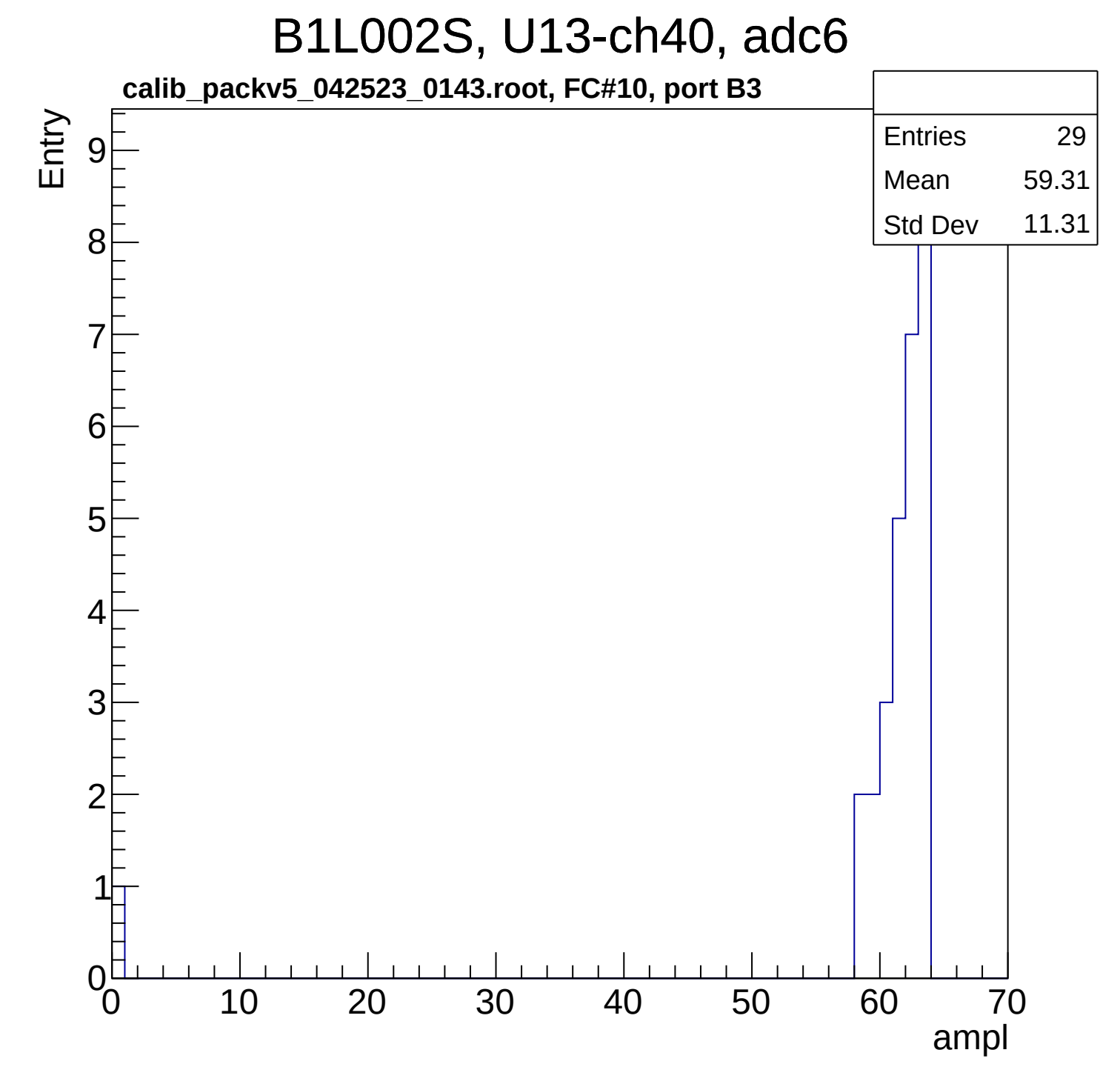
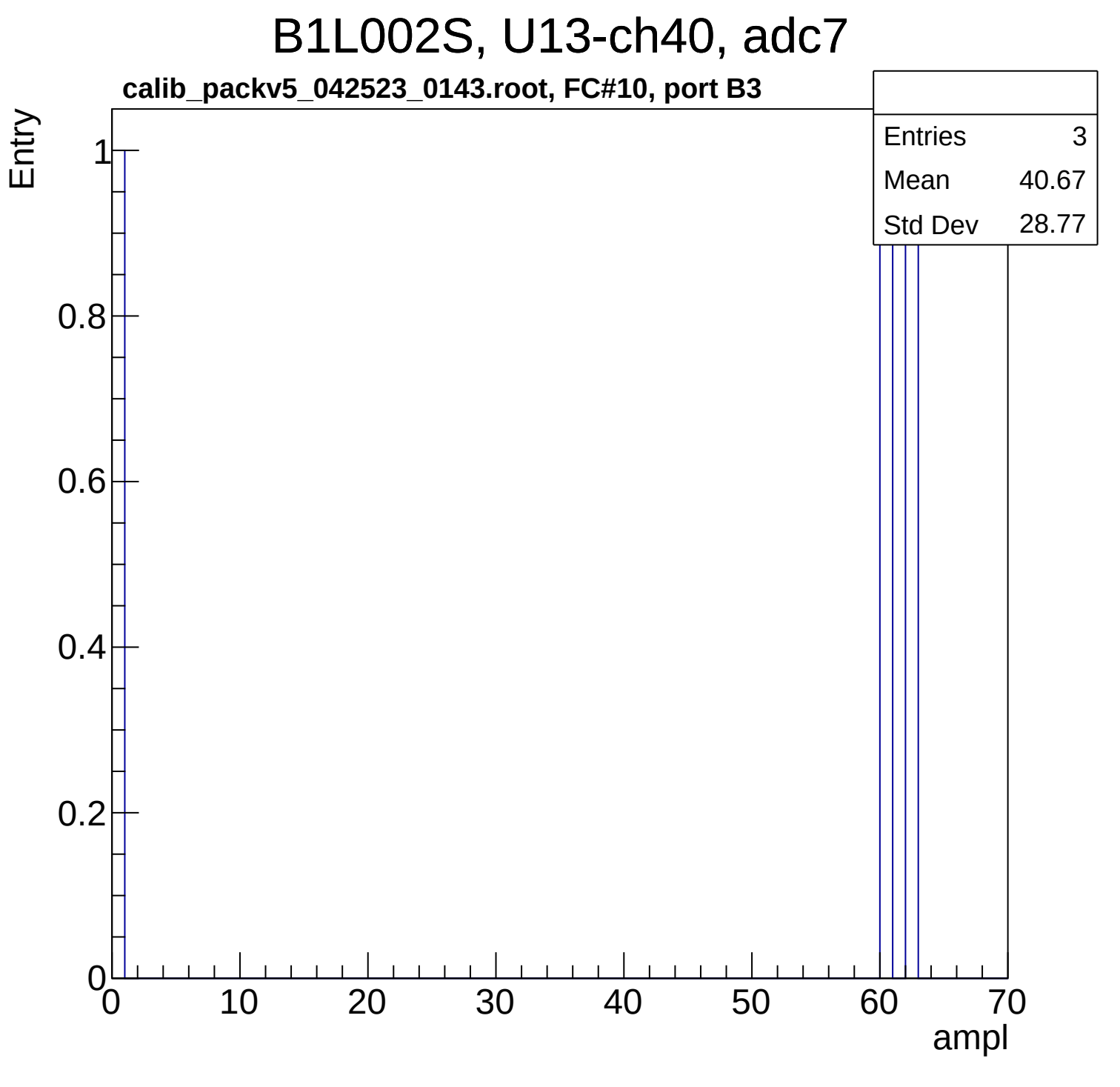
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.31
Std Dev 11.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 40.67
Std Dev 28.77
Entry
ampl
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0.4
0.6
0.8
1
0
10
20
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40
50
60
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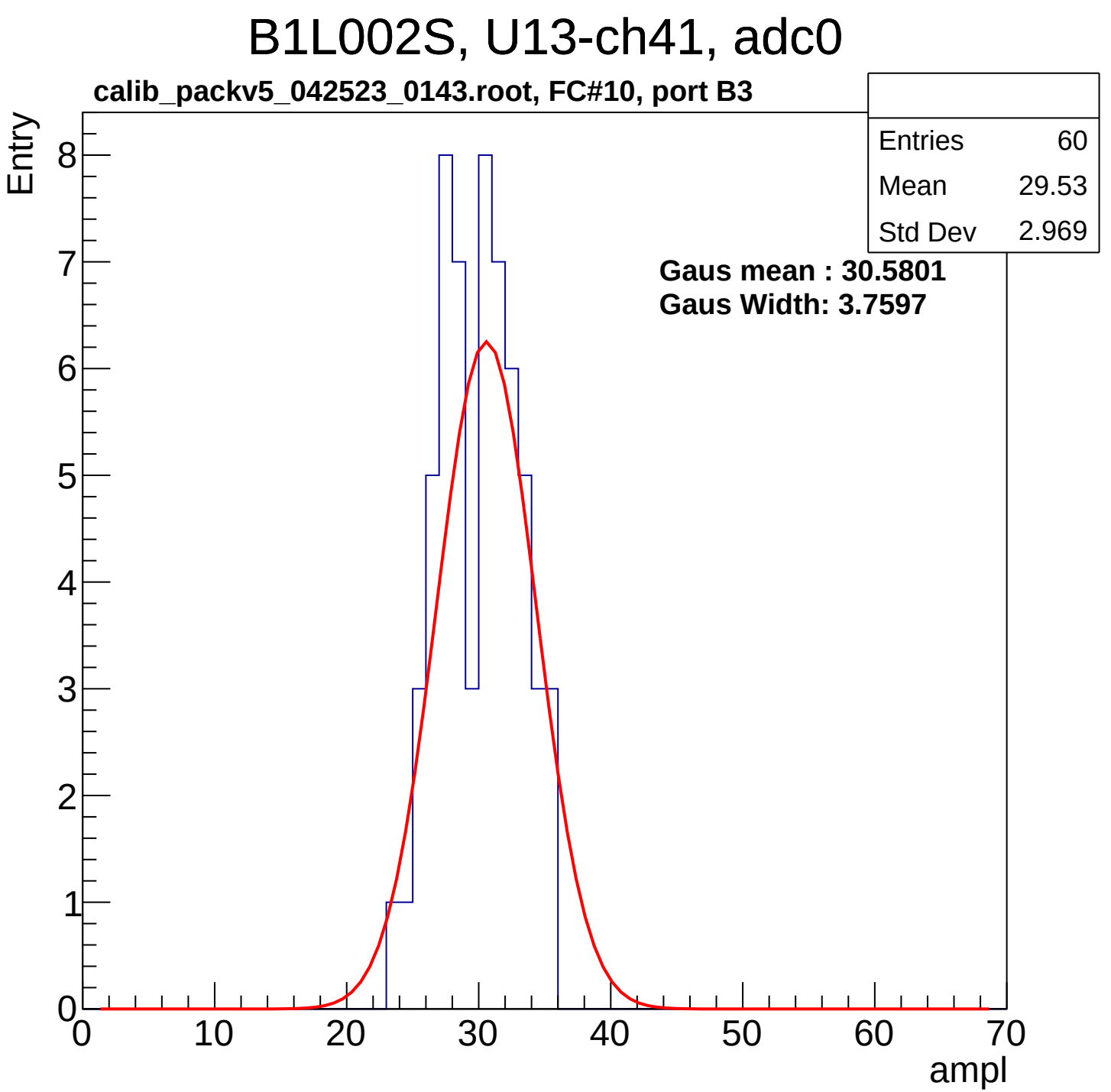
B1L002S, U13-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.53
Std Dev 2.969
Gaus mean : 30.5801
Gaus Width: 3.7597
Entry
ampl
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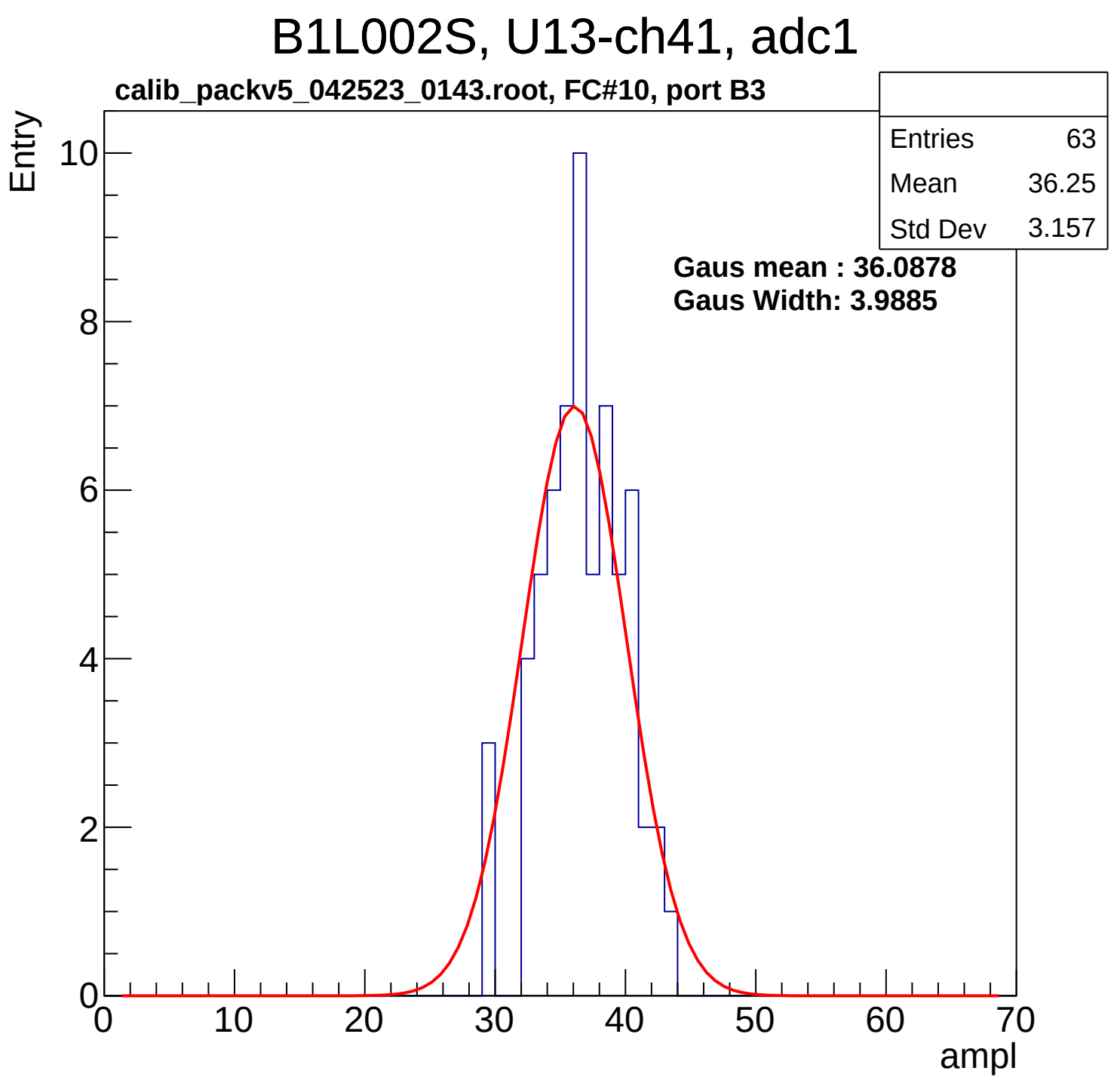
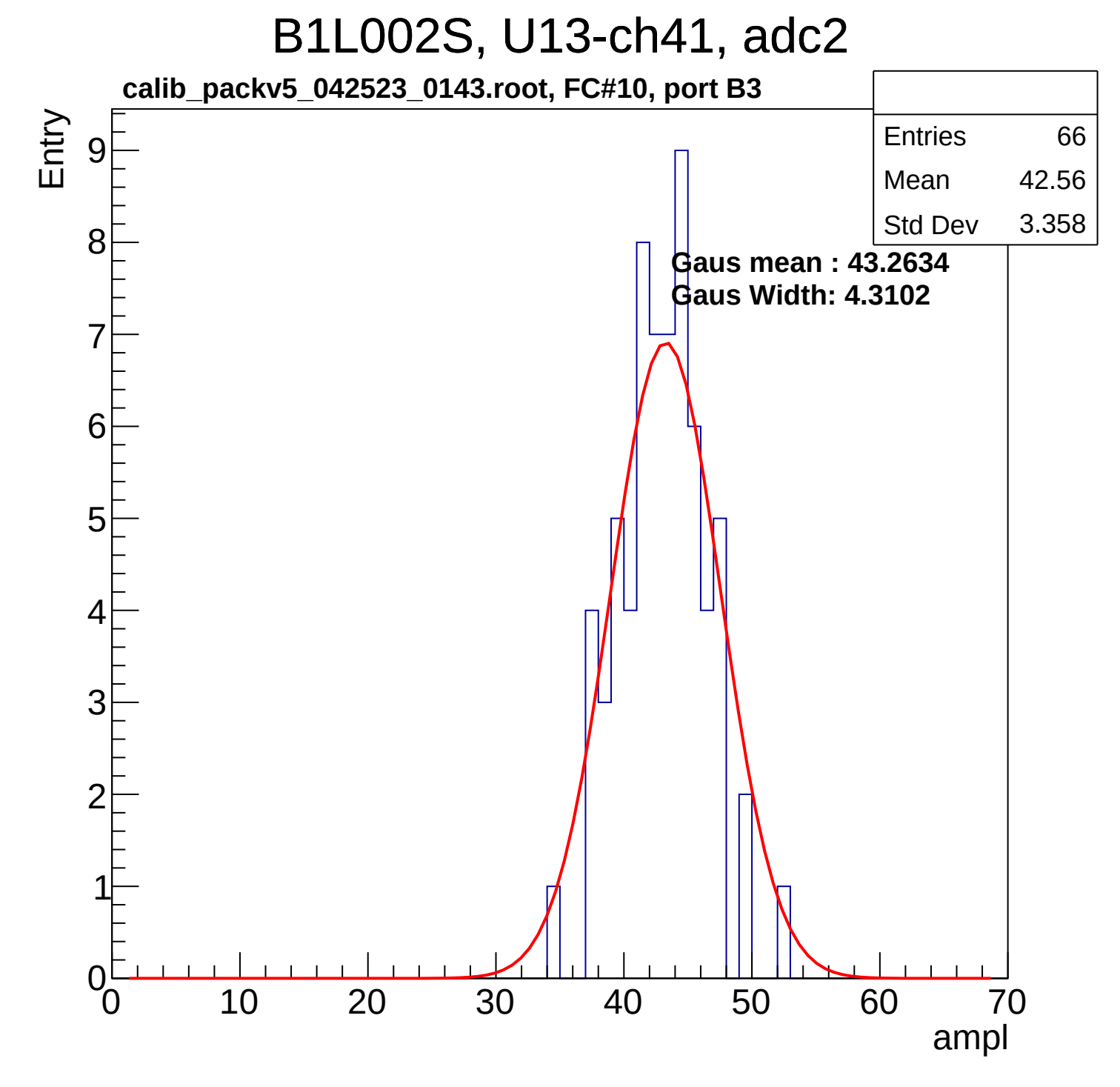
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.25
Std Dev 3.157
Gaus mean : 36.0878
Gaus Width: 3.9885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.56
Std Dev 3.358
Gaus mean : 43.2634
Gaus Width: 4.3102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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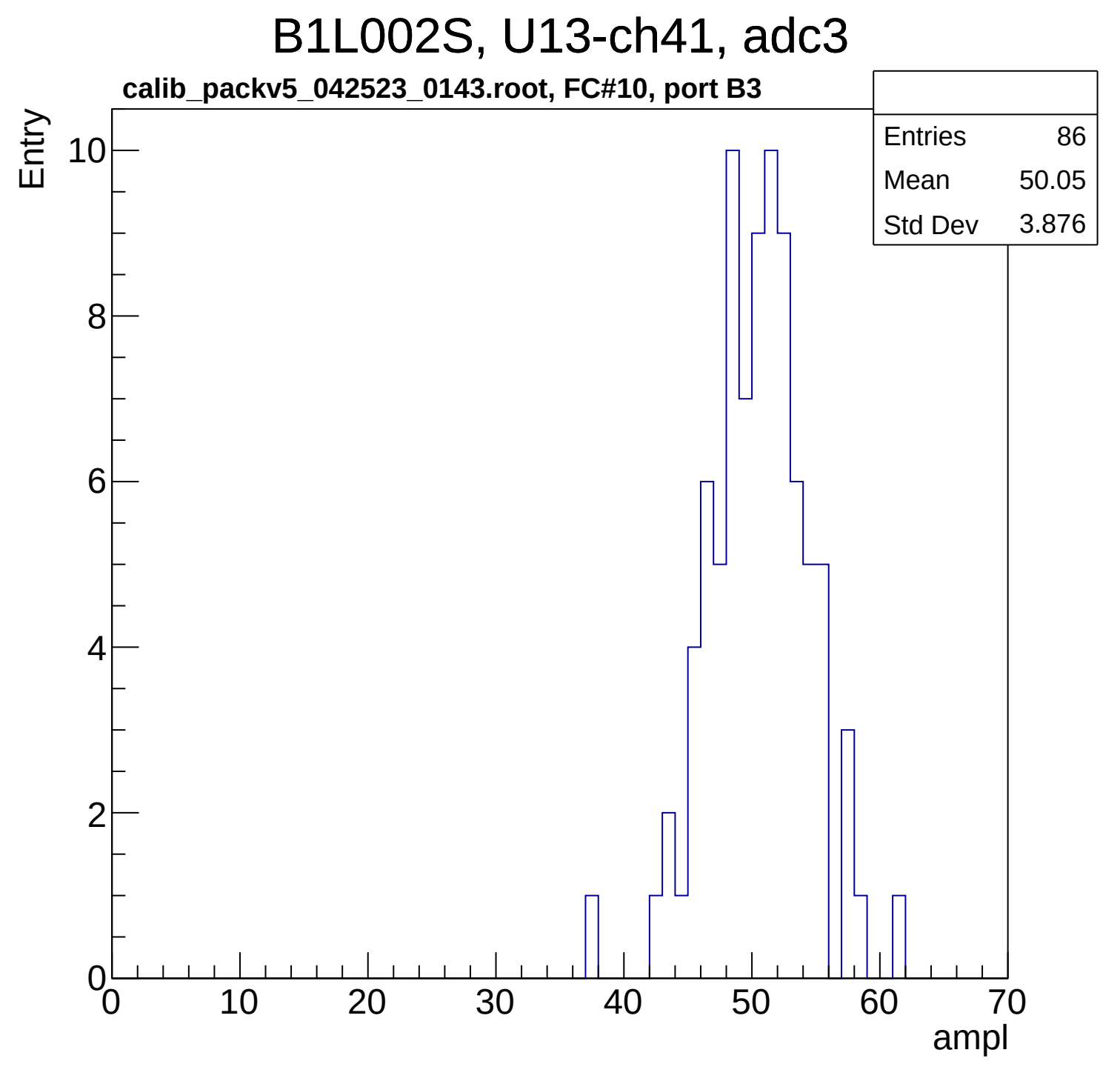
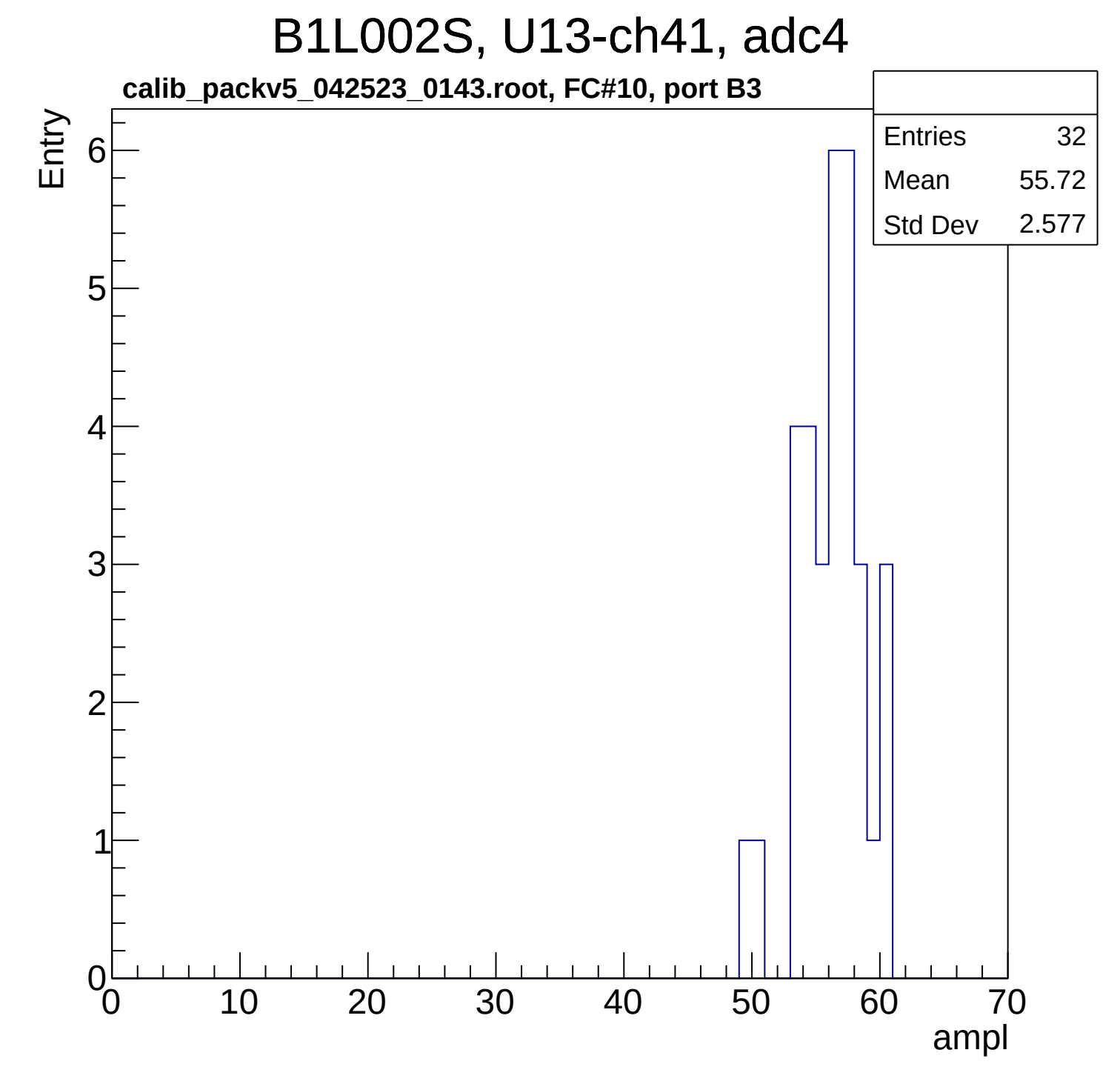

B1L002S, U13-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 50.05
Std Dev 3.876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 55.72
Std Dev 2.577
Entry
ampl
0
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5
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0
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20
30
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50
60
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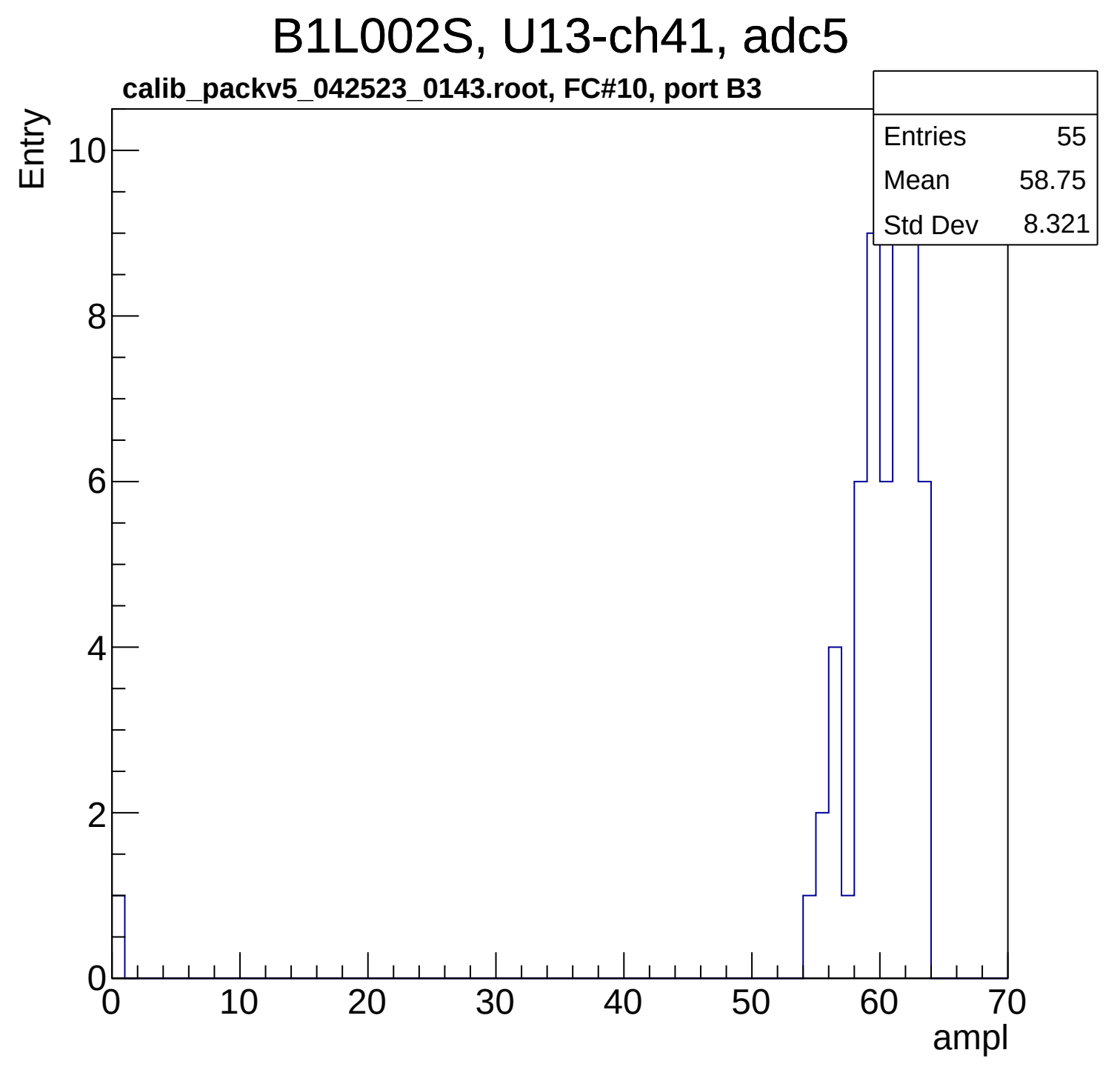
B1L002S, U13-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.75
Std Dev 8.321
Entry
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4
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10
20
30
40
50
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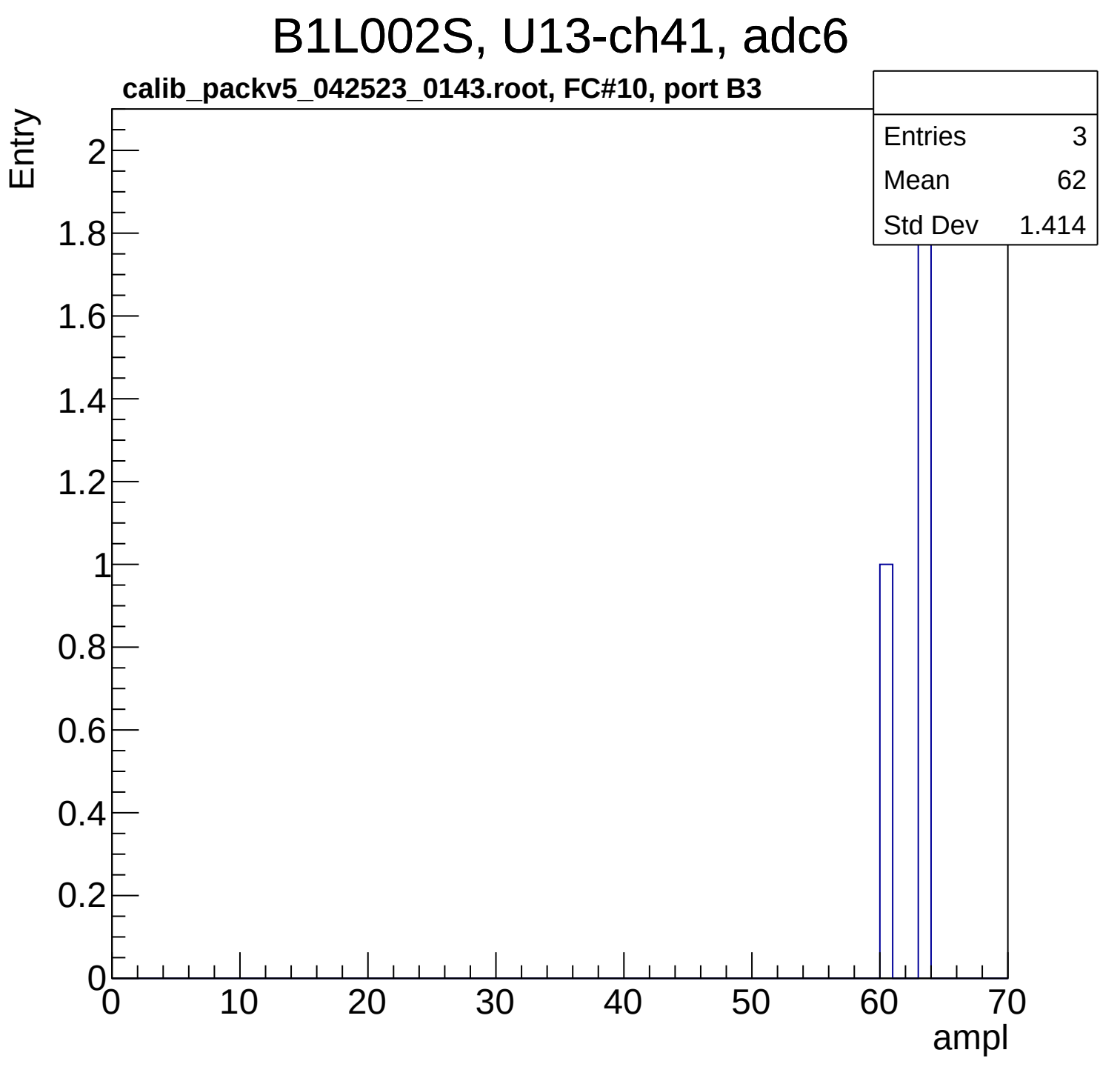
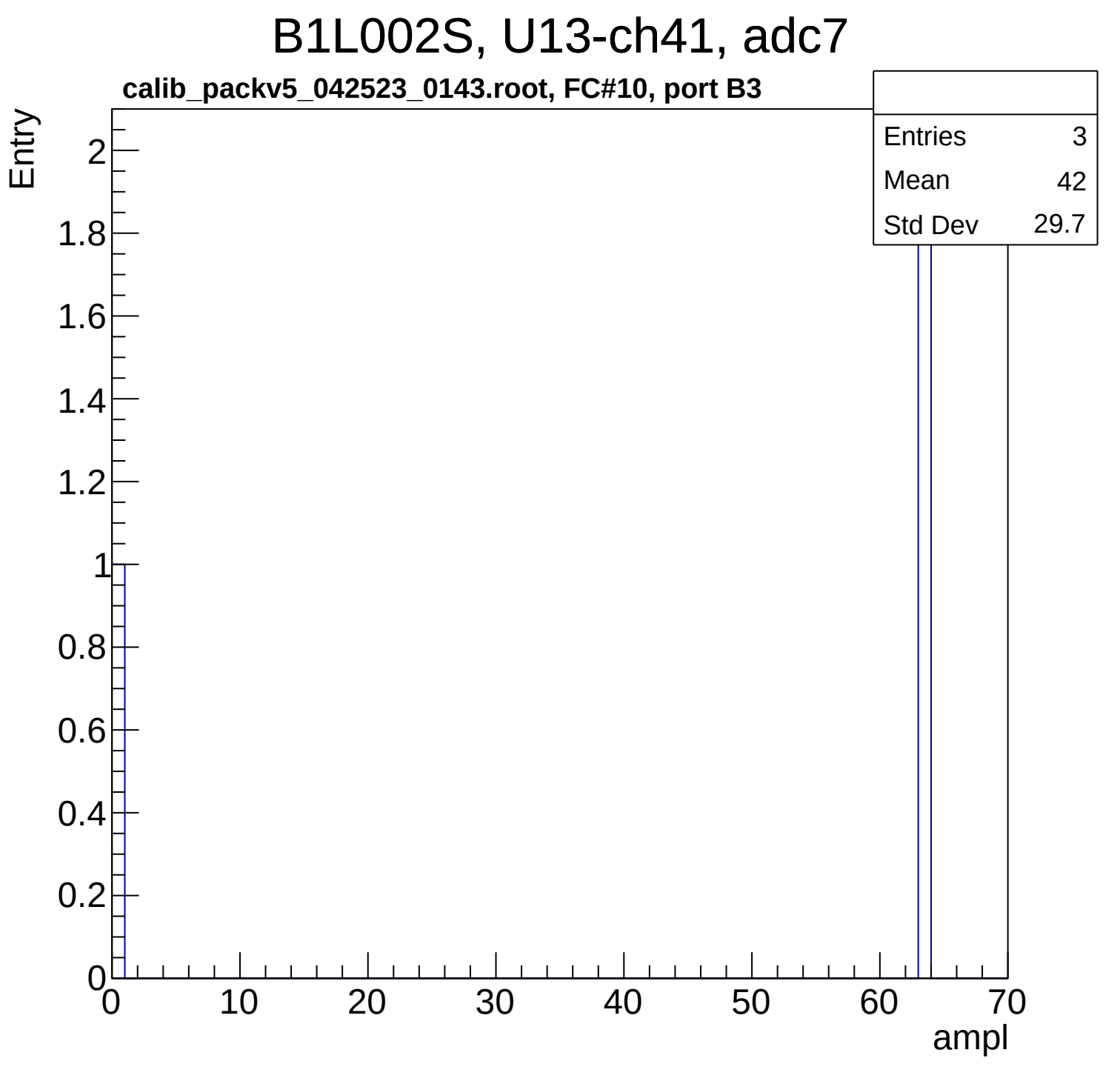
70
ampl


B1L002S, U13-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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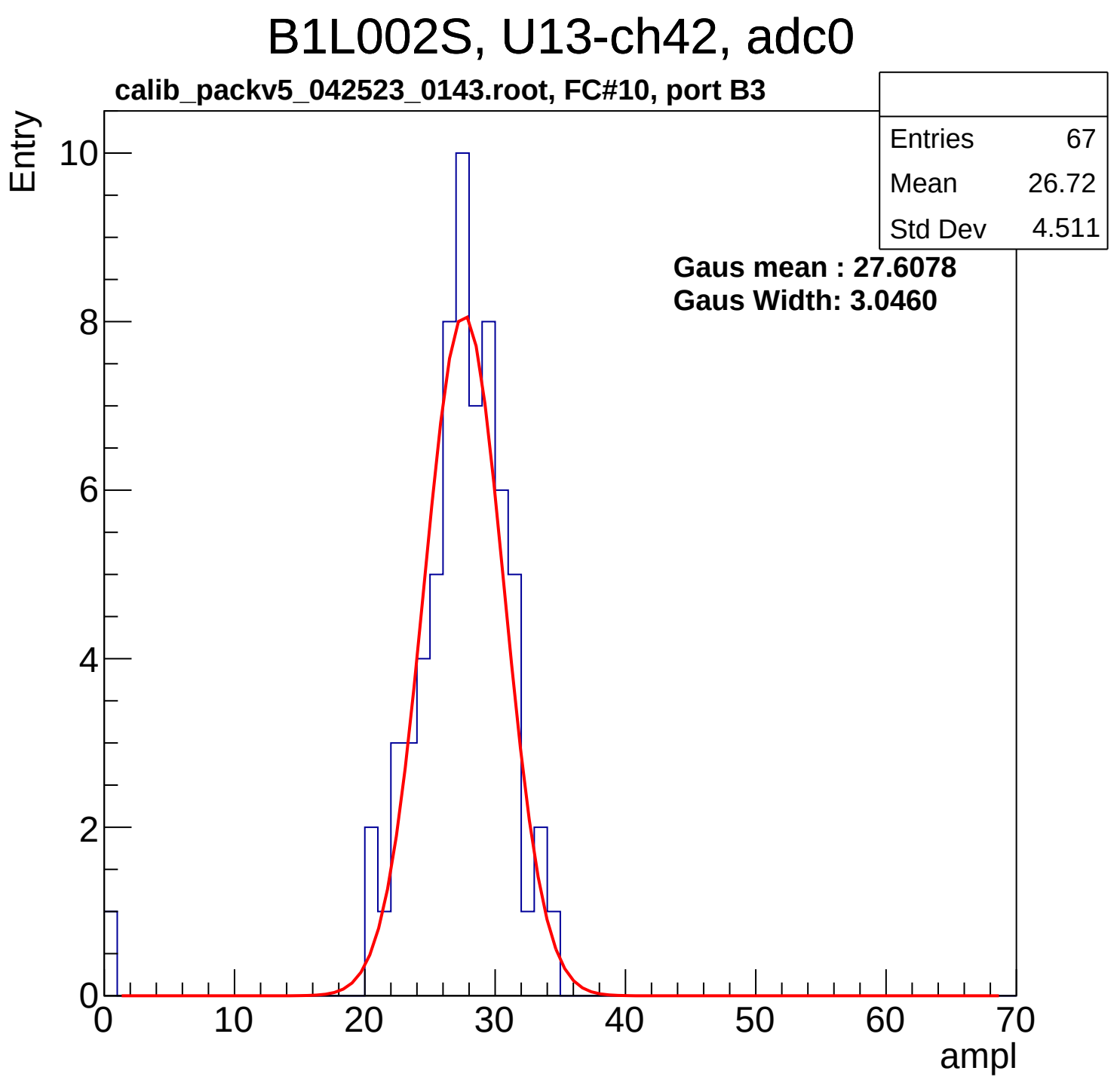
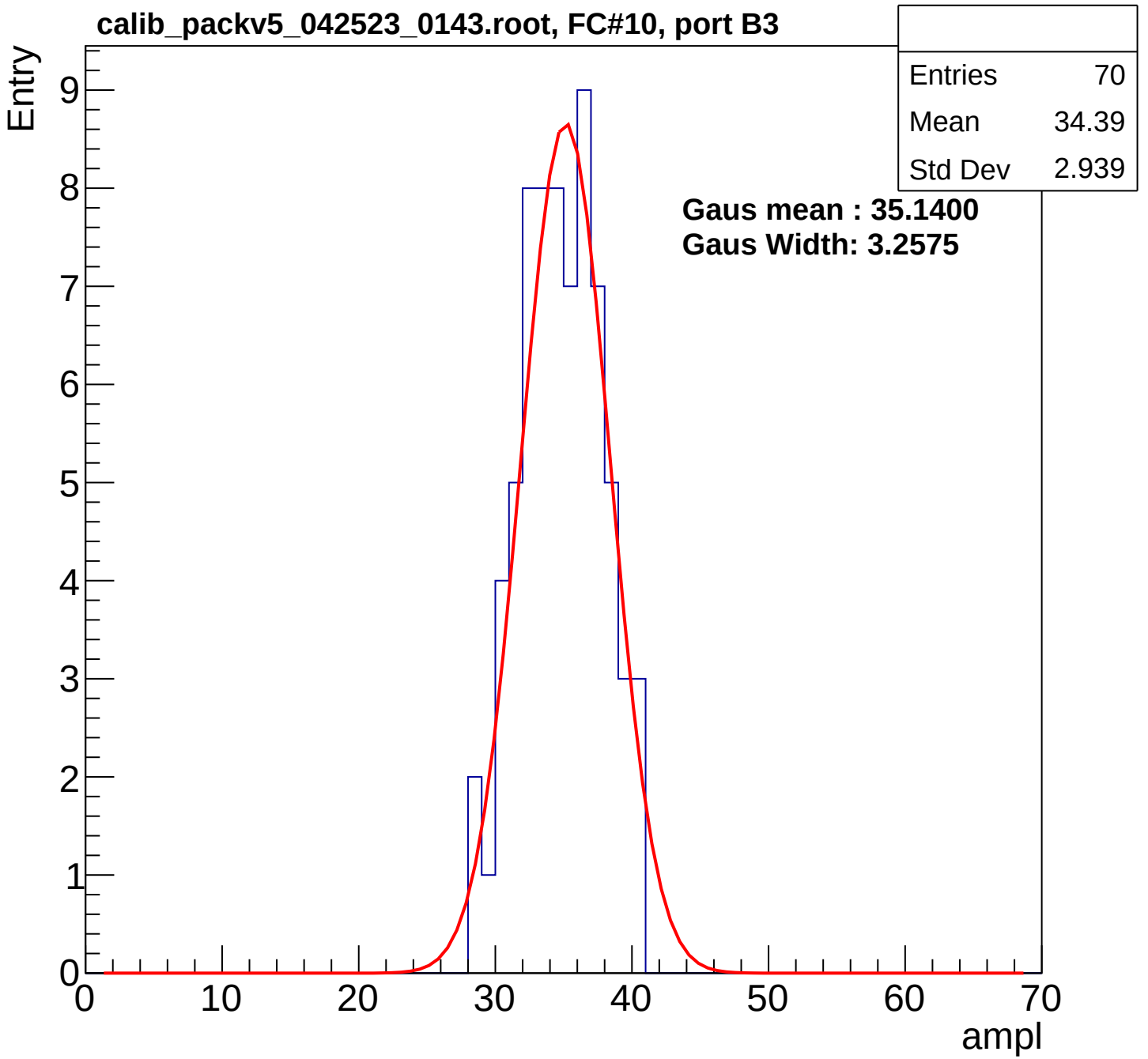

B1L002S, U13-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 26.72
Std Dev 4.511
Gaus mean : 27.6078
Gaus Width: 3.0460
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.39
Std Dev 2.939
Gaus mean : 35.1400
Gaus Width: 3.2575
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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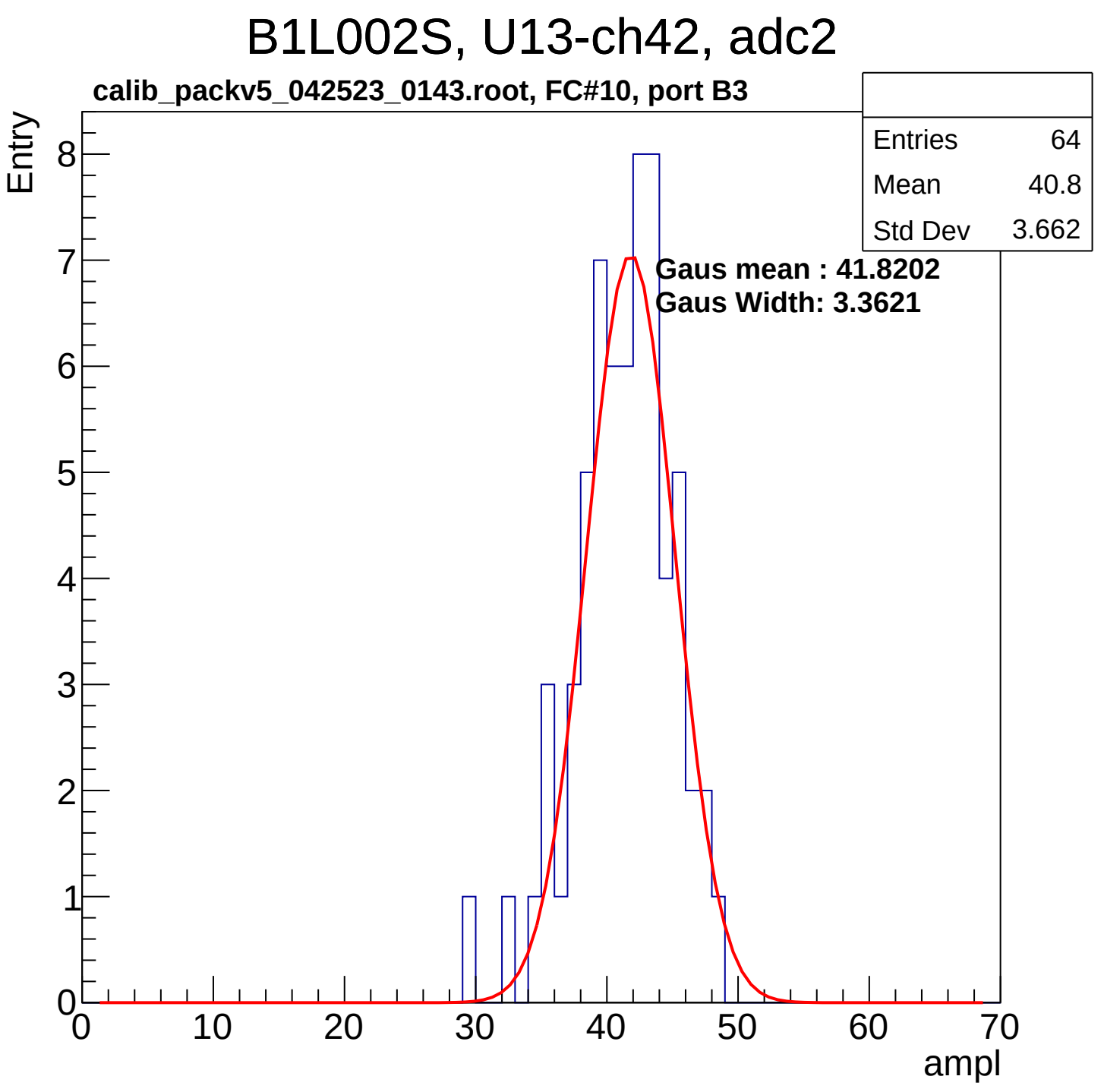
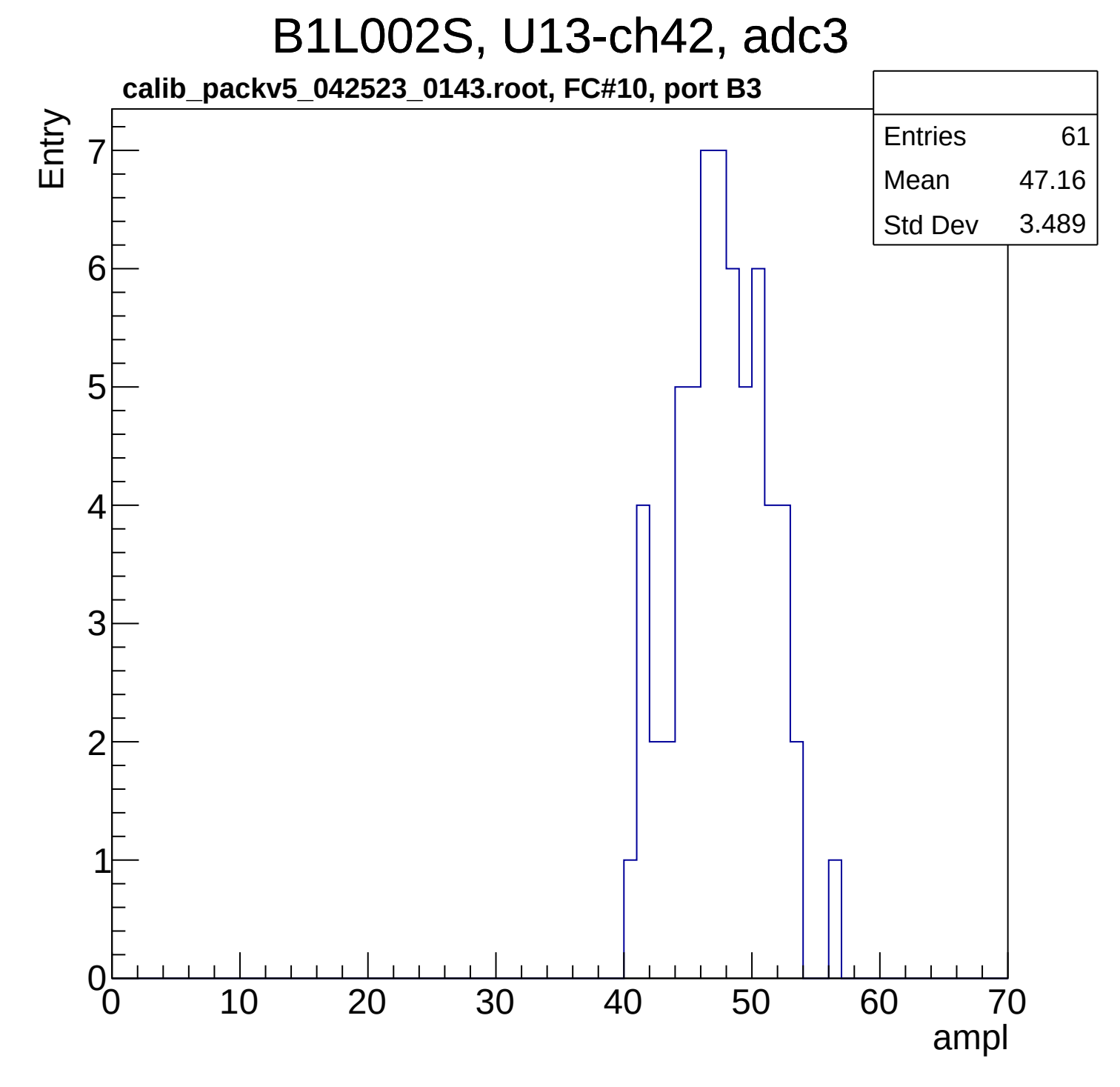

B1L002S, U13-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.8
Std Dev 3.662
Gaus mean : 41.8202
Gaus Width: 3.3621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.16
Std Dev 3.489
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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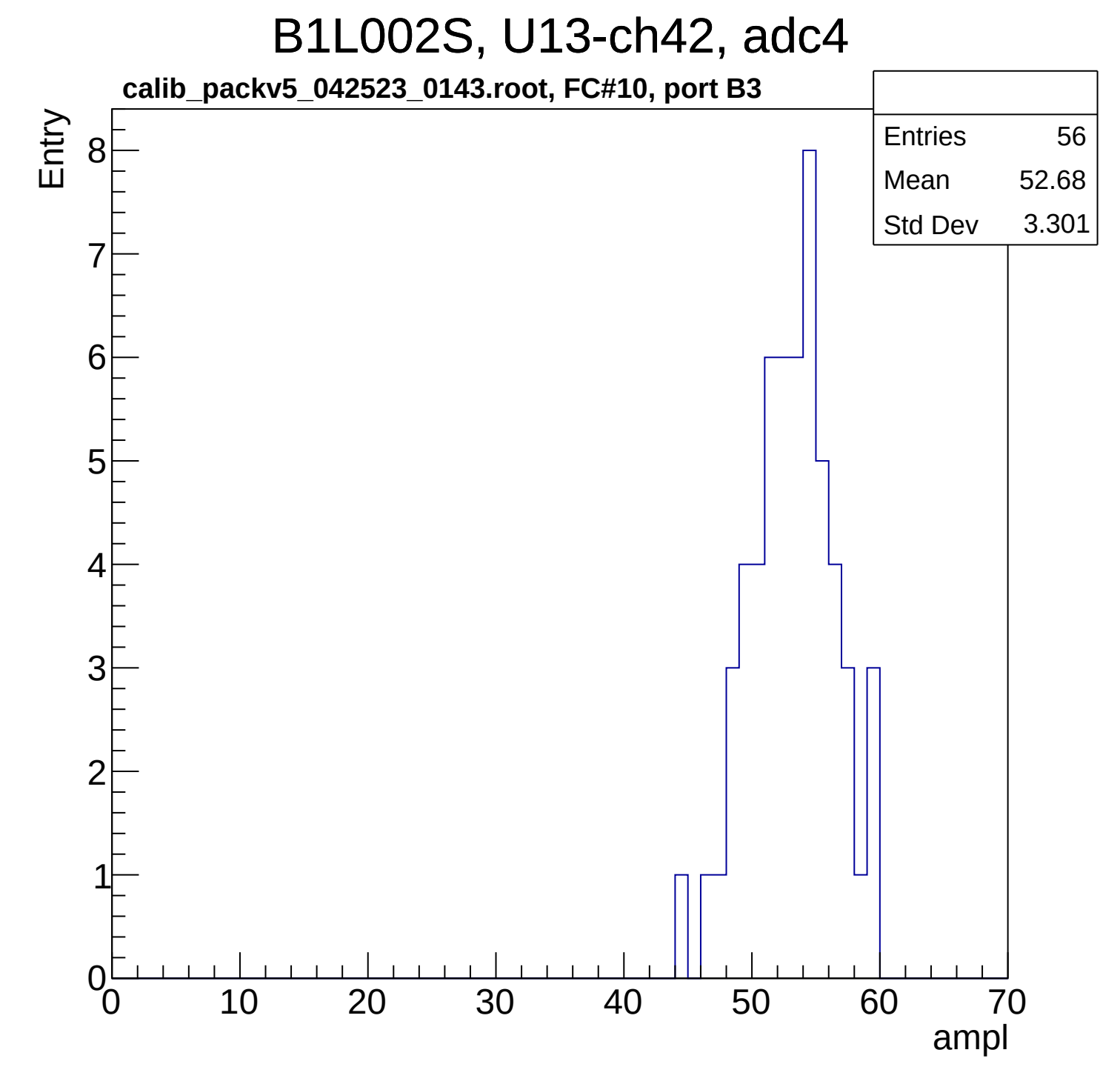
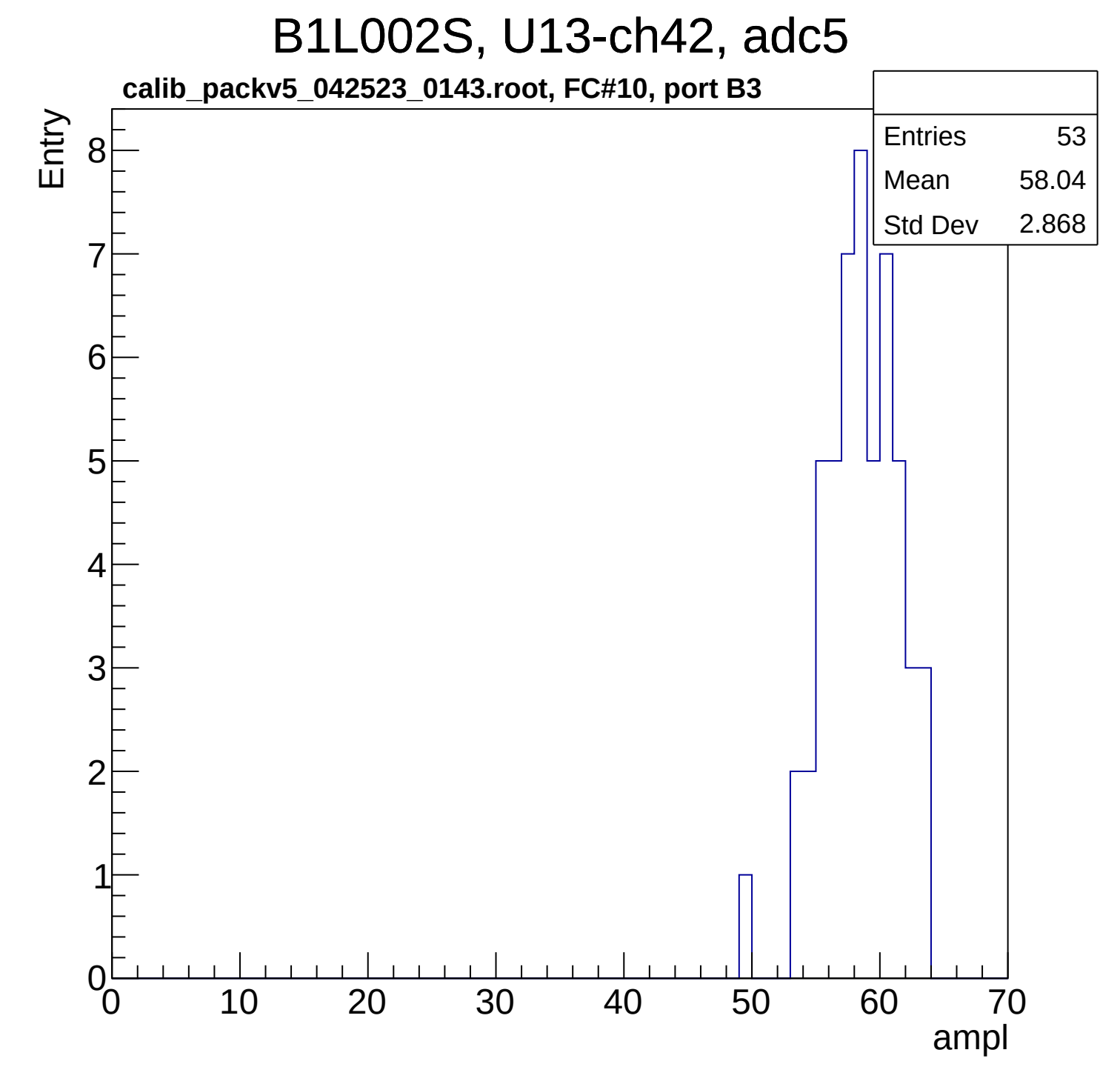

B1L002S, U13-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.68
Std Dev 3.301
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.04
Std Dev 2.868
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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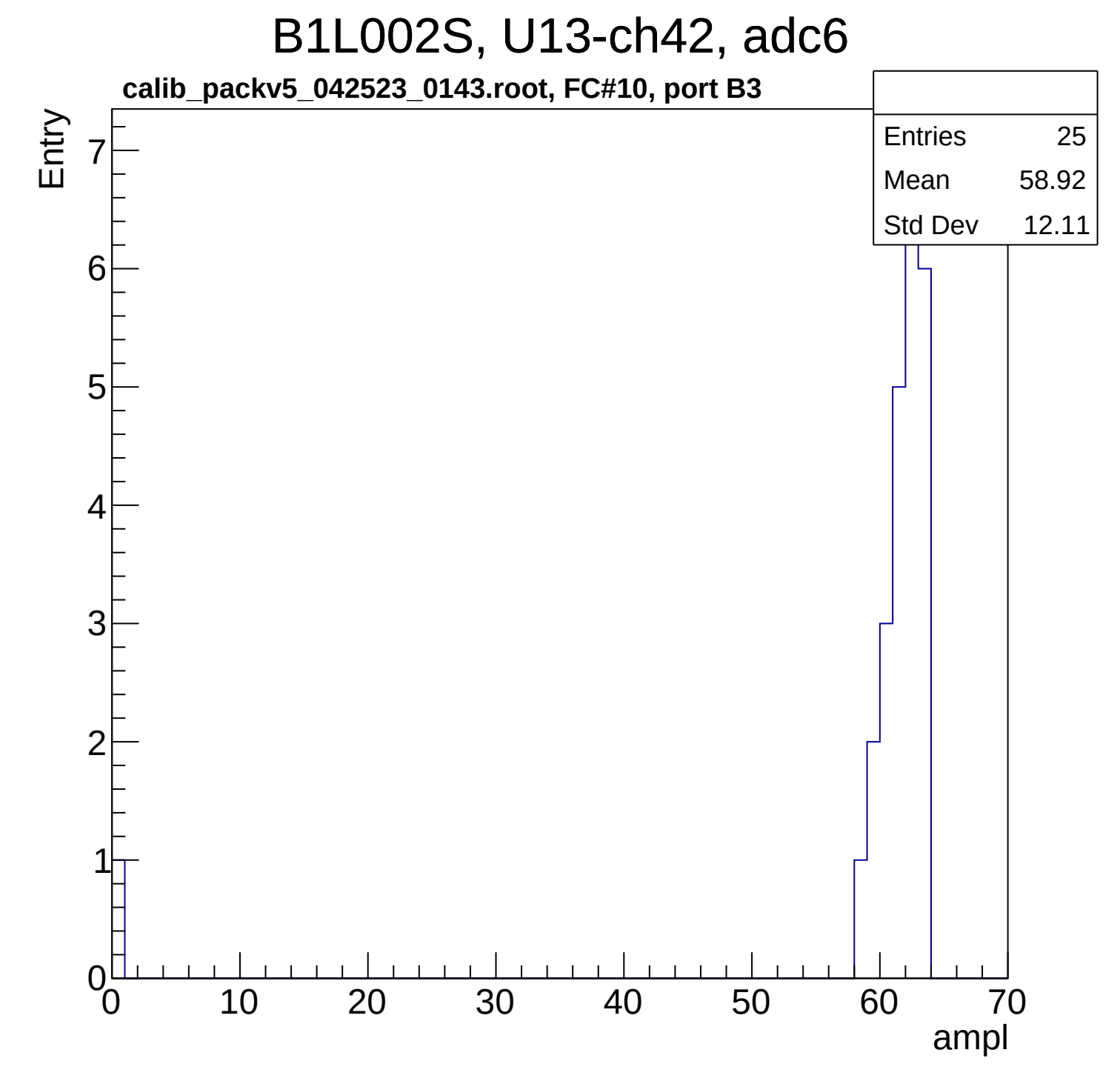
B1L002S, U13-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.92
Std Dev 12.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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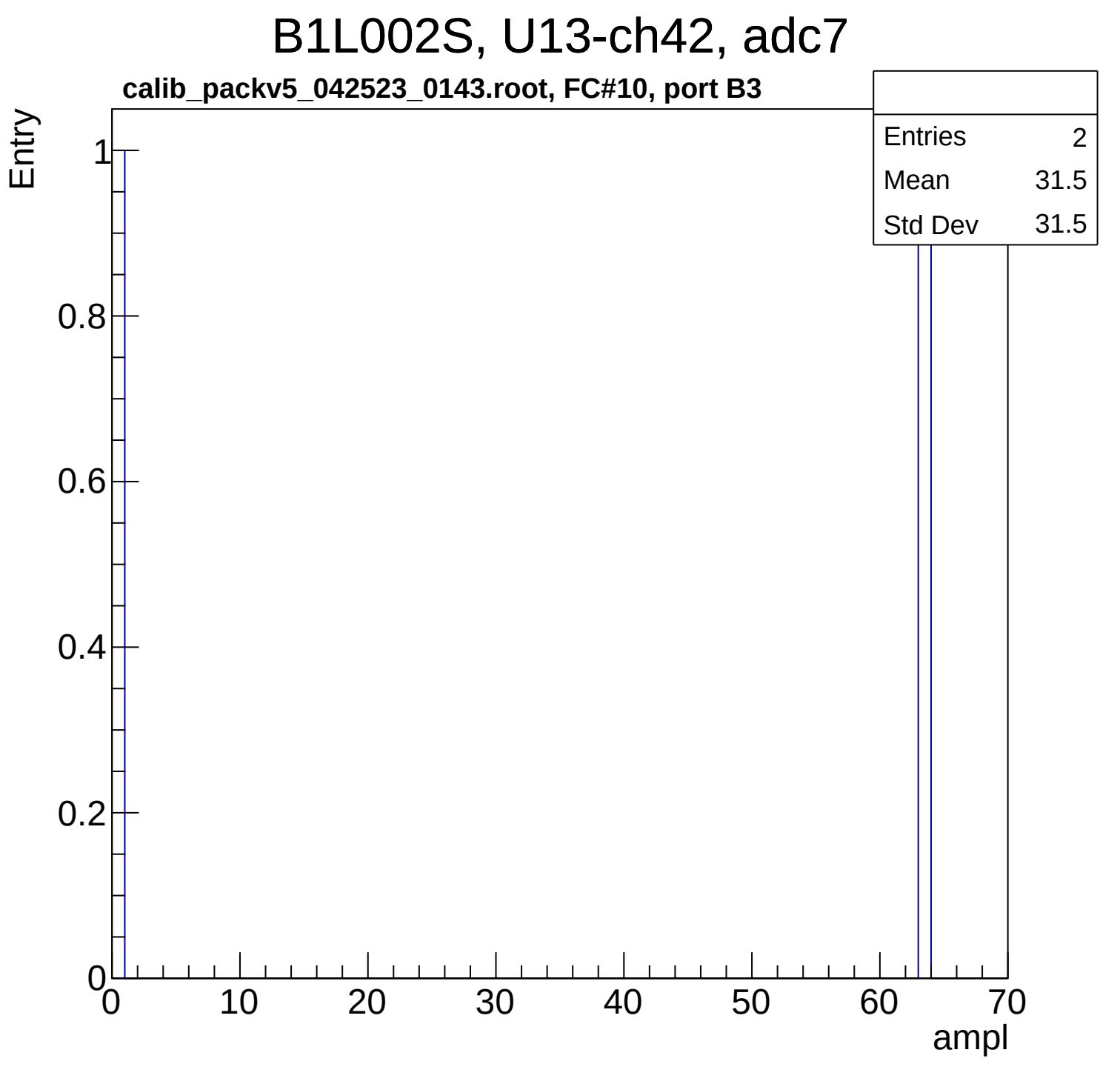
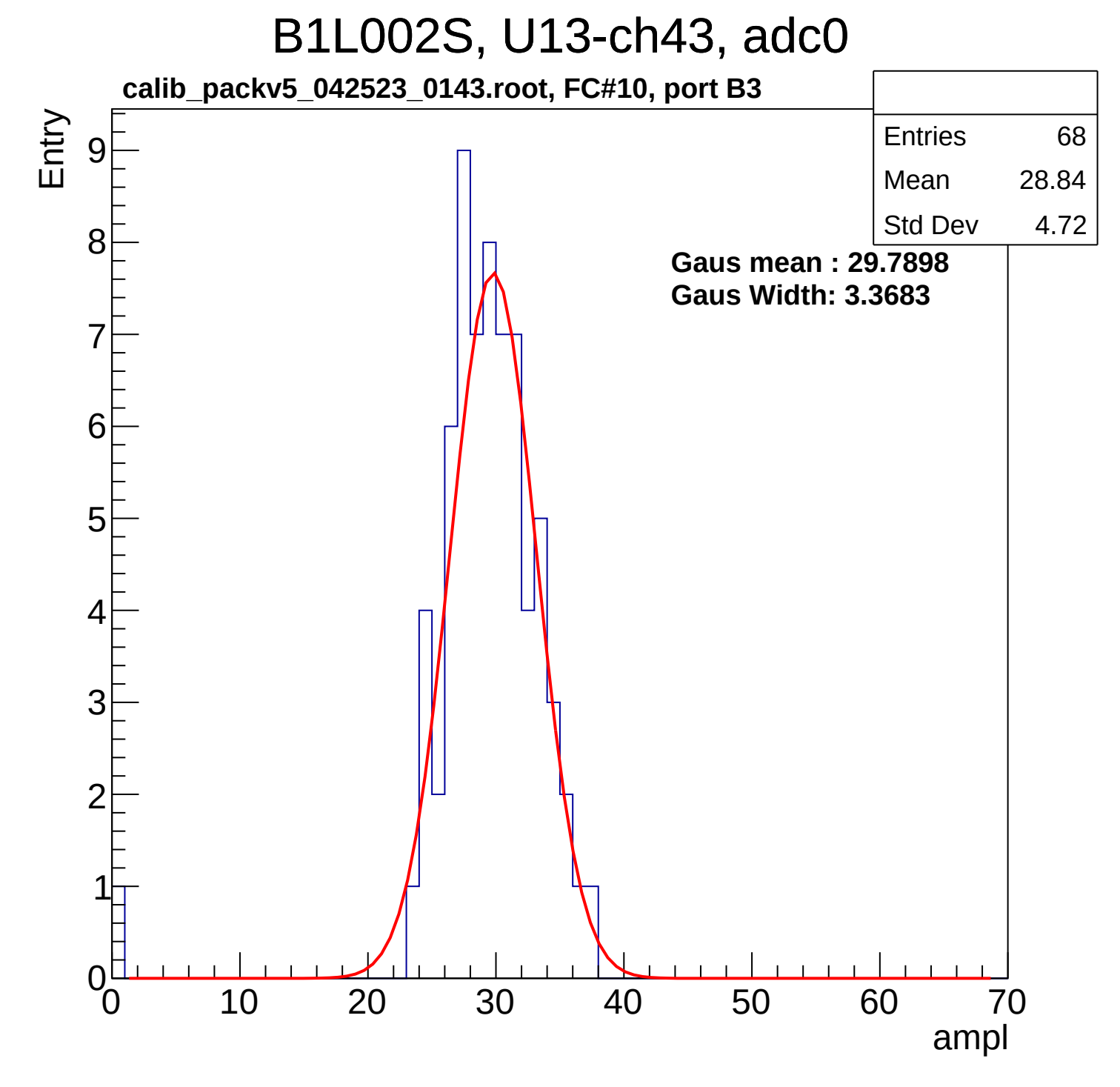

B1L002S, U13-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.84
Std Dev 4.72
Gaus mean : 29.7898
Gaus Width: 3.3683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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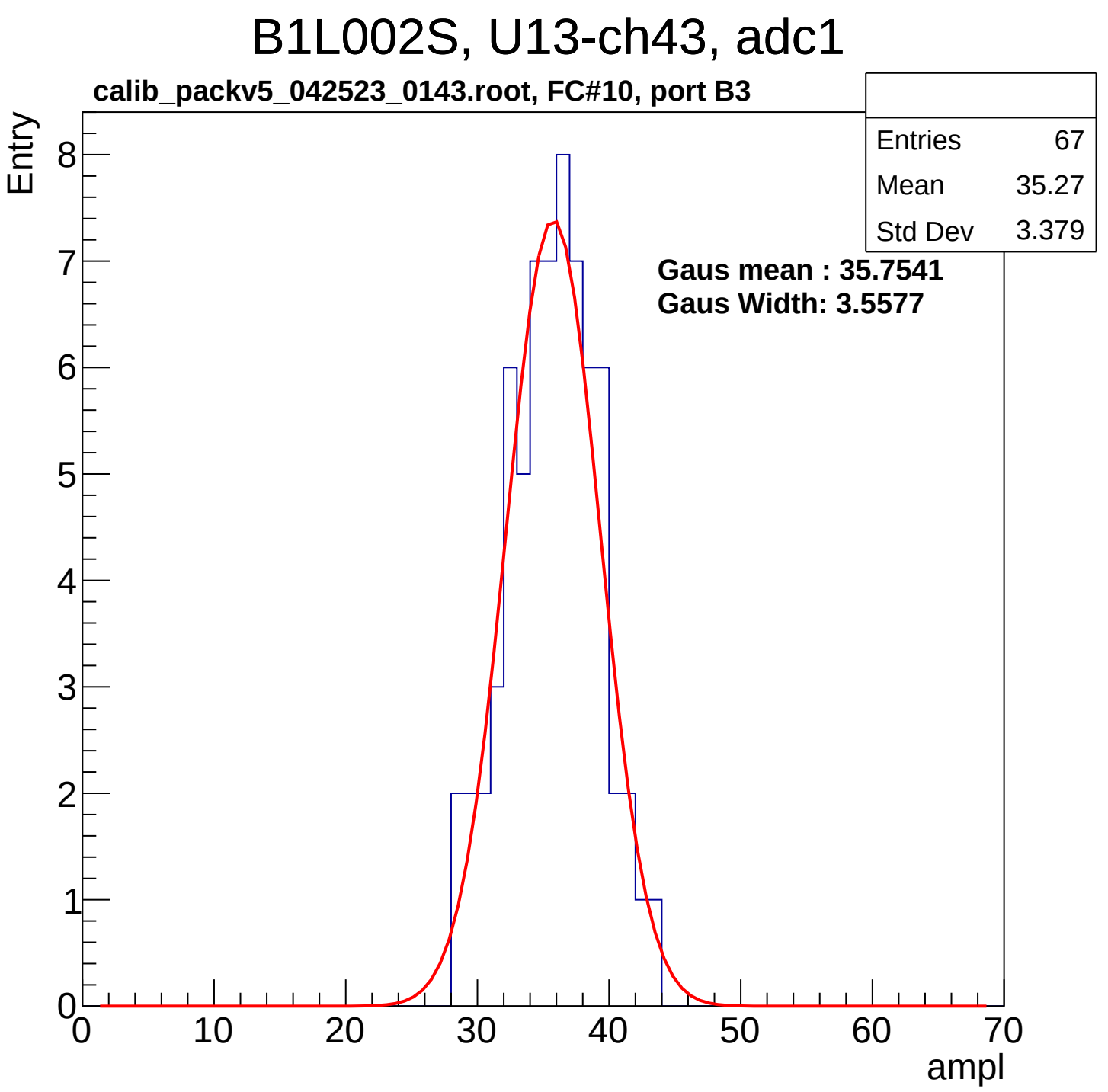
B1L002S, U13-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.27
Std Dev 3.379
Gaus mean : 35.7541
Gaus Width: 3.5577
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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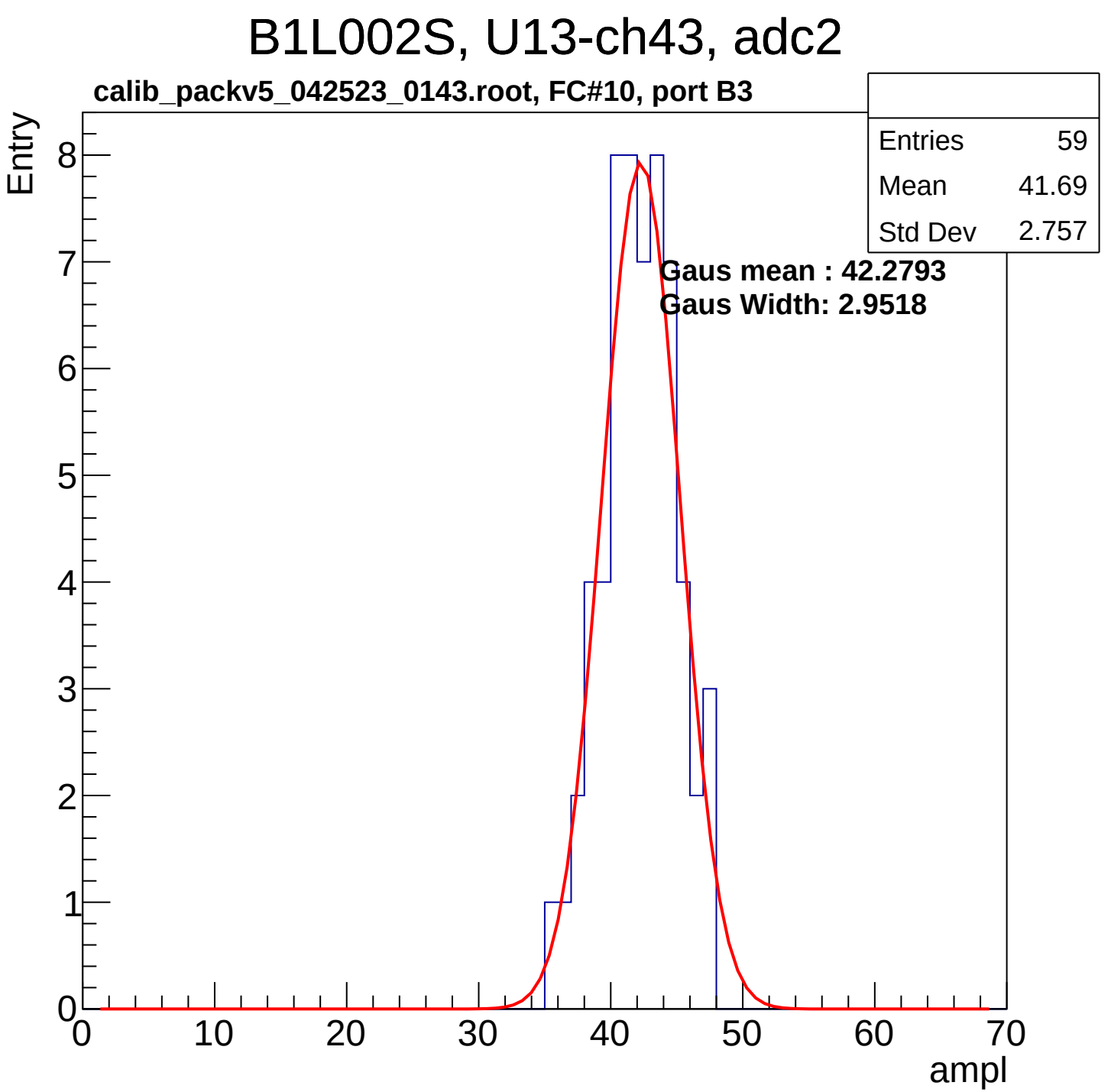
B1L002S, U13-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.69
Std Dev 2.757
Gaus mean : 42.2793
Gaus Width: 2.9518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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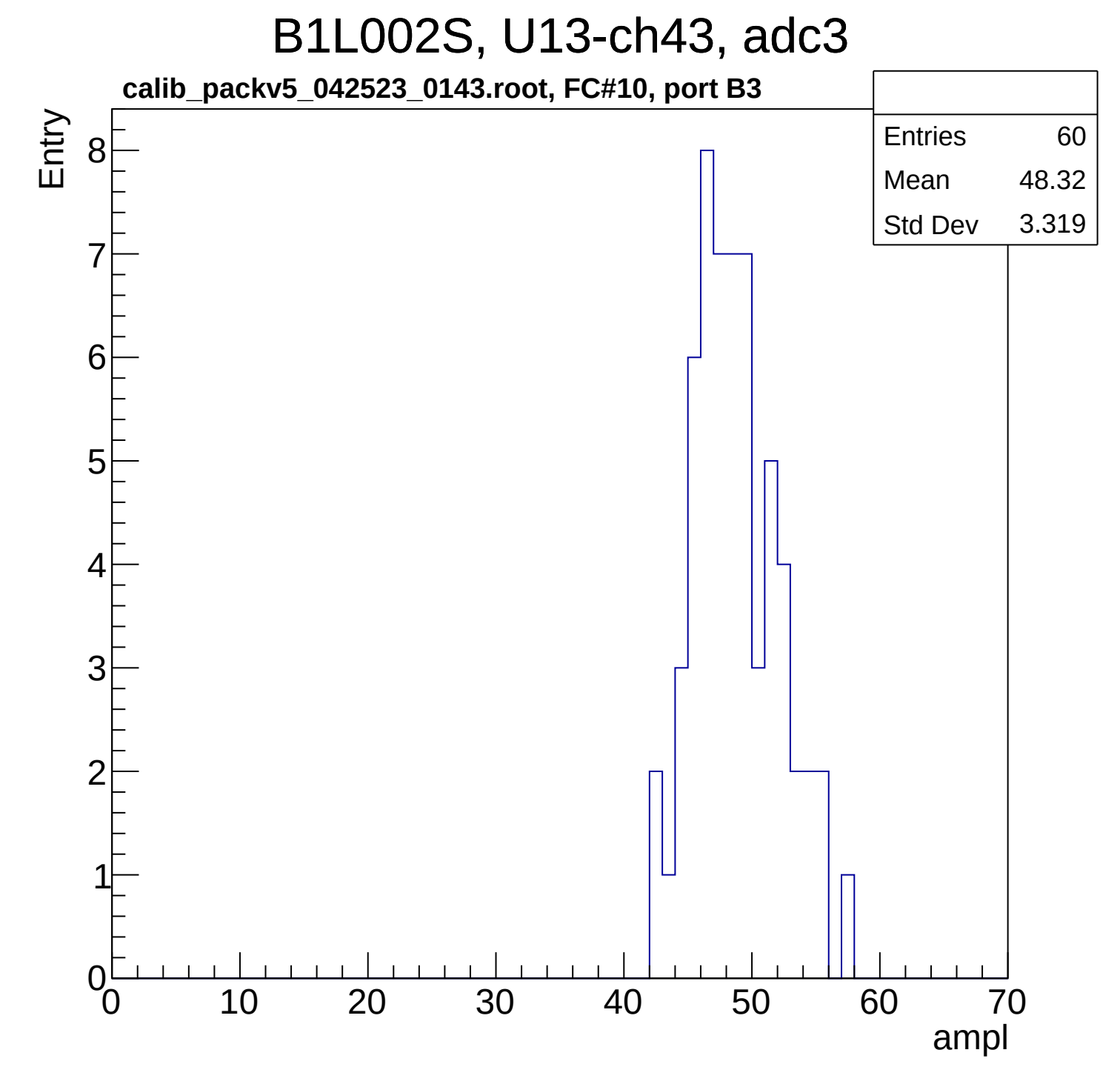
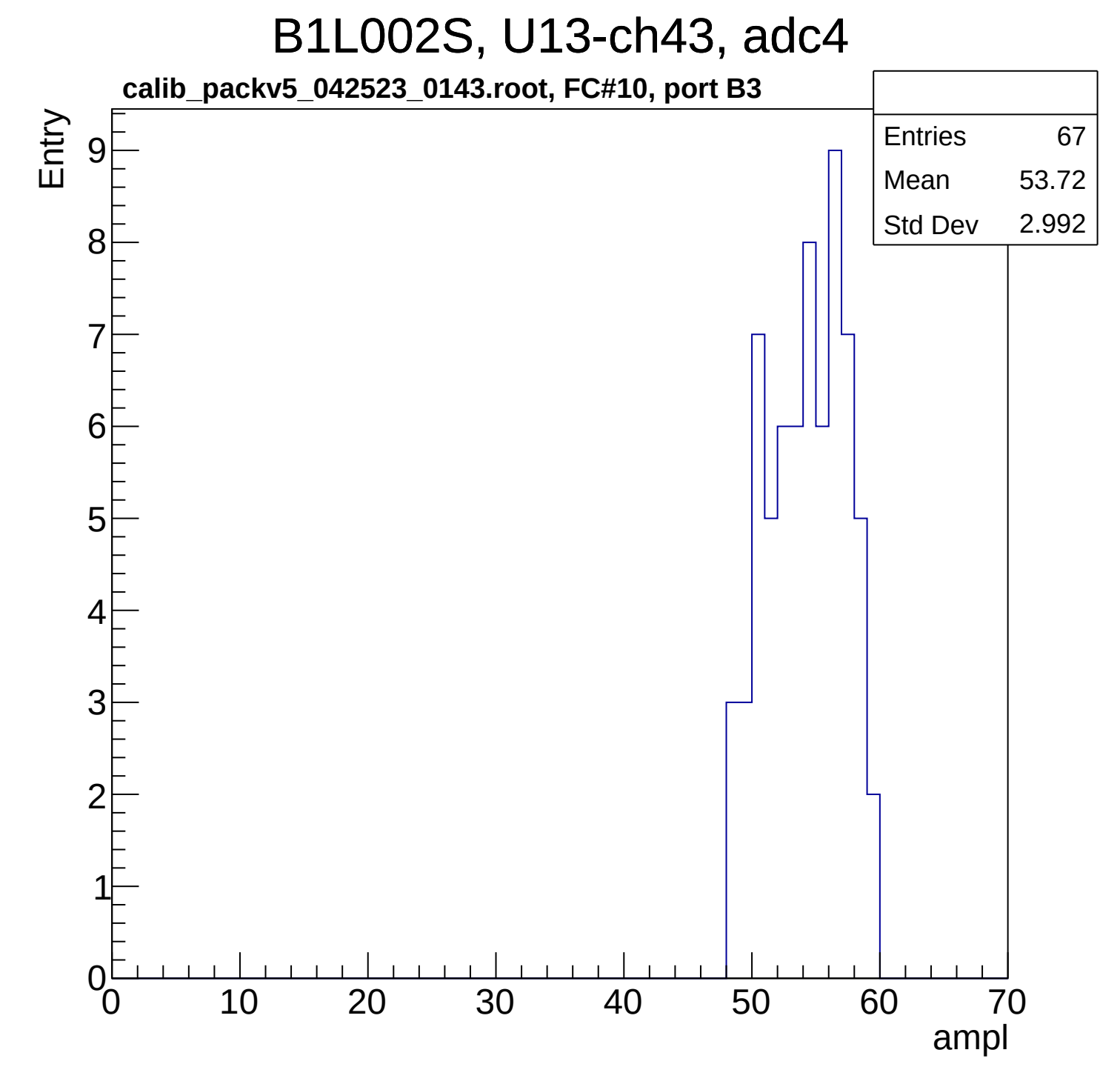

B1L002S, U13-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.32
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.72
Std Dev 2.992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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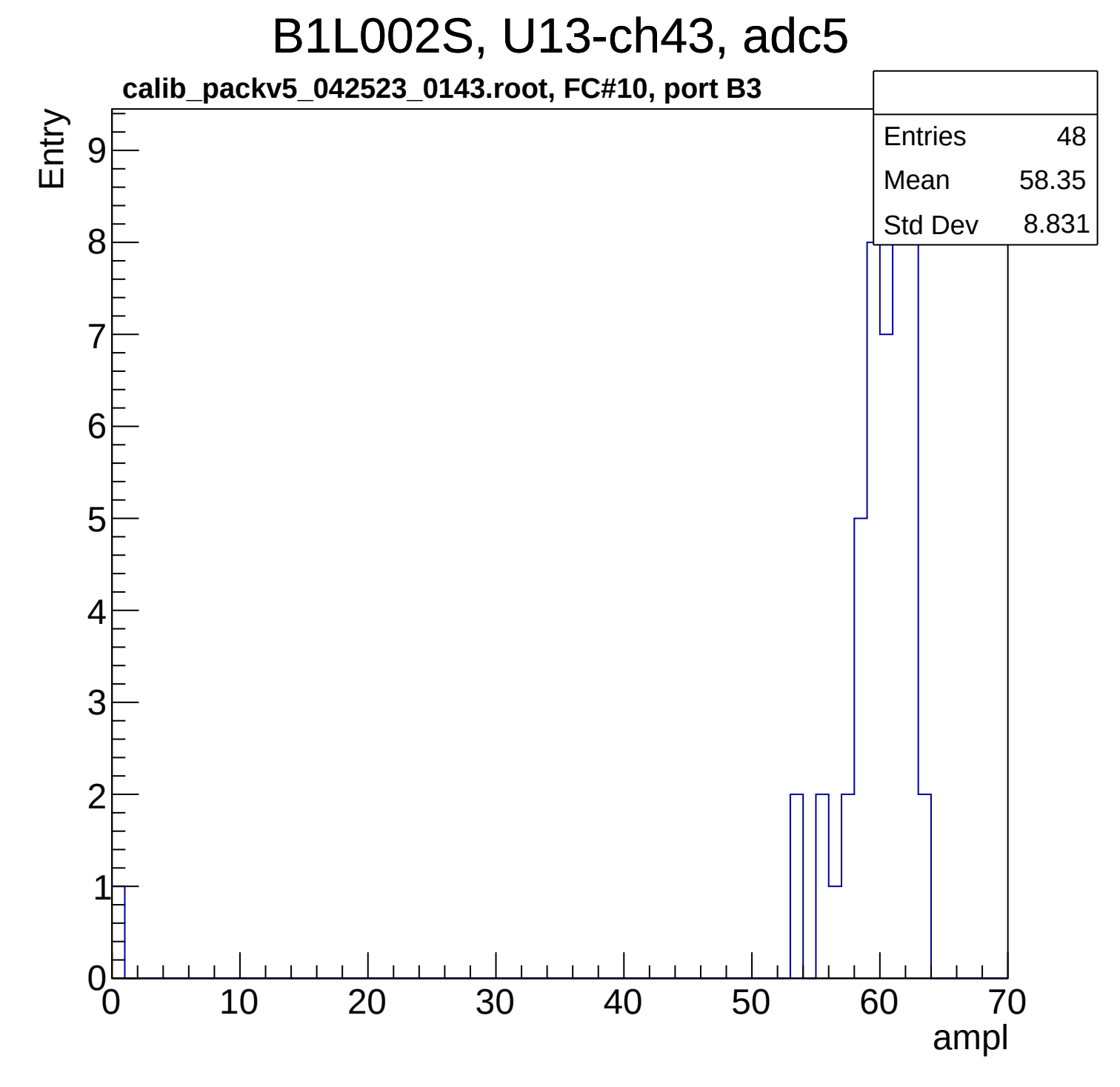
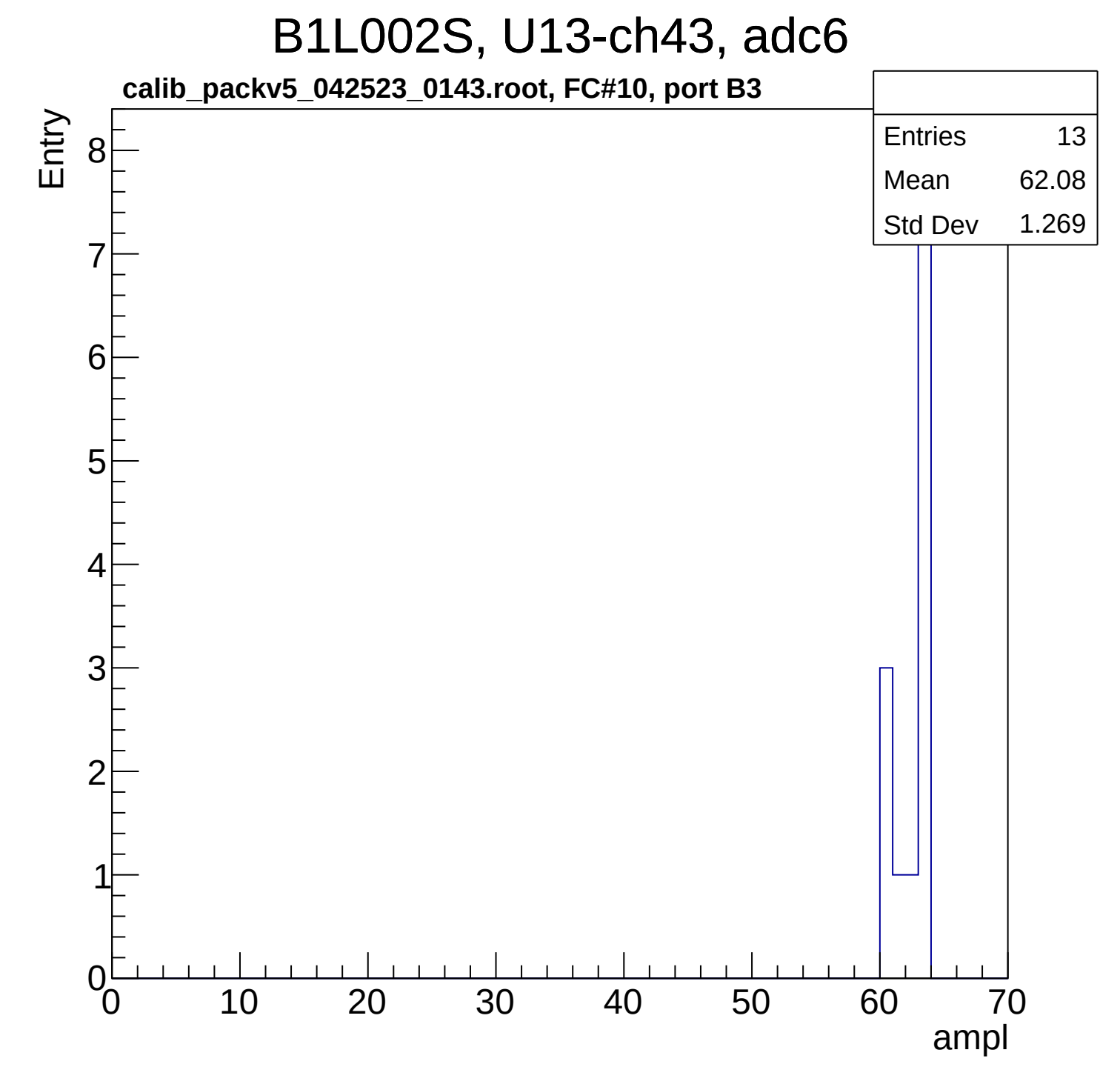

B1L002S, U13-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.35
Std Dev 8.831
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 62.08
Std Dev 1.269
Entry
ampl
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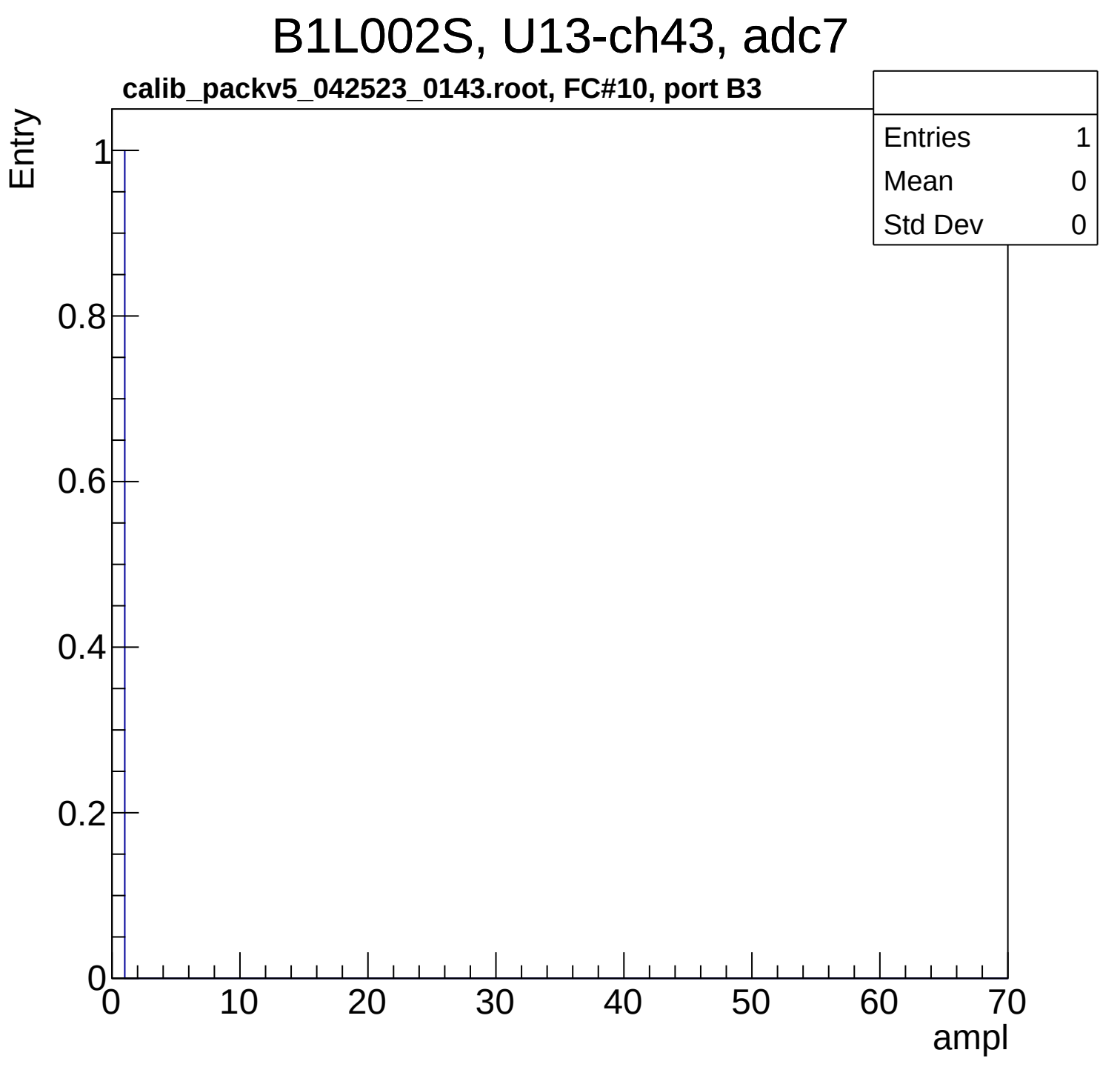
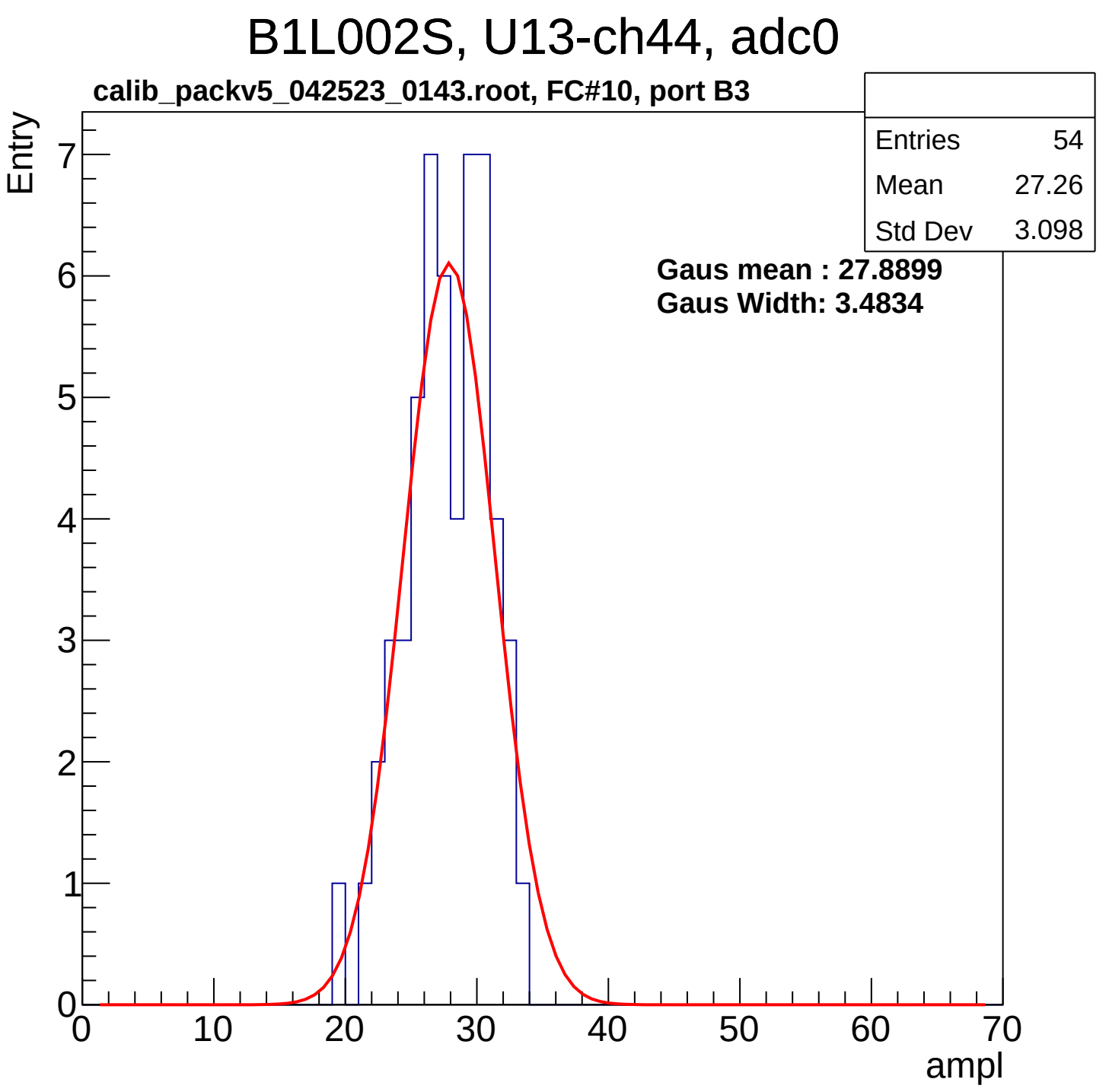
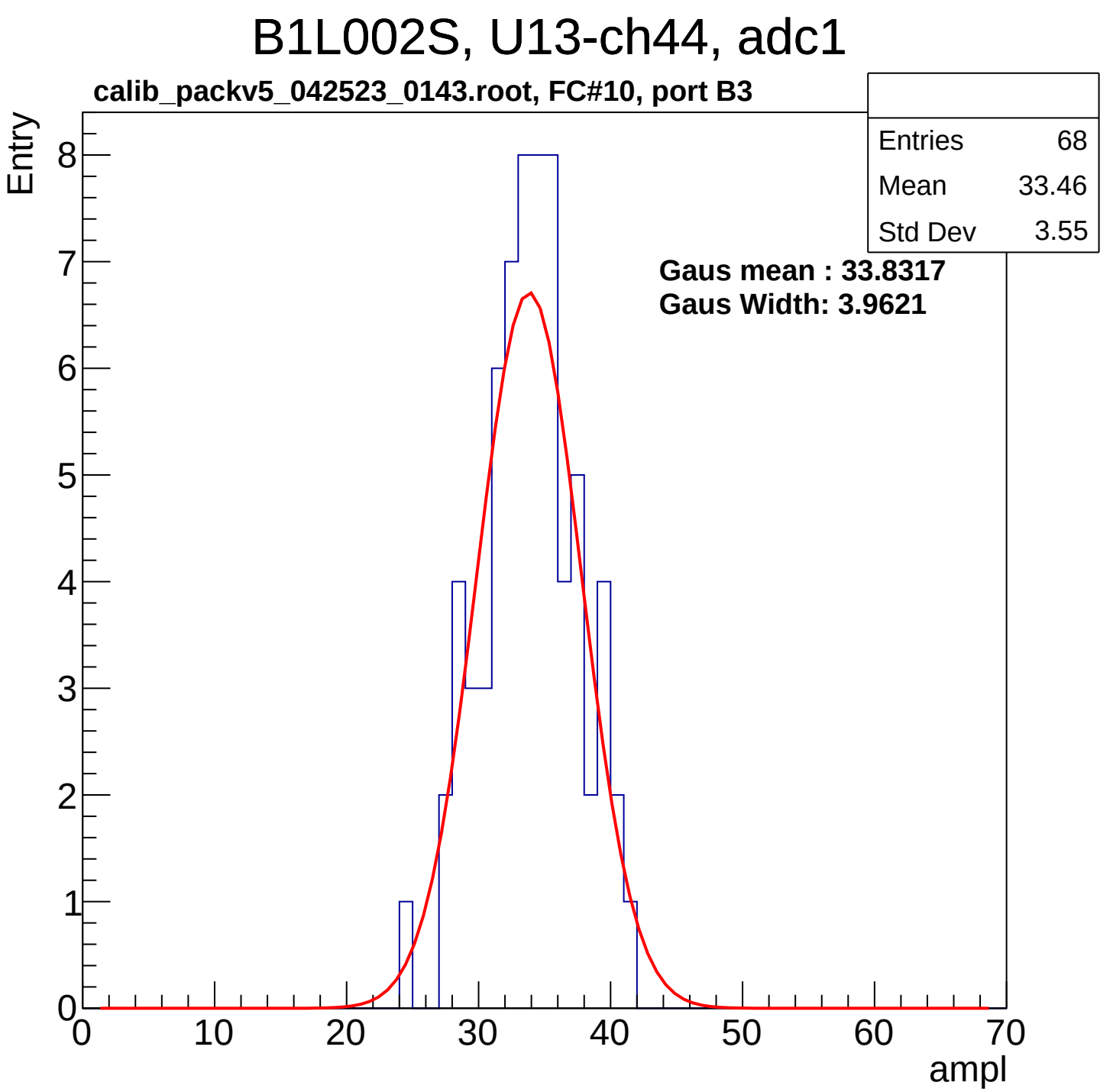
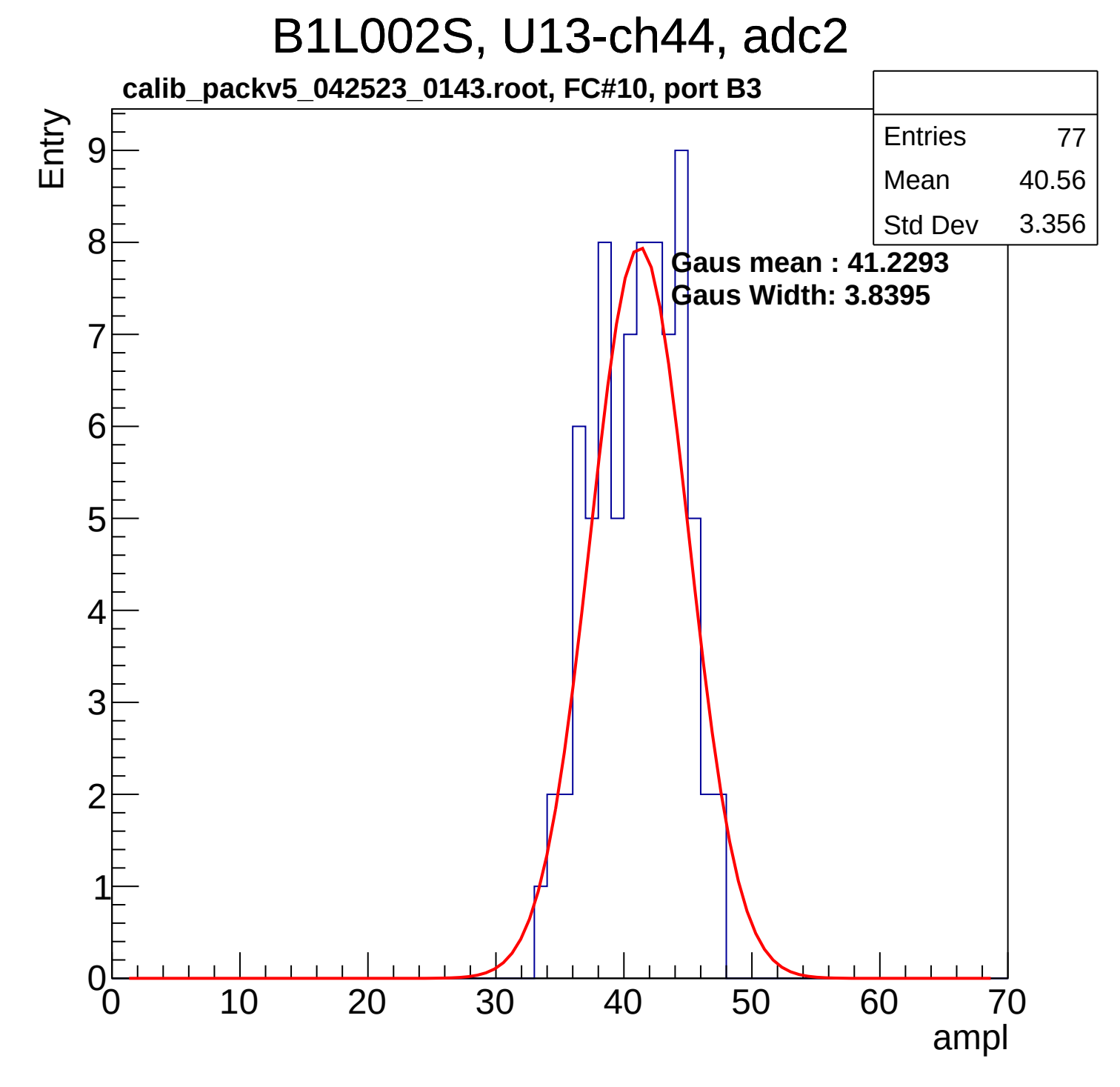
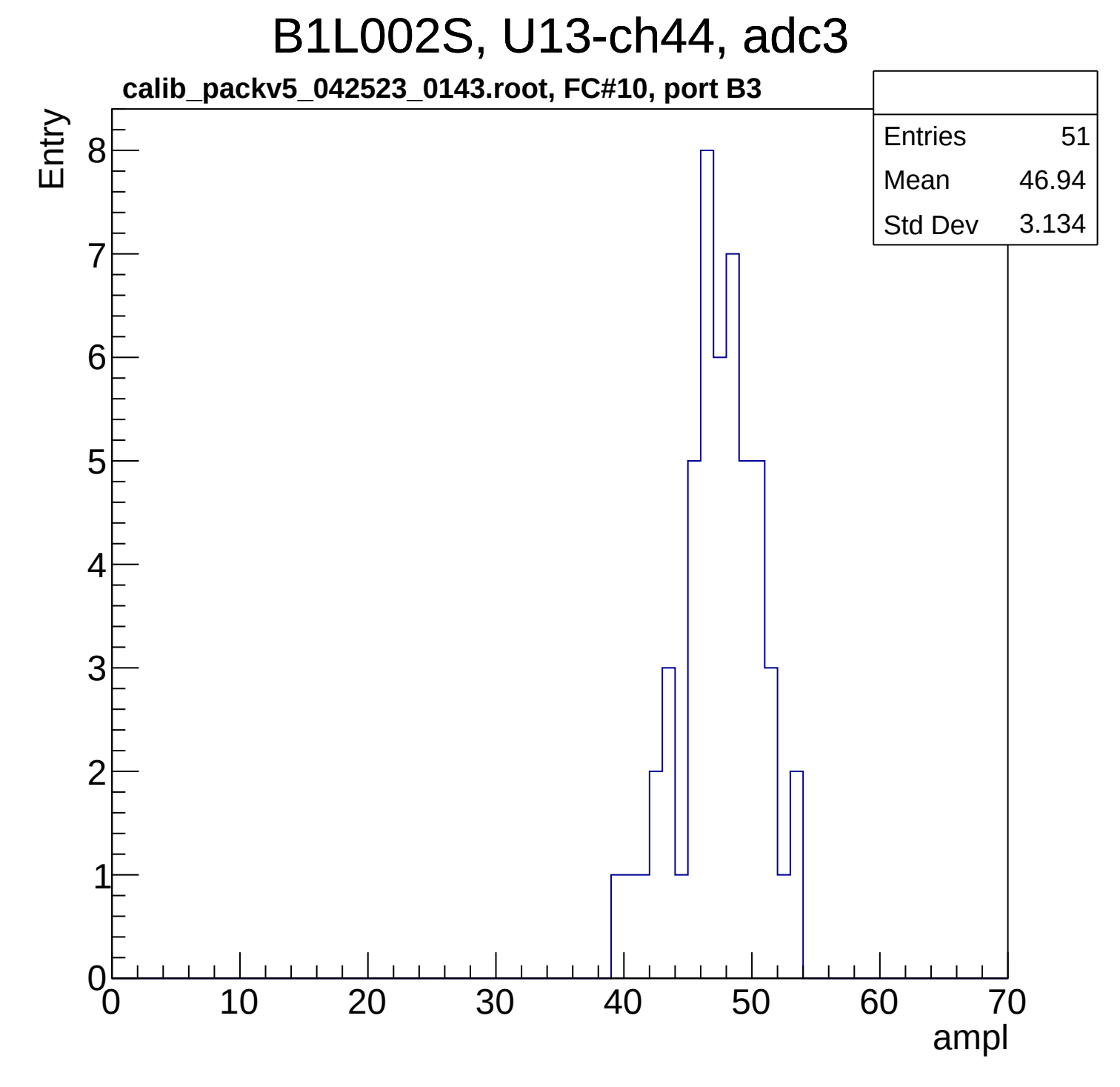
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

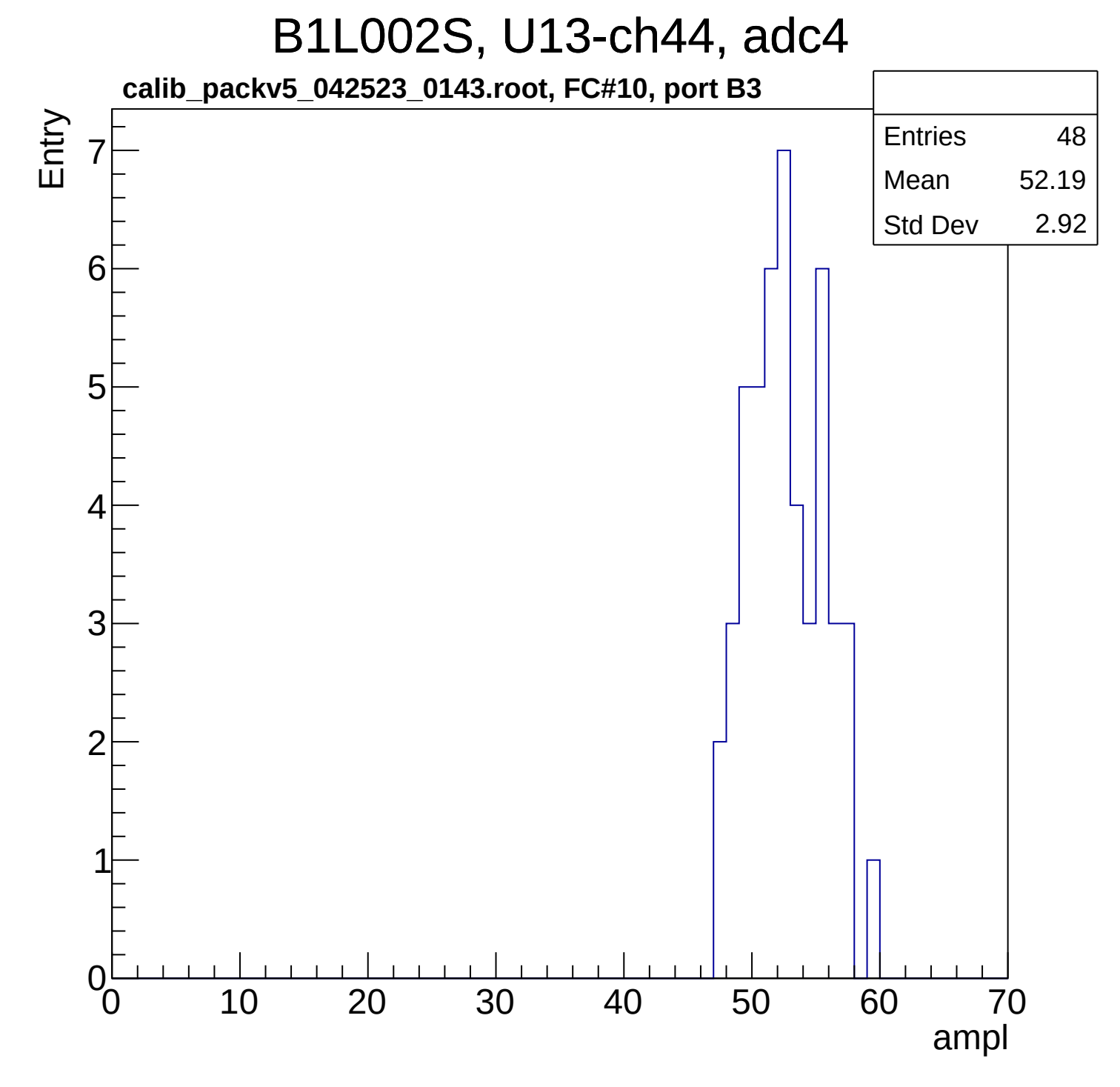

B1L002S, U13-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

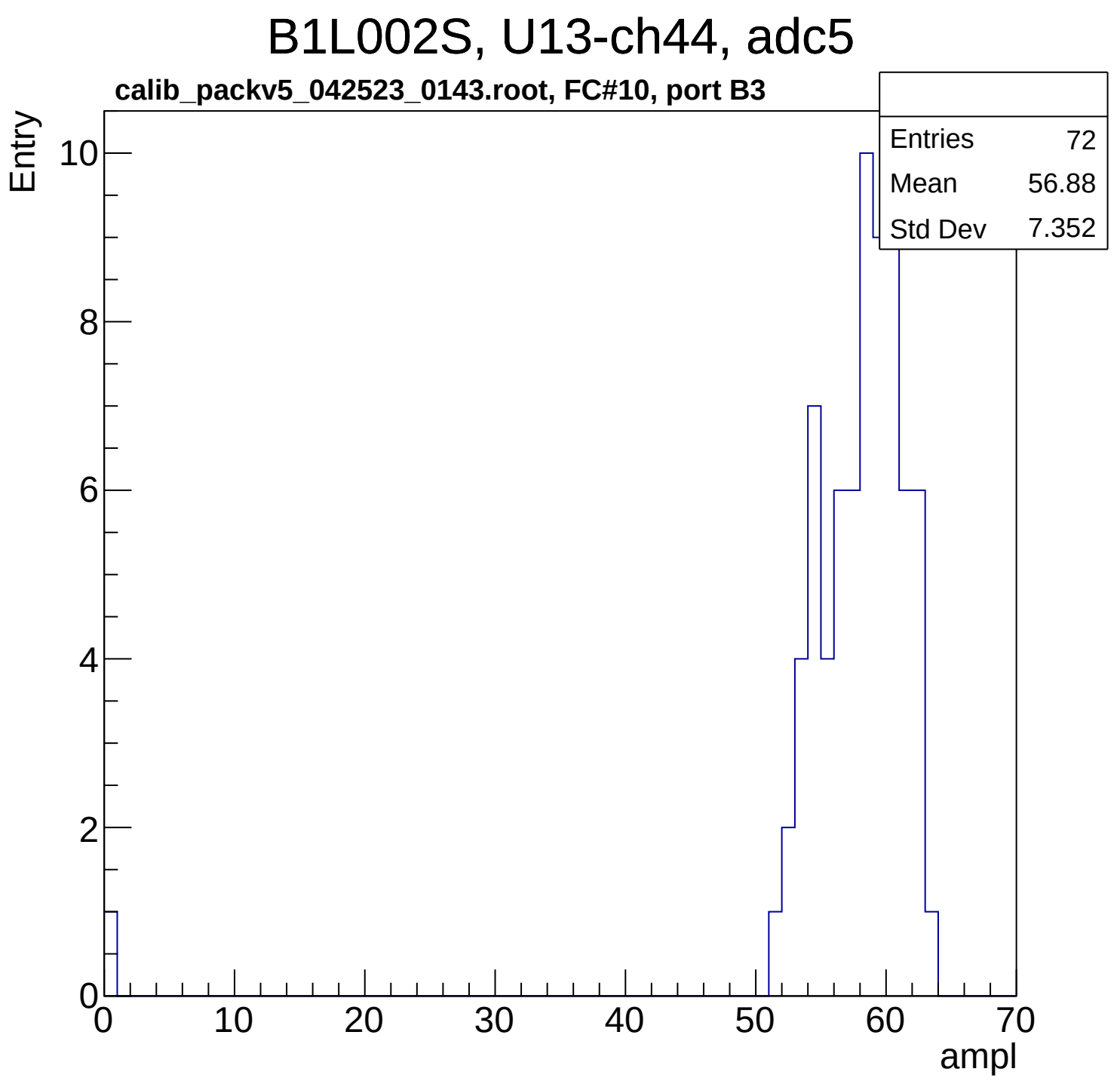

B1L002S, U13-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.26
Std Dev 3.098
Gaus mean : 27.8899
Gaus Width: 3.4834
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.46
Std Dev 3.55
Gaus mean : 33.8317
Gaus Width: 3.9621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 40.56
Std Dev 3.356
Gaus mean : 41.2293
Gaus Width: 3.8395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.94
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.19
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 56.88
Std Dev 7.352
Entry
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6
8
10
0
10
20
30
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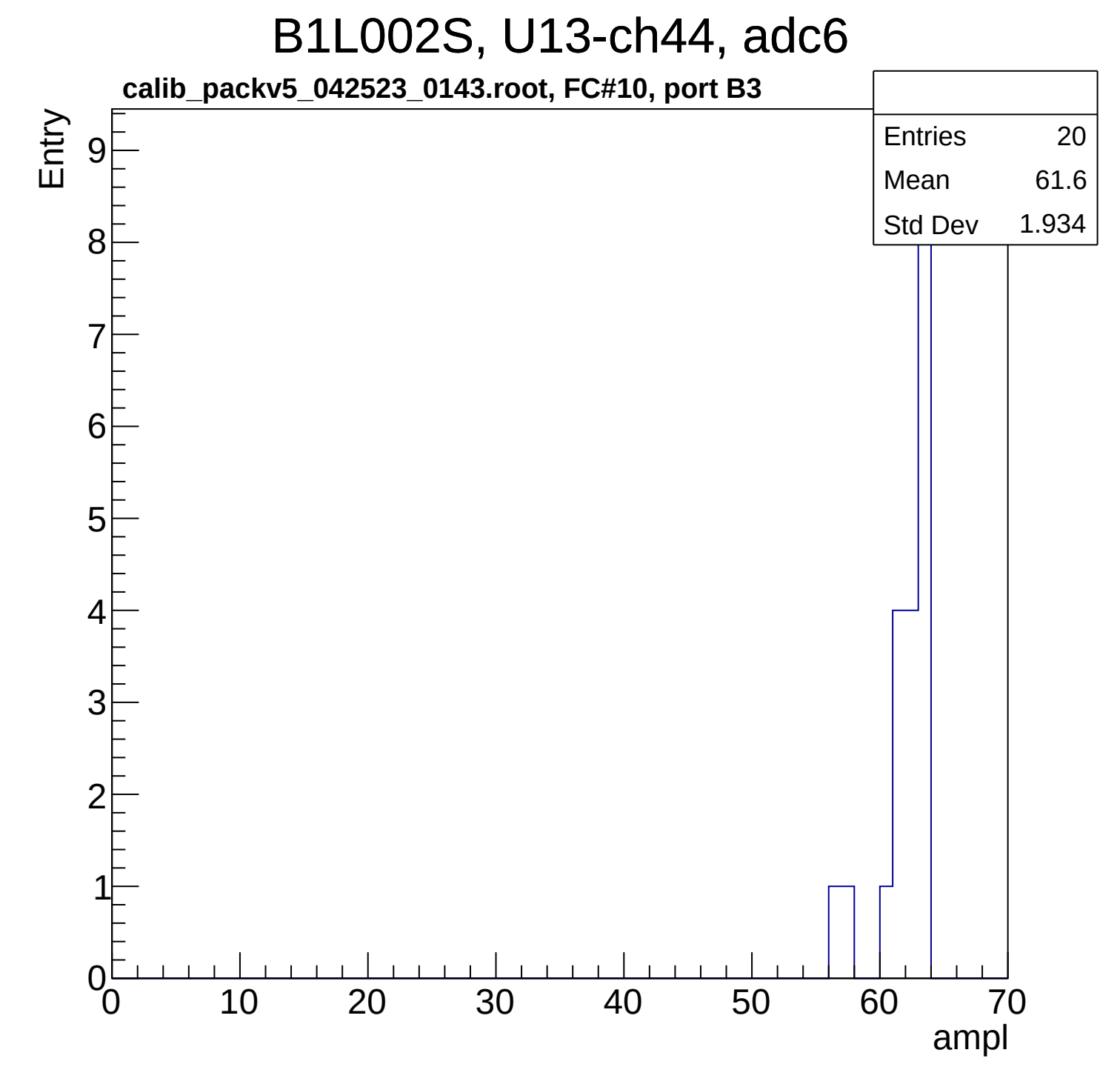
50
60
70
ampl


B1L002S, U13-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.6
Std Dev 1.934
Entry
ampl
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2
3
4
5
6
7
8
9
0
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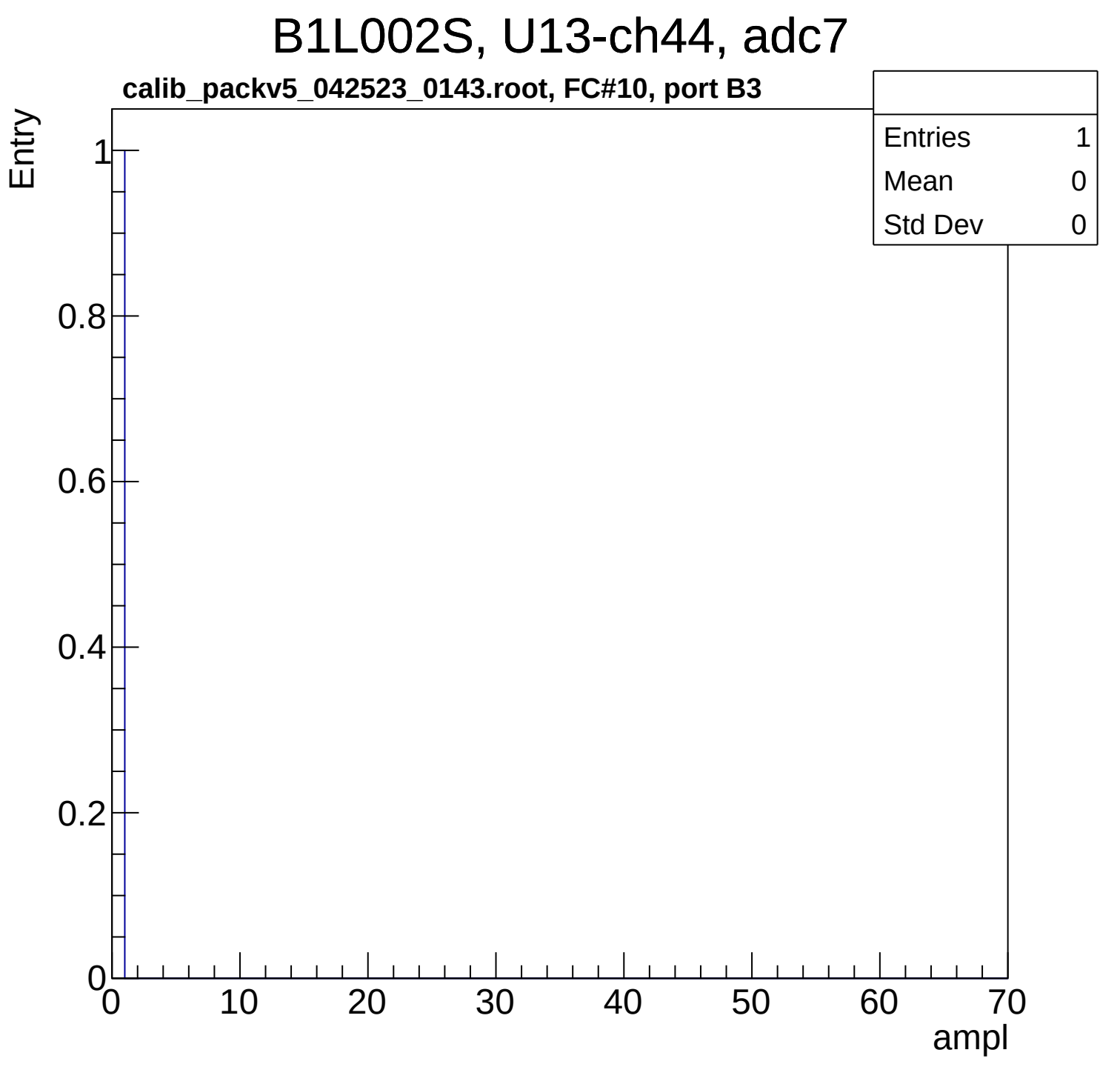
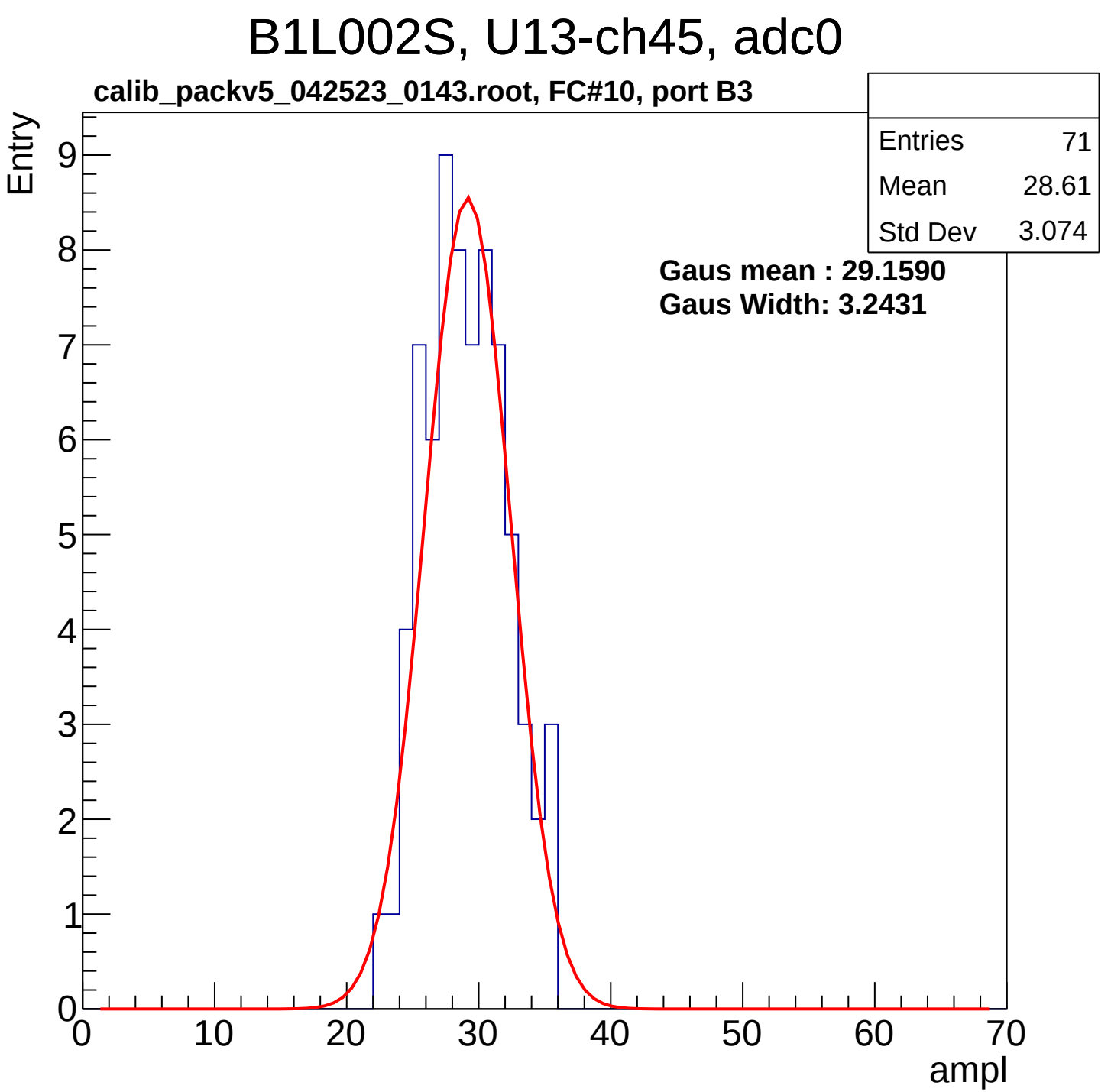
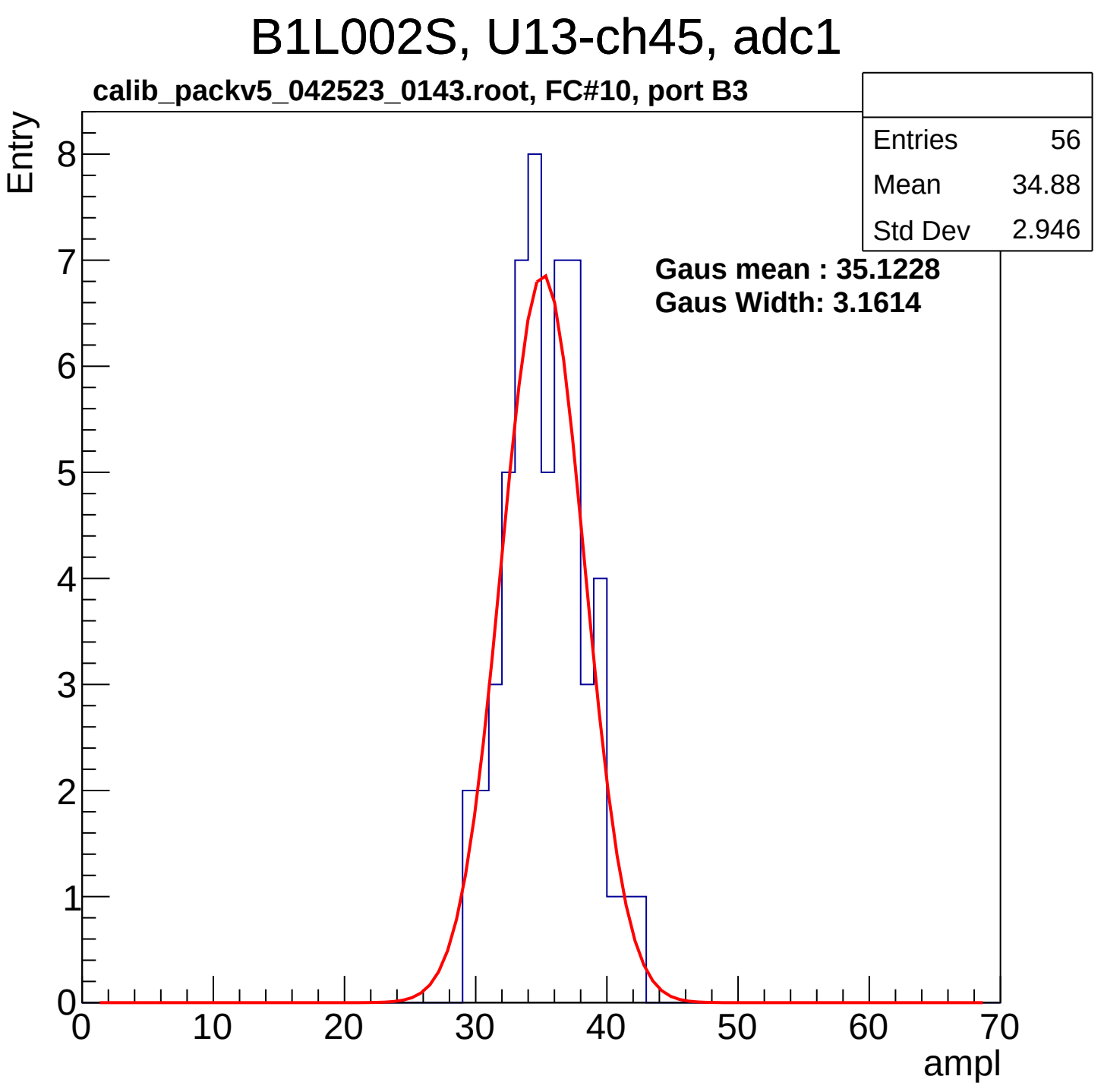
20
30
40
50
60
70


B1L002S, U13-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.61
Std Dev 3.074
Gaus mean : 29.1590
Gaus Width: 3.2431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.88
Std Dev 2.946
Gaus mean : 35.1228
Gaus Width: 3.1614
Entry
ampl
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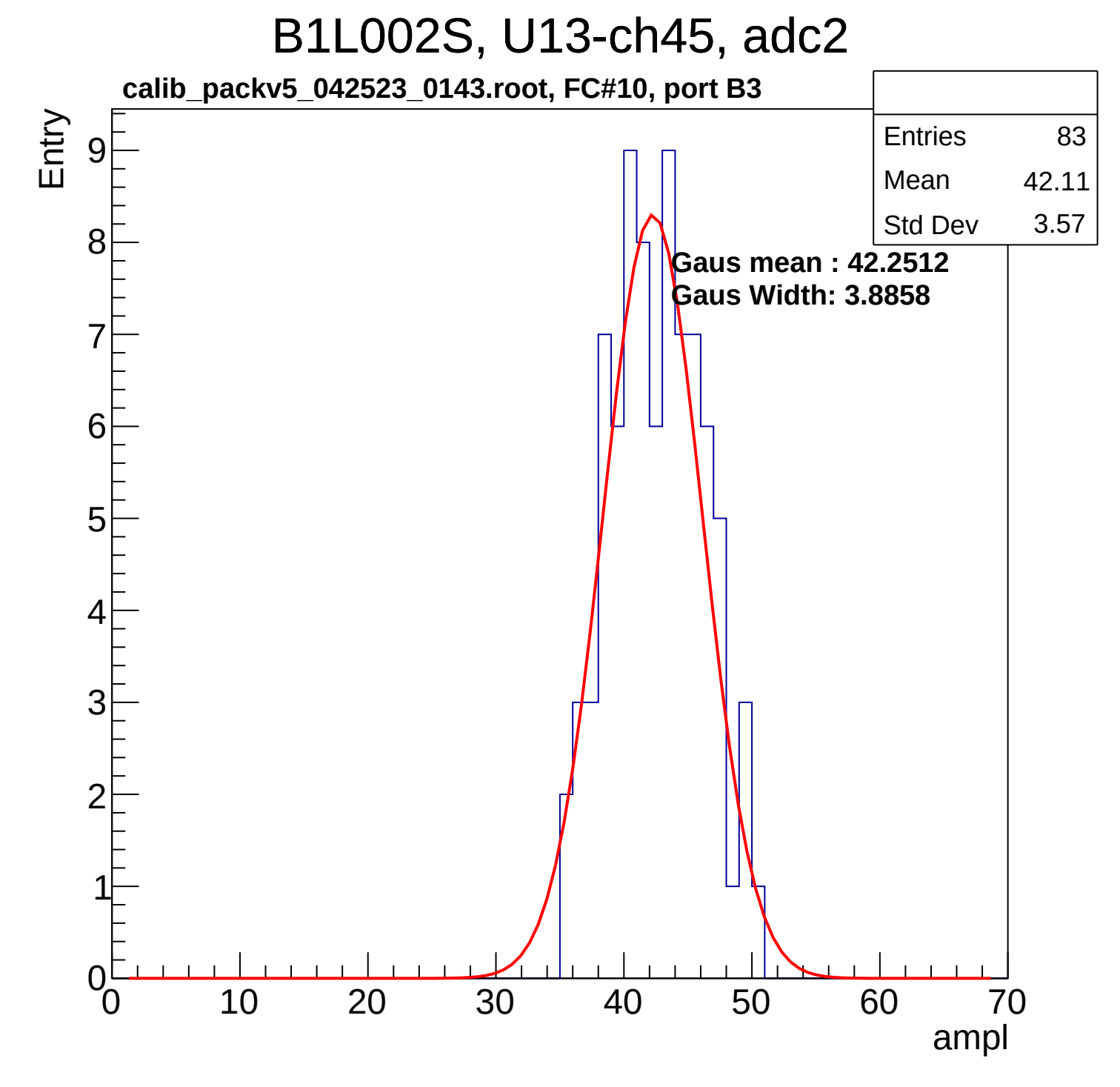
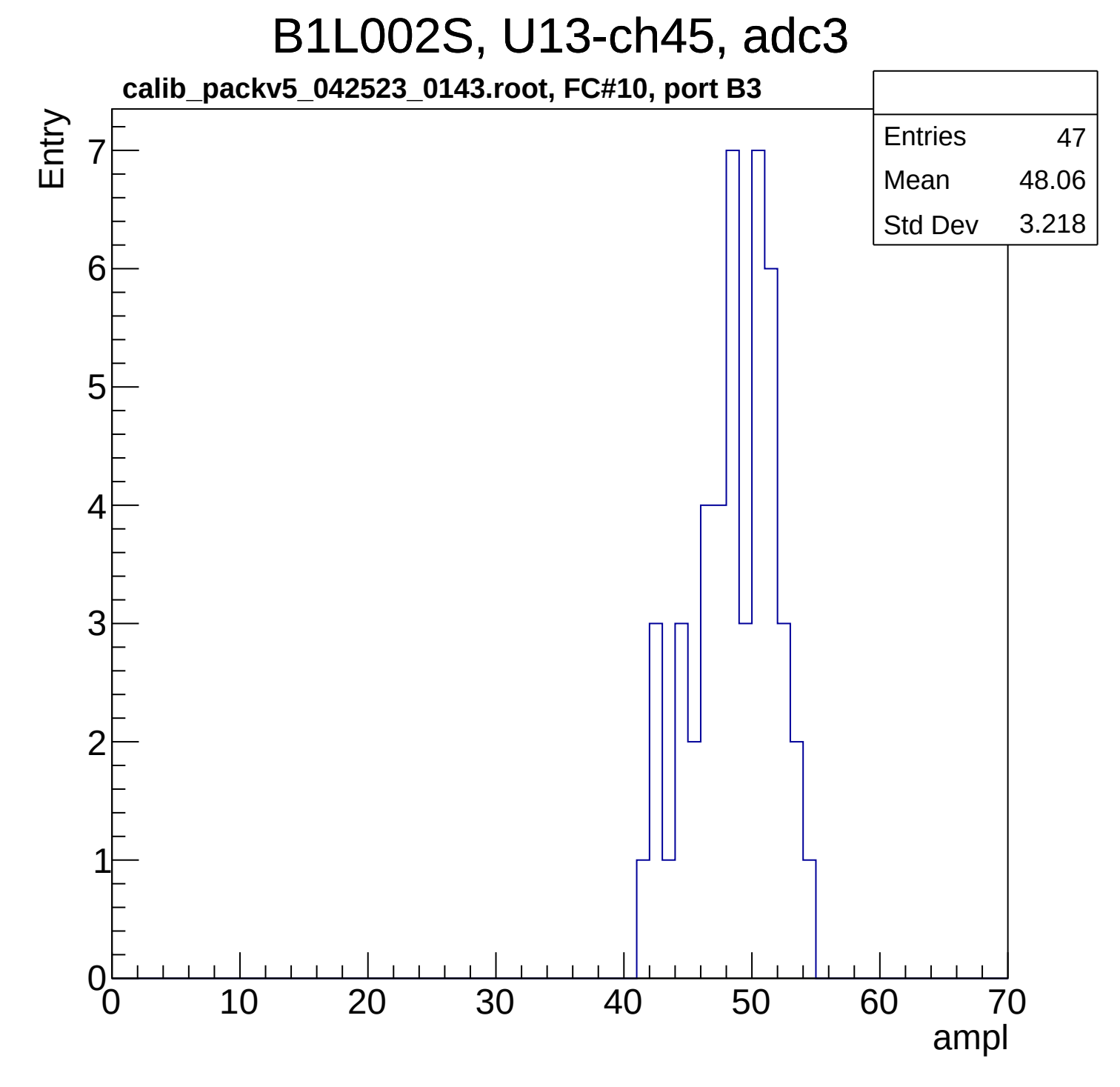
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 42.11
Std Dev 3.57
Gaus mean : 42.2512
Gaus Width: 3.8858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 48.06
Std Dev 3.218
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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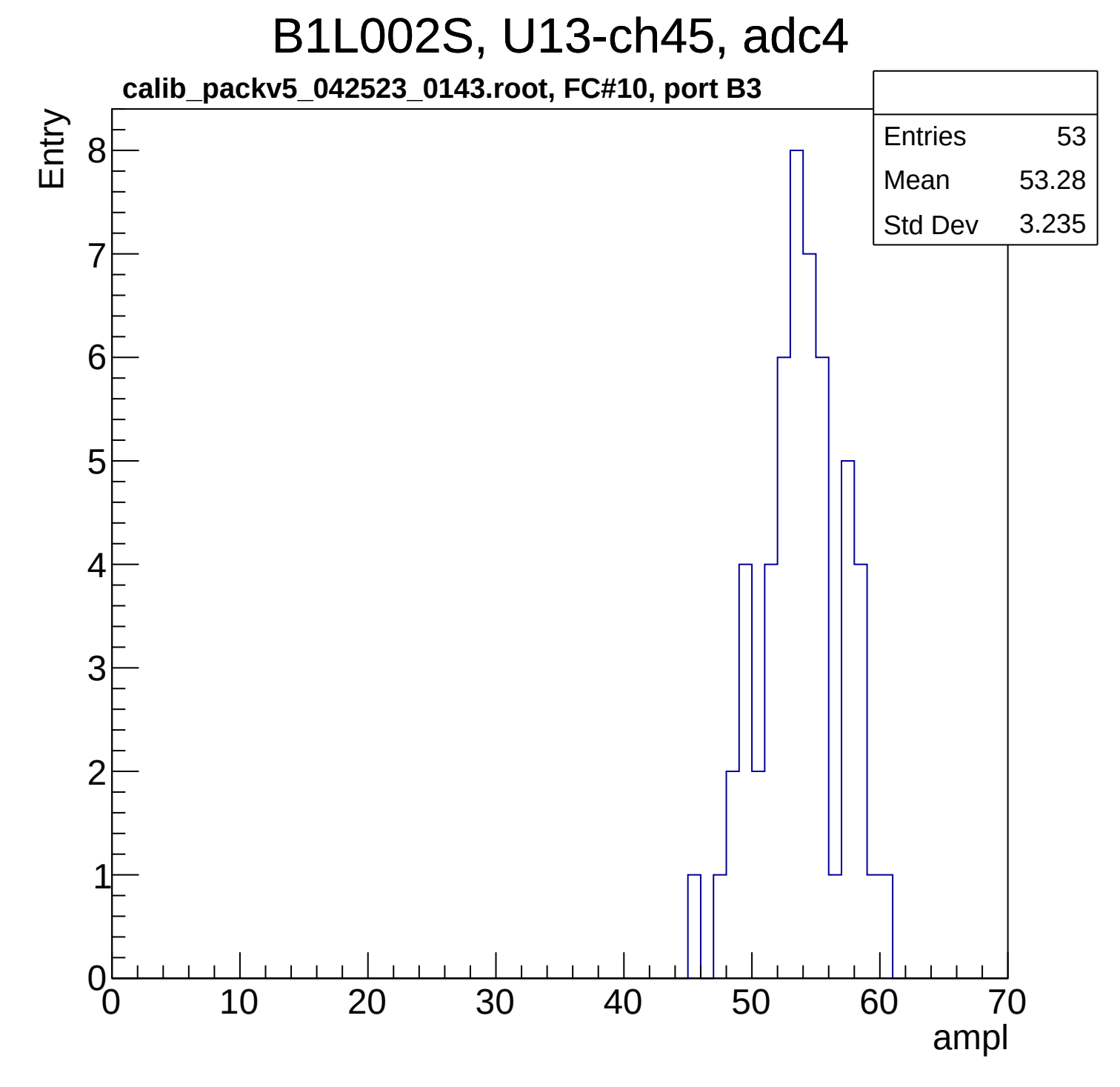
B1L002S, U13-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.28
Std Dev 3.235
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
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40
50
60
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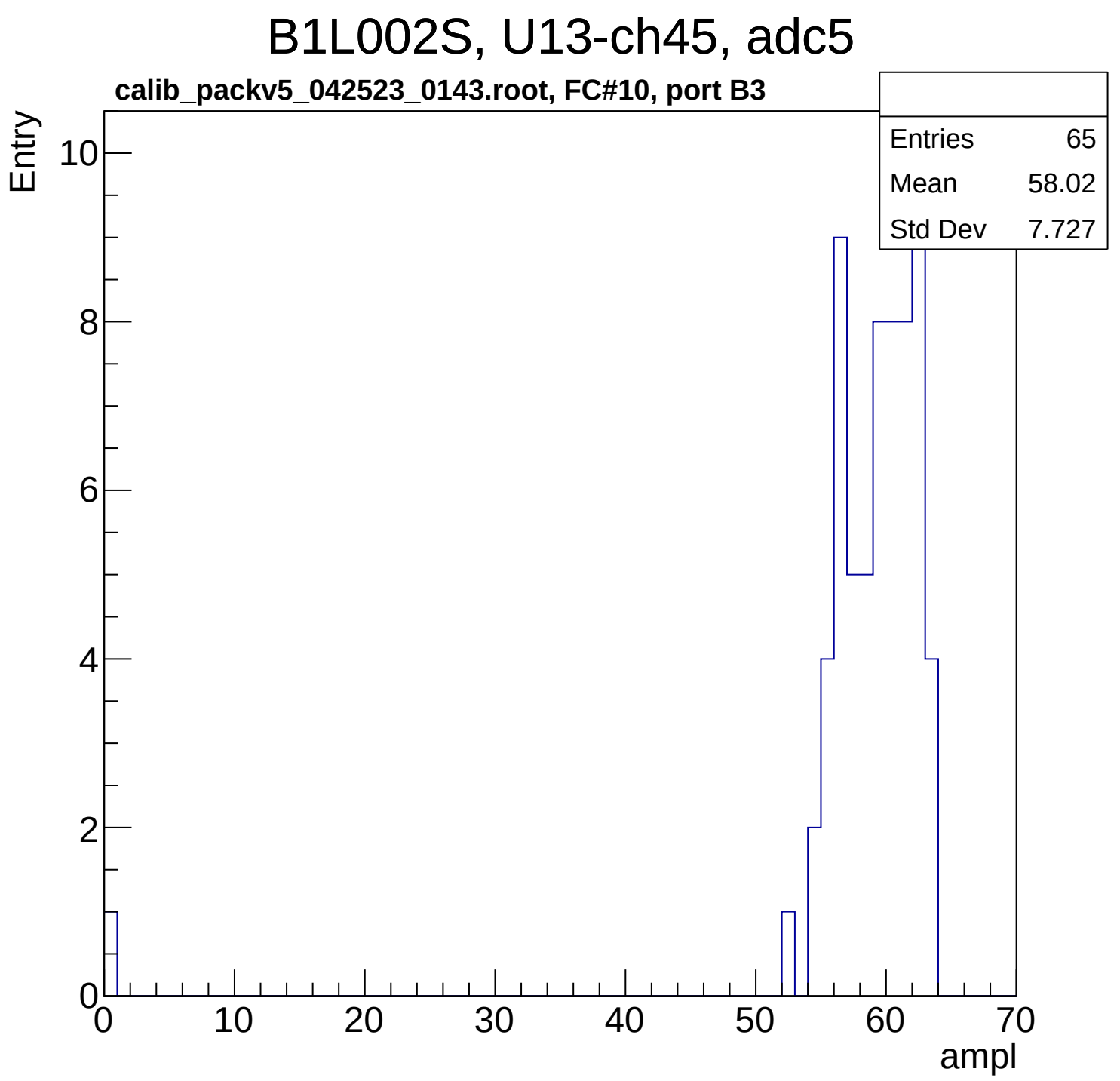
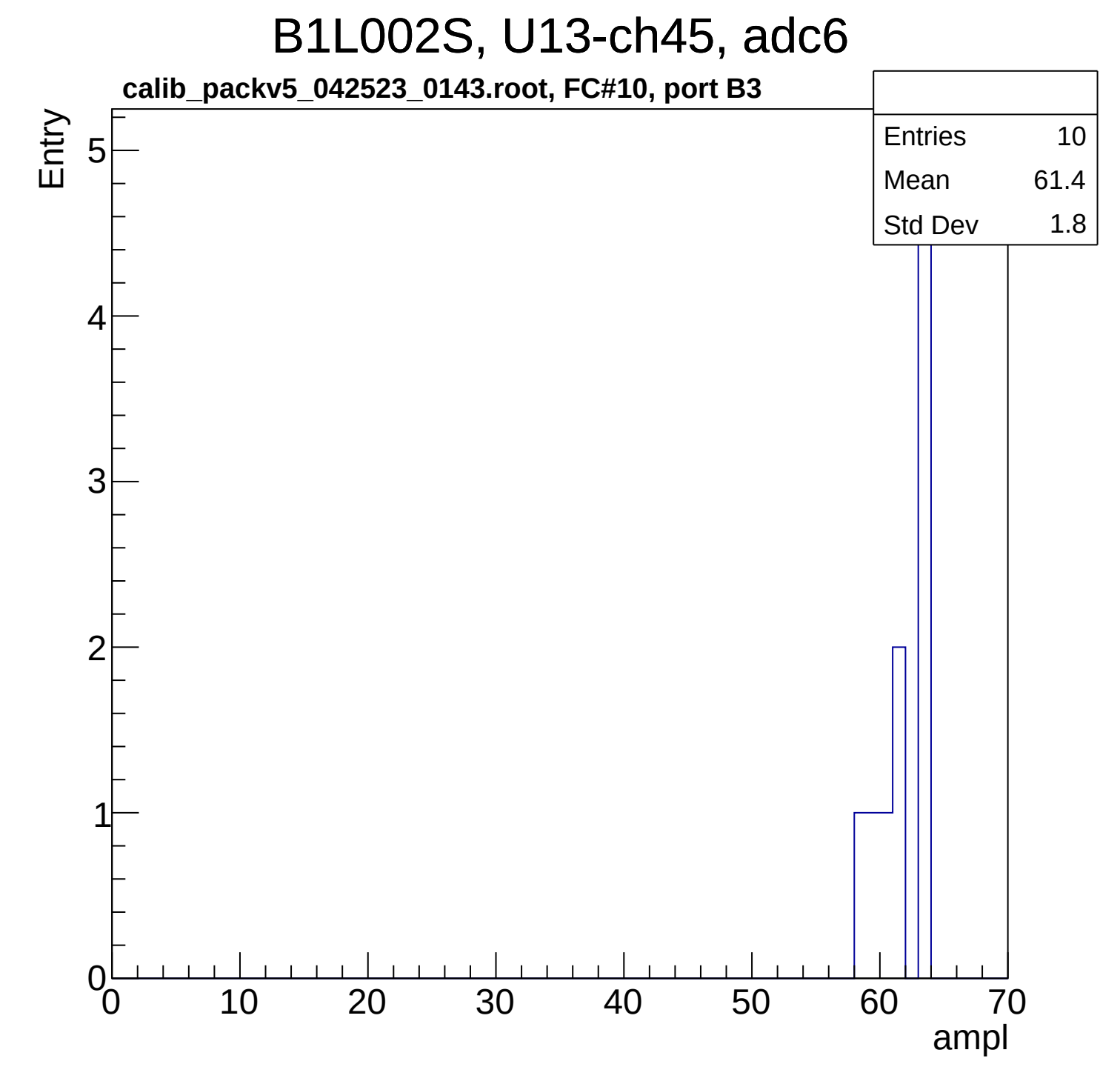
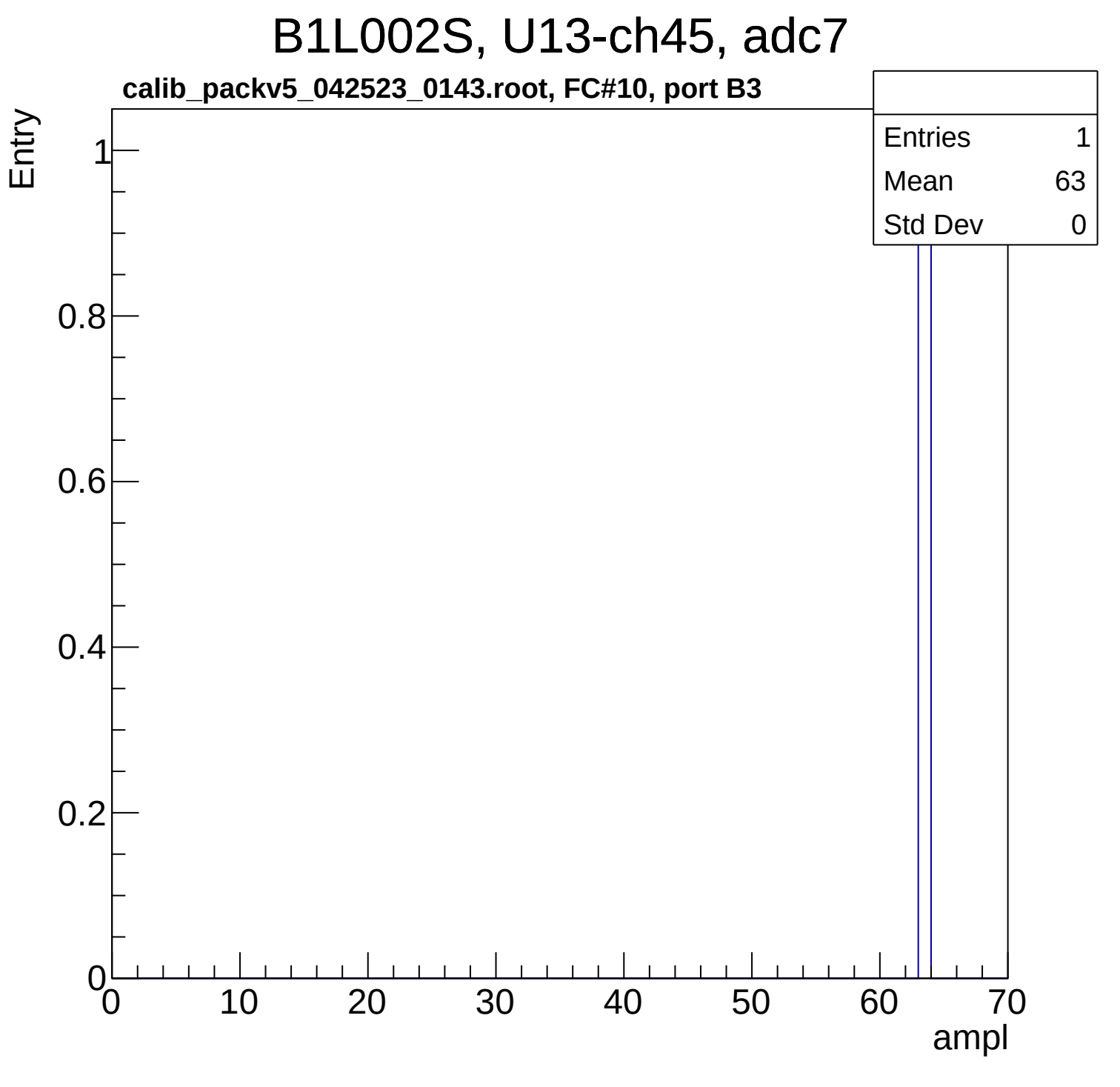

B1L002S, U13-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.02
Std Dev 7.727
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.4
Std Dev 1.8
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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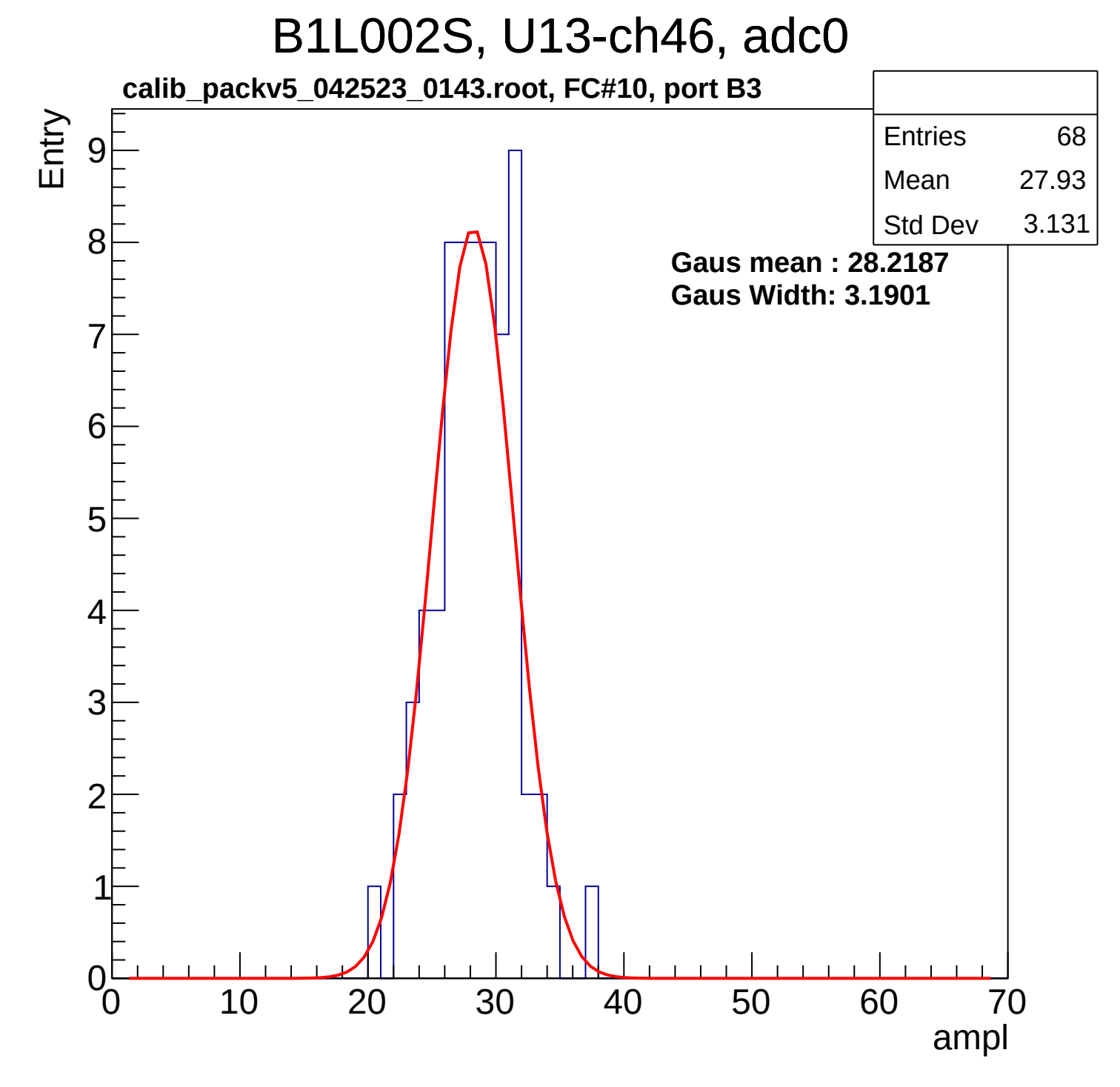
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.93
Std Dev 3.131
Gaus mean : 28.2187
Gaus Width: 3.1901
Entry
ampl
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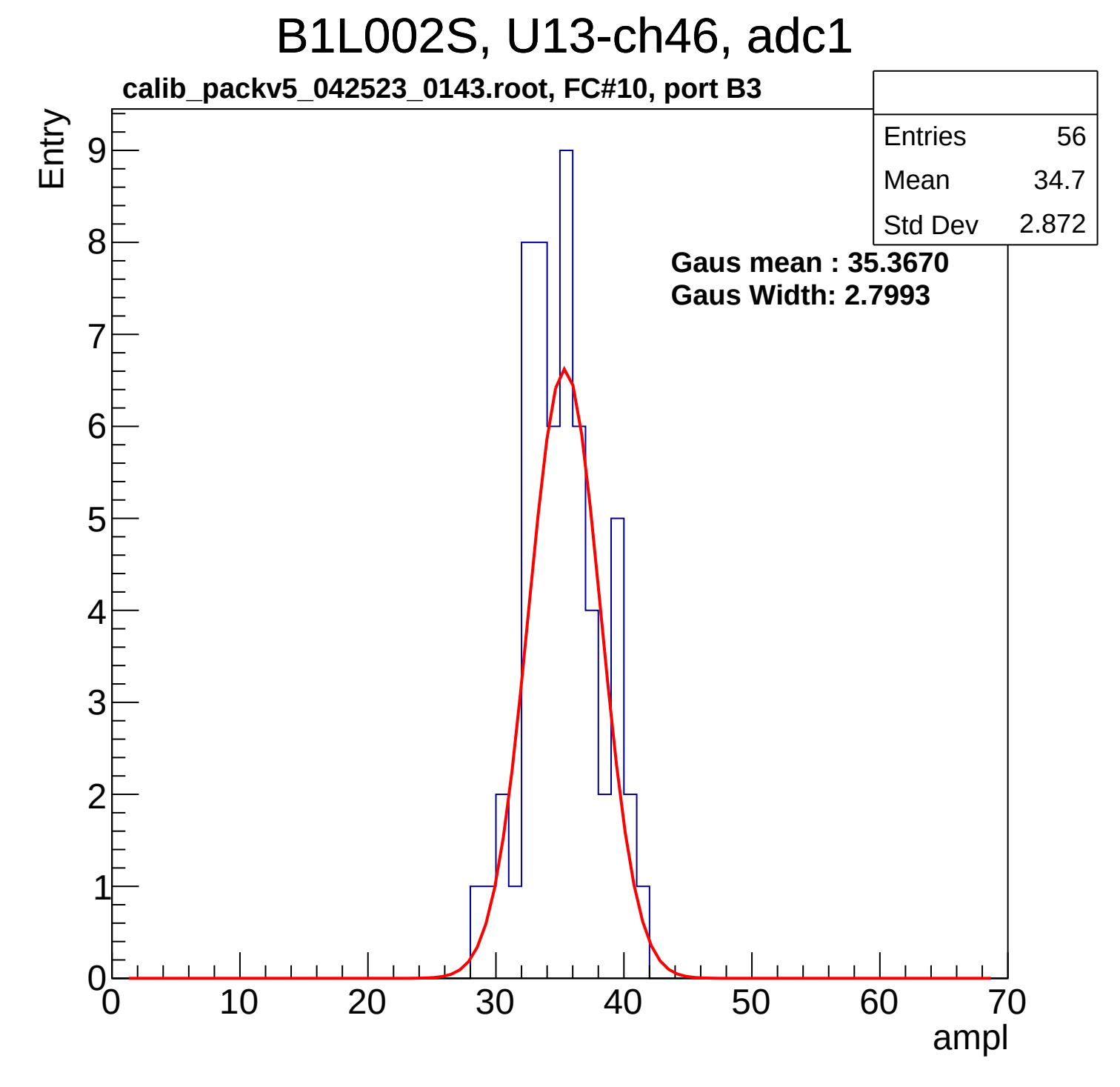
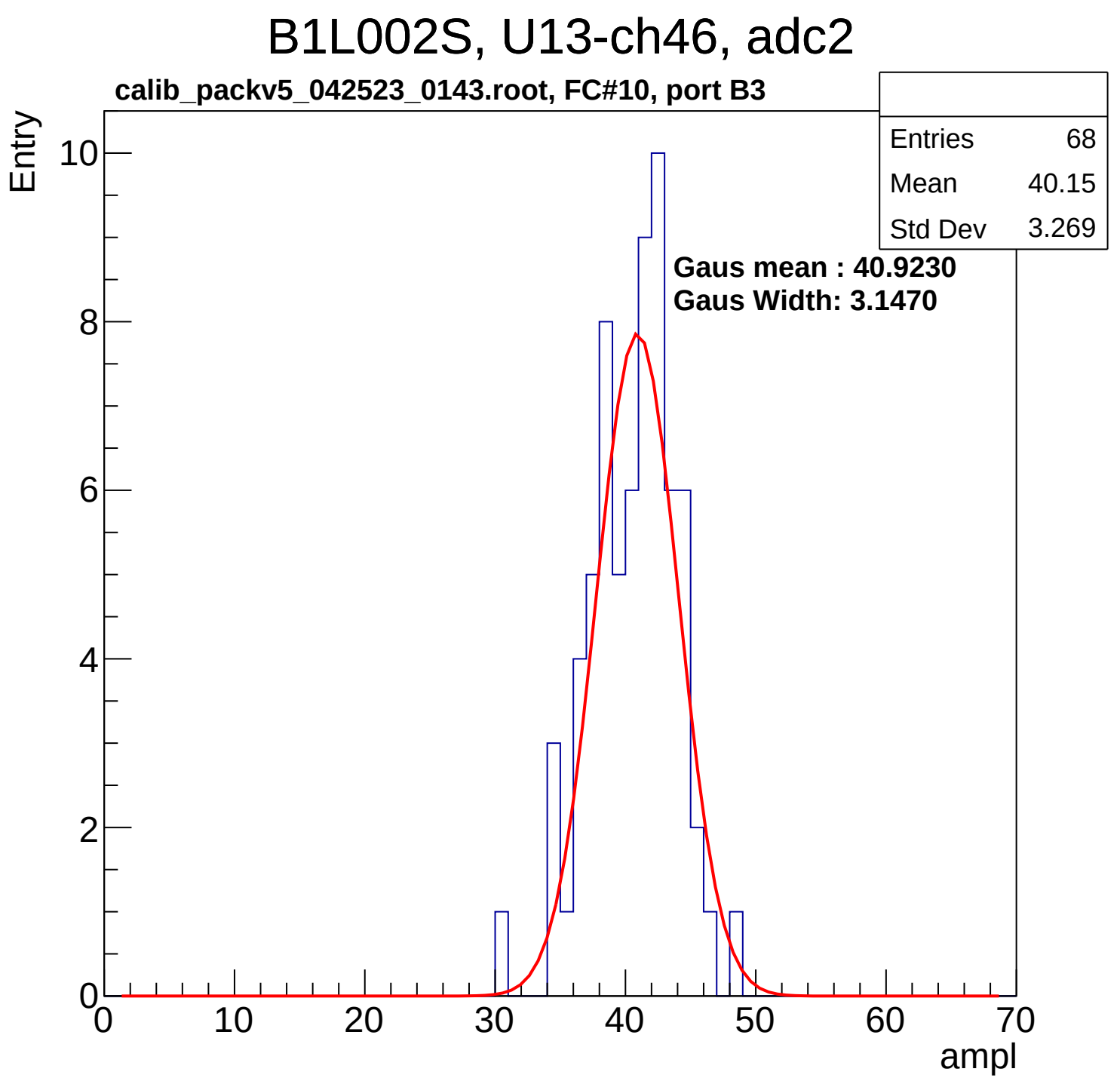
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.7
Std Dev 2.872
Gaus mean : 35.3670
Gaus Width: 2.7993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.15
Std Dev 3.269
Gaus mean : 40.9230
Gaus Width: 3.1470
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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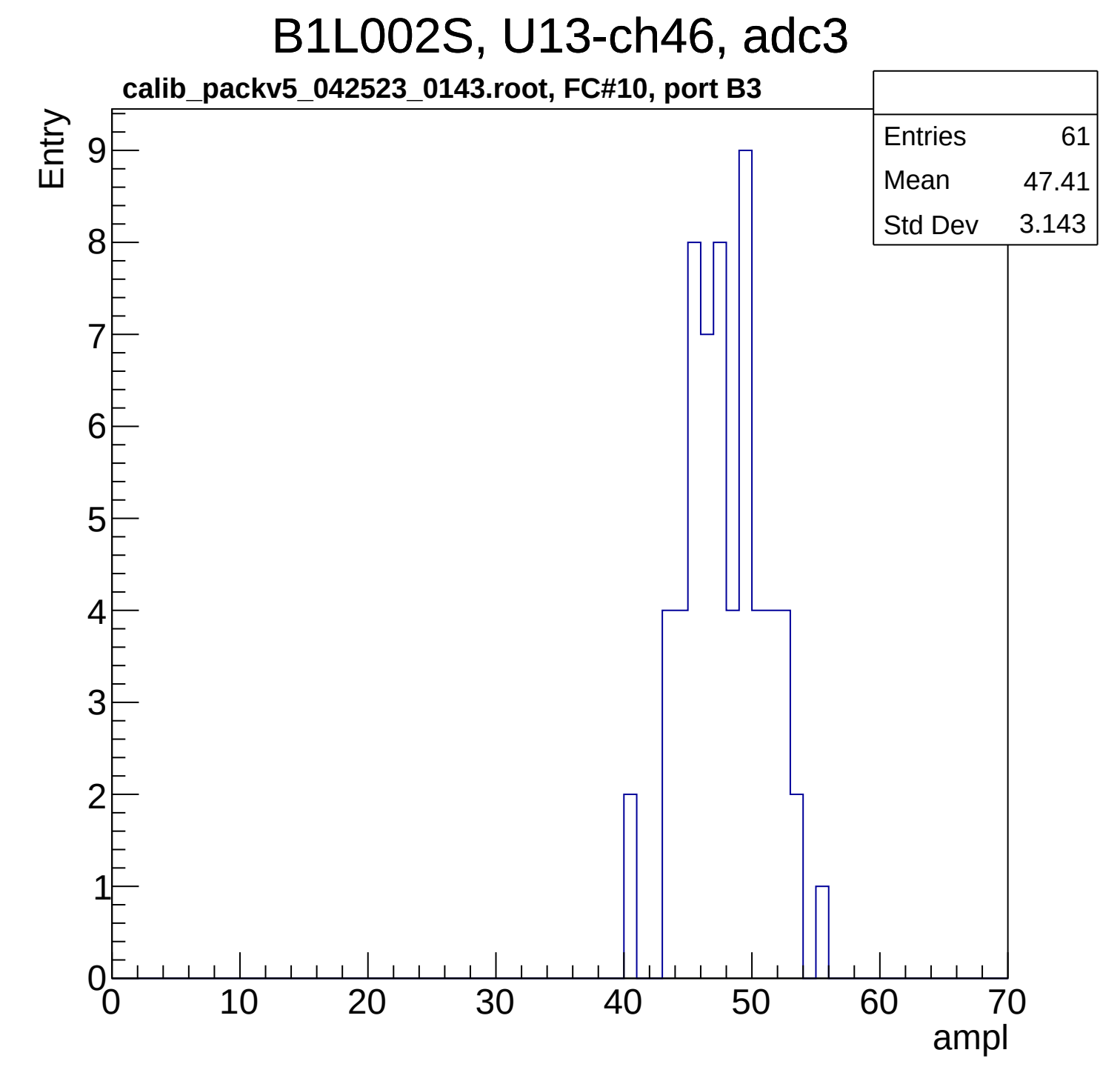
B1L002S, U13-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.41
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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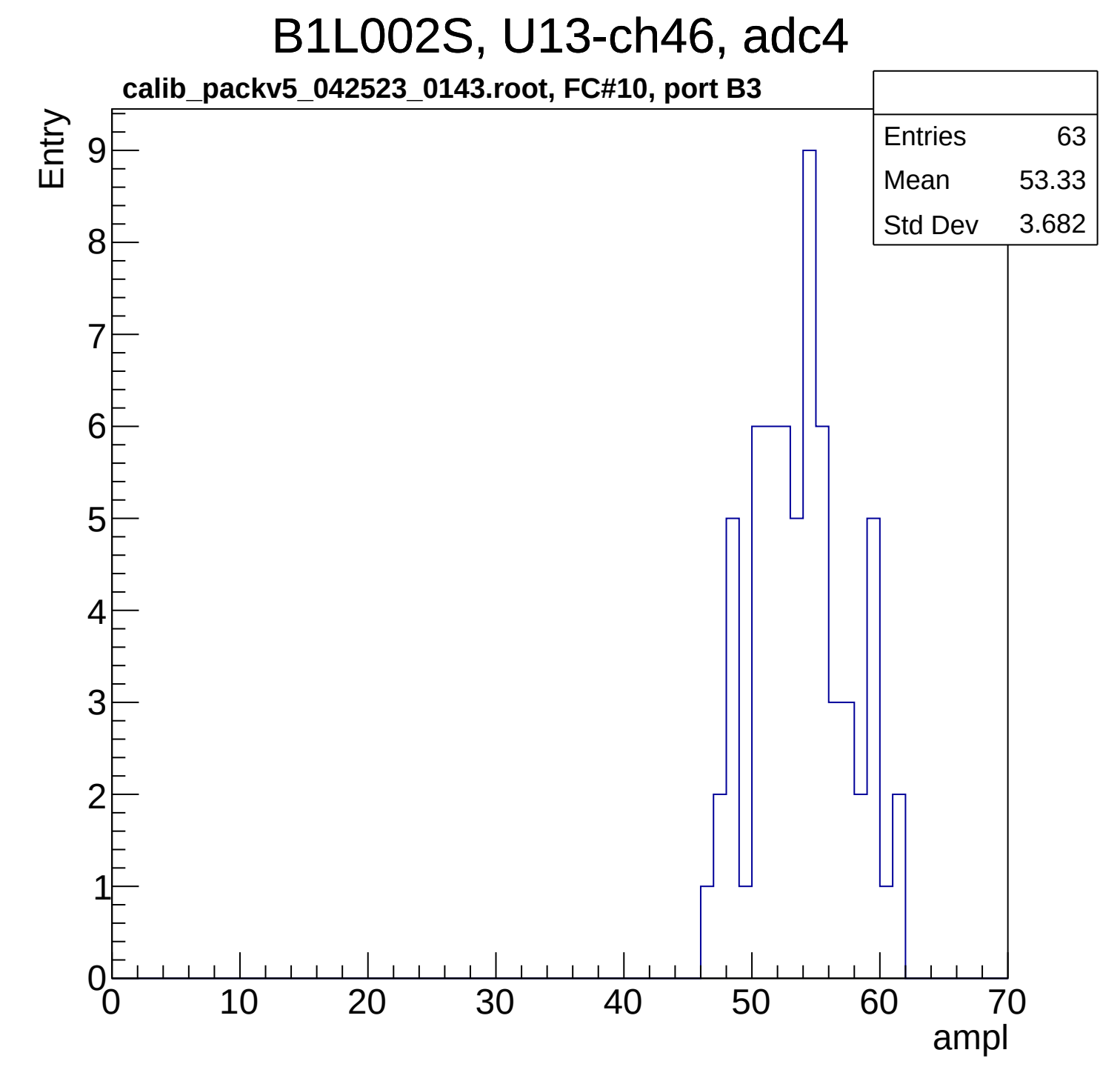
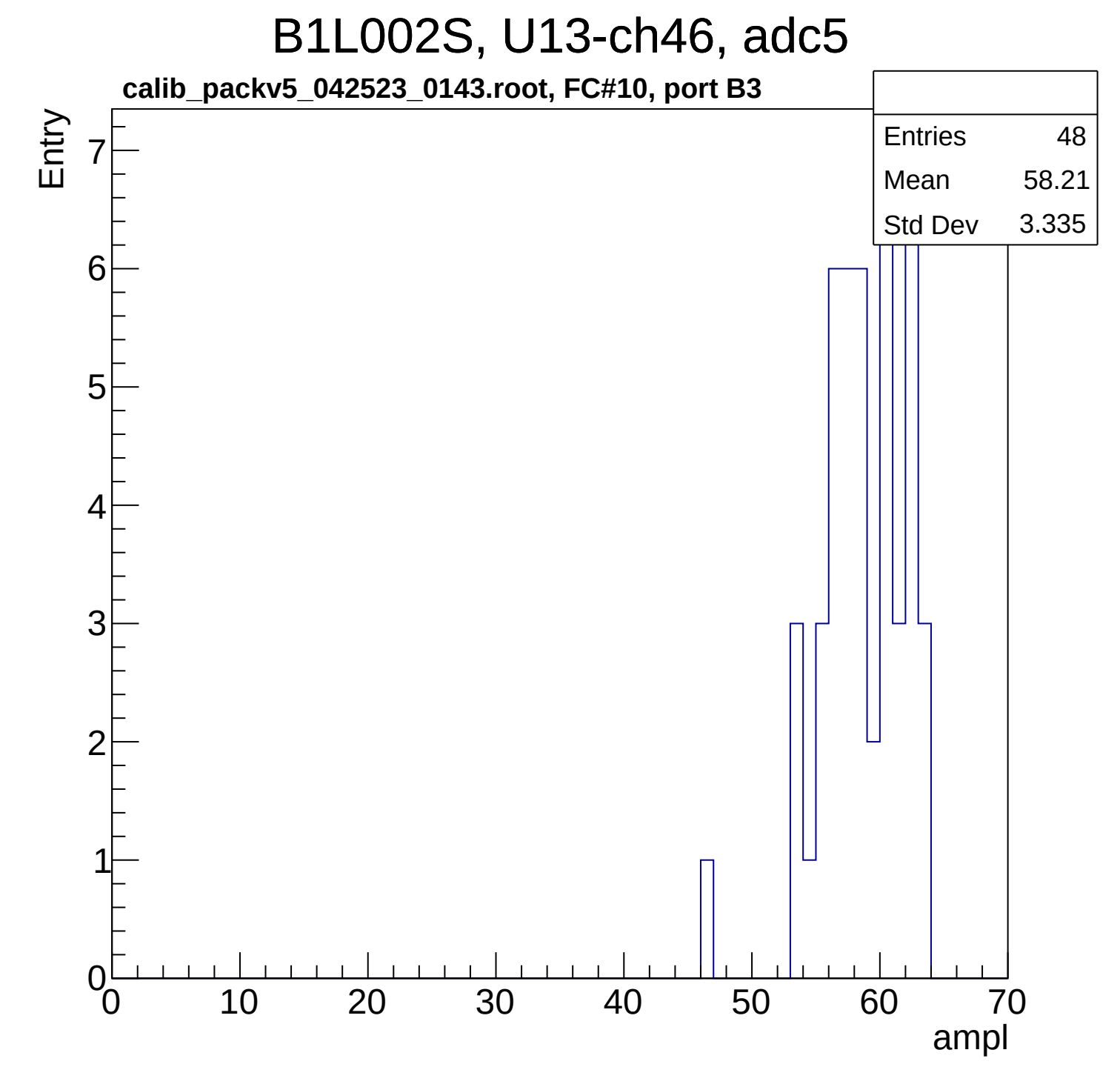
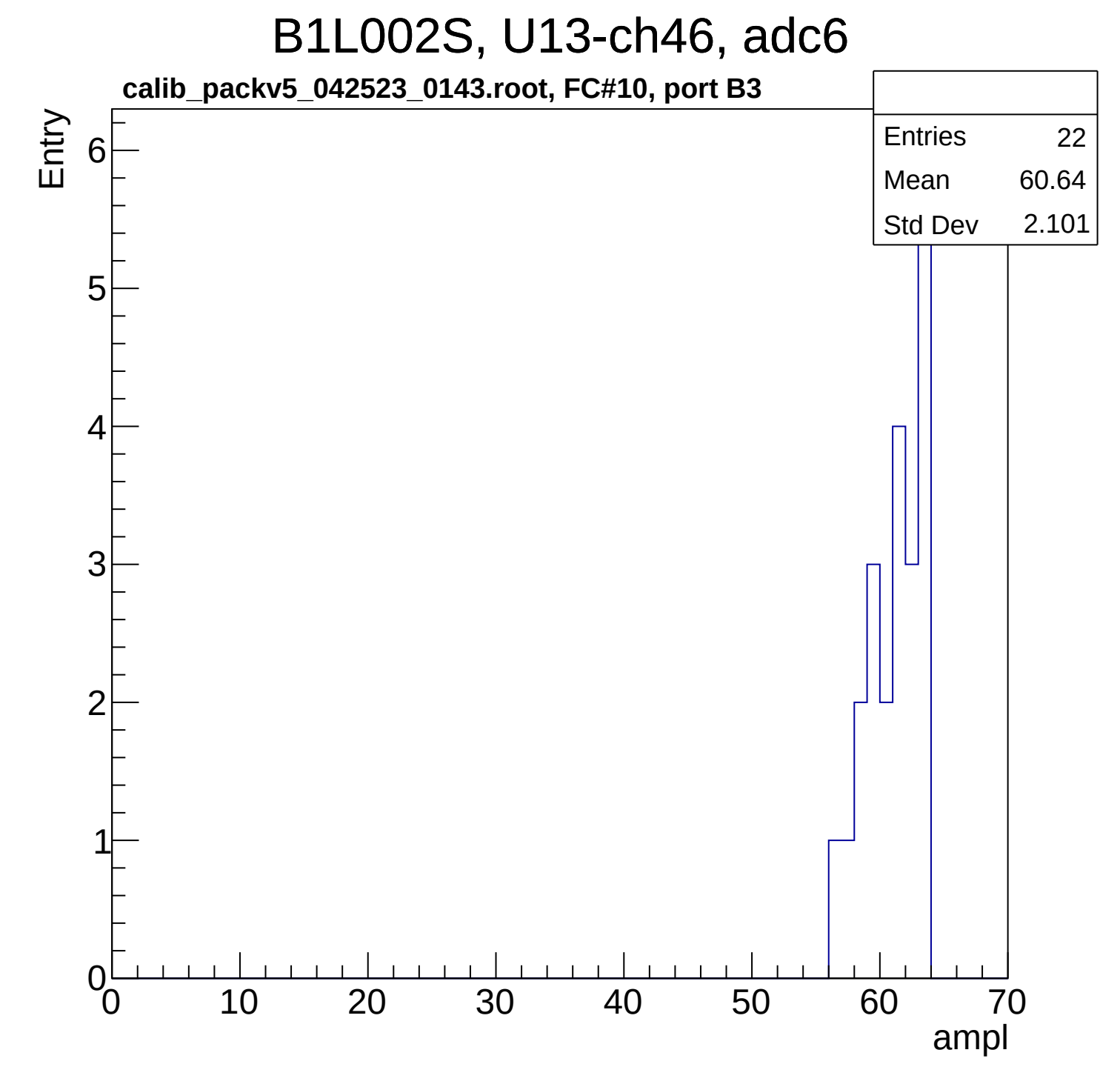
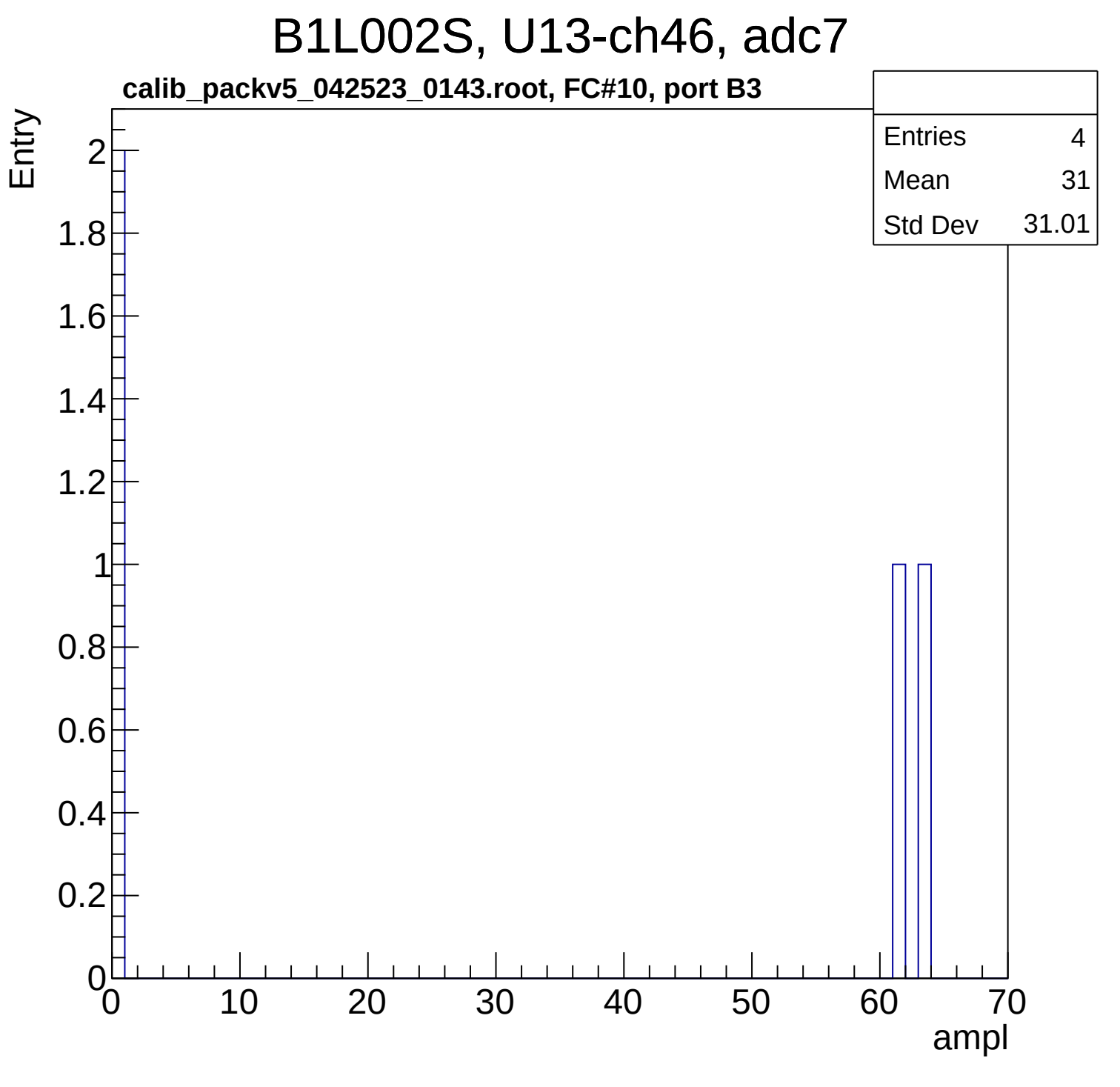

B1L002S, U13-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.33
Std Dev 3.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.21
Std Dev 3.335
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 60.64
Std Dev 2.101
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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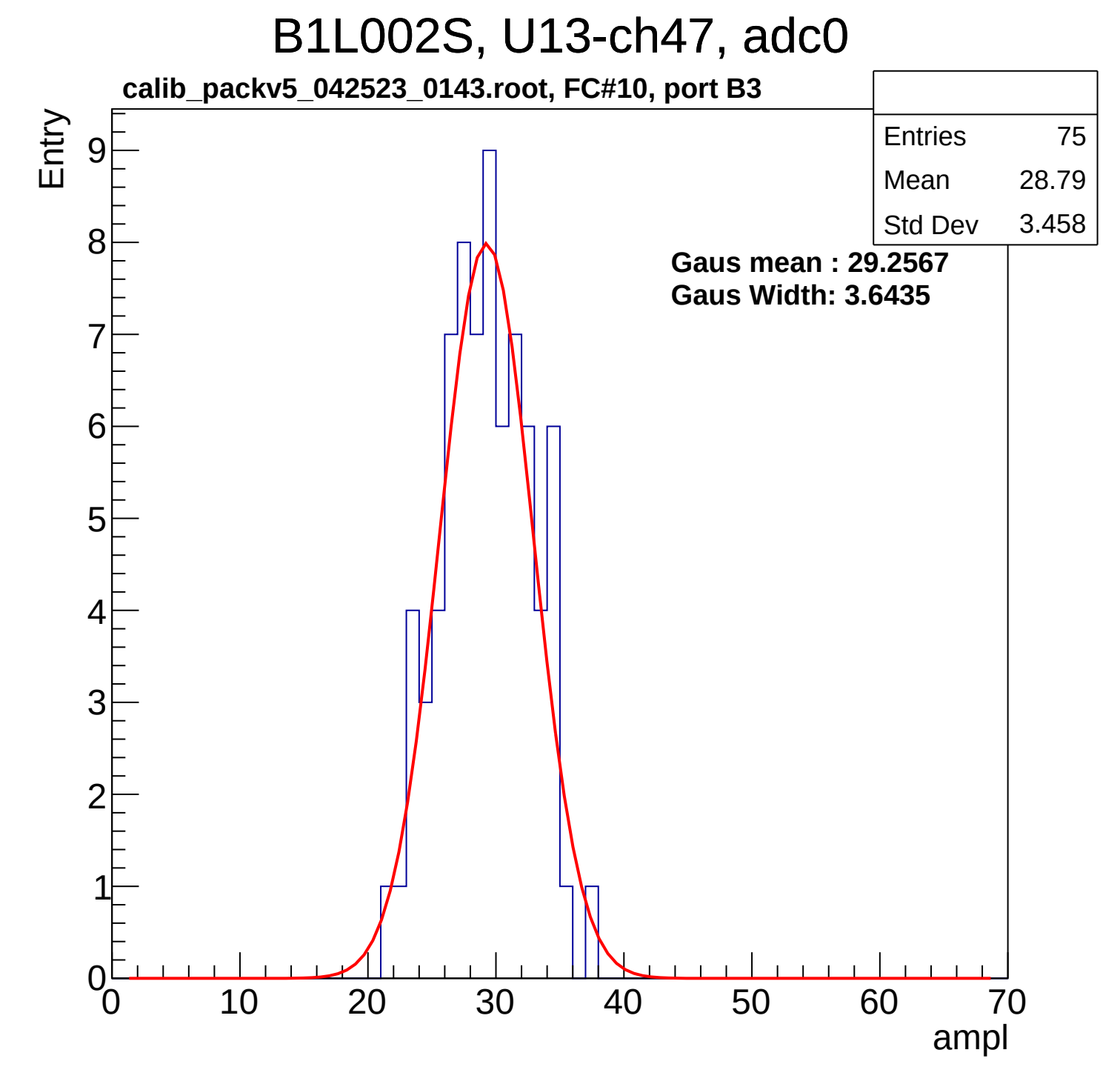
B1L002S, U13-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.79
Std Dev 3.458
Gaus mean : 29.2567
Gaus Width: 3.6435
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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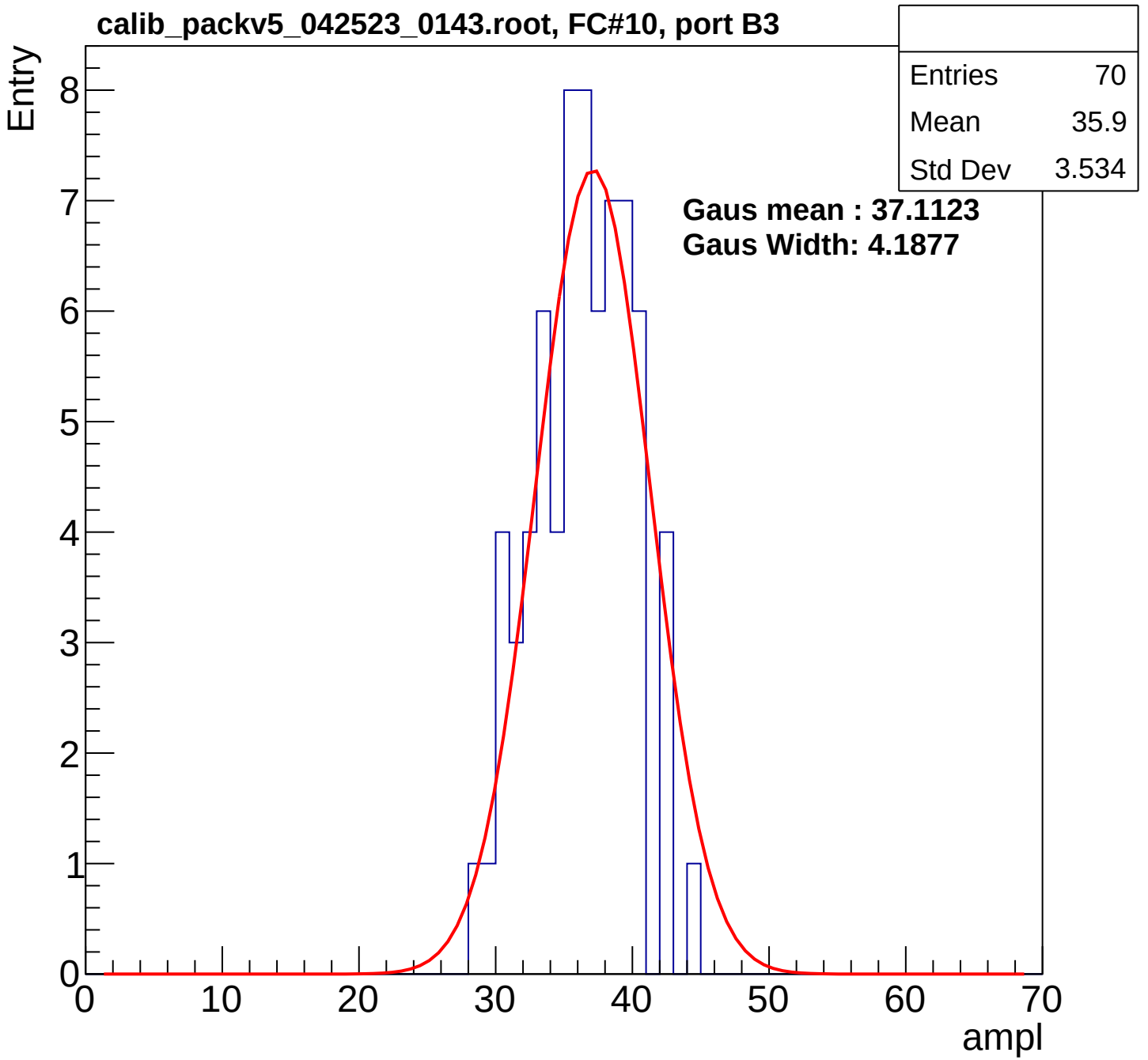
B1L002S, U13-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.9
Std Dev 3.534
Gaus mean : 37.1123
Gaus Width: 4.1877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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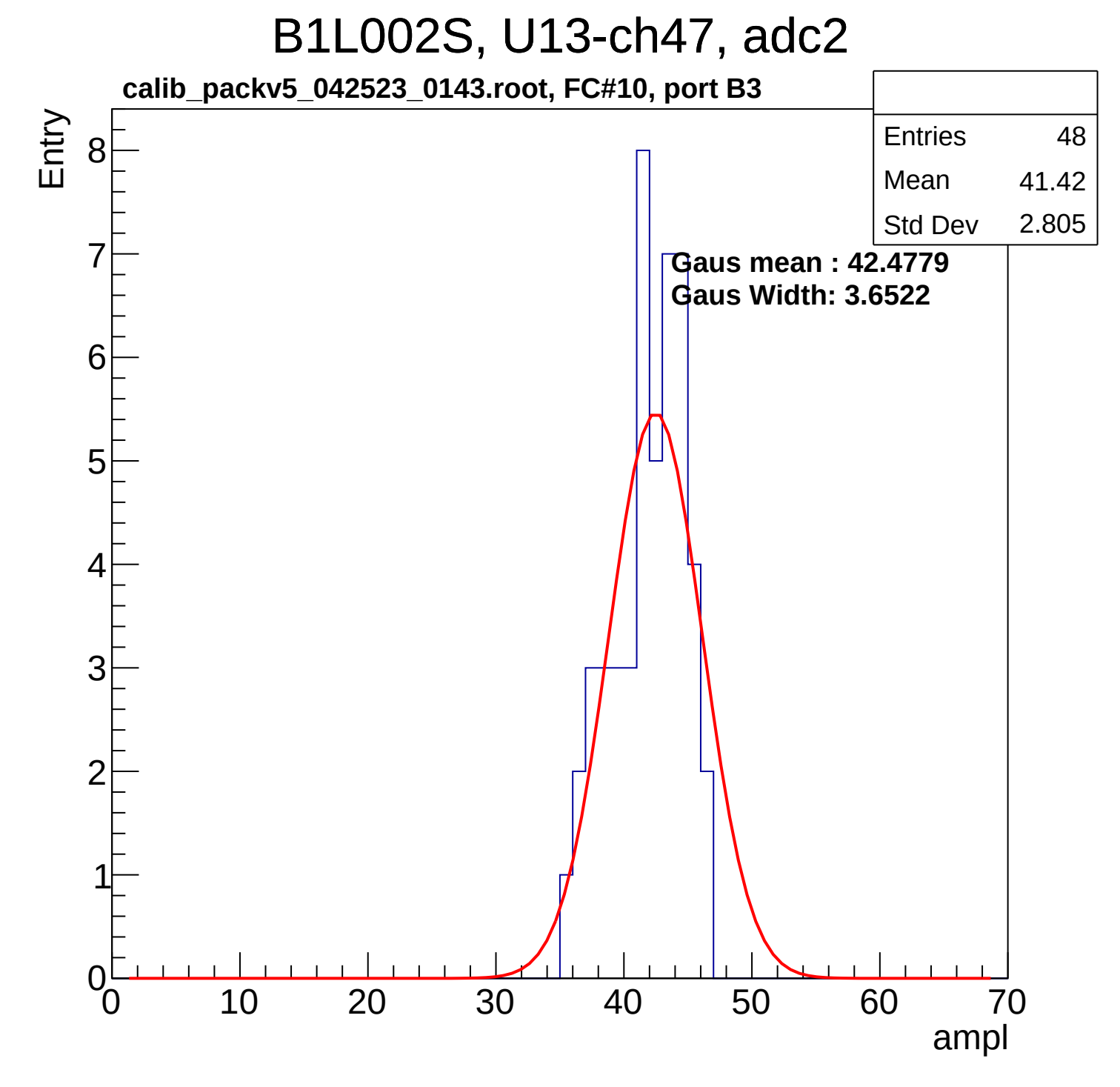
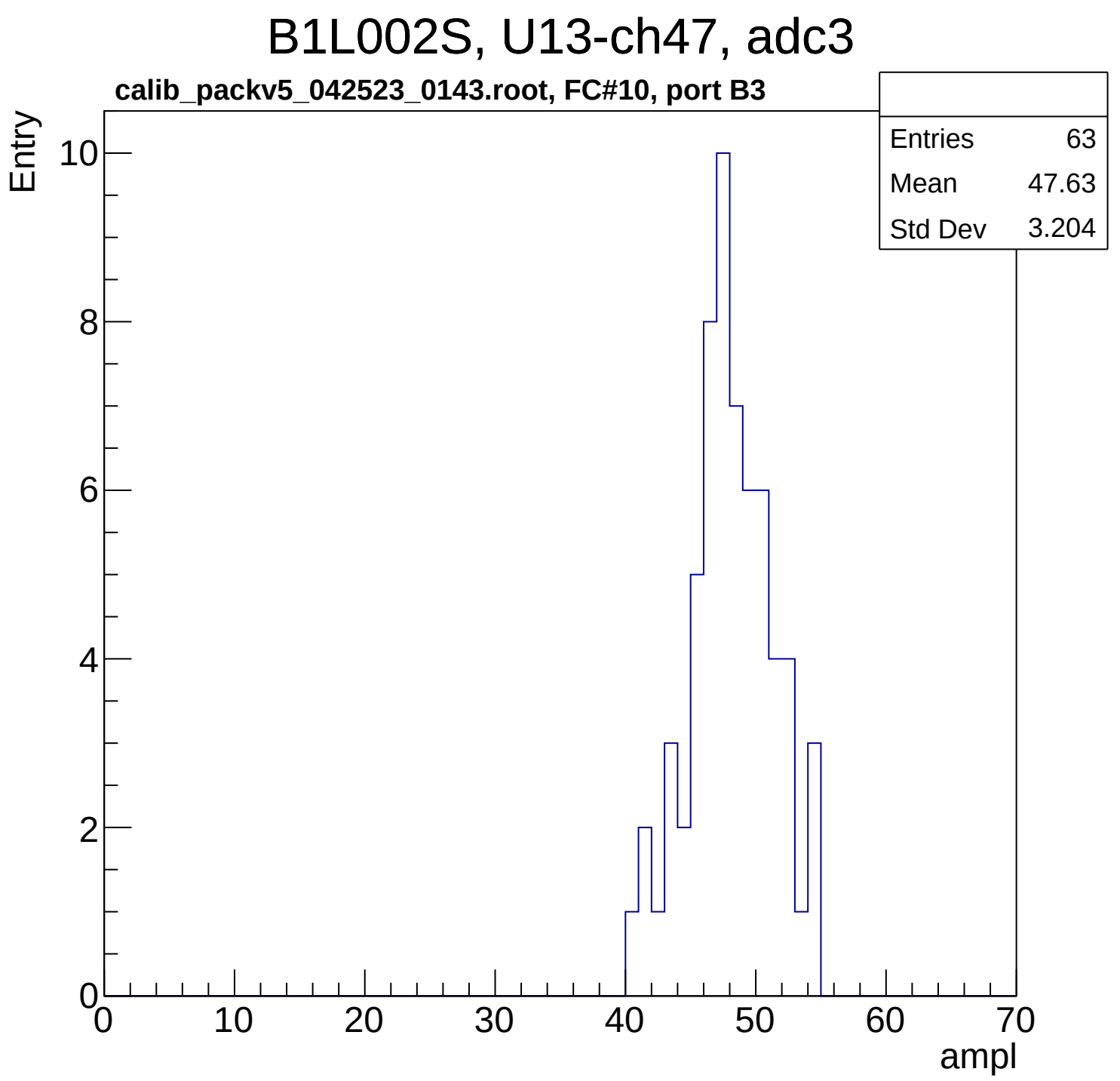
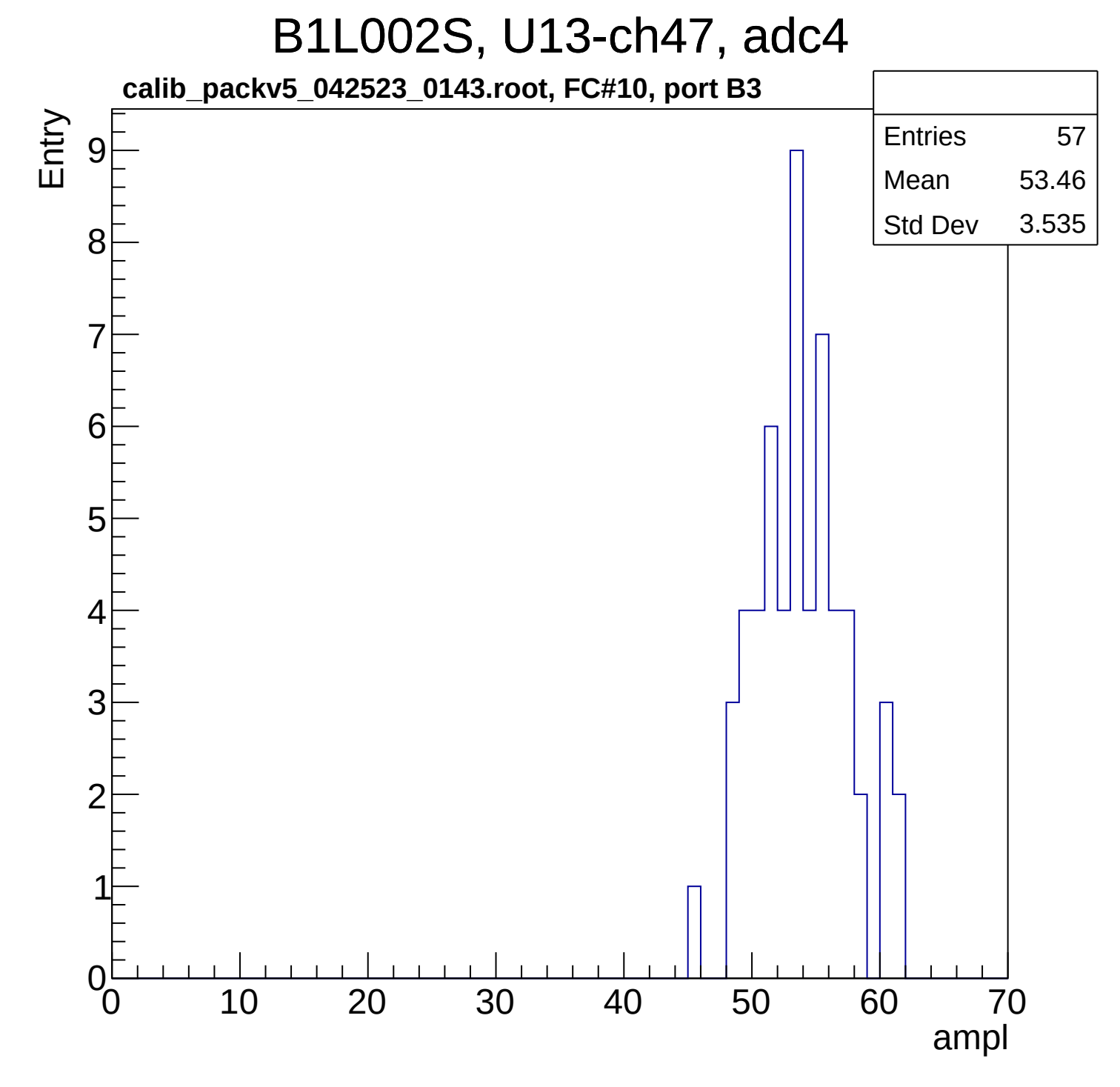
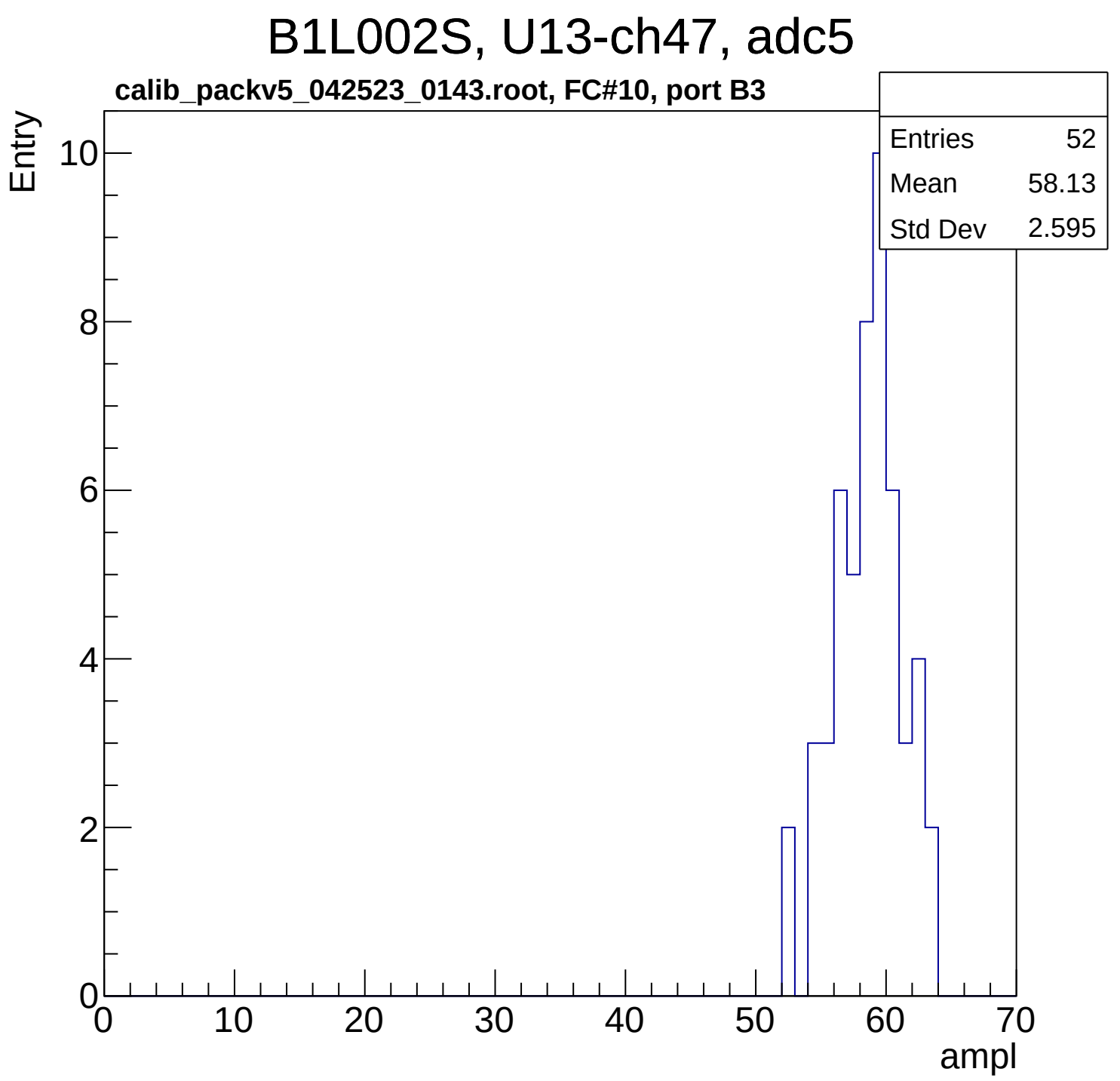
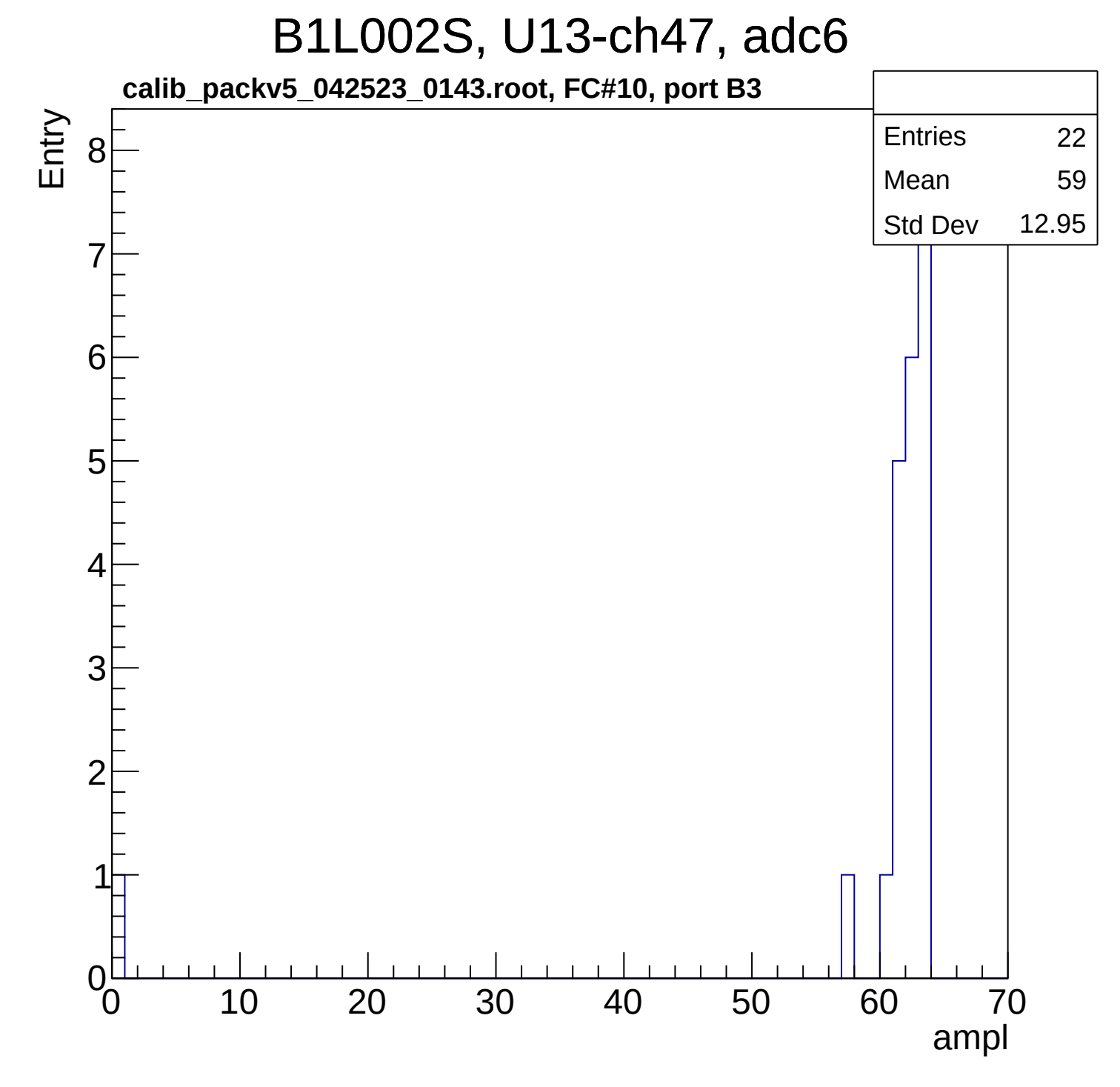
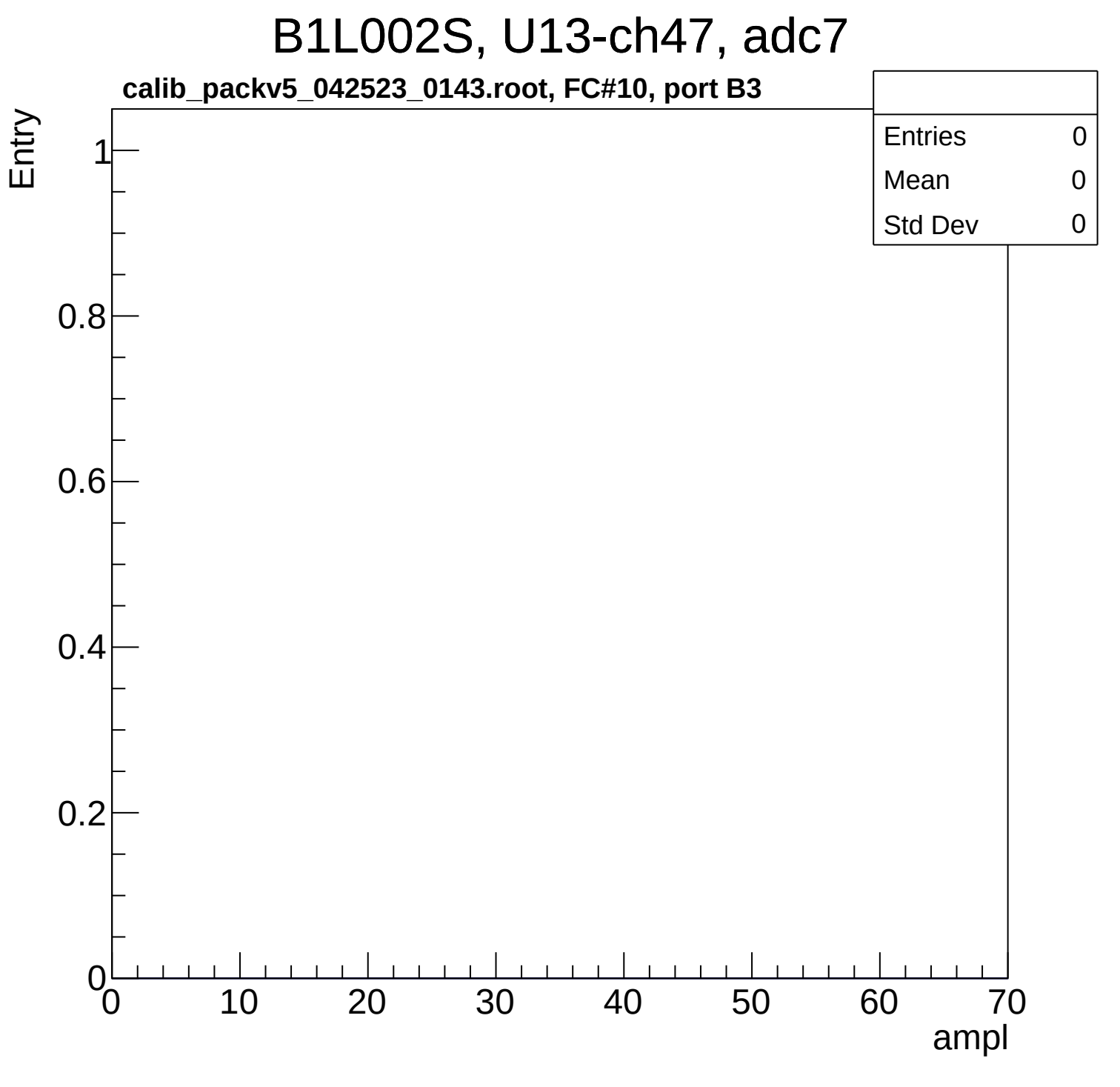

B1L002S, U13-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.42
Std Dev 2.805
Gaus mean : 42.4779
Gaus Width: 3.6522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

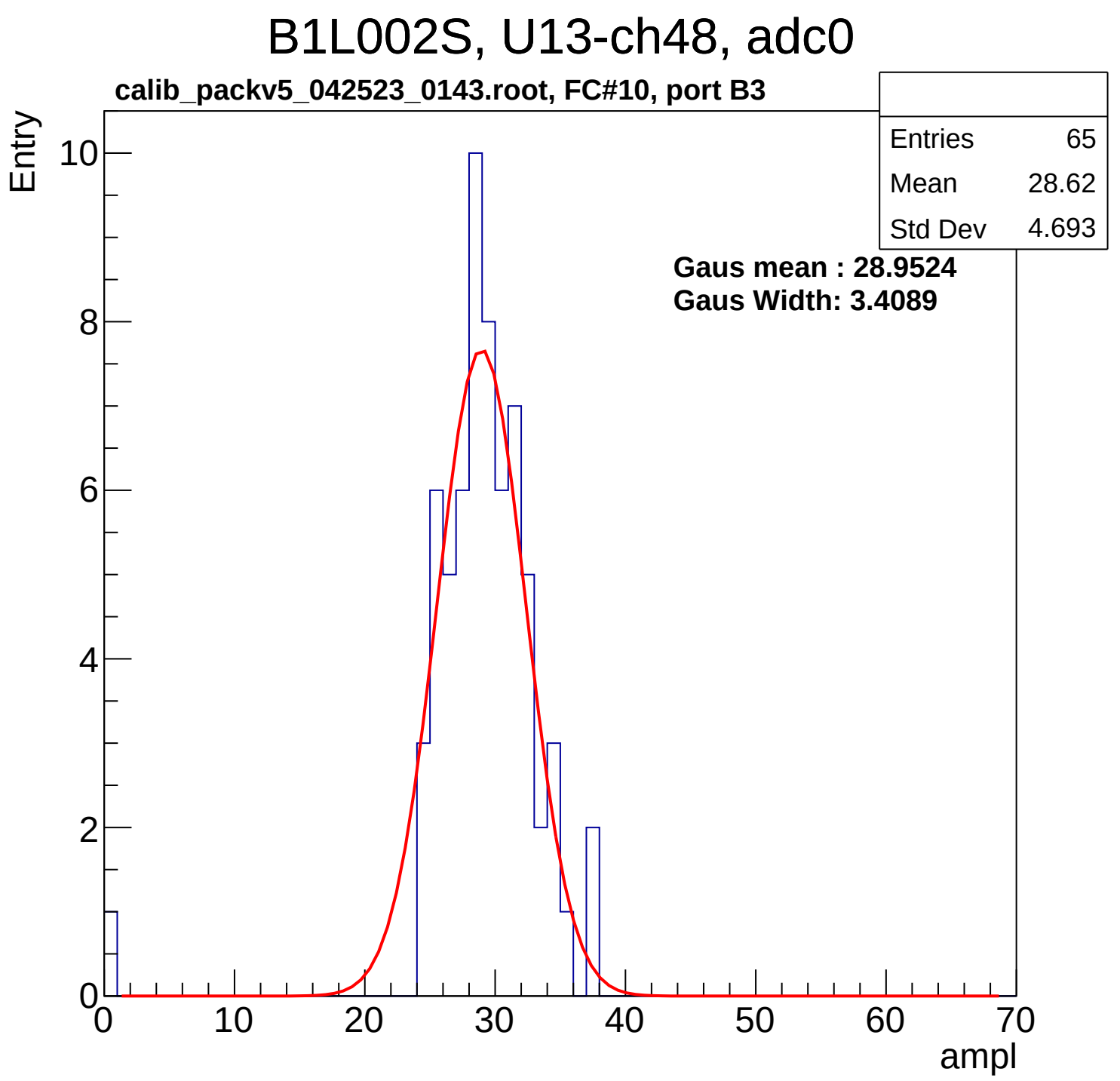

B1L002S, U13-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.63
Std Dev 3.204
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

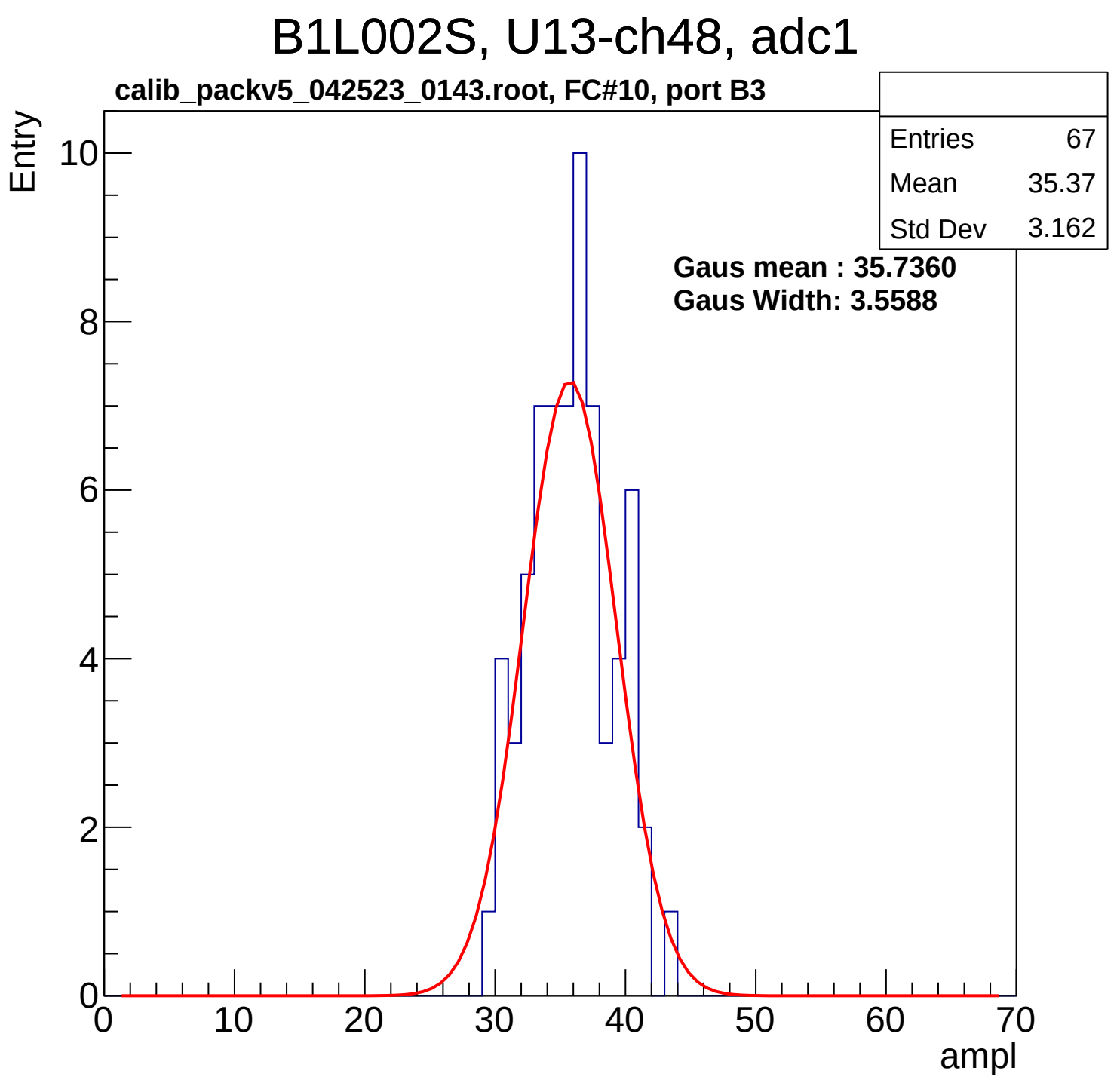

B1L002S, U13-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.46
Std Dev 3.535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

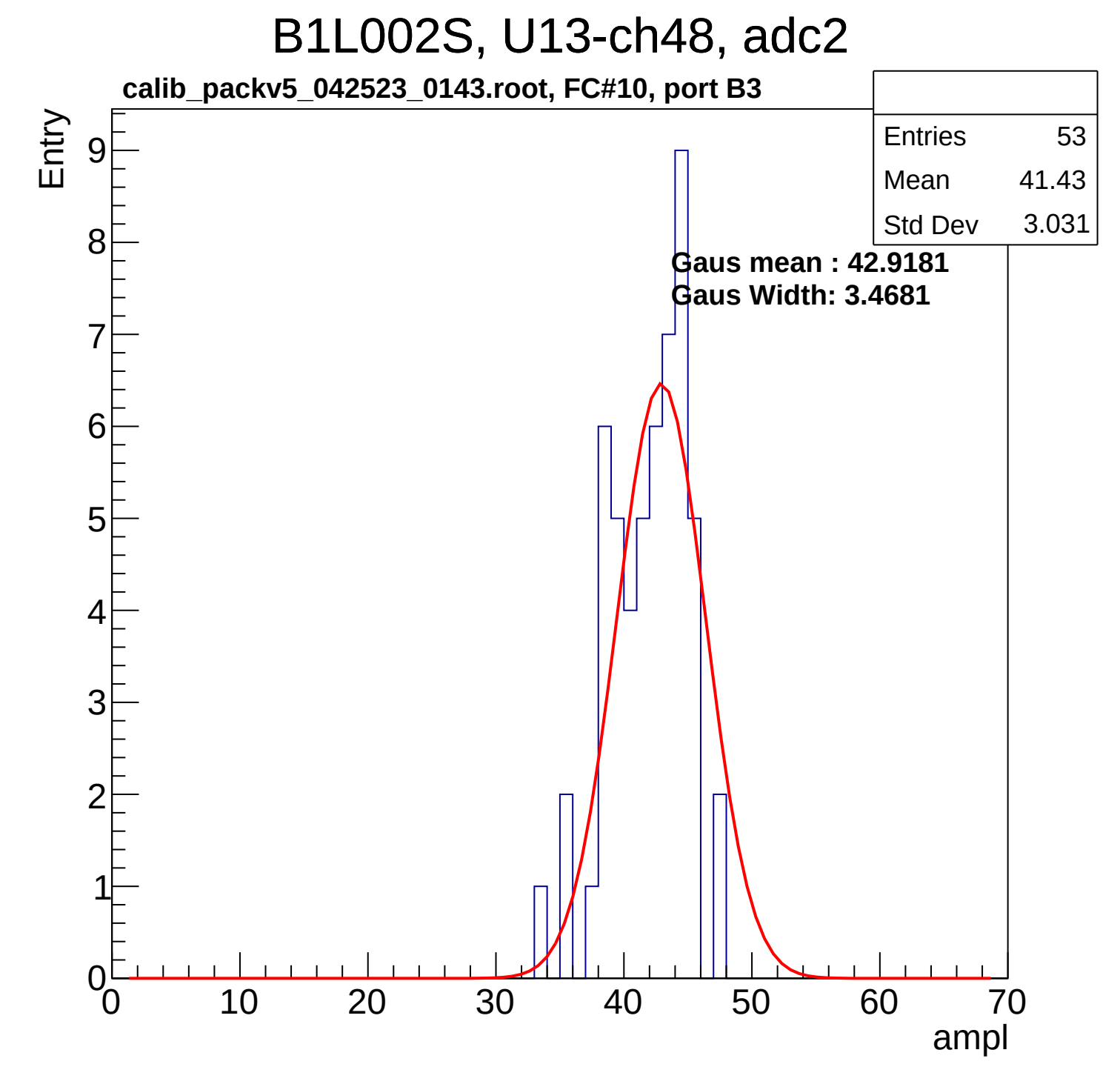

B1L002S, U13-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.13
Std Dev 2.595
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

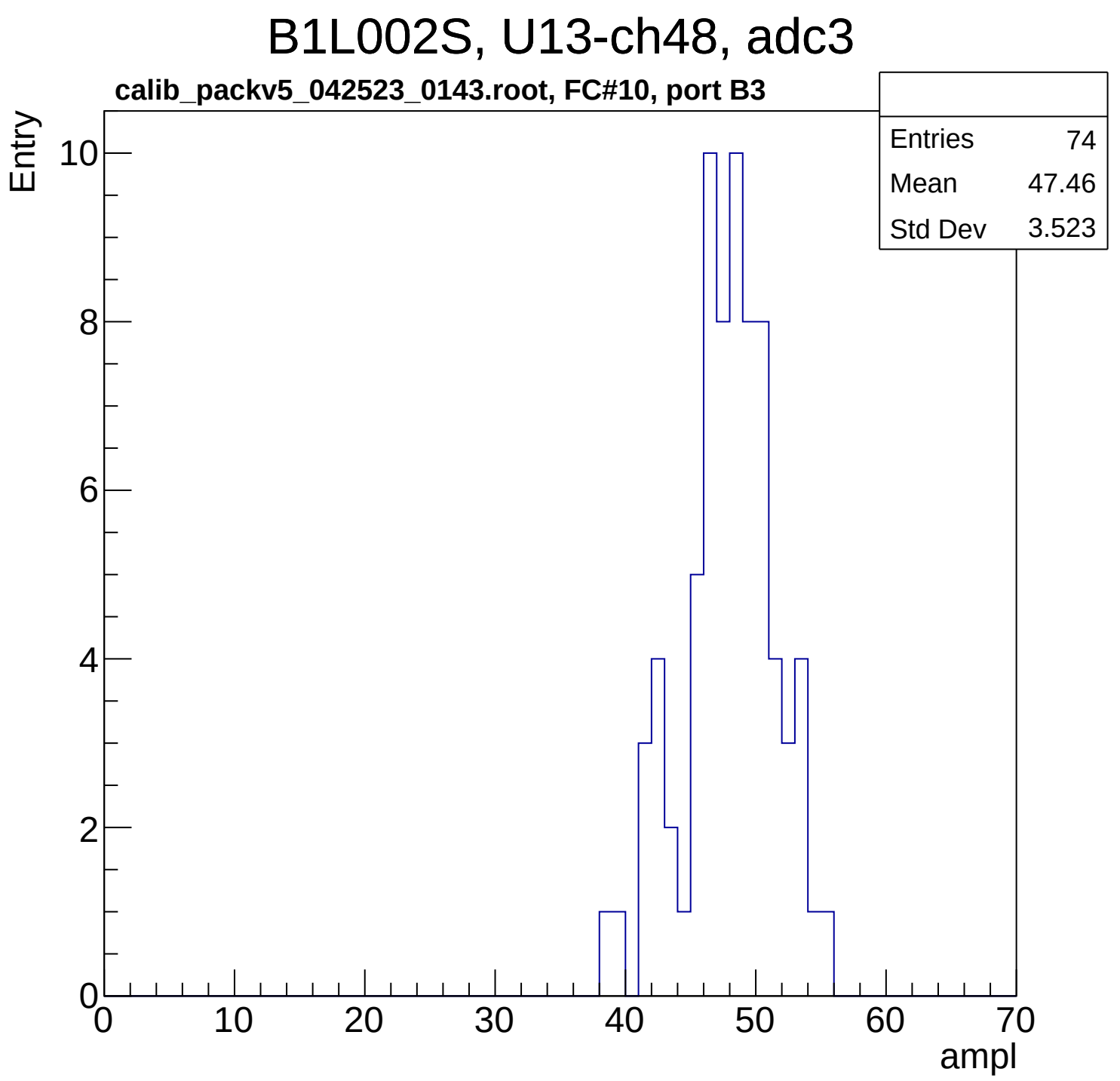

B1L002S, U13-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 59
Std Dev 12.95
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

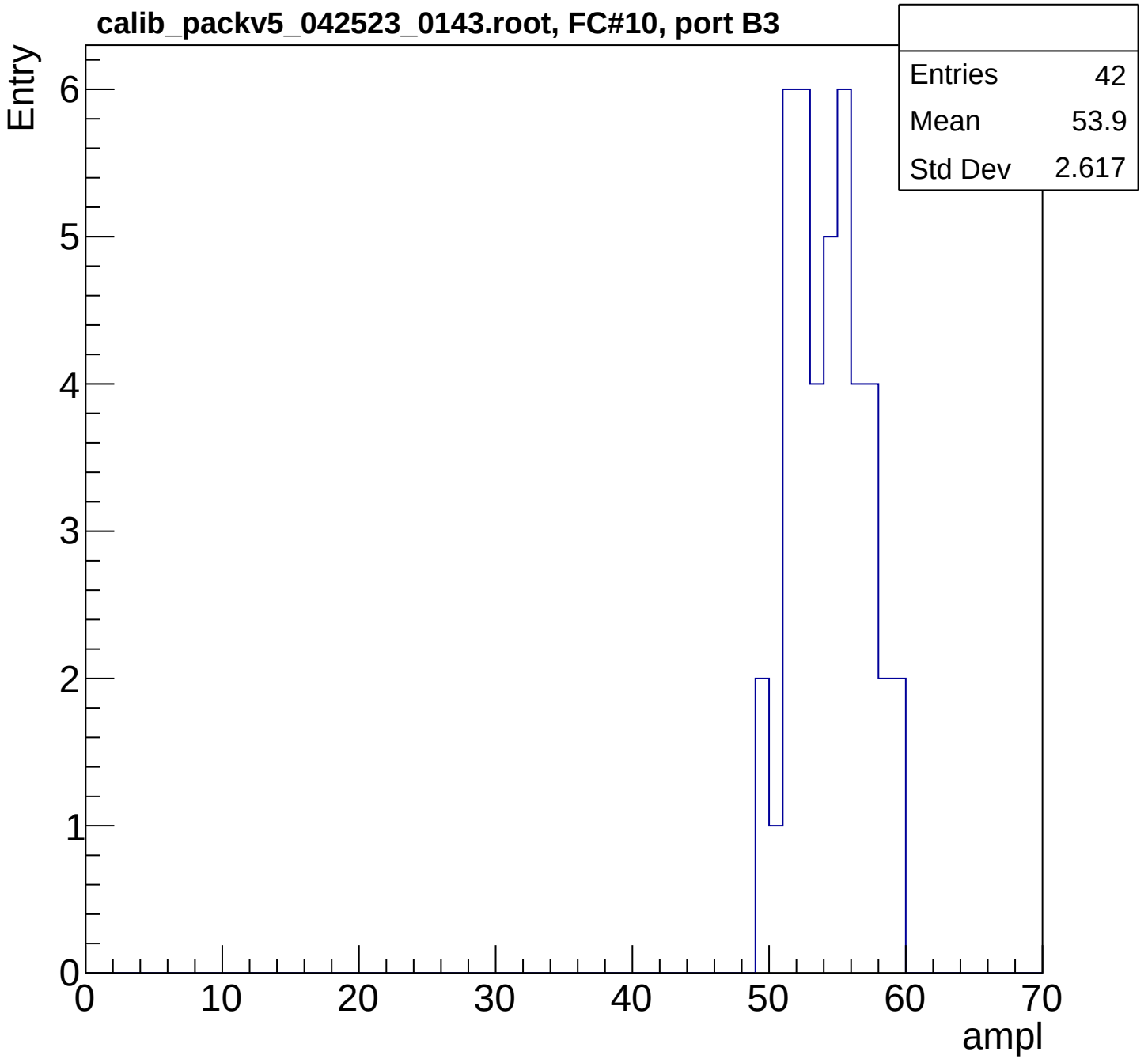

B1L002S, U13-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

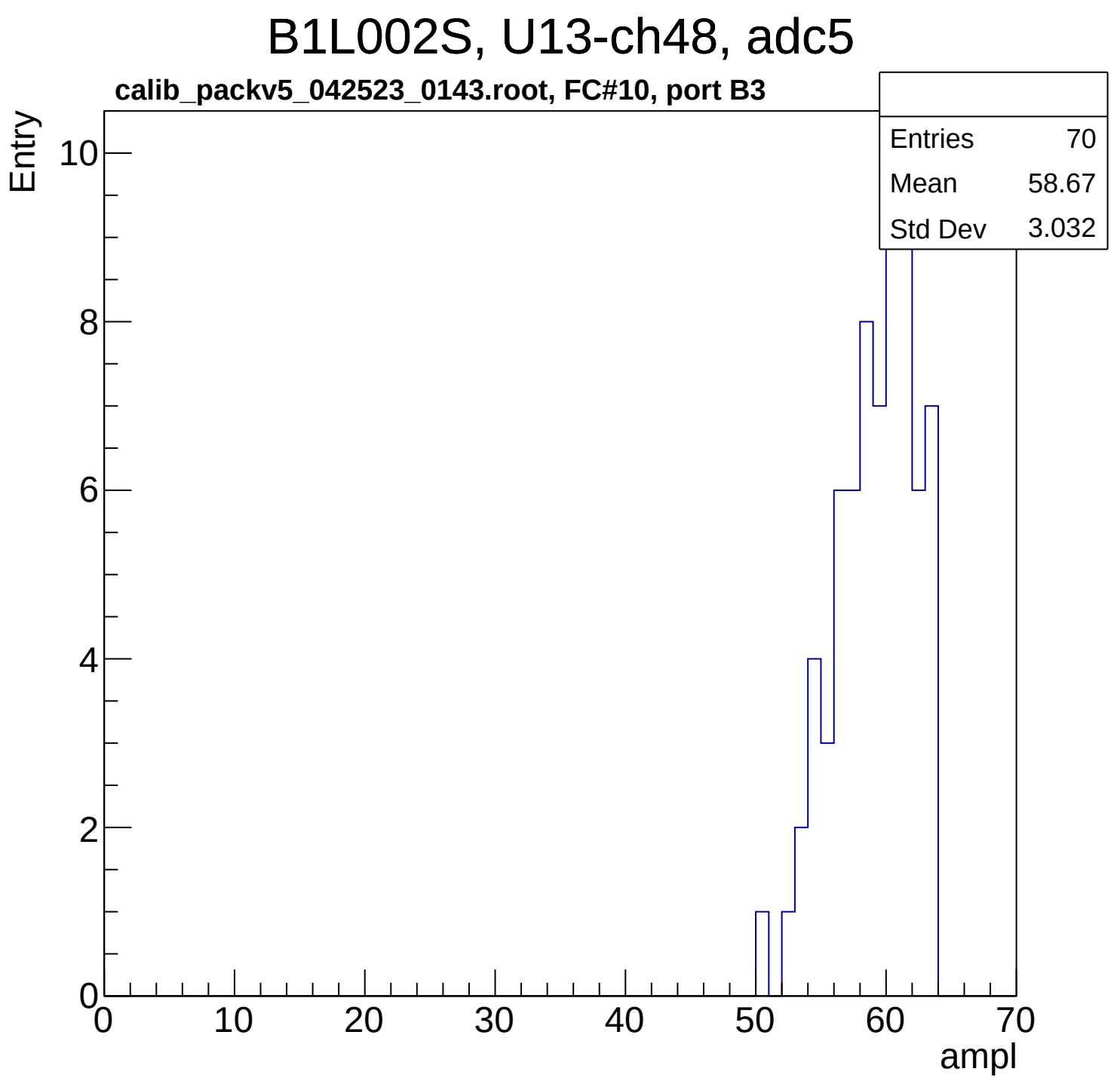

B1L002S, U13-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.62
Std Dev 4.693
Gaus mean : 28.9524
Gaus Width: 3.4089
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.37
Std Dev 3.162
Gaus mean : 35.7360
Gaus Width: 3.5588
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.43
Std Dev 3.031
Gaus mean : 42.9181
Gaus Width: 3.4681
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.46
Std Dev 3.523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 53.9
Std Dev 2.617
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 58.67
Std Dev 3.032
Entry
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2
4
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10
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10
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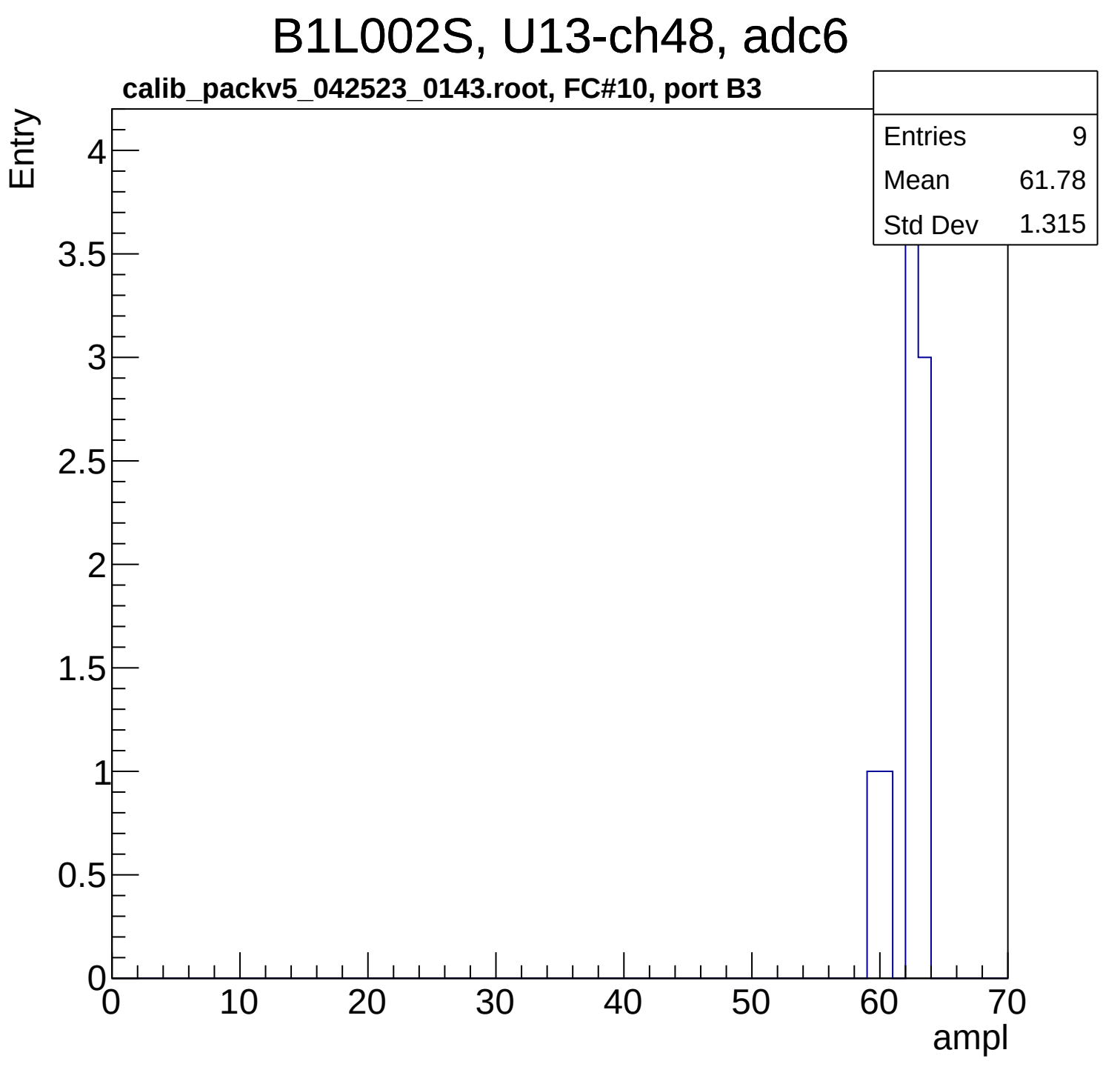
50
60
70
ampl


B1L002S, U13-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.78
Std Dev 1.315
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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60
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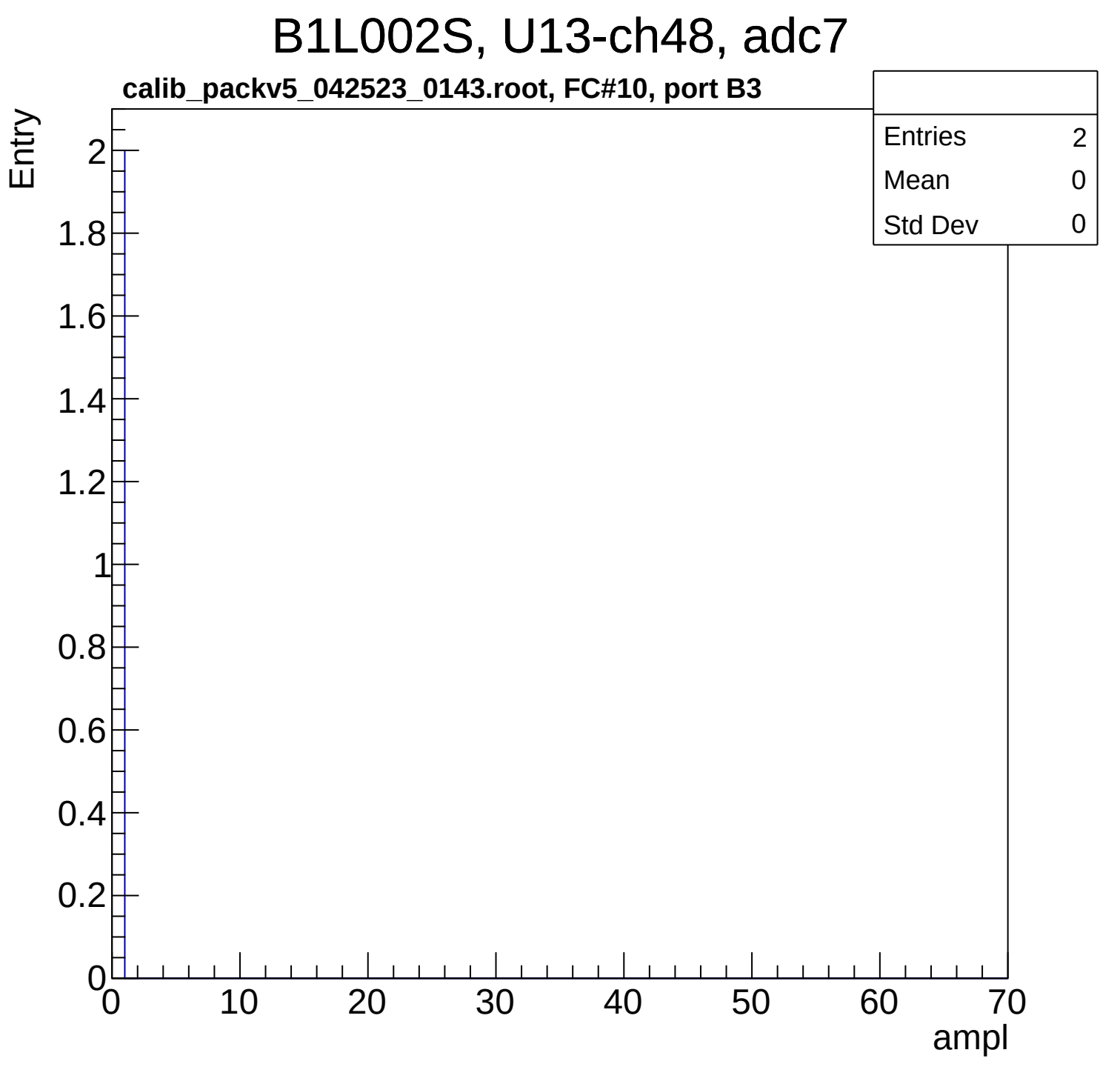
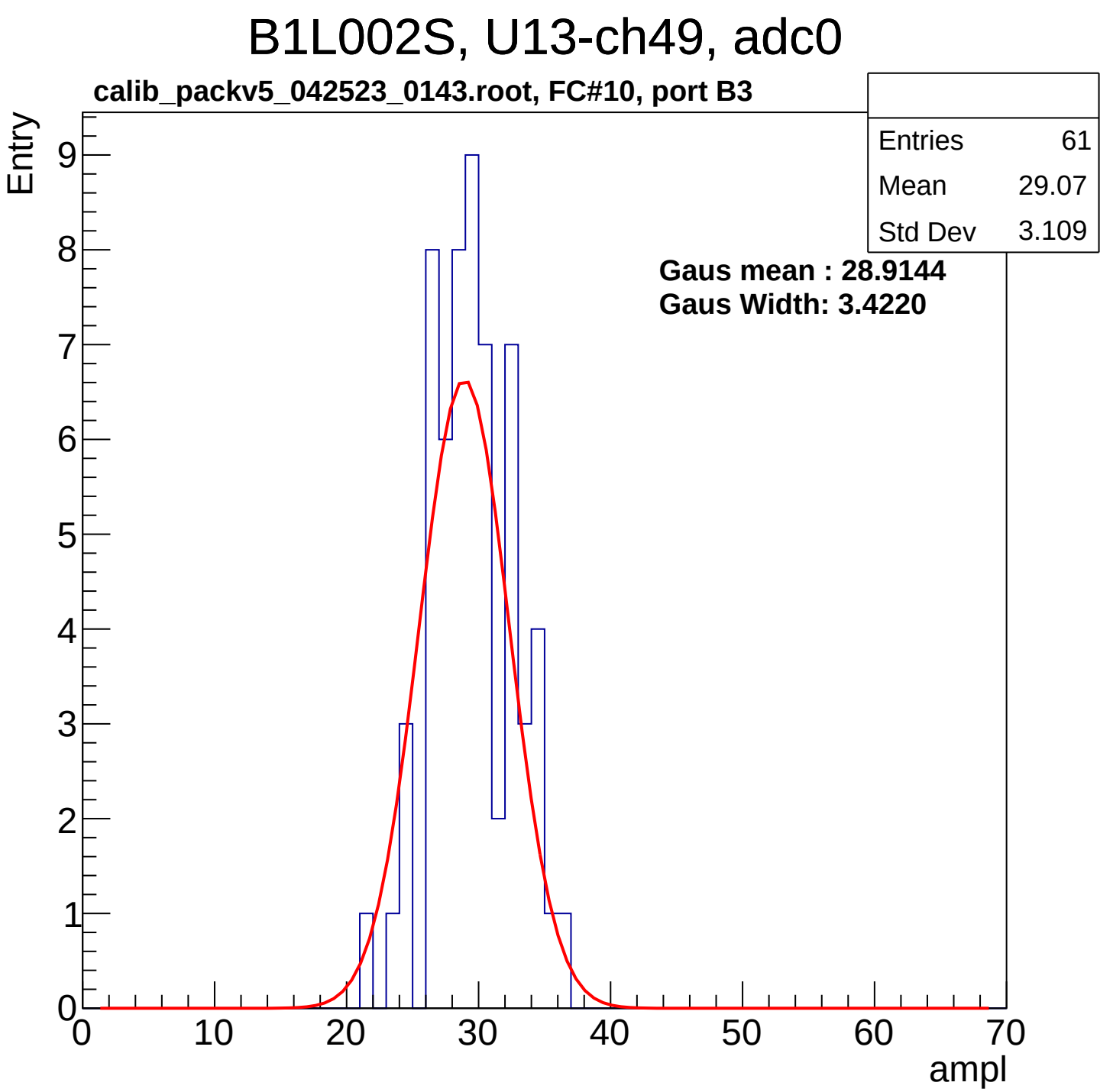
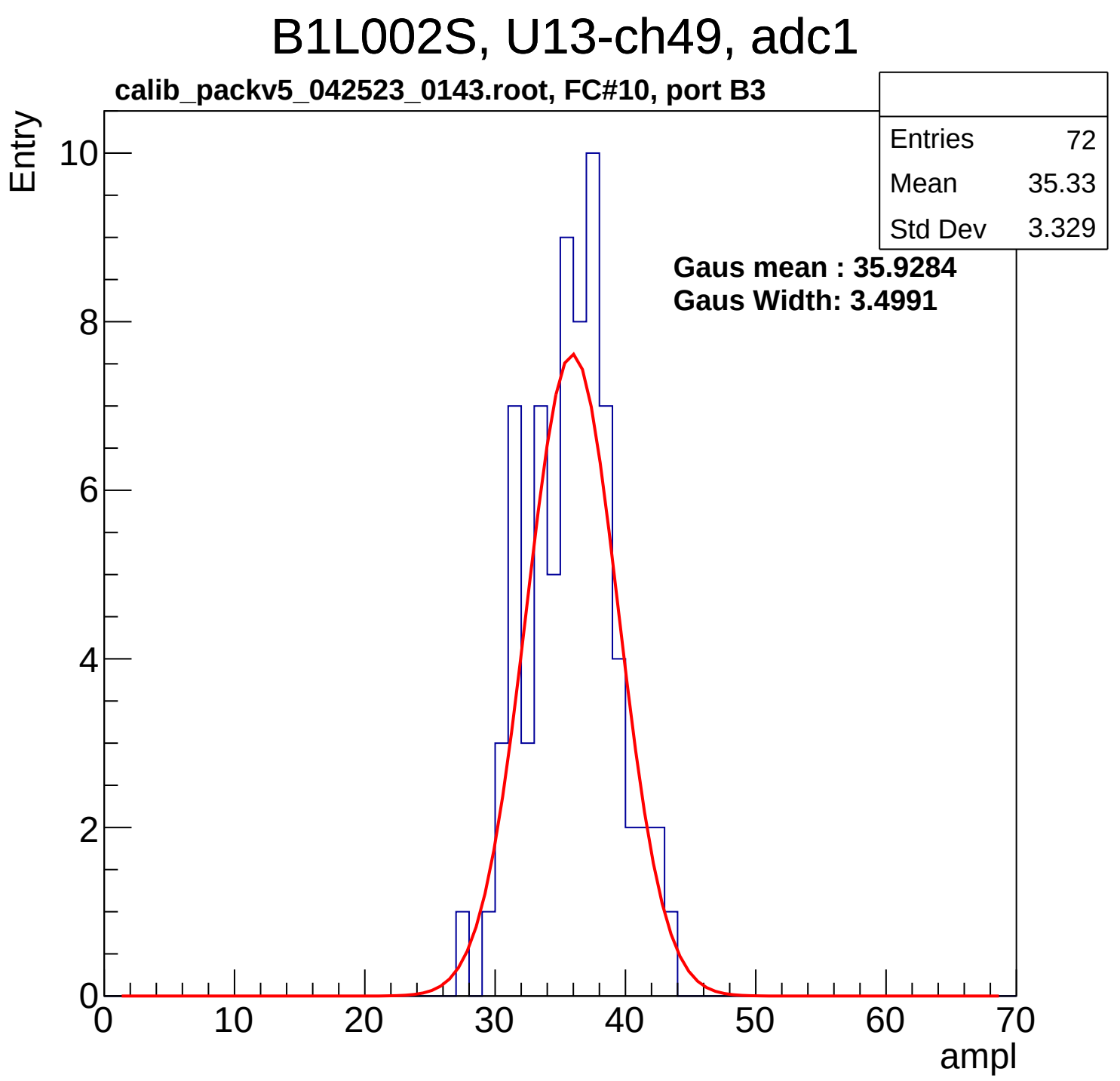
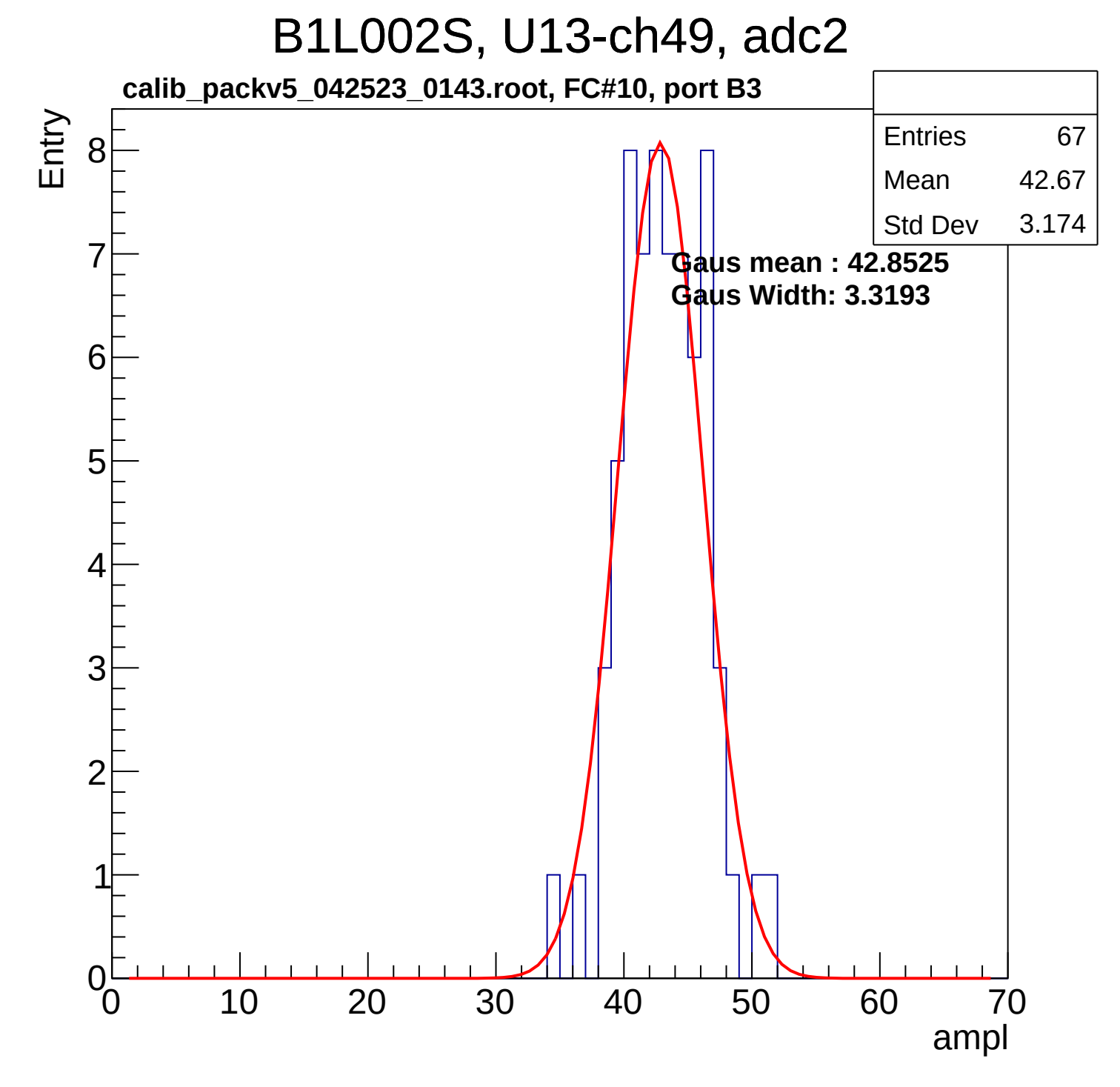
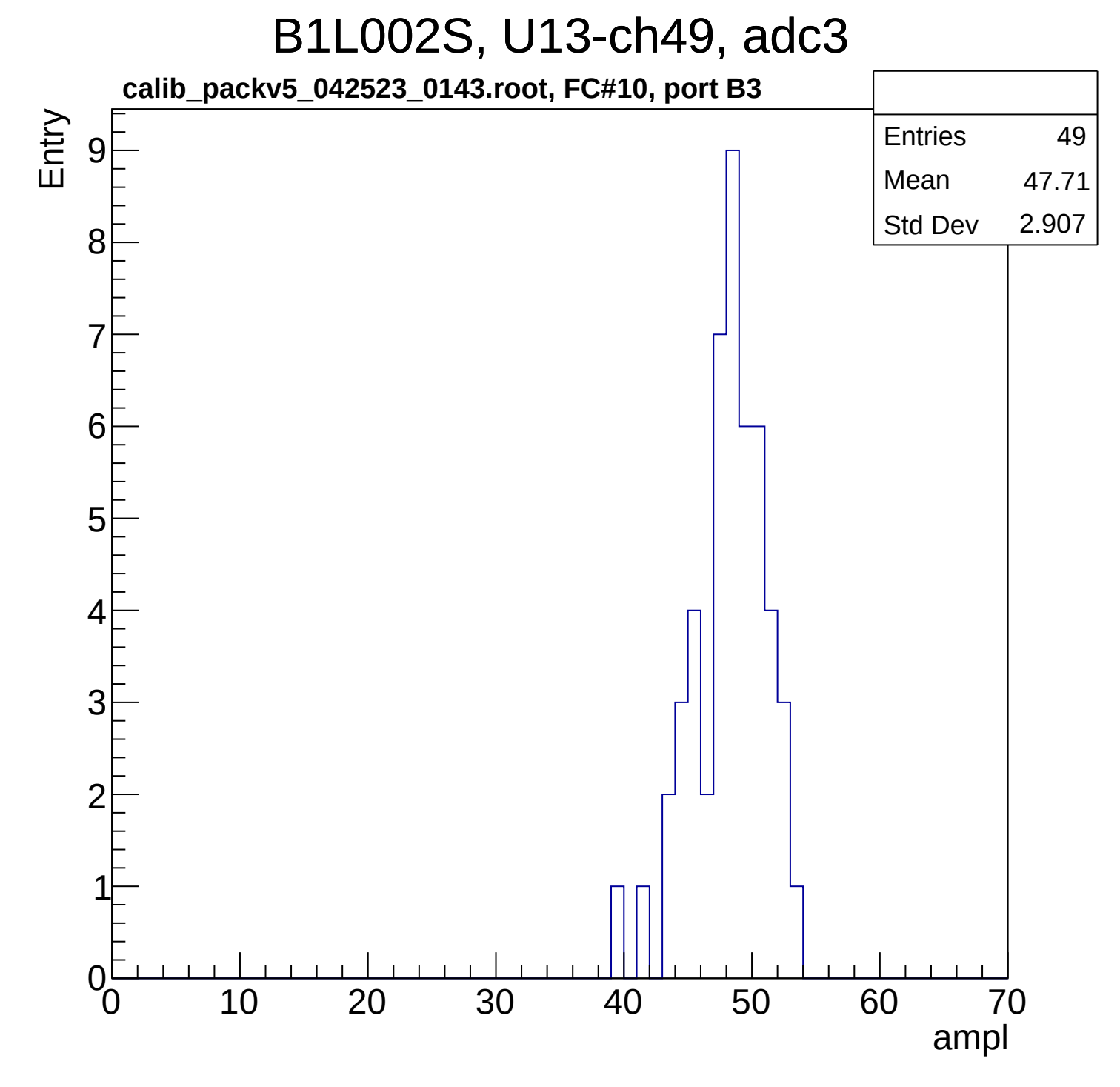

B1L002S, U13-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.07
Std Dev 3.109
Gaus mean : 28.9144
Gaus Width: 3.4220
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.33
Std Dev 3.329
Gaus mean : 35.9284
Gaus Width: 3.4991
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 42.67
Std Dev 3.174
Gaus mean : 42.8525
Gaus Width: 3.3193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 47.71
Std Dev 2.907
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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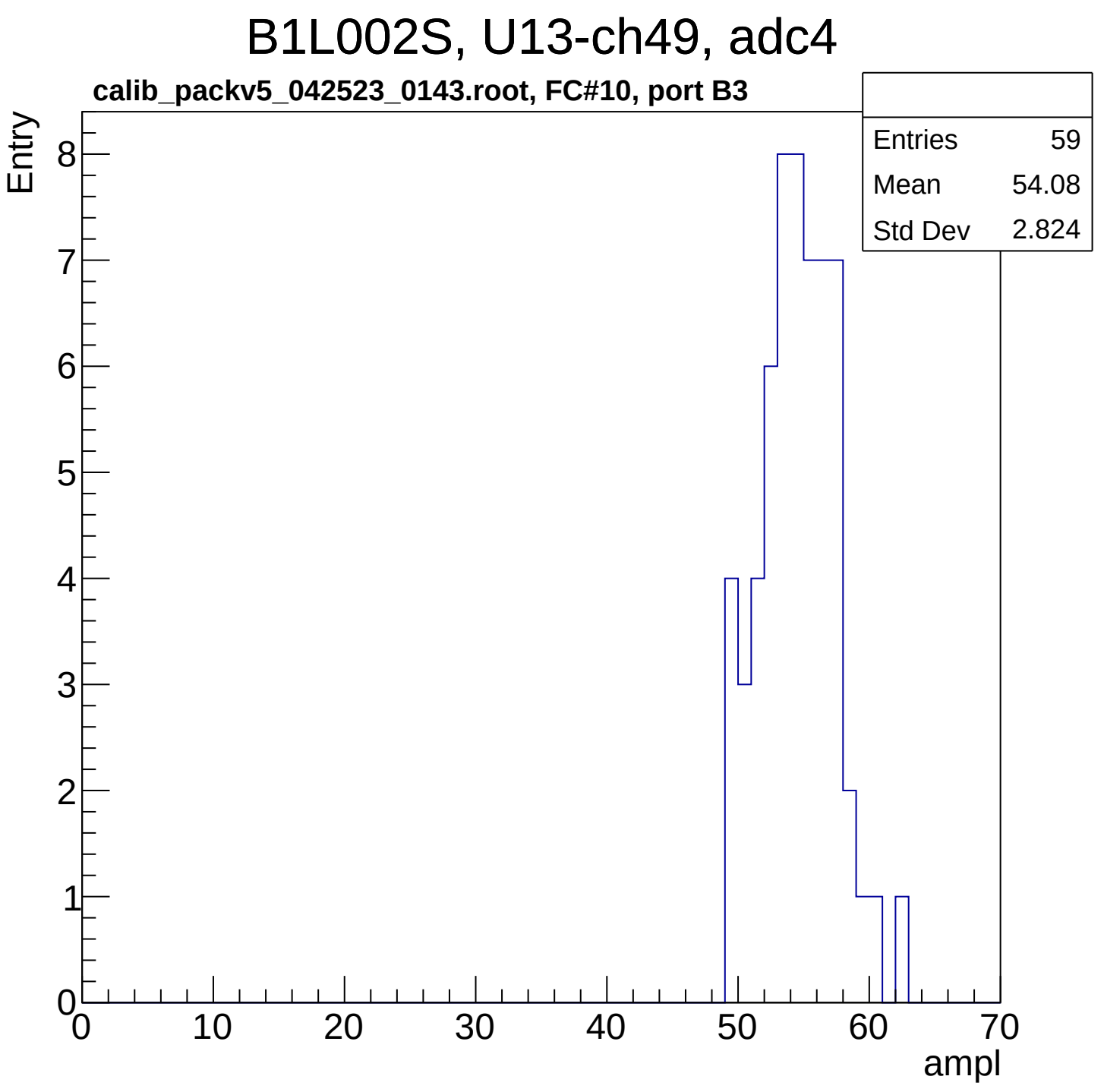
60
70


B1L002S, U13-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.08
Std Dev 2.824
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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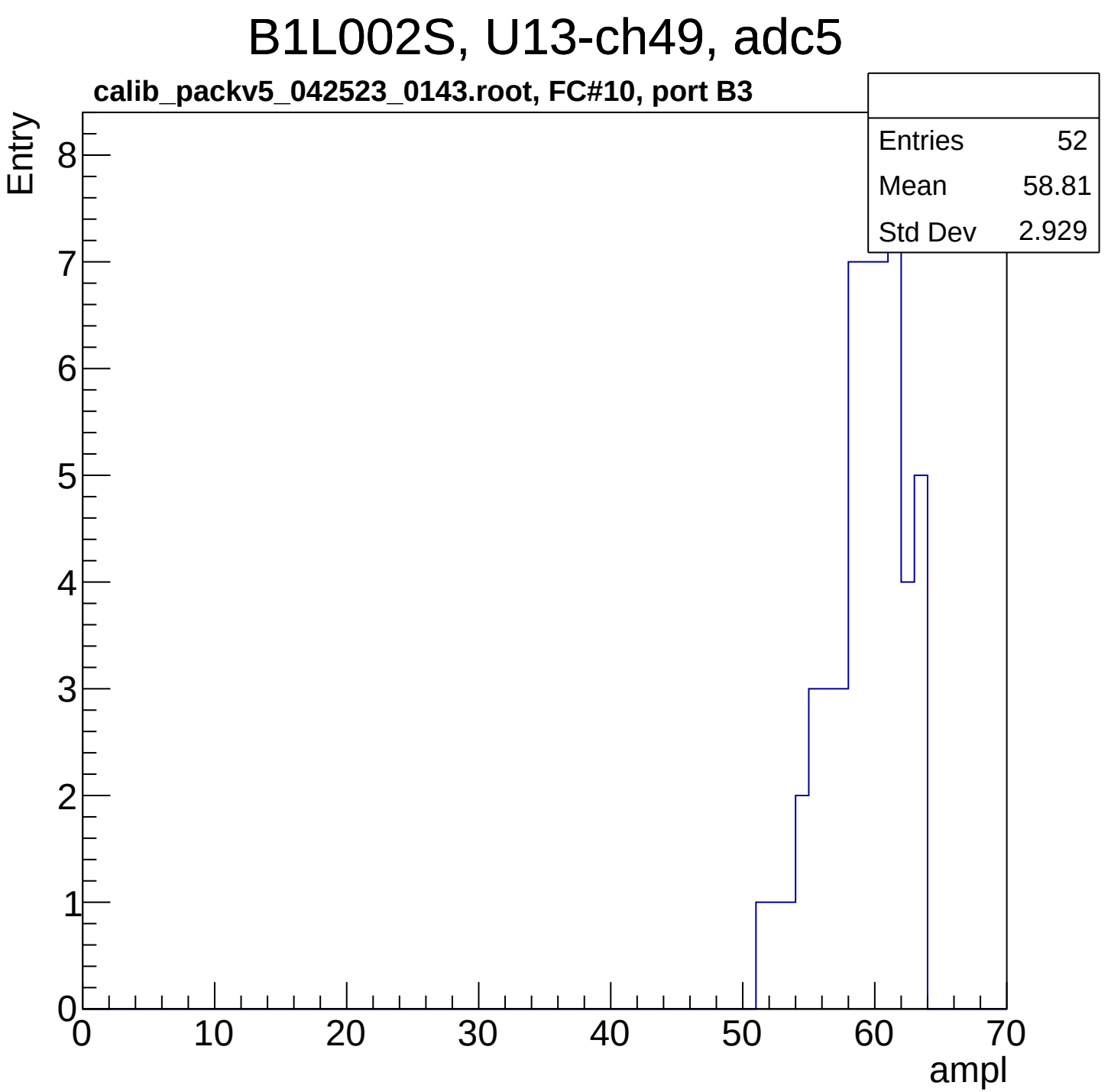
B1L002S, U13-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.81
Std Dev 2.929
Entry
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2
3
4
5
6
7
8
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10
20
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50
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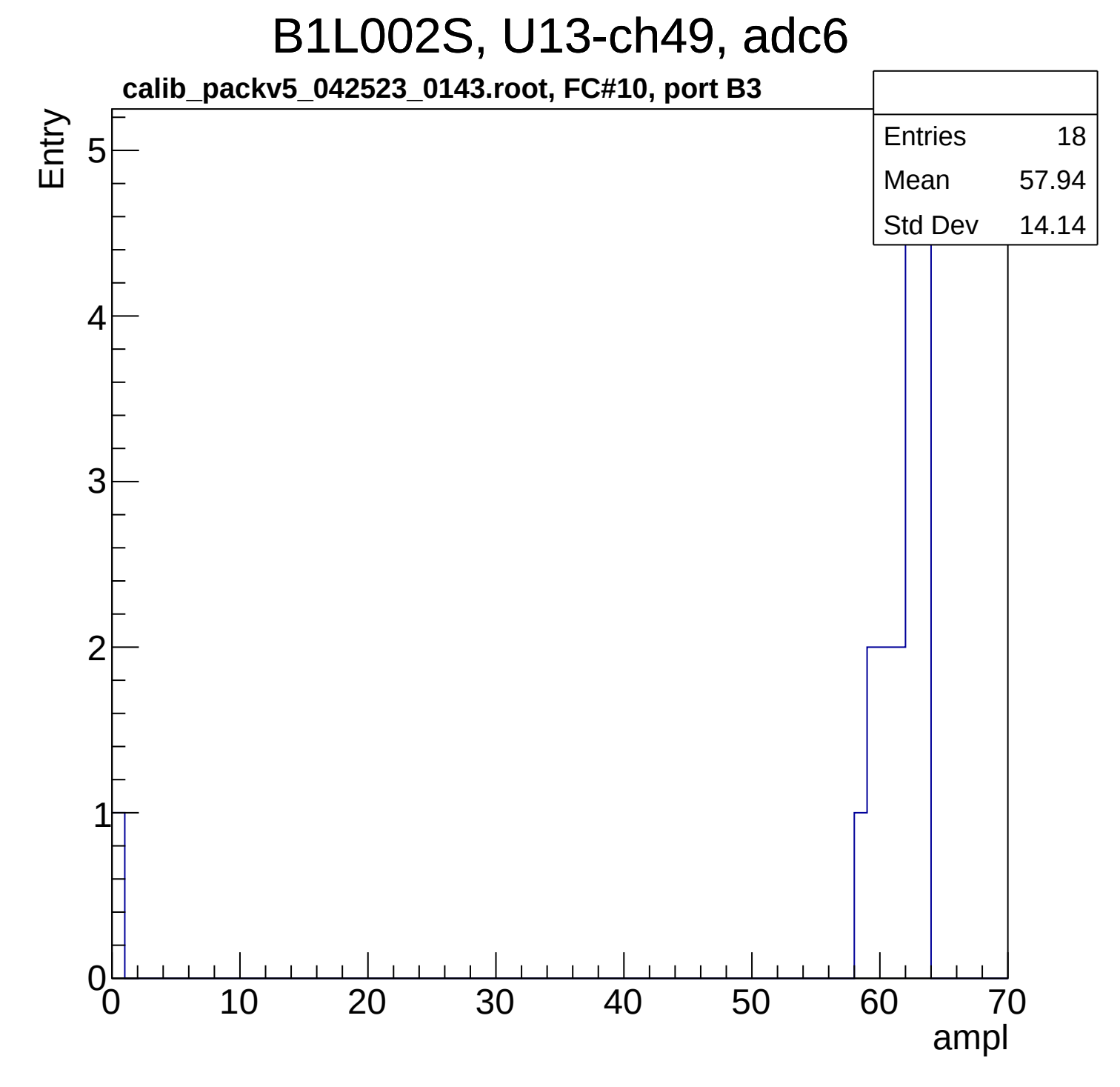
70
ampl


B1L002S, U13-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.94
Std Dev 14.14
Entry
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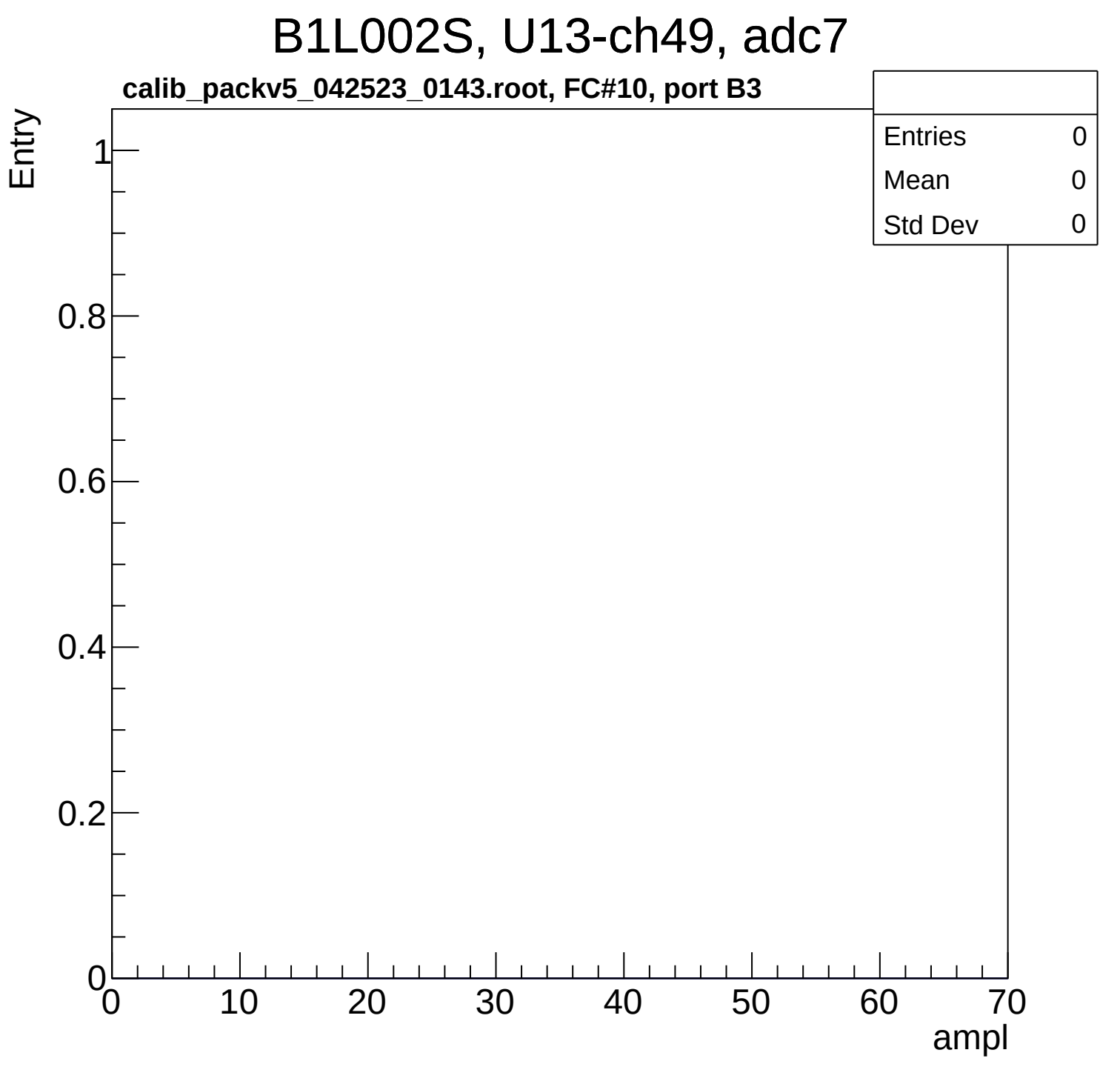
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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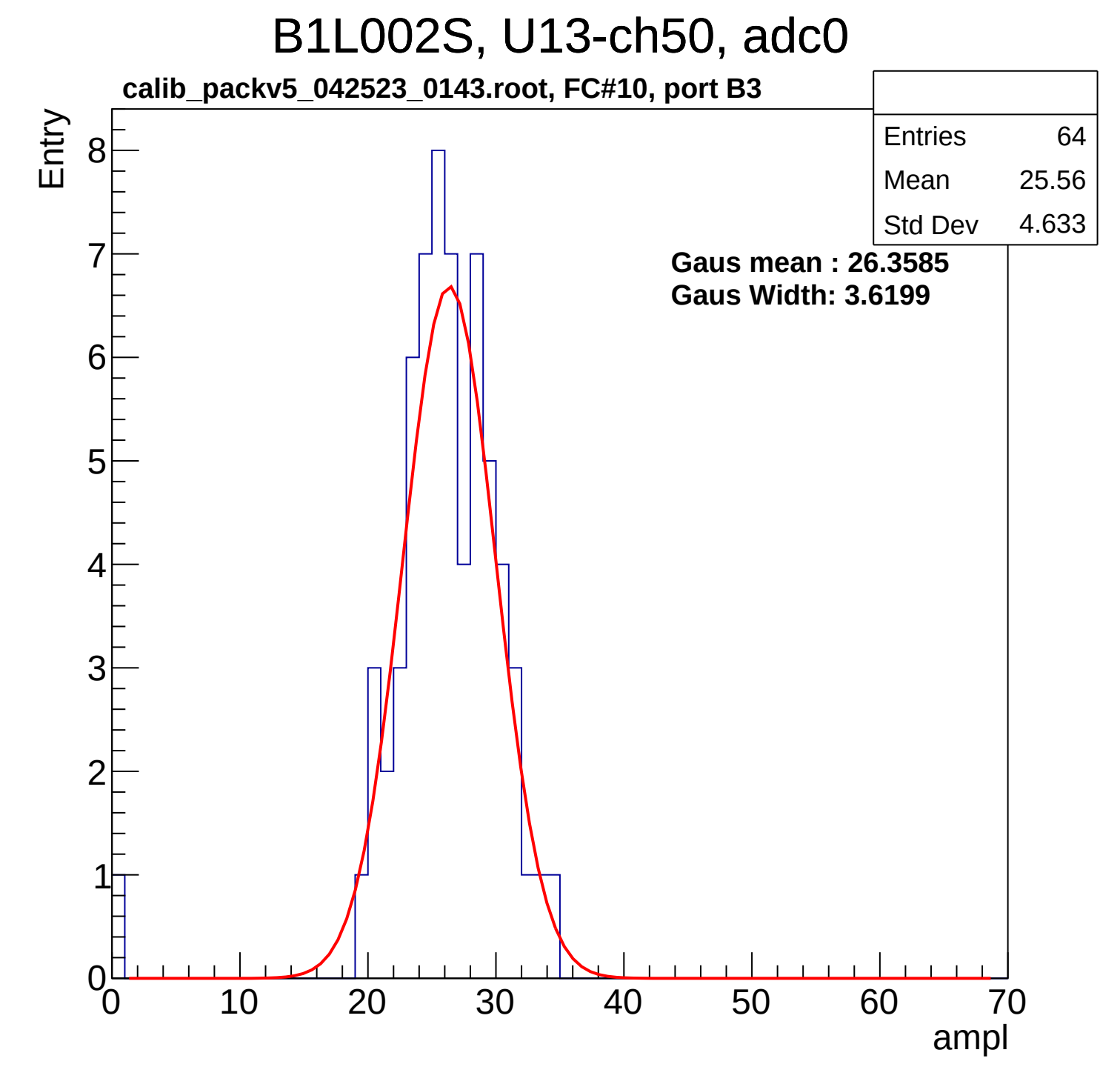
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 25.56
Std Dev 4.633
Gaus mean : 26.3585
Gaus Width: 3.6199
Entry
ampl
0
1
2
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5
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7
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0
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20
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60
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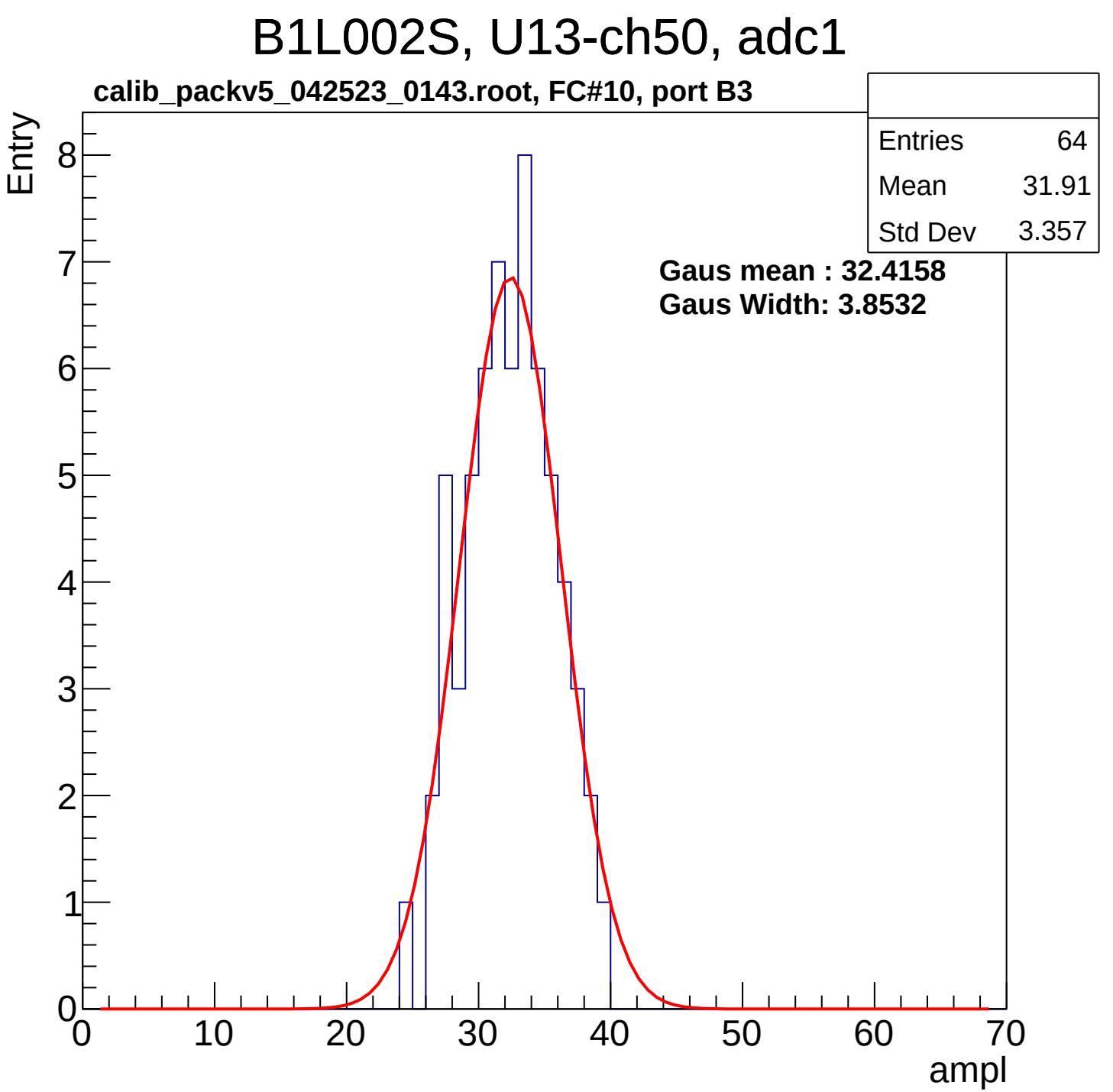
B1L002S, U13-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 31.91
Std Dev 3.357
Gaus mean : 32.4158
Gaus Width: 3.8532
Entry
ampl
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2
3
4
5
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0
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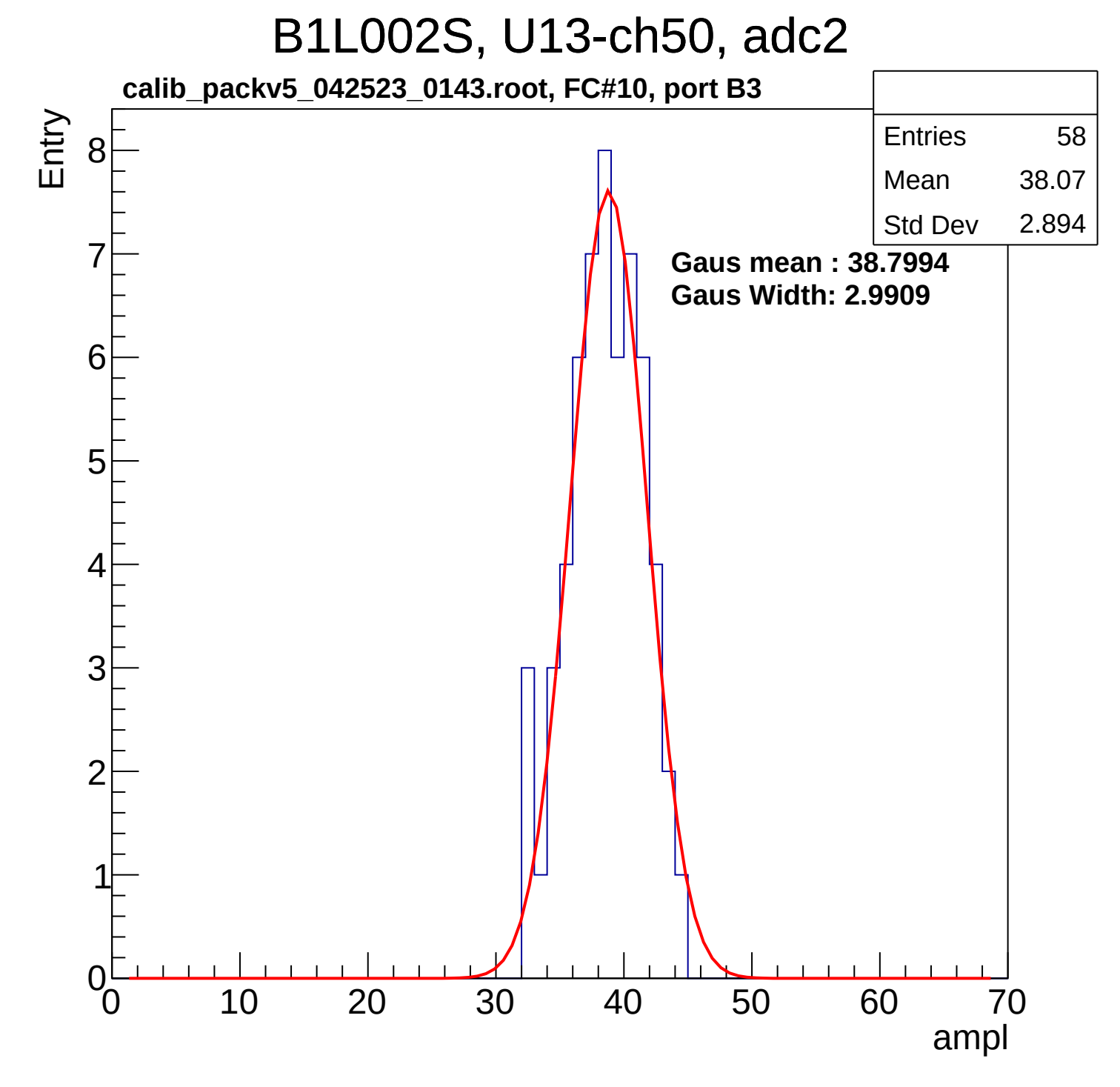
B1L002S, U13-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 38.07
Std Dev 2.894
Gaus mean : 38.7994
Gaus Width: 2.9909
Entry
ampl
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1
2
3
4
5
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7
8
0
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60
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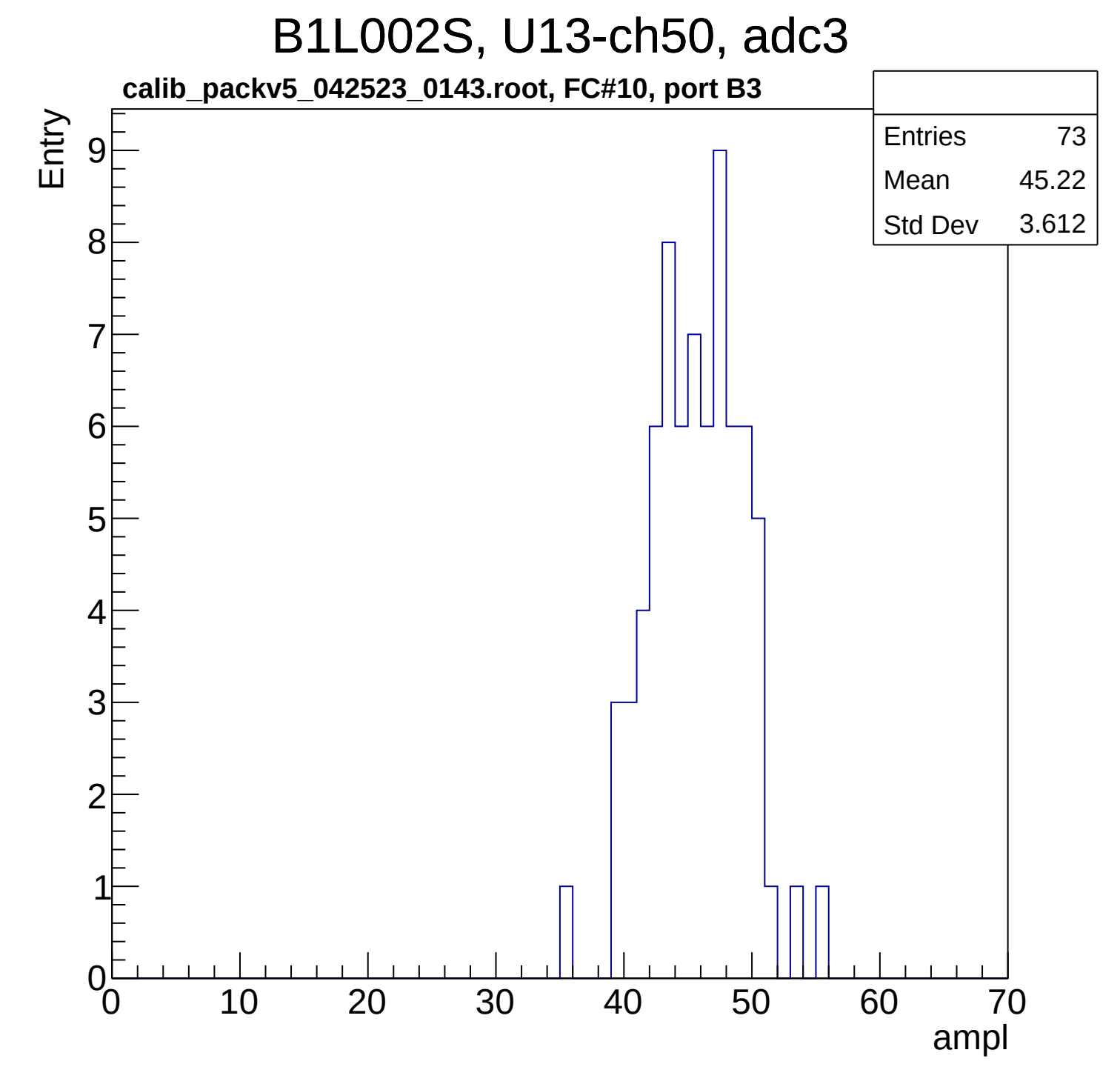
B1L002S, U13-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.22
Std Dev 3.612
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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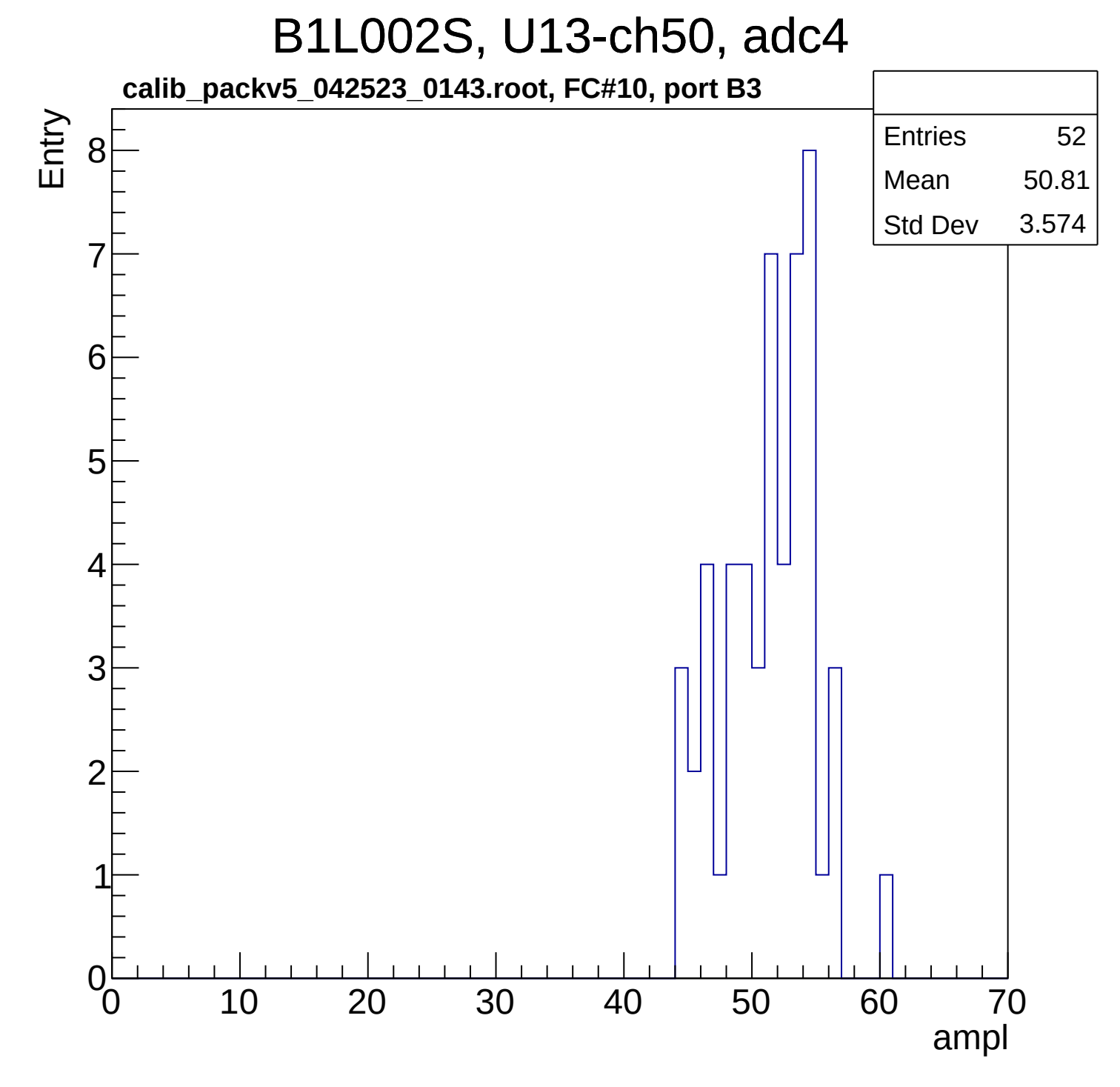
B1L002S, U13-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 50.81
Std Dev 3.574
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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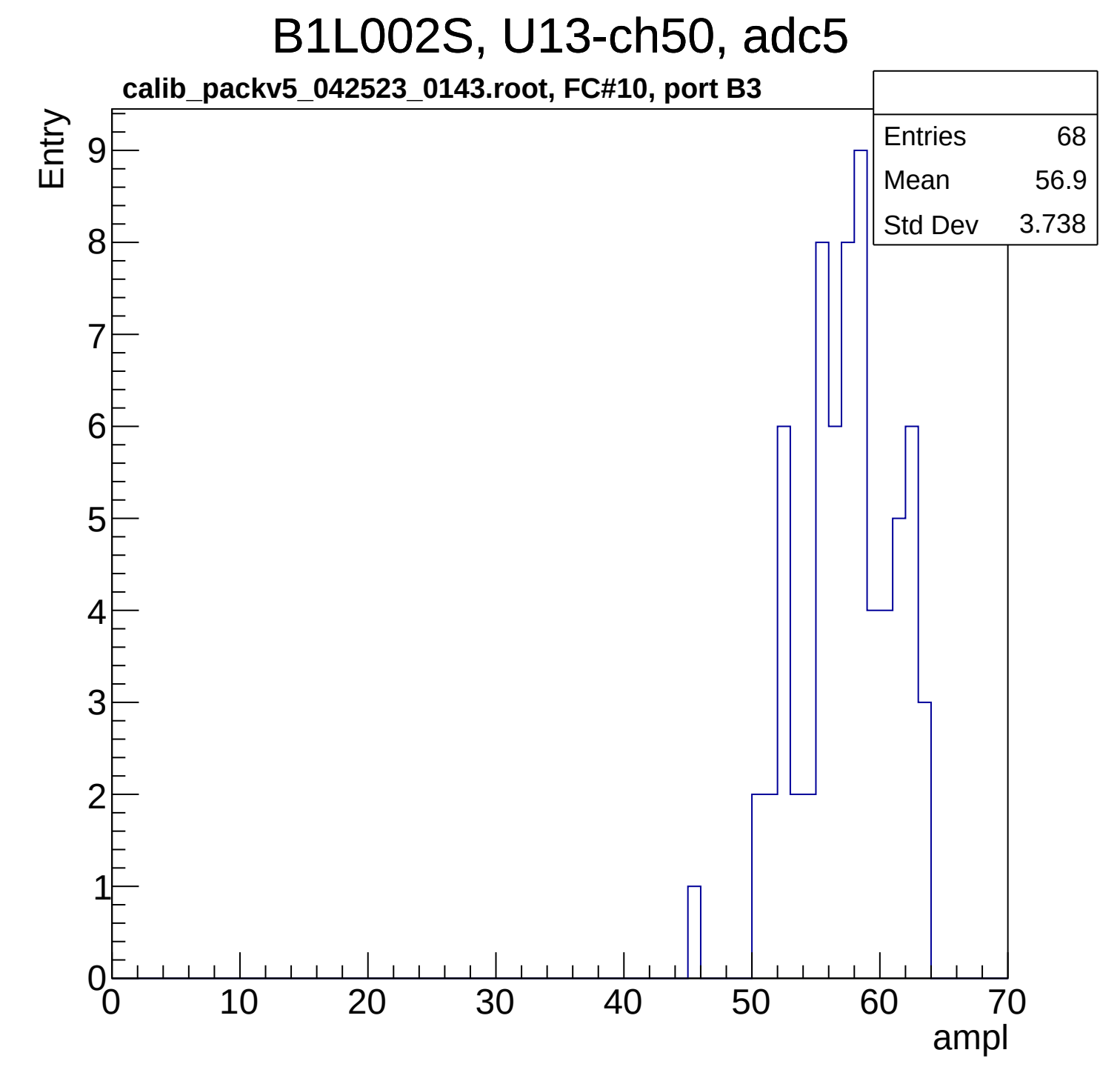
B1L002S, U13-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 56.9
Std Dev 3.738
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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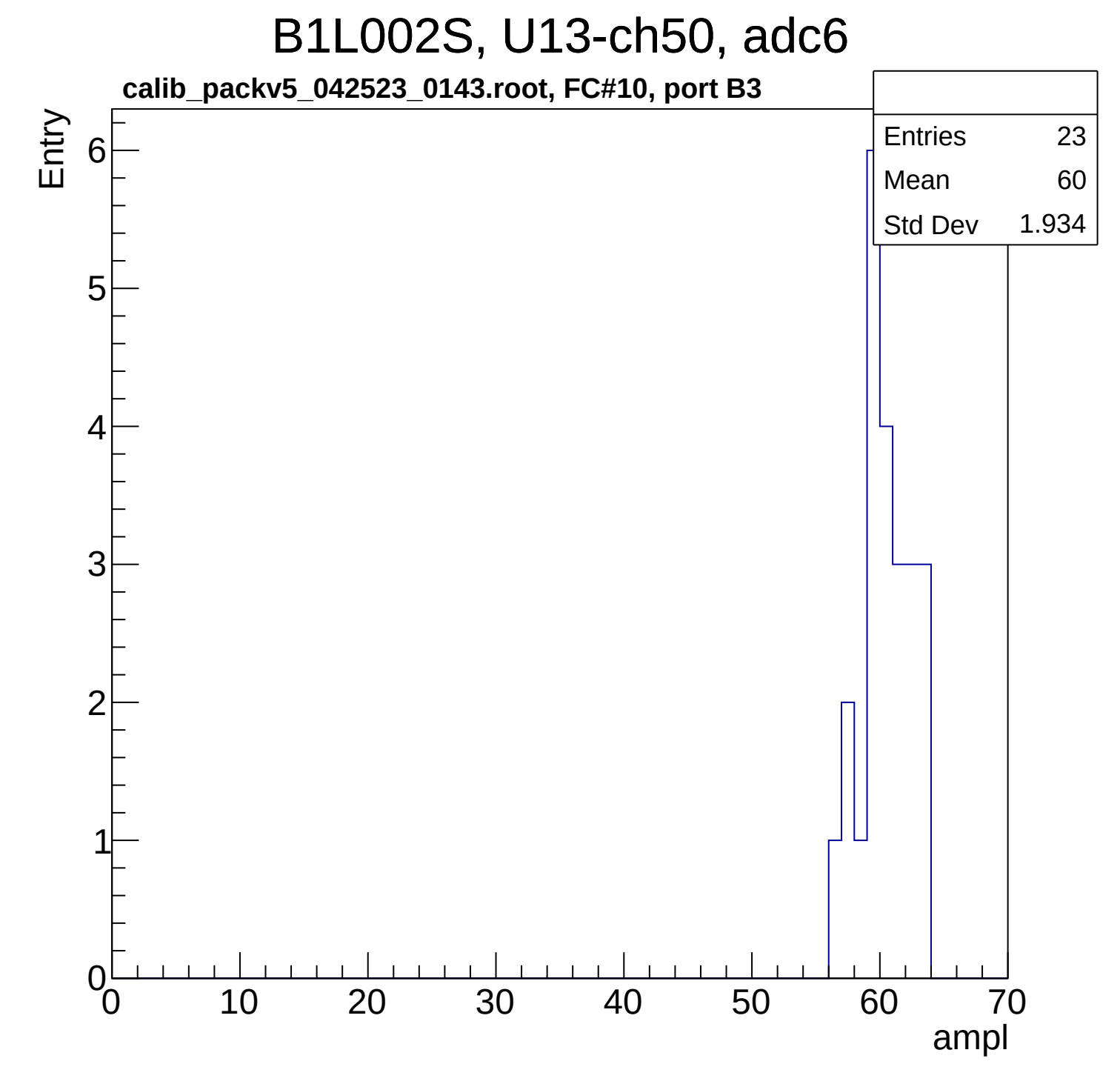
B1L002S, U13-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 60
Std Dev 1.934
Entry
0
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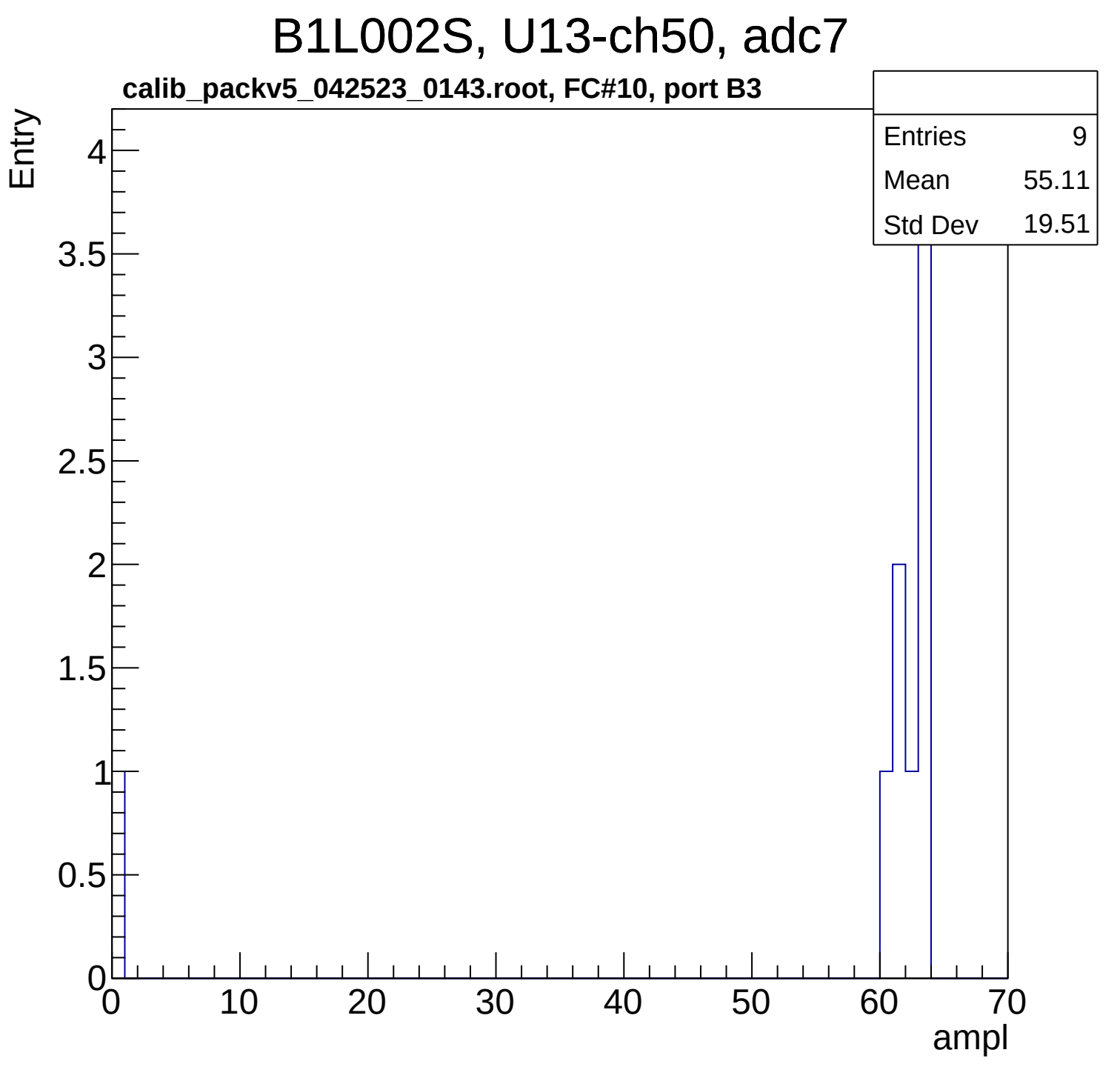
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.11
Std Dev 19.51
Entry
ampl
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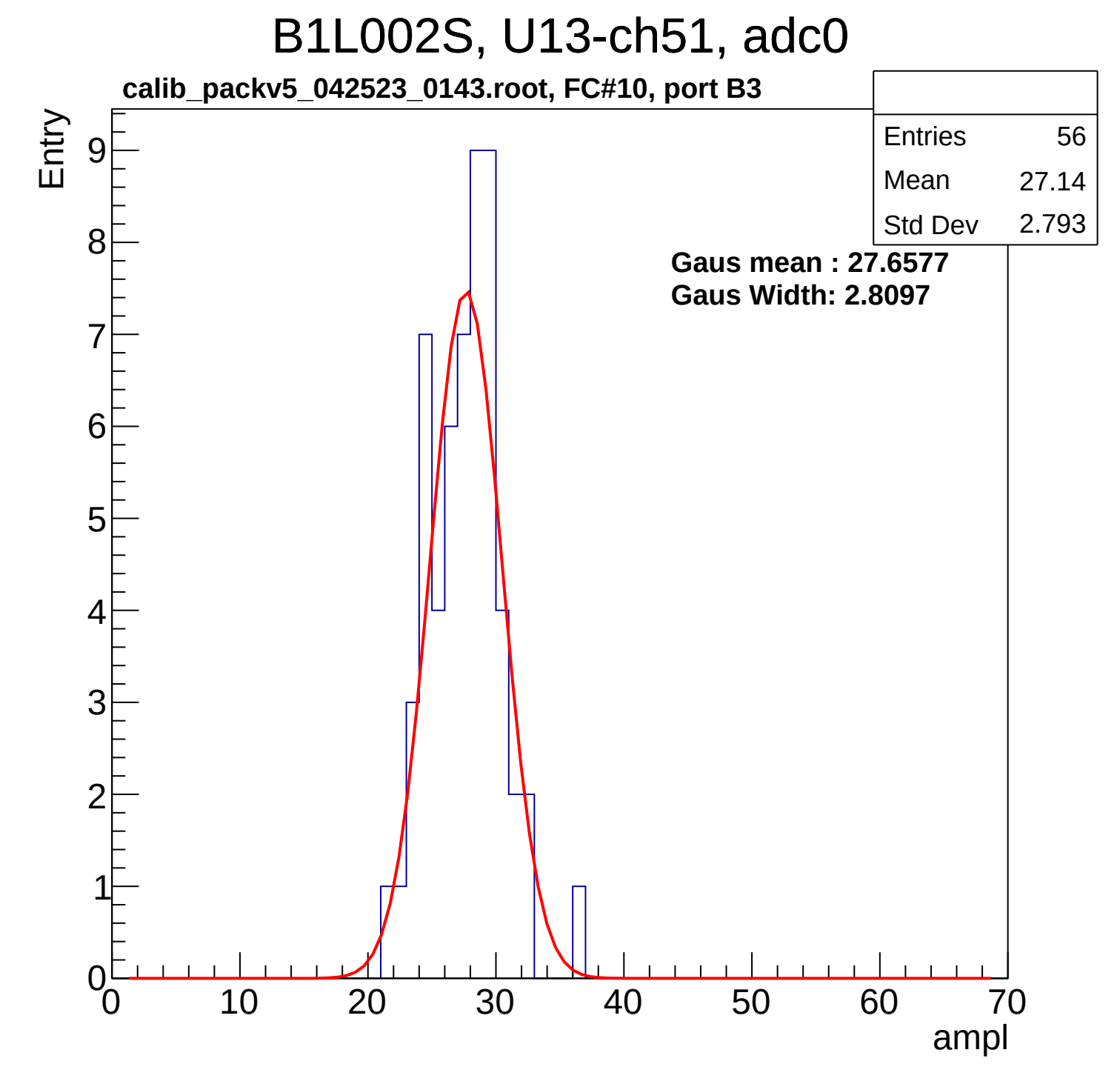
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.14
Std Dev 2.793
Gaus mean : 27.6577
Gaus Width: 2.8097
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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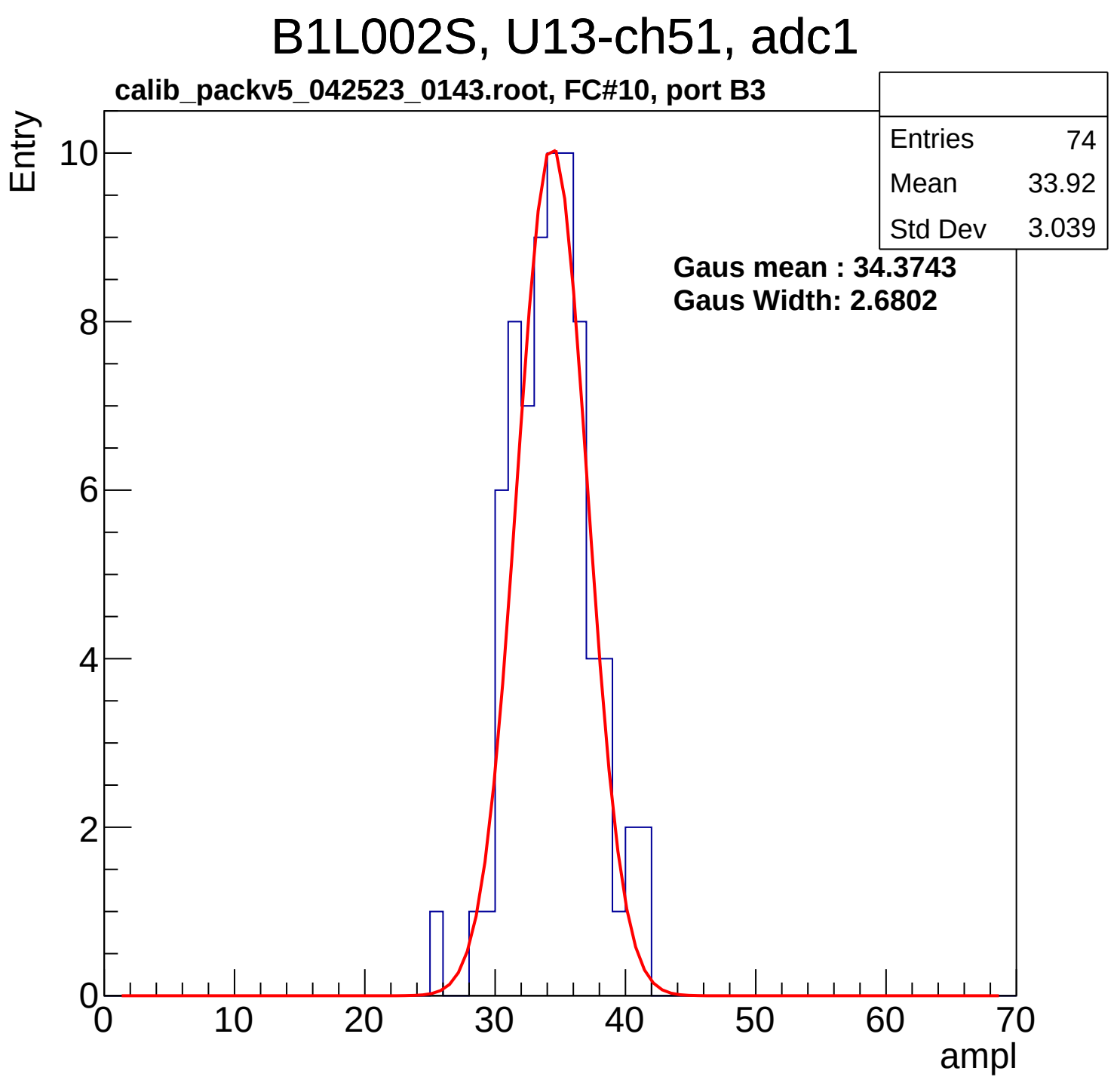
B1L002S, U13-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.92
Std Dev 3.039
Gaus mean : 34.3743
Gaus Width: 2.6802
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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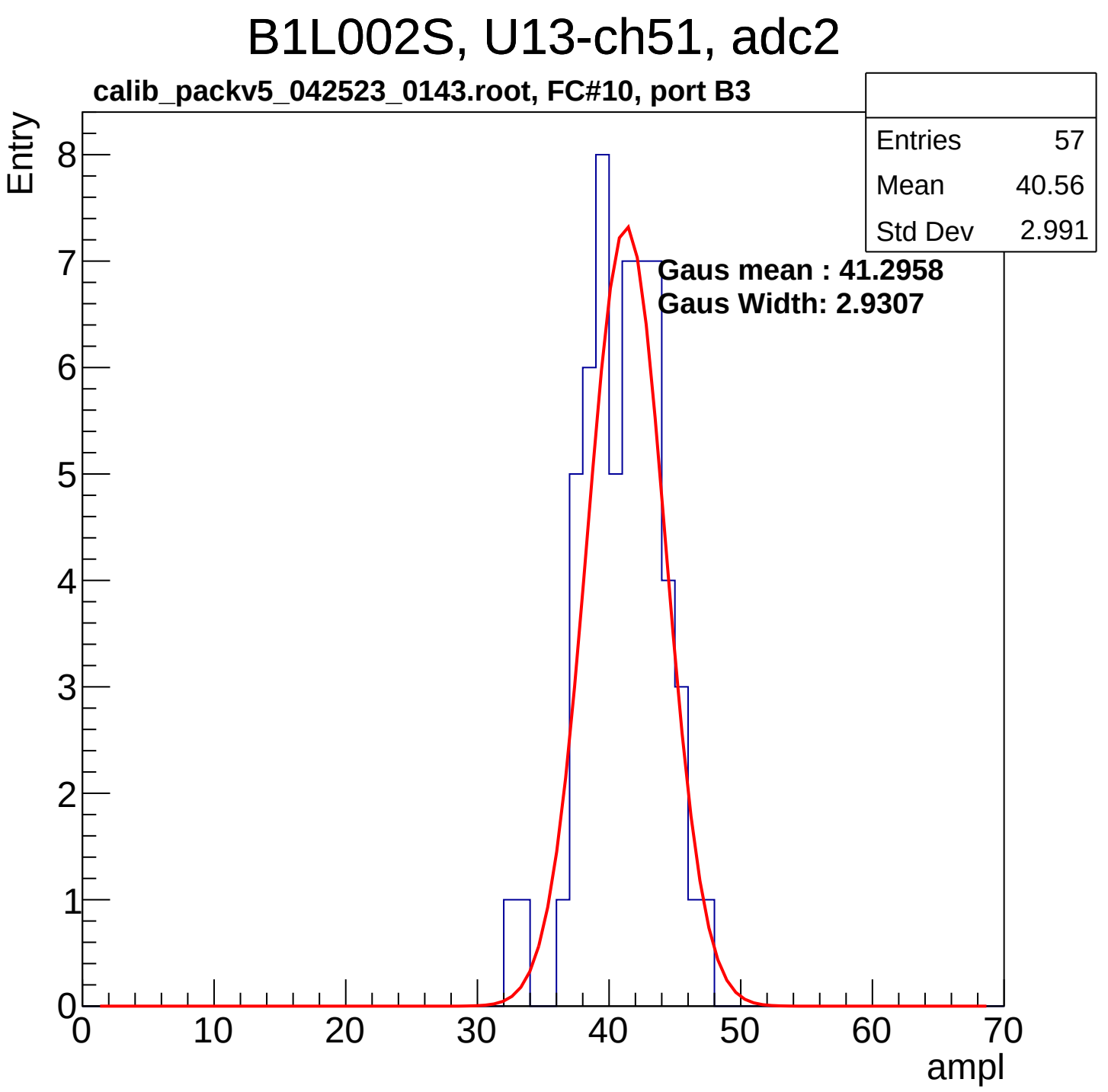
B1L002S, U13-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.56
Std Dev 2.991
Gaus mean : 41.2958
Gaus Width: 2.9307
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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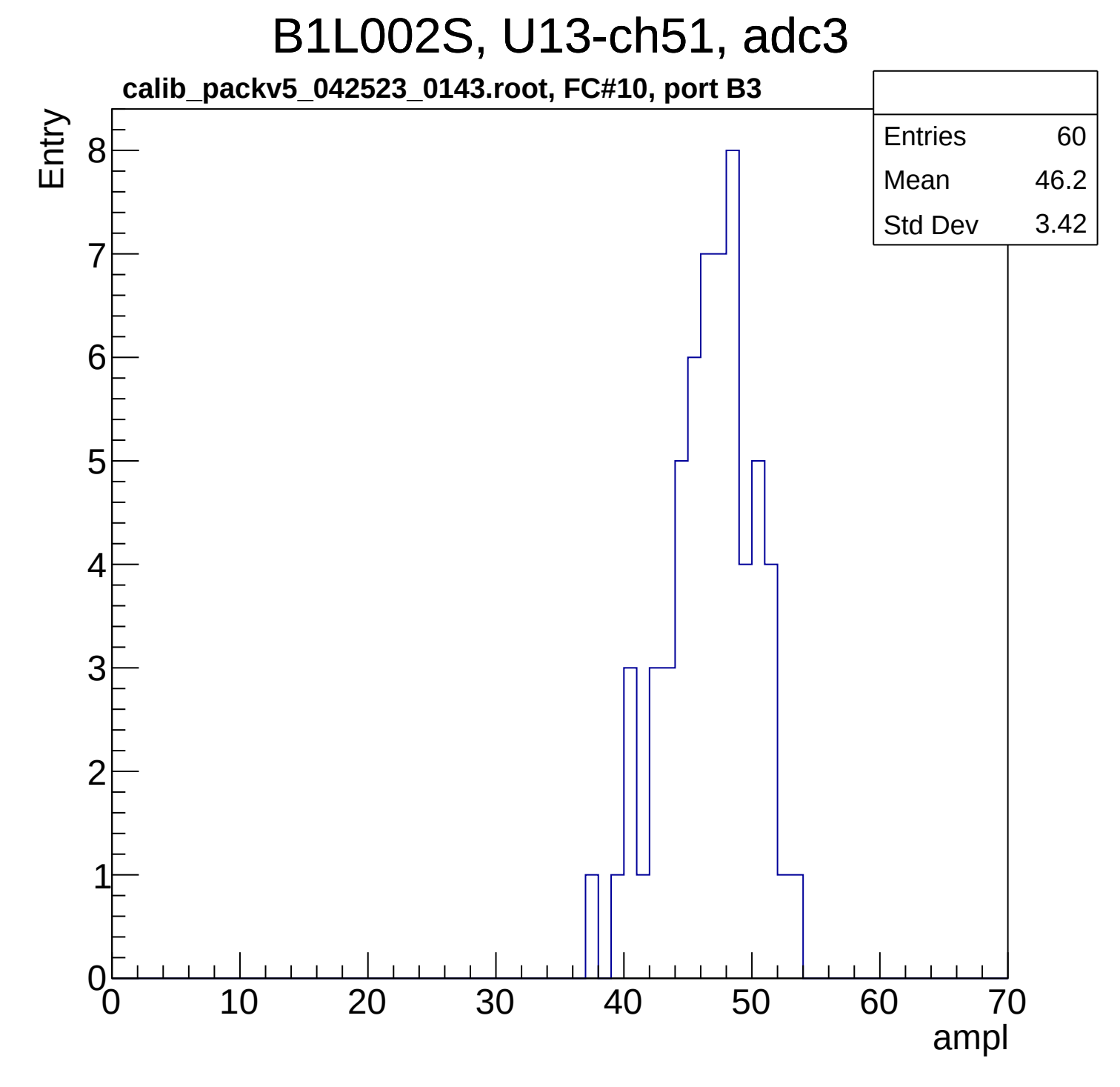
B1L002S, U13-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.2
Std Dev 3.42
Entry
ampl
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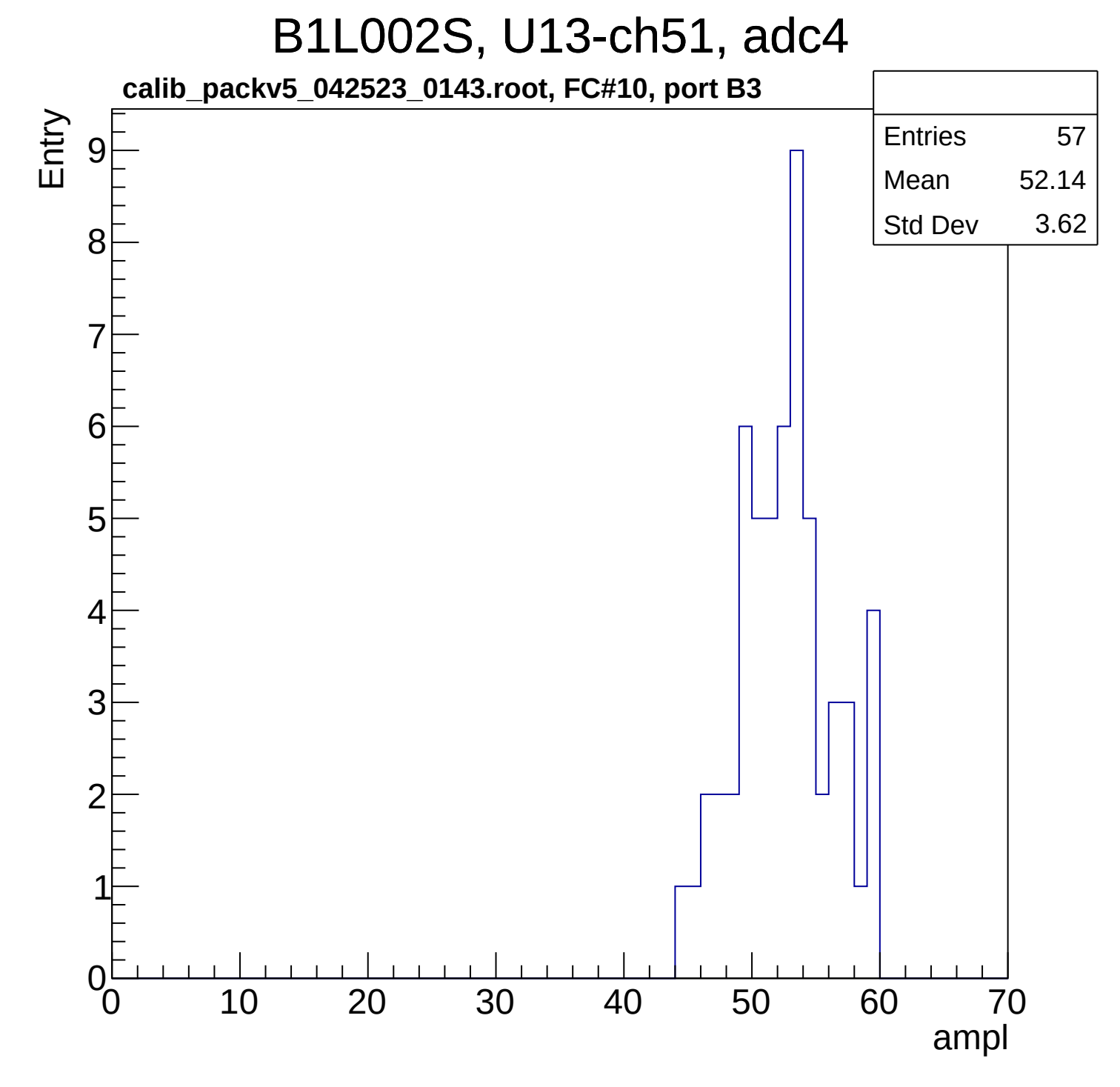
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.14
Std Dev 3.62
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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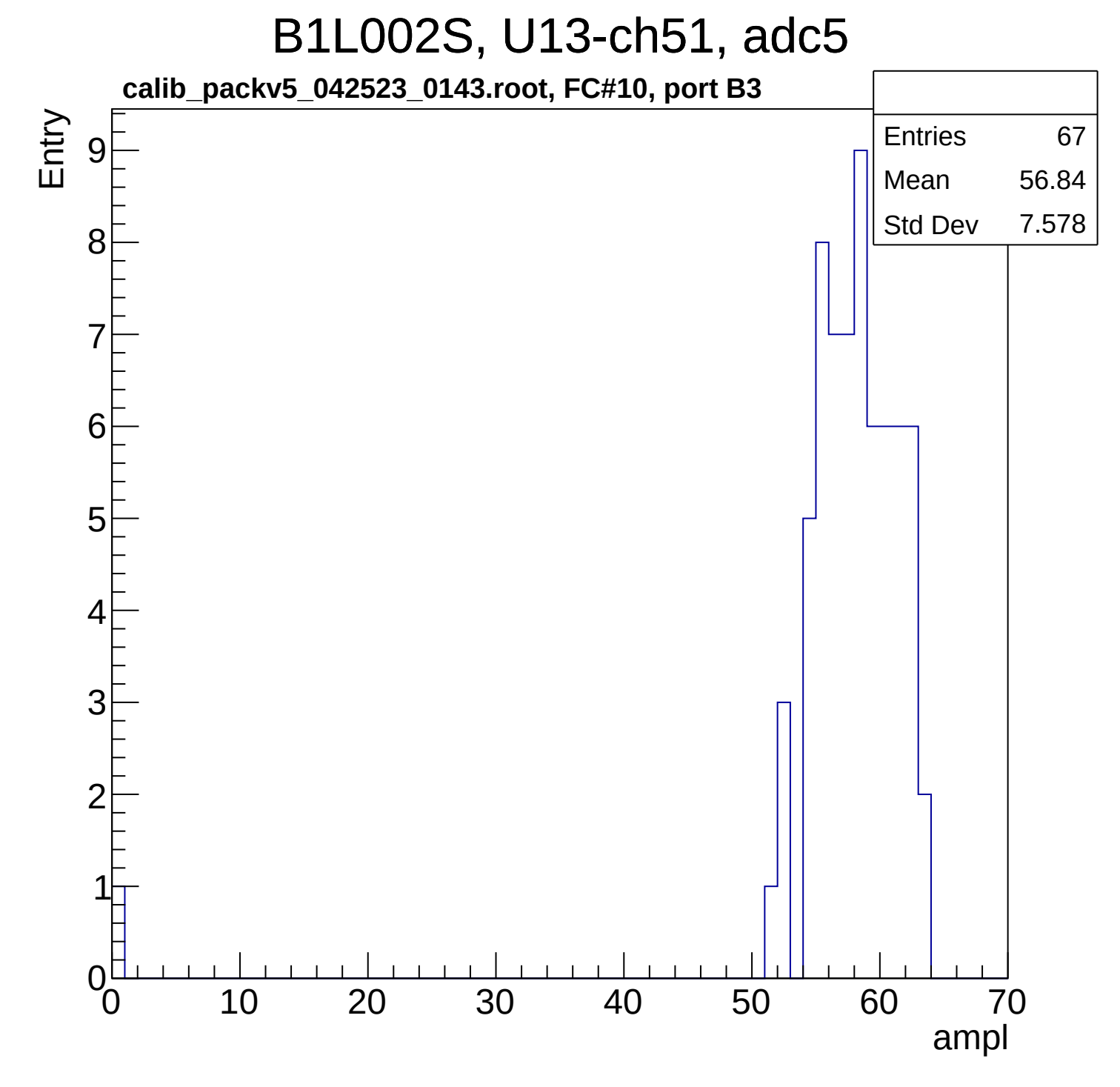
B1L002S, U13-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.84
Std Dev 7.578
Entry
ampl
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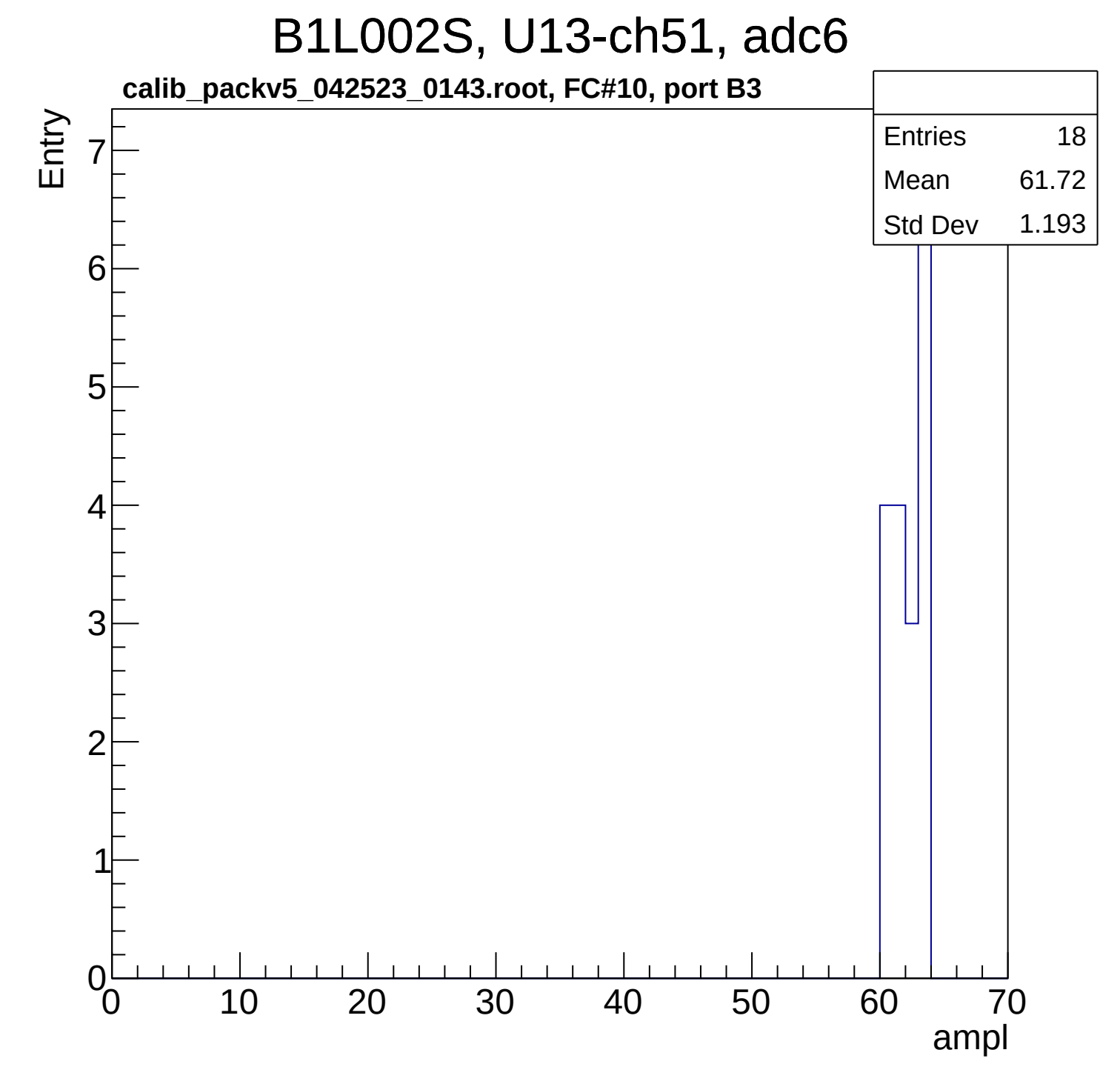
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.72
Std Dev 1.193
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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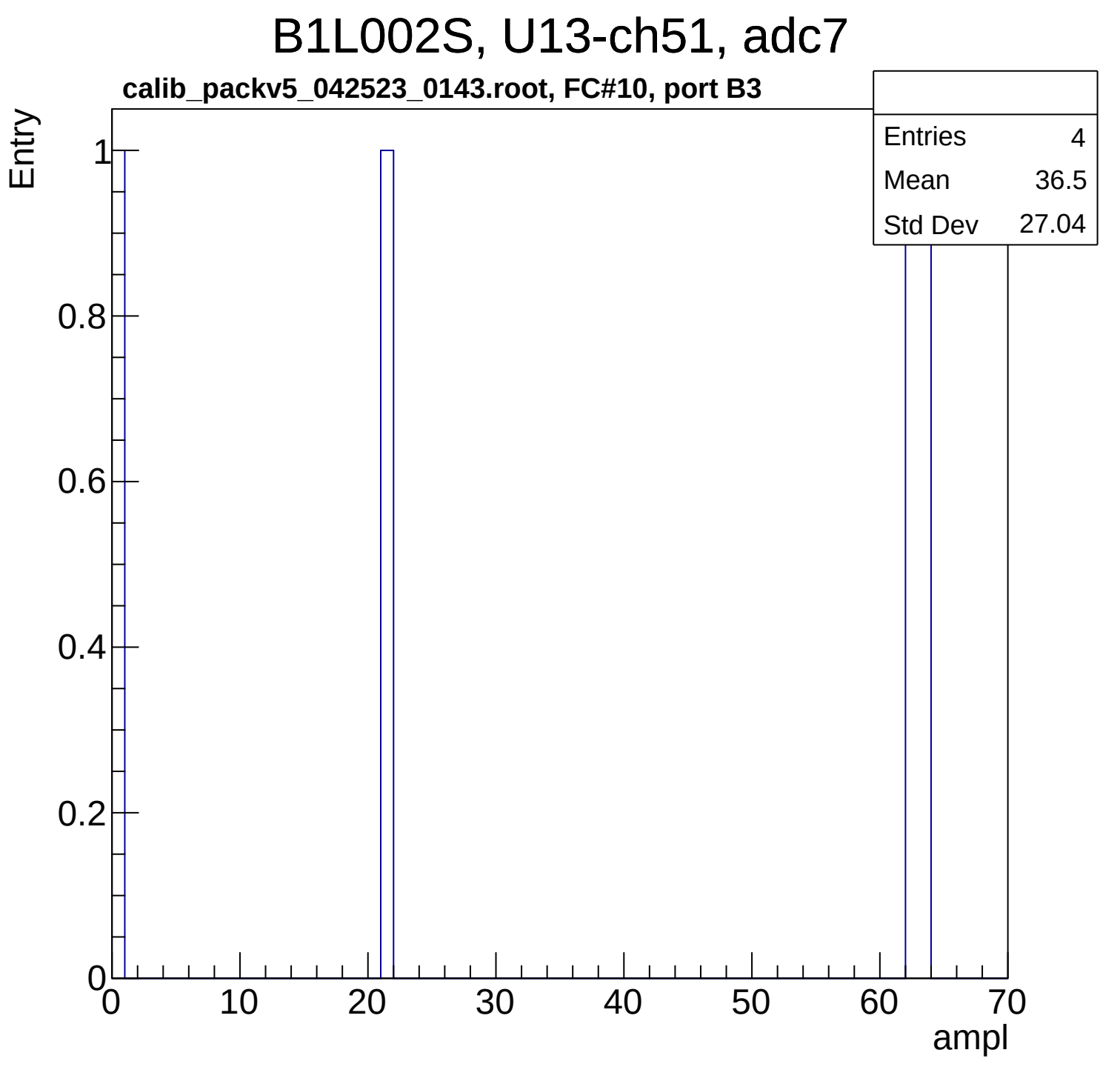
B1L002S, U13-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.5
Std Dev 27.04
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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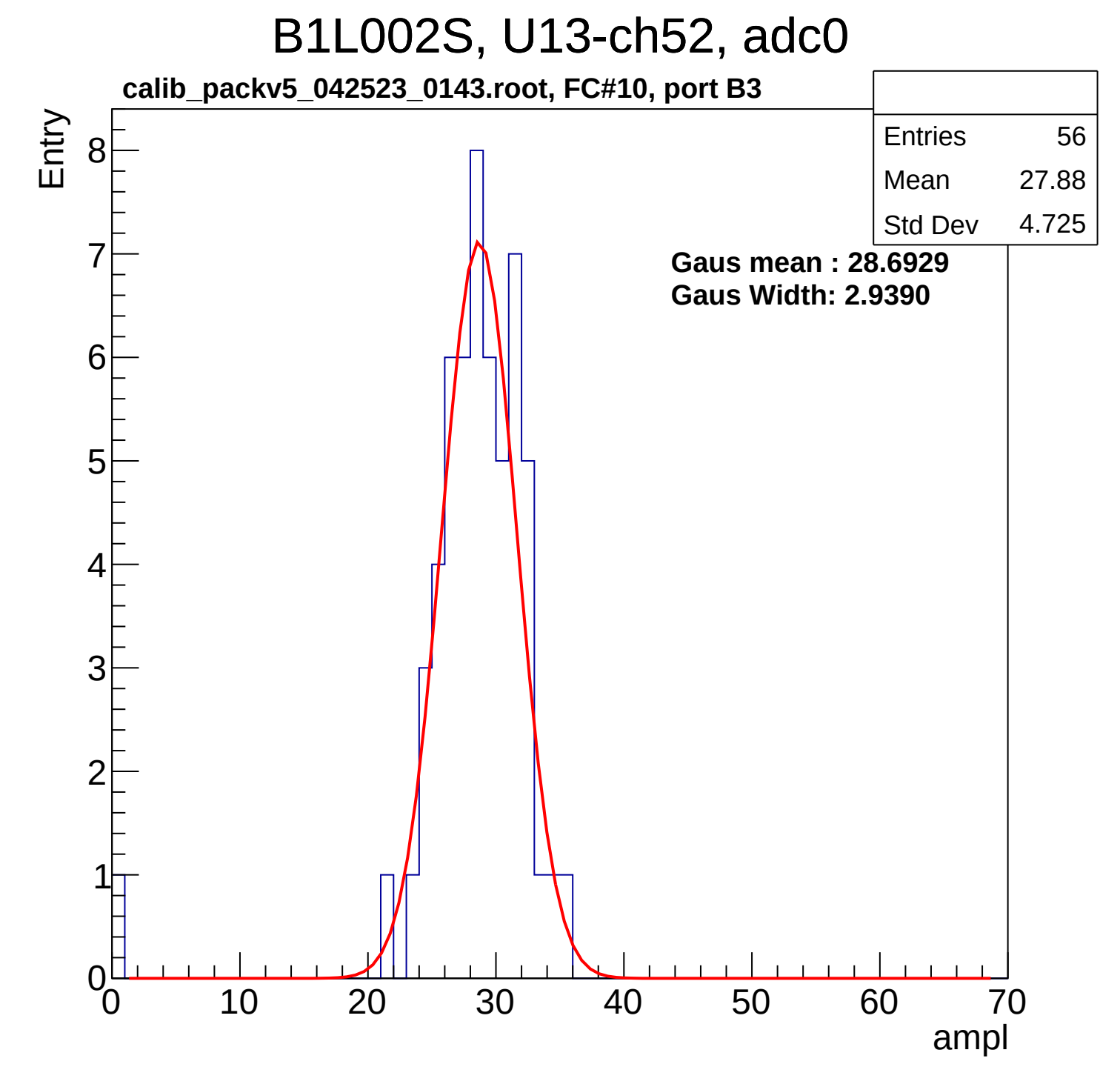
B1L002S, U13-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.88
Std Dev 4.725
Gaus mean : 28.6929
Gaus Width: 2.9390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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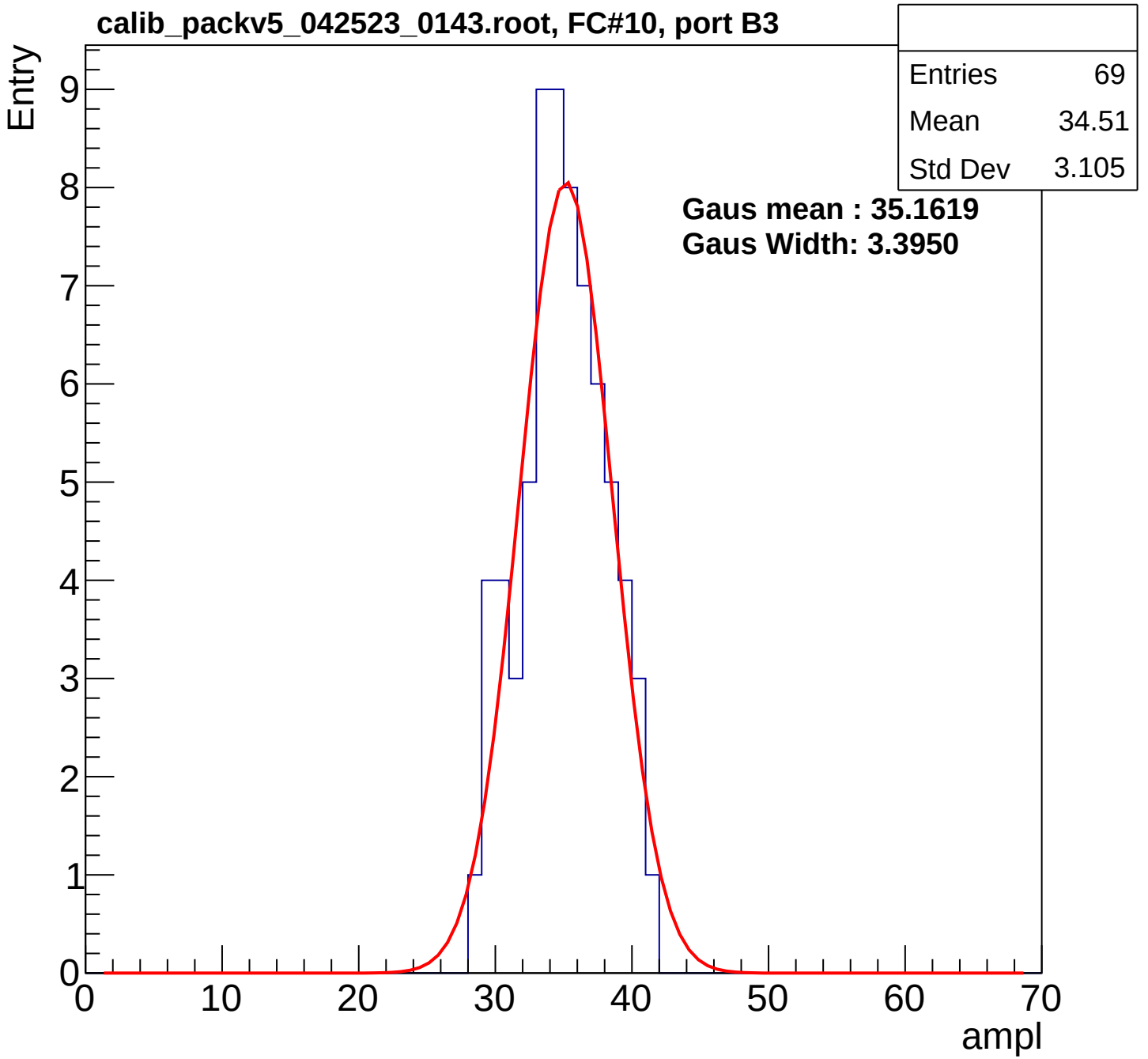
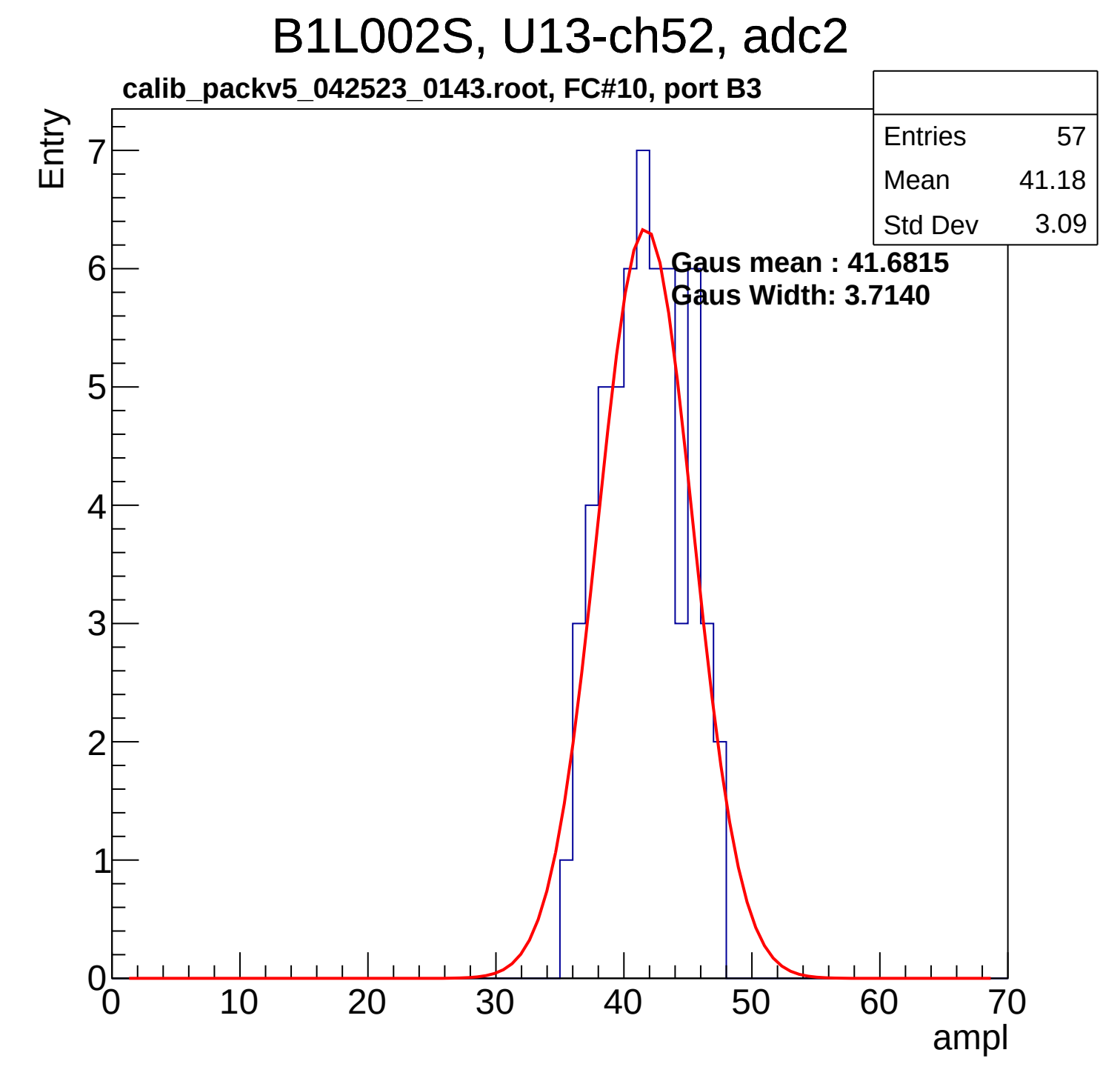

B1L002S, U13-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.51
Std Dev 3.105
Gaus mean : 35.1619
Gaus Width: 3.3950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.18
Std Dev 3.09
Gaus mean : 41.6815
Gaus Width: 3.7140
Entry
ampl
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3
4
5
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0
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60
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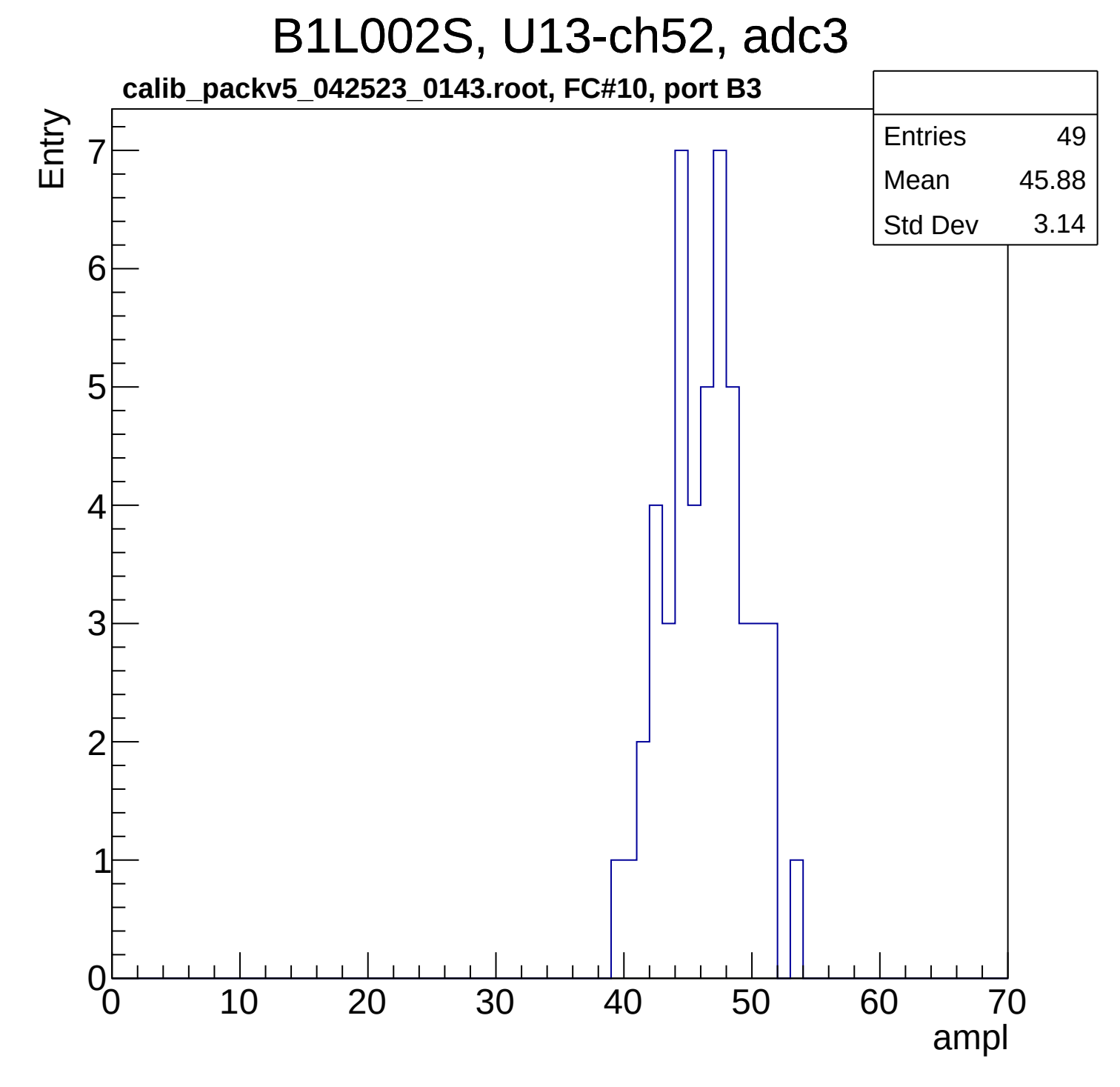
B1L002S, U13-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 45.88
Std Dev 3.14
Entry
ampl
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4
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60
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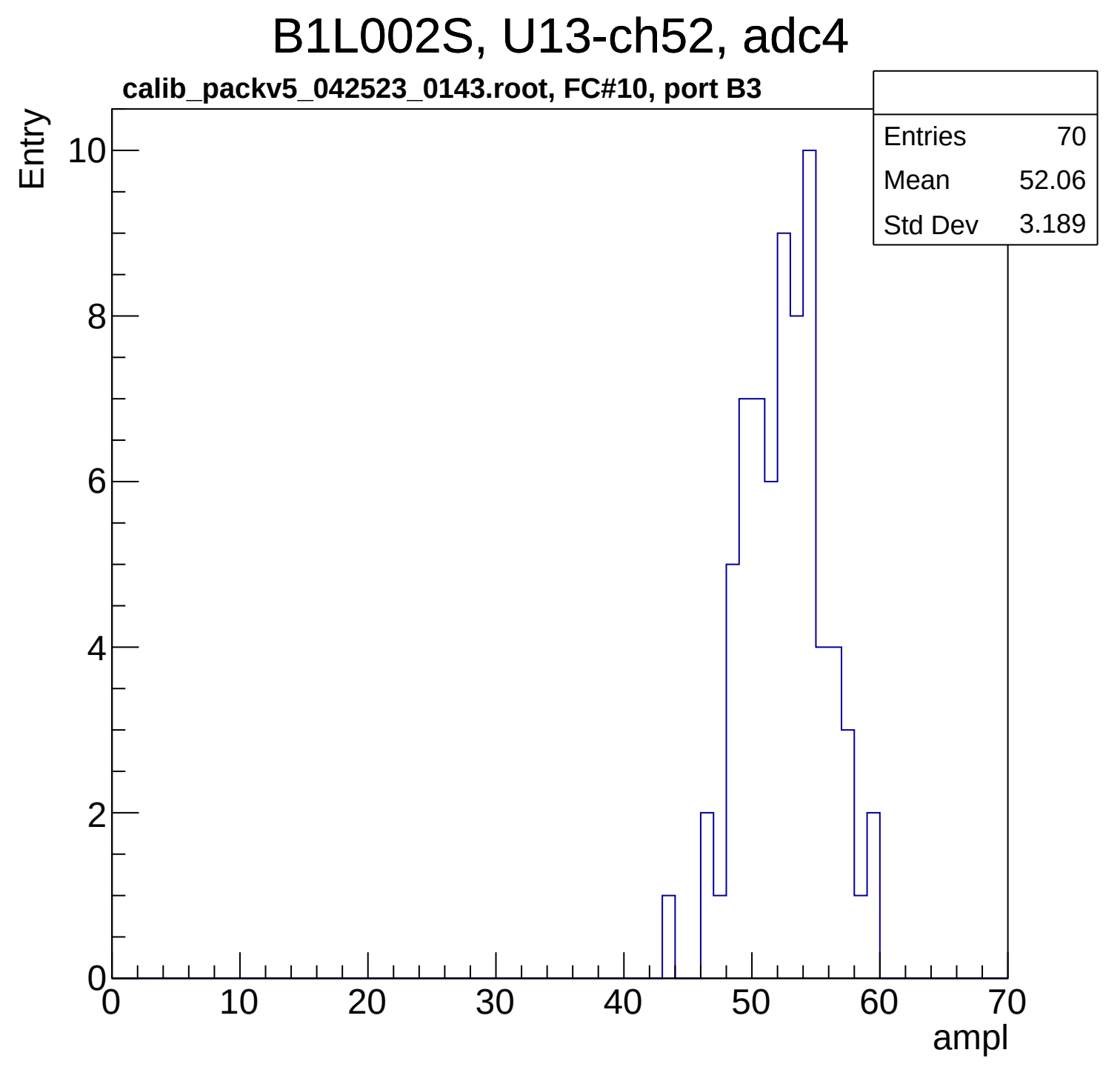
B1L002S, U13-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 52.06
Std Dev 3.189
Entry
ampl
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2
4
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0
10
20
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60
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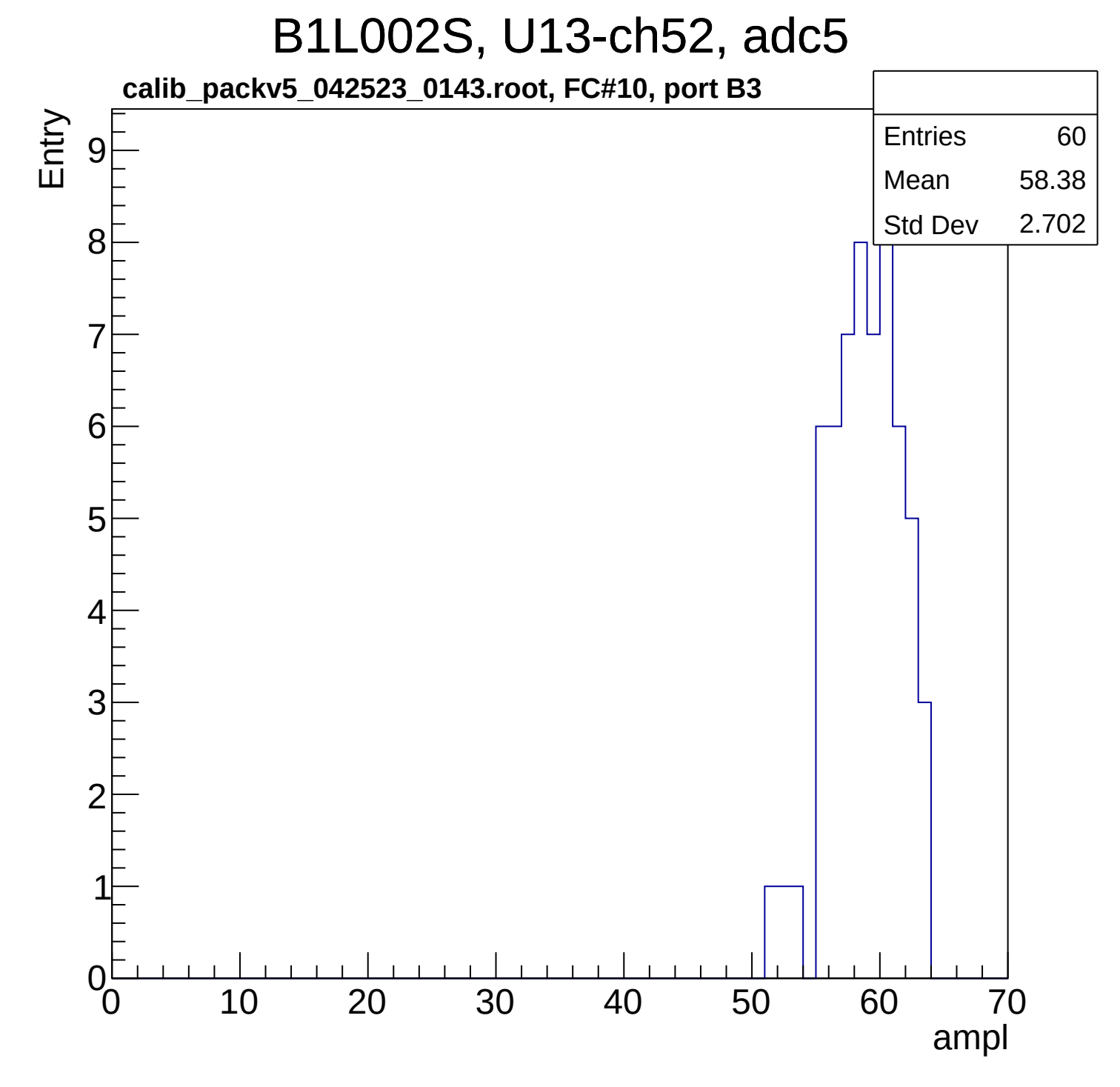
B1L002S, U13-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.38
Std Dev 2.702
Entry
ampl
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5
6
7
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0
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60
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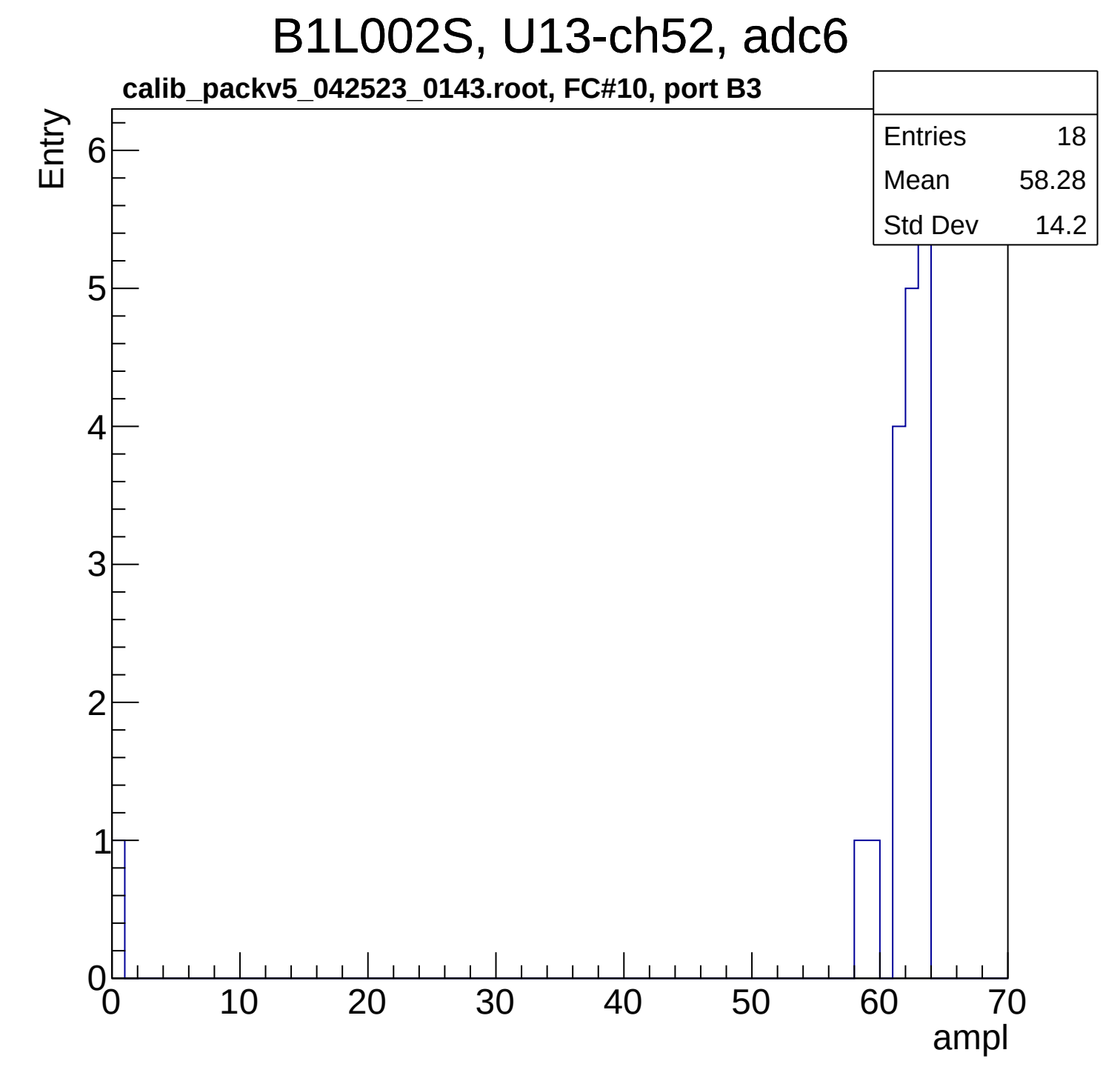
B1L002S, U13-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.28
Std Dev 14.2
Entry
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2
3
4
5
6
0
10
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30
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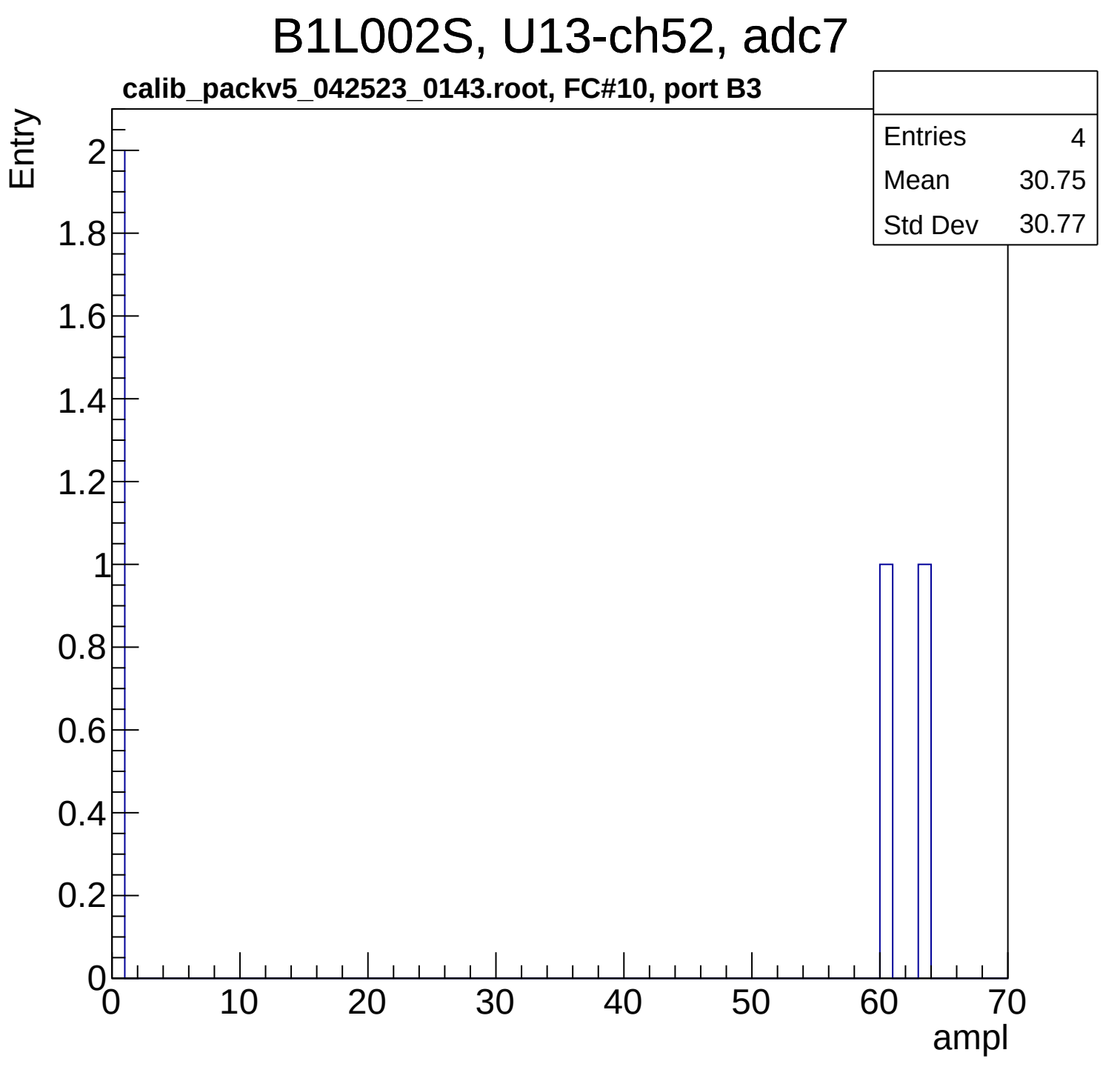
50
60
70
ampl


B1L002S, U13-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.75
Std Dev 30.77
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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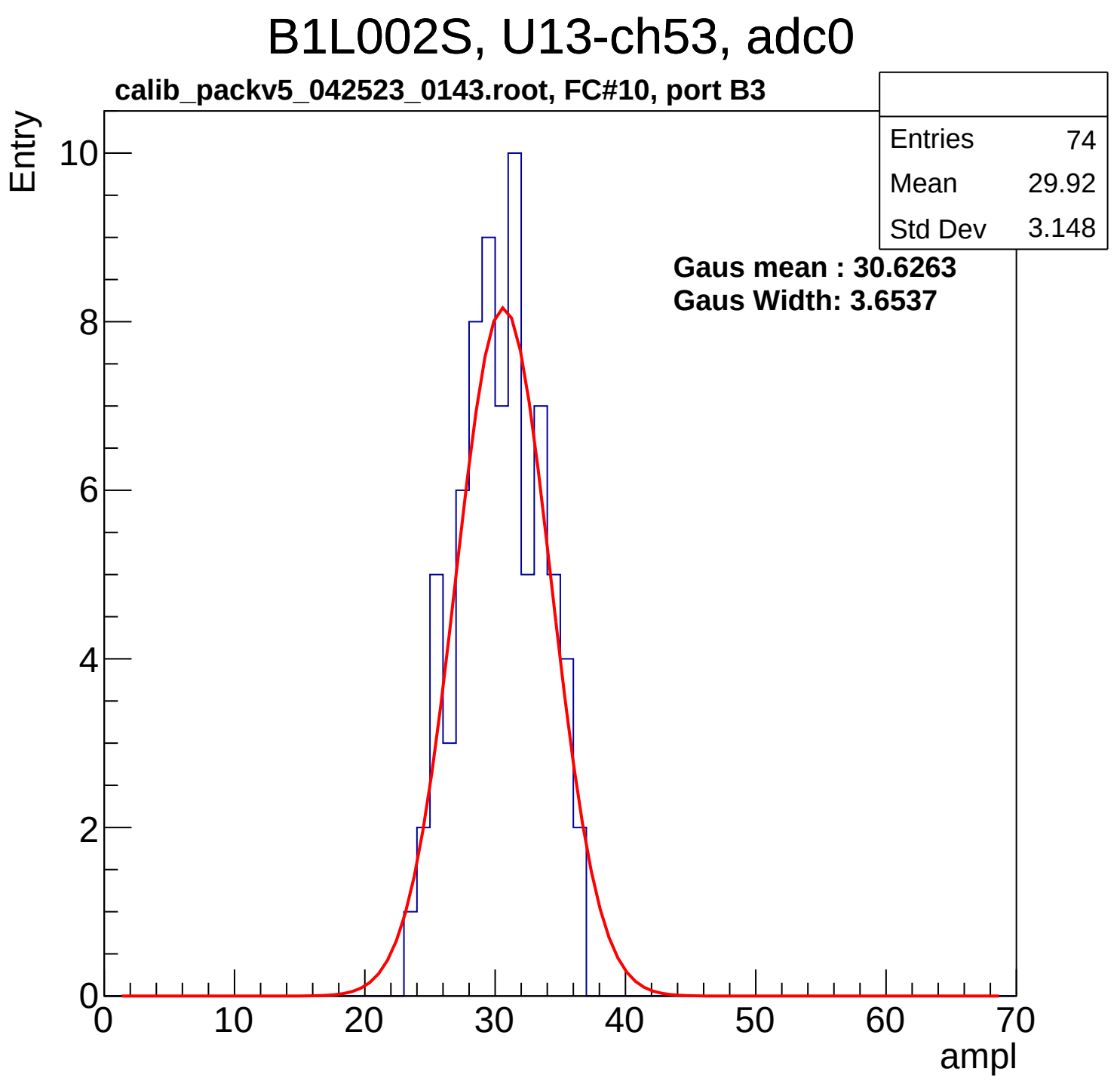
B1L002S, U13-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 29.92
Std Dev 3.148
Gaus mean : 30.6263
Gaus Width: 3.6537
Entry
ampl
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6
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0
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60
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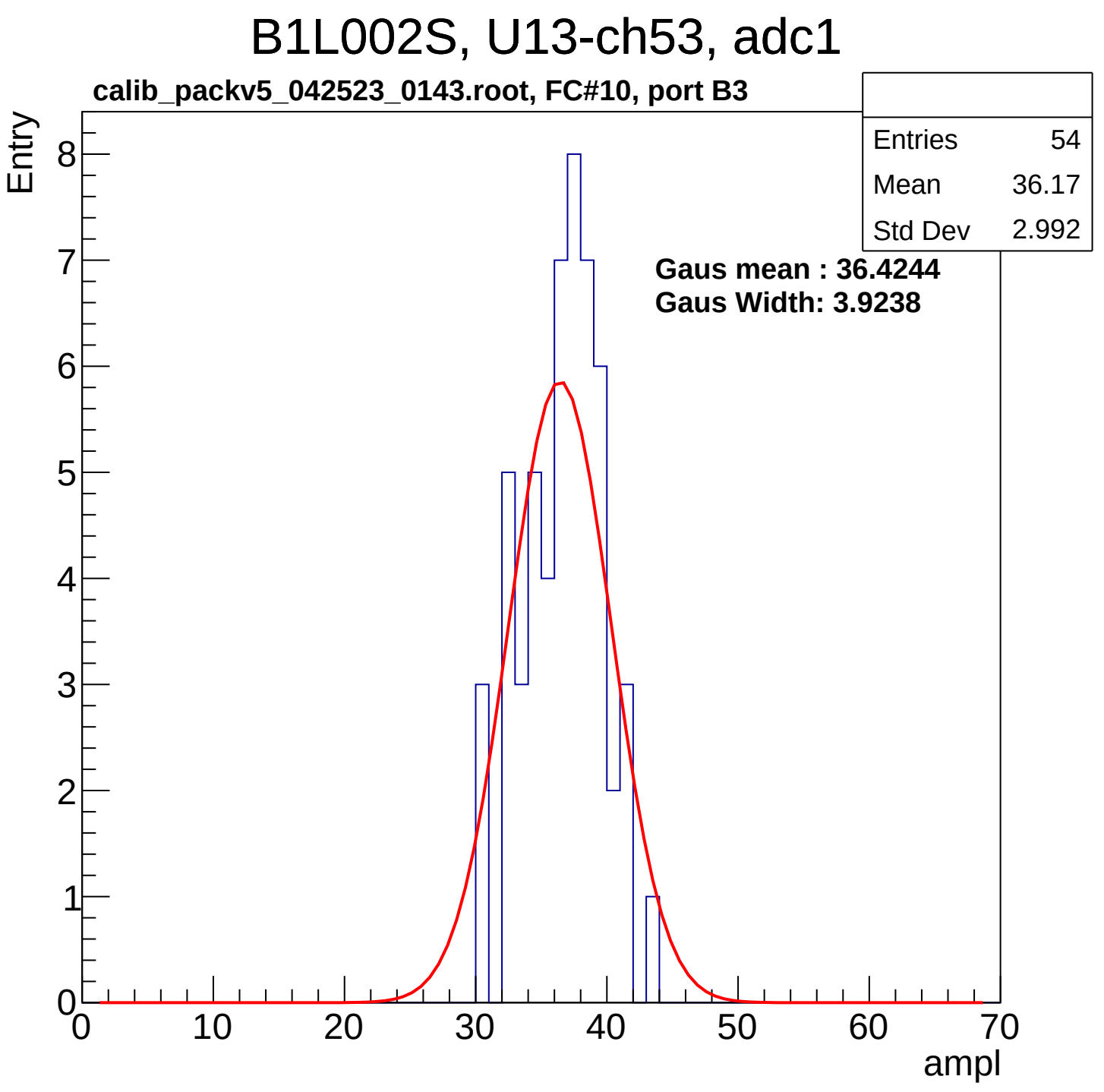
B1L002S, U13-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 36.17
Std Dev 2.992
Gaus mean : 36.4244
Gaus Width: 3.9238
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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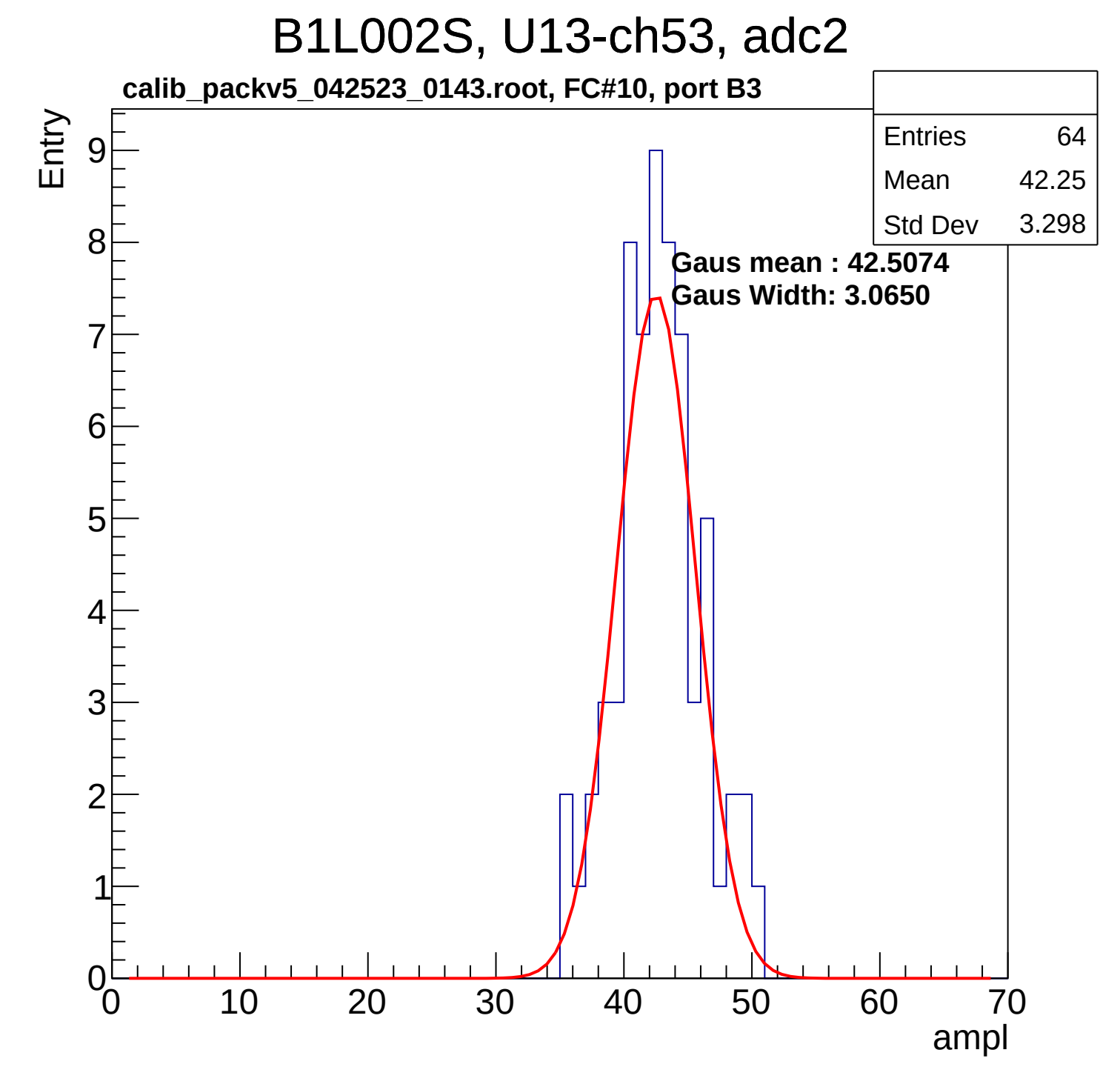
B1L002S, U13-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.25
Std Dev 3.298
Gaus mean : 42.5074
Gaus Width: 3.0650
Entry
ampl
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2
3
4
5
6
7
8
9
0
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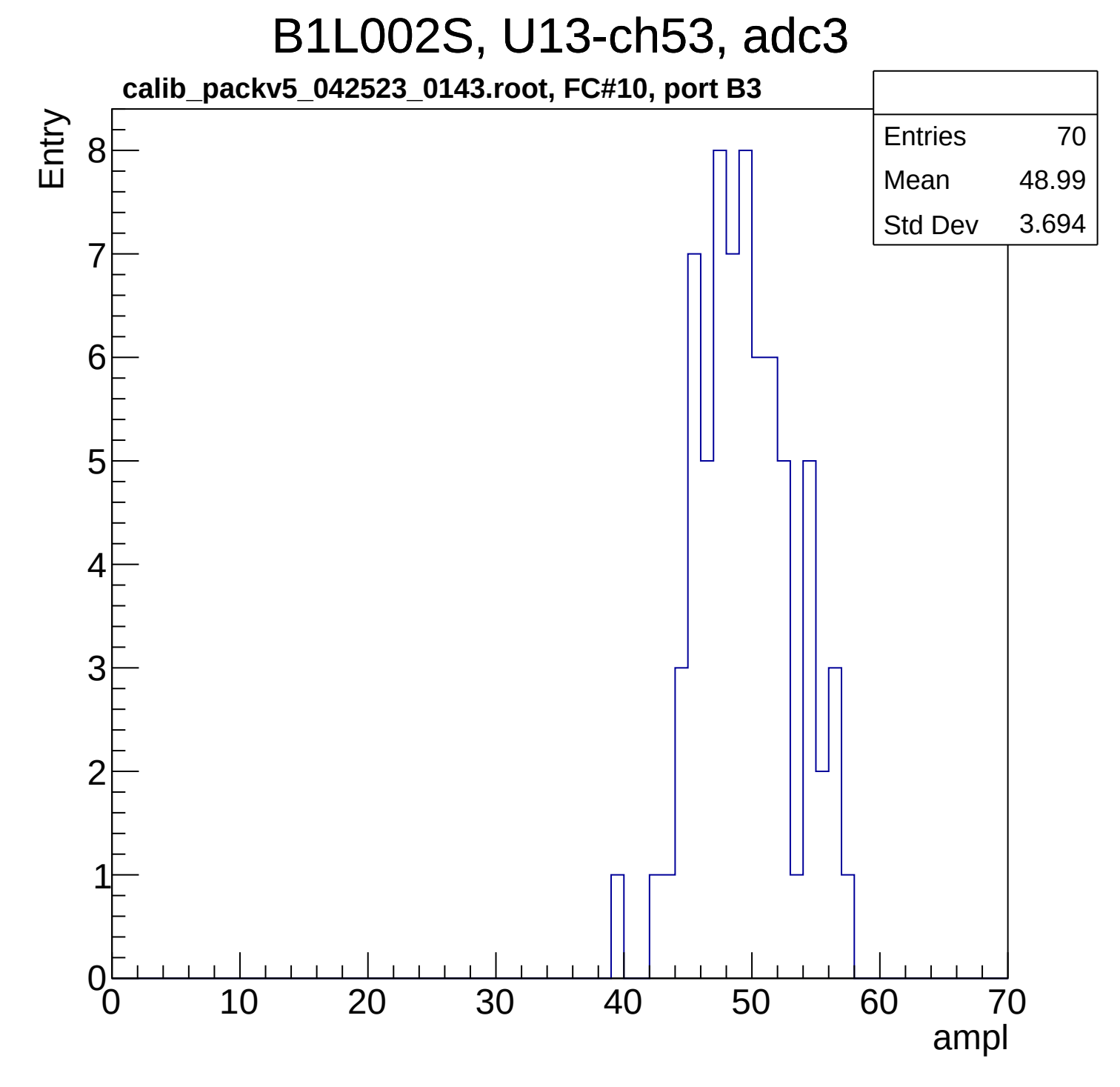
B1L002S, U13-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.99
Std Dev 3.694
Entry
ampl
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5
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7
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0
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60
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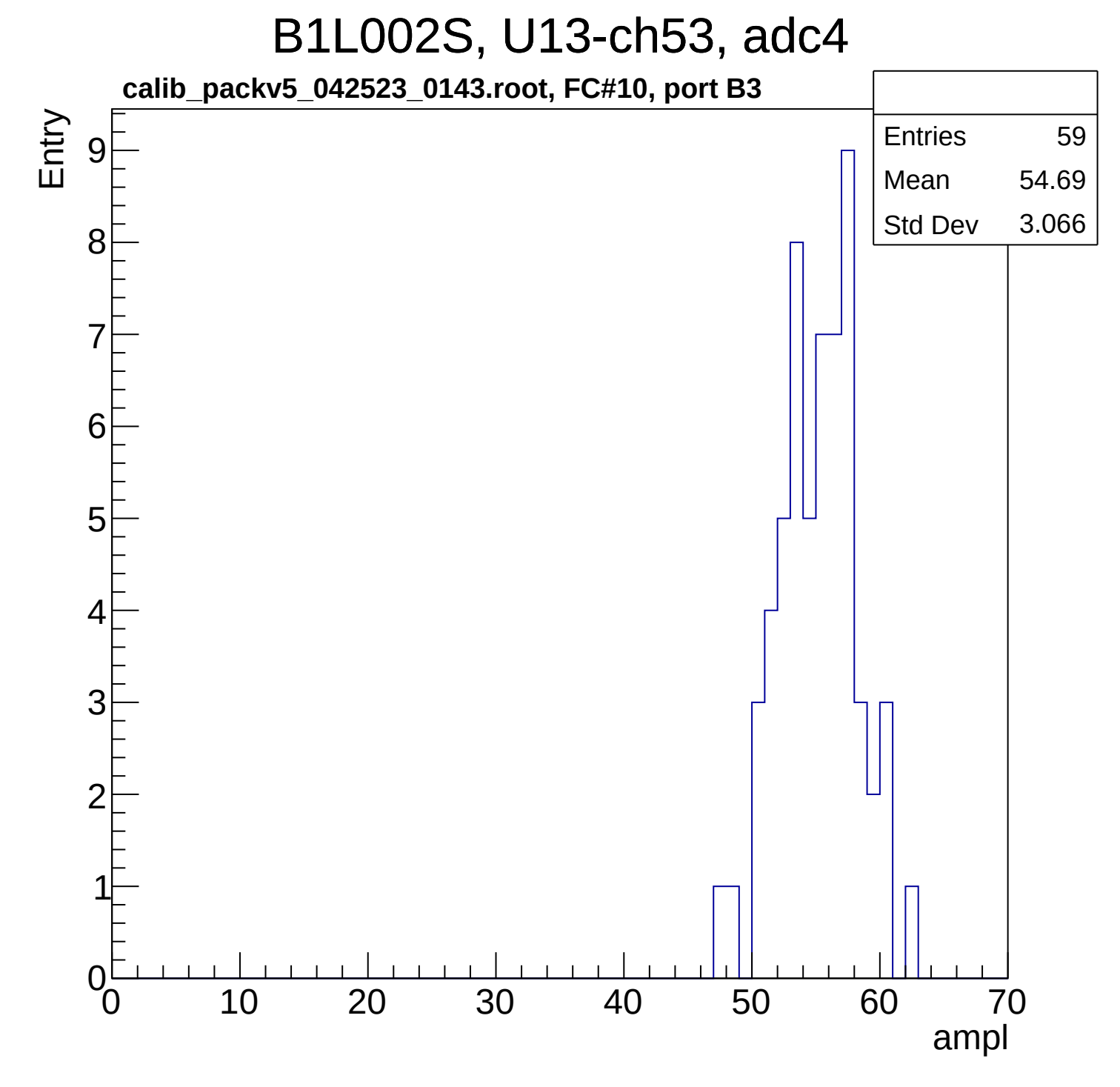
B1L002S, U13-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.69
Std Dev 3.066
Entry
ampl
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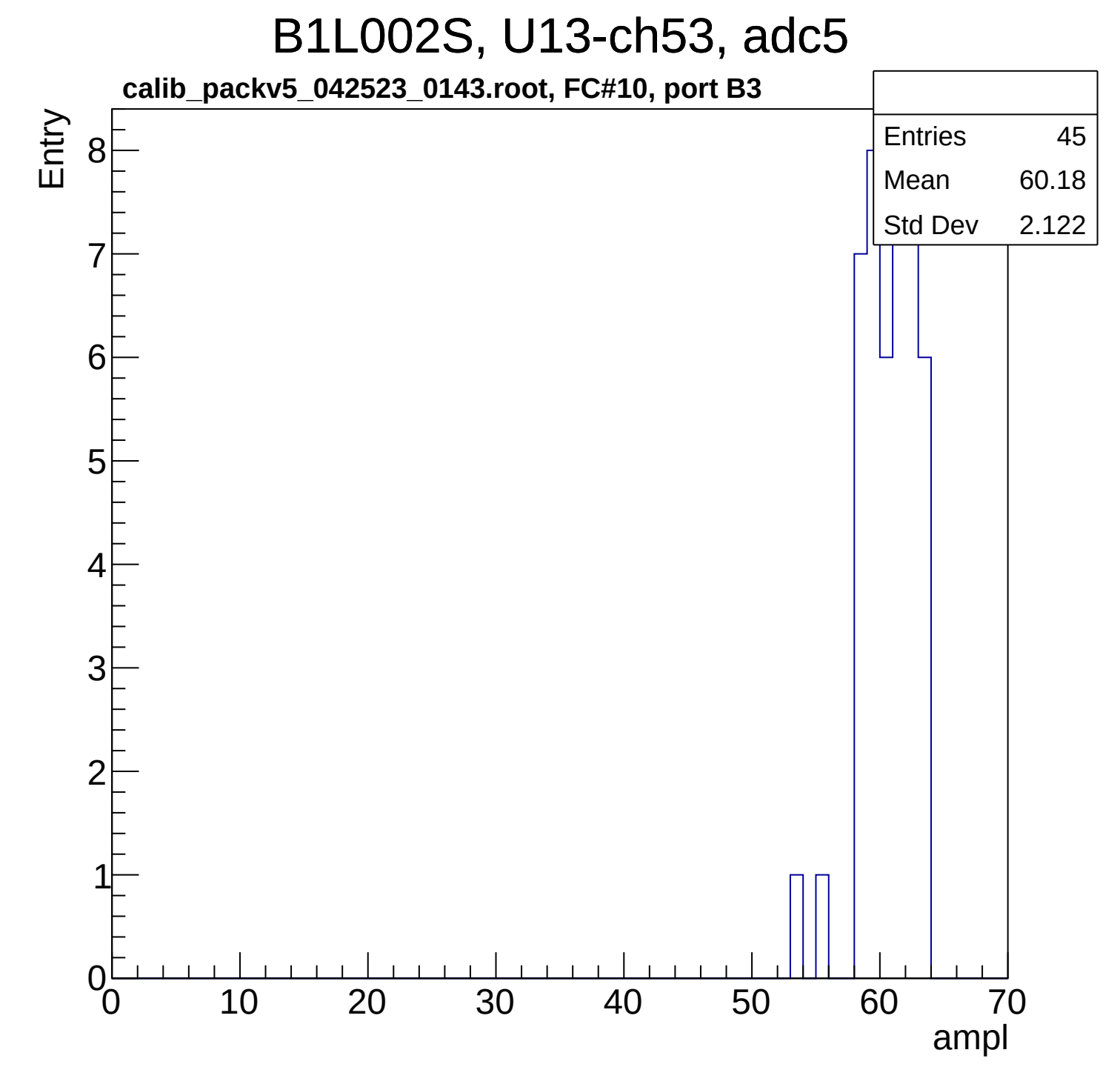
B1L002S, U13-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 60.18
Std Dev 2.122
Entry
ampl
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3
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5
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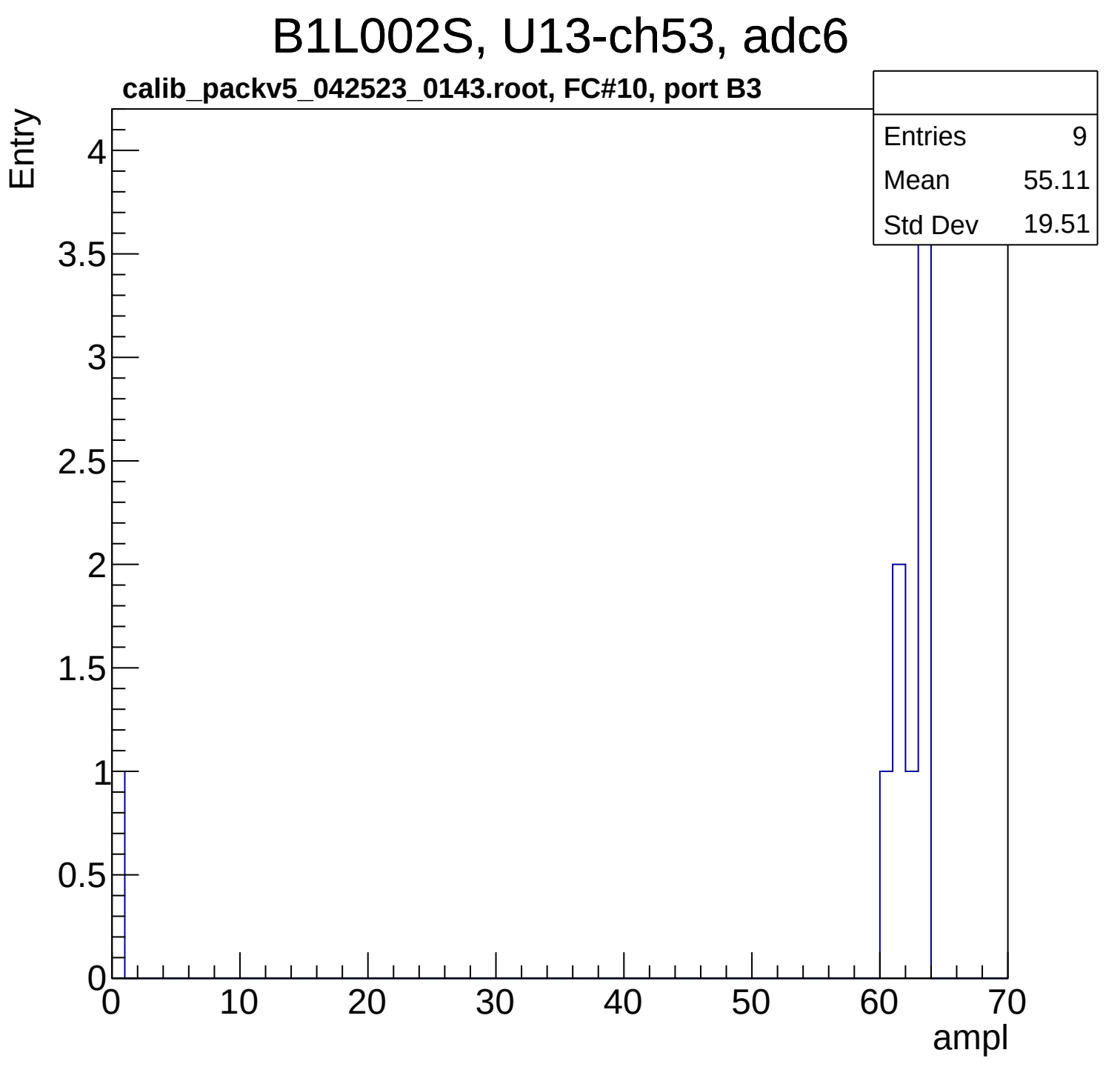
B1L002S, U13-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.11
Std Dev 19.51
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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60
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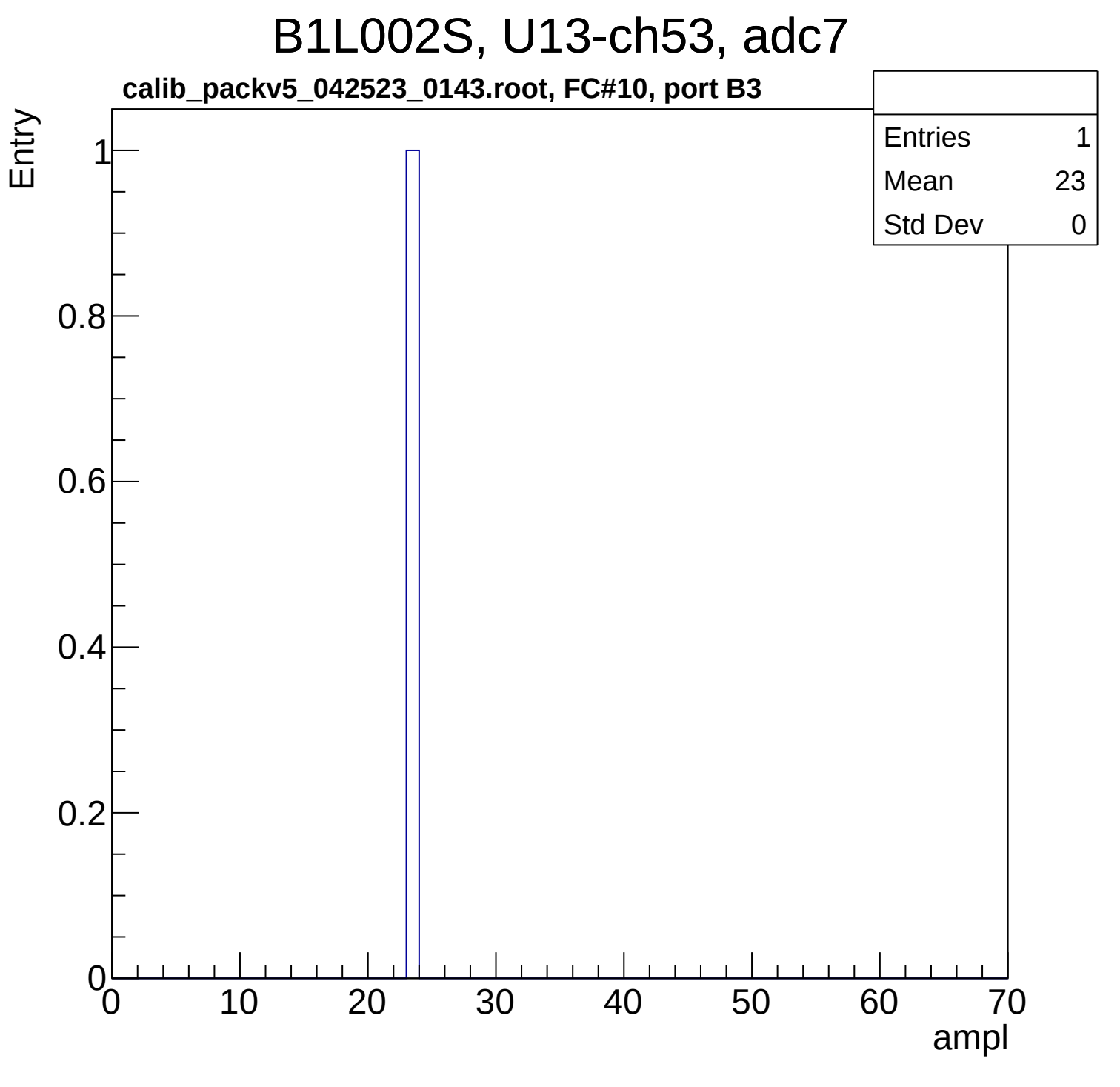
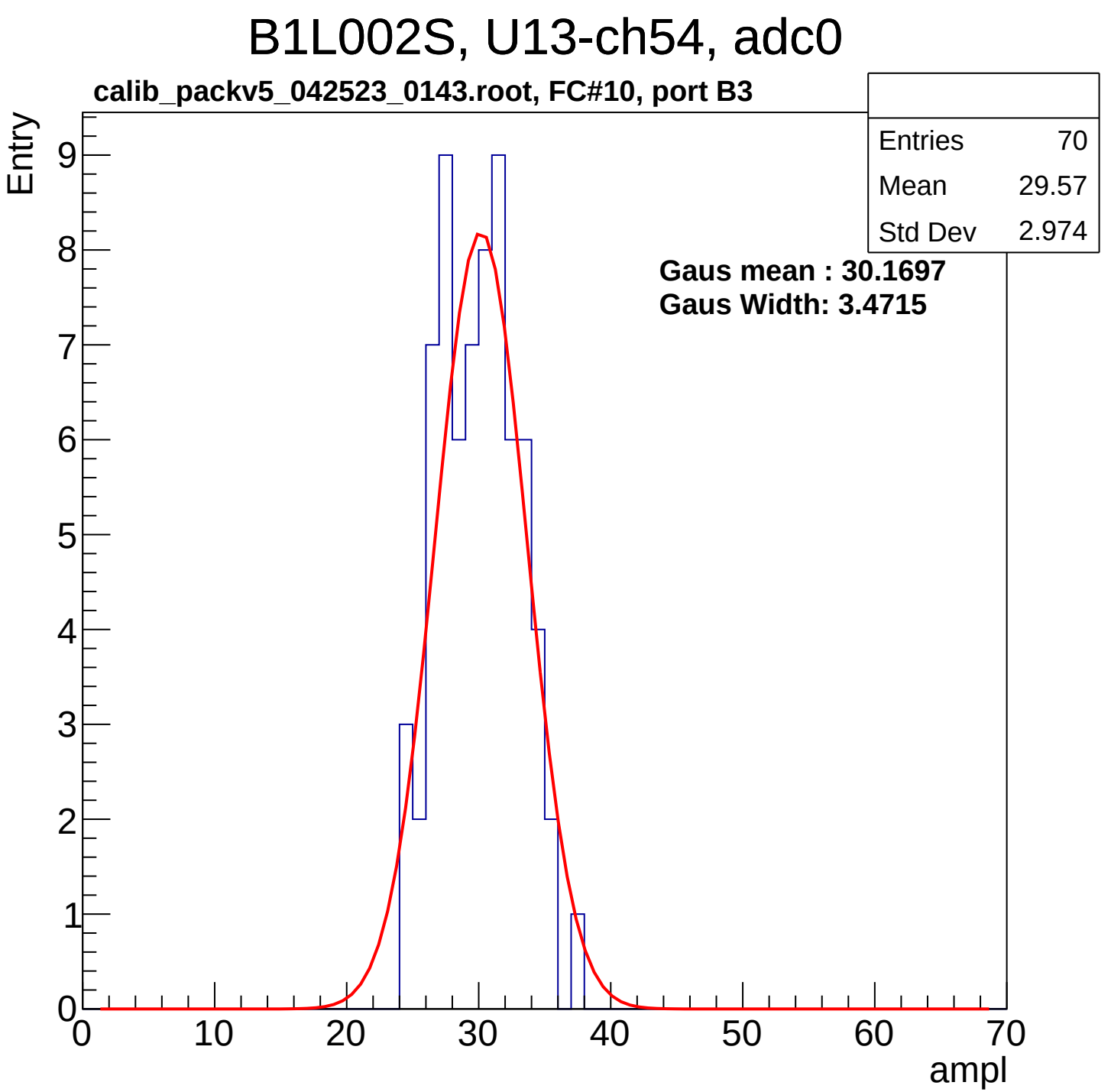
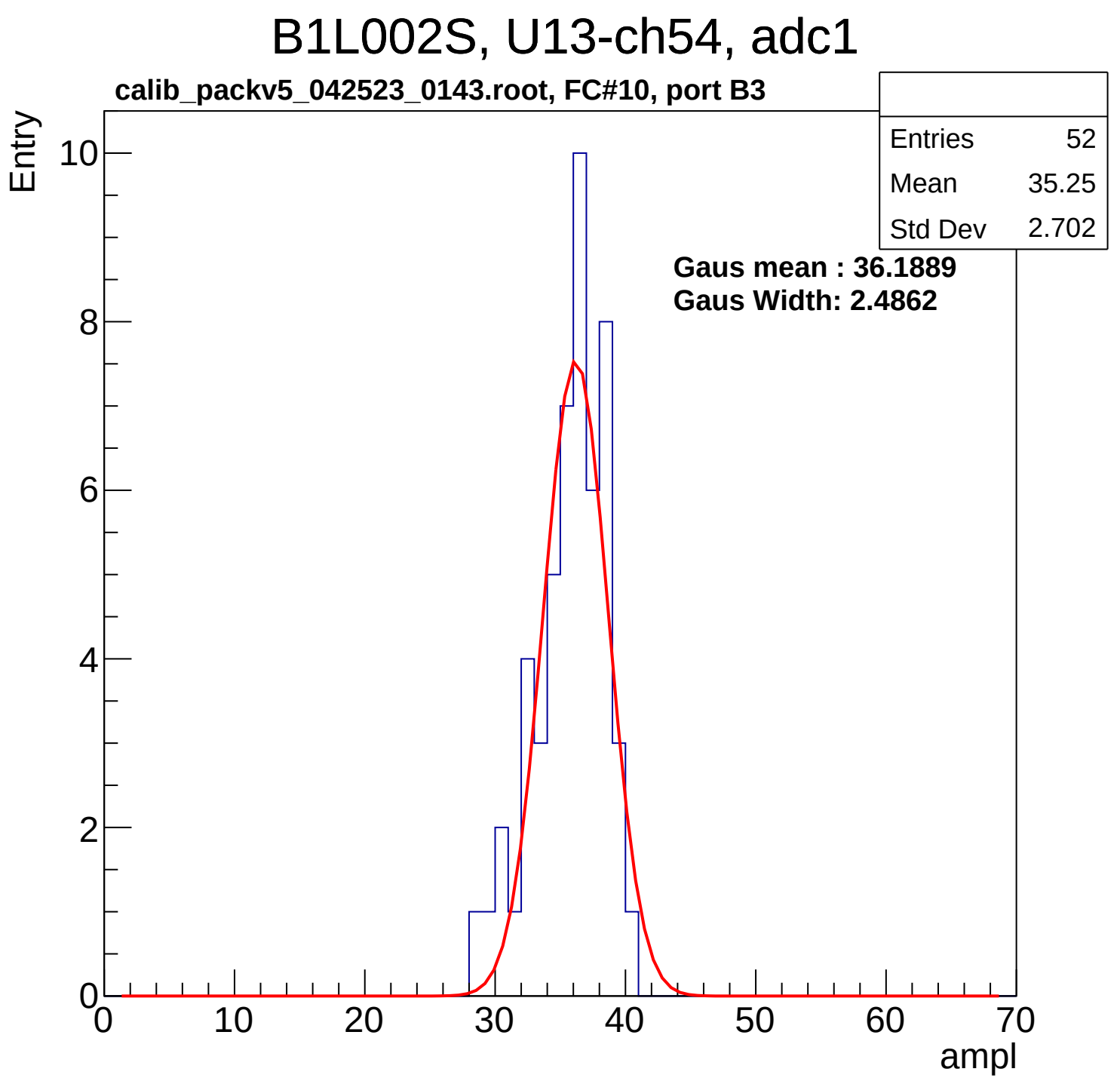

B1L002S, U13-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.57
Std Dev 2.974
Gaus mean : 30.1697
Gaus Width: 3.4715
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.25
Std Dev 2.702
Gaus mean : 36.1889
Gaus Width: 2.4862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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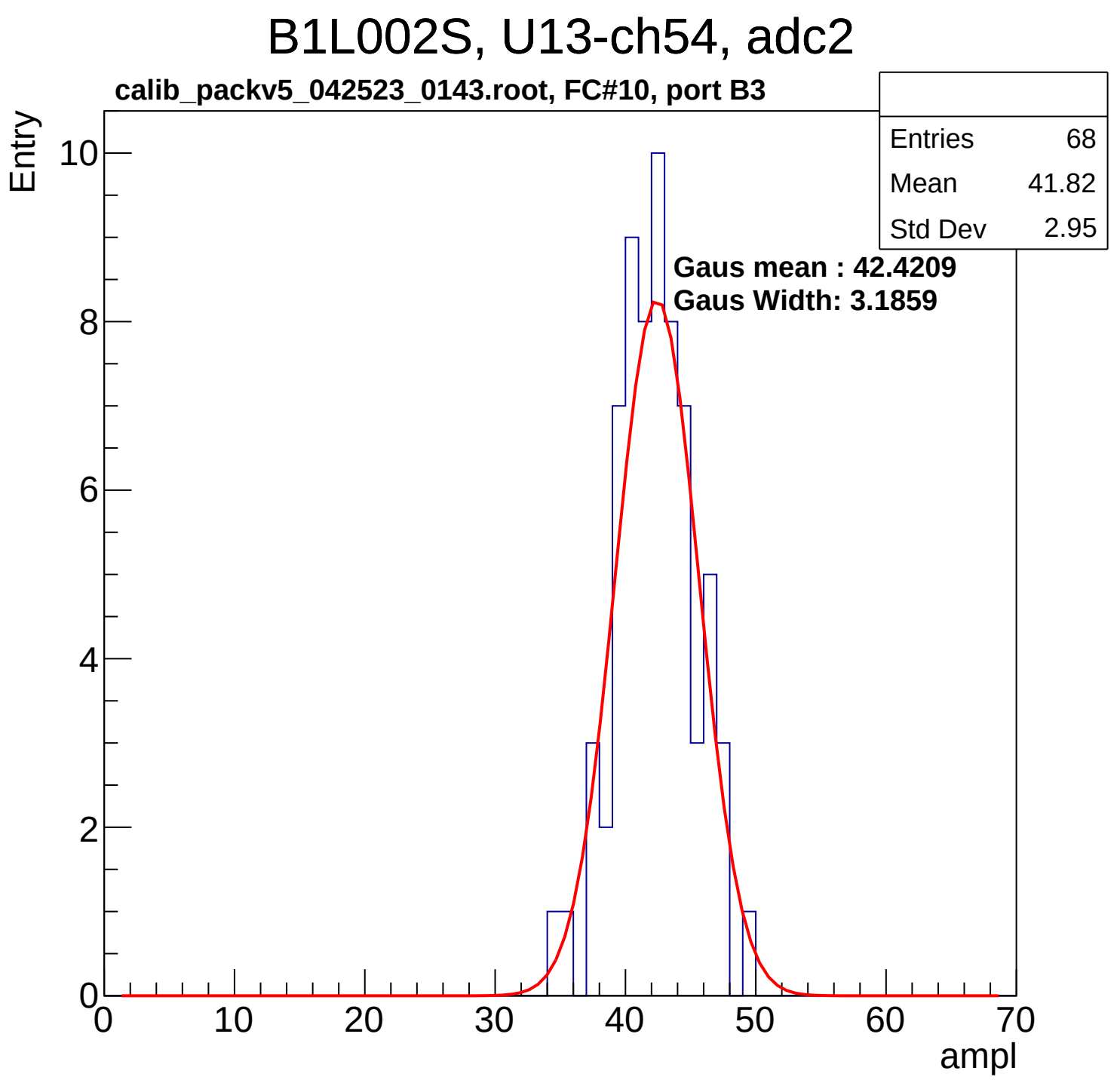
B1L002S, U13-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.82
Std Dev 2.95
Gaus mean : 42.4209
Gaus Width: 3.1859
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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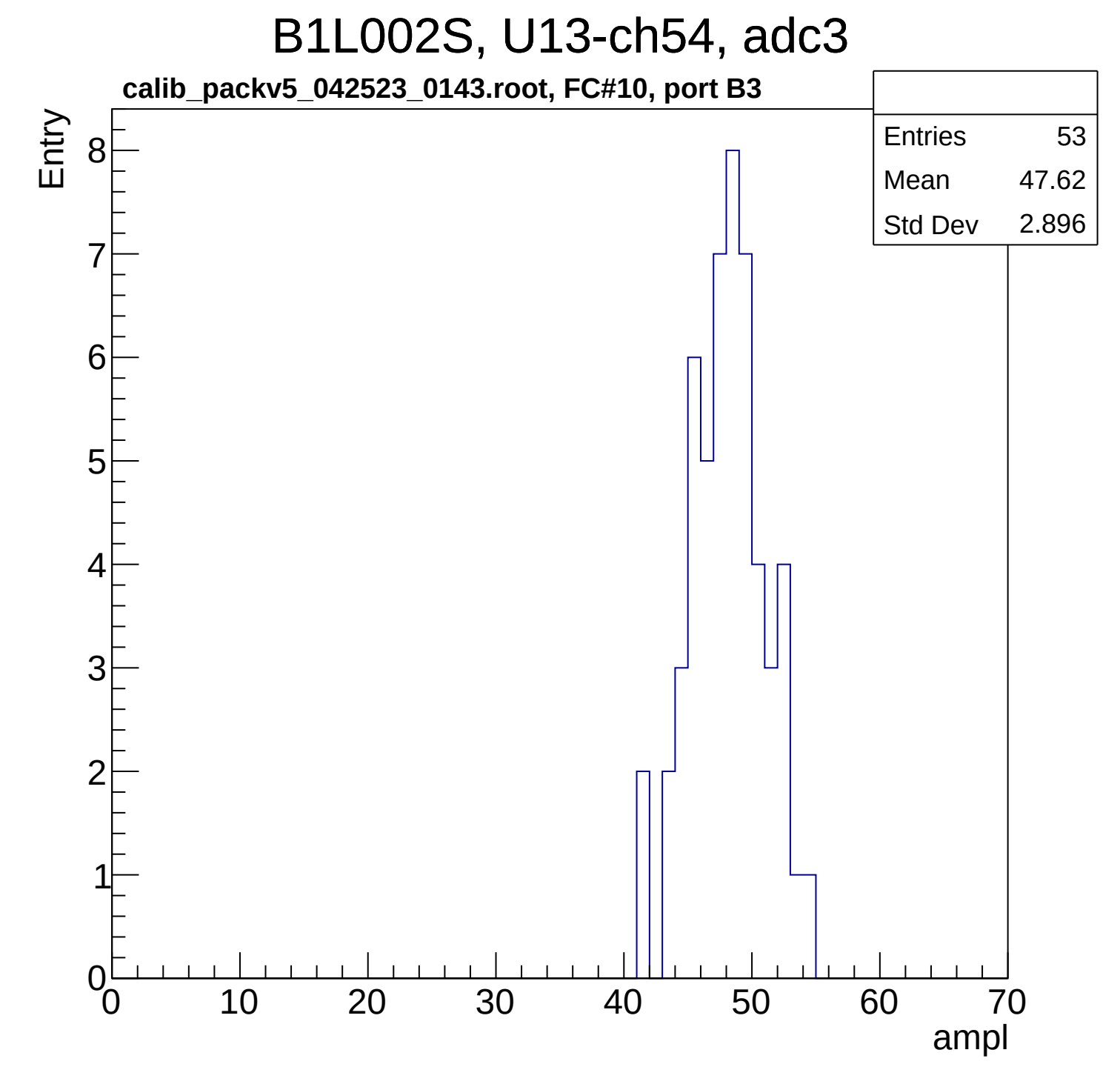
B1L002S, U13-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.62
Std Dev 2.896
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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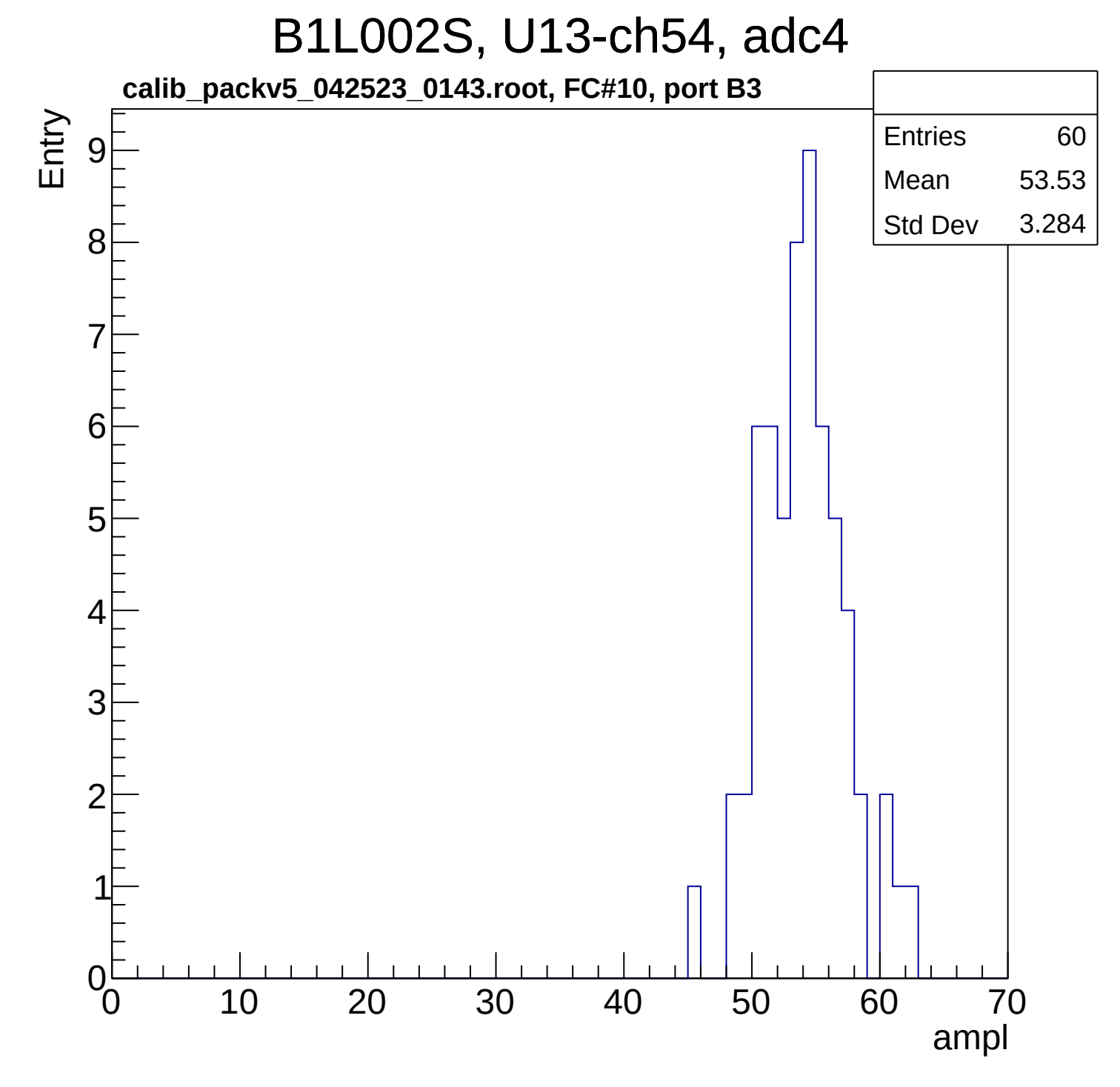
40
50
60
70


B1L002S, U13-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.53
Std Dev 3.284
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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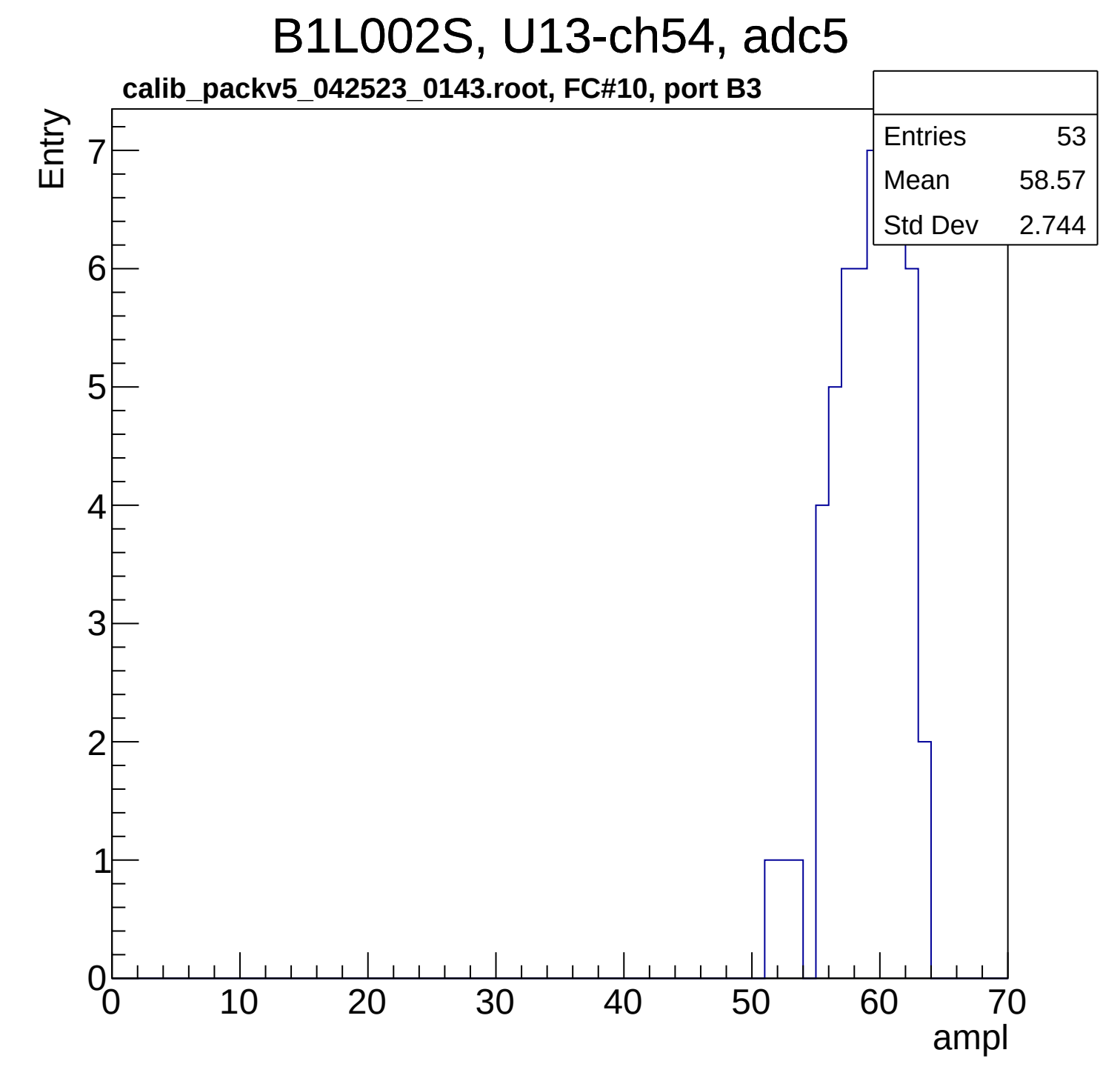
B1L002S, U13-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.57
Std Dev 2.744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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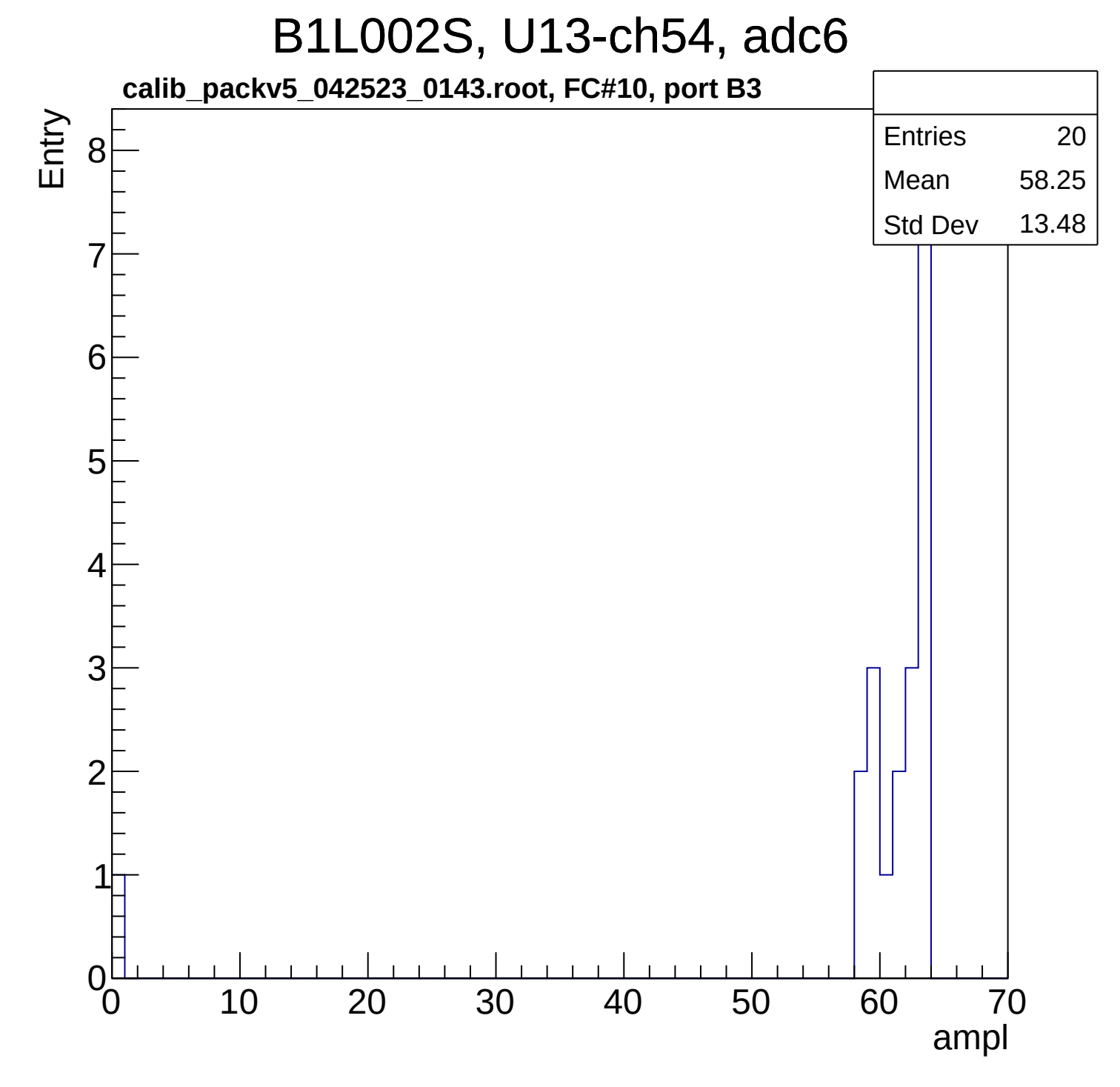
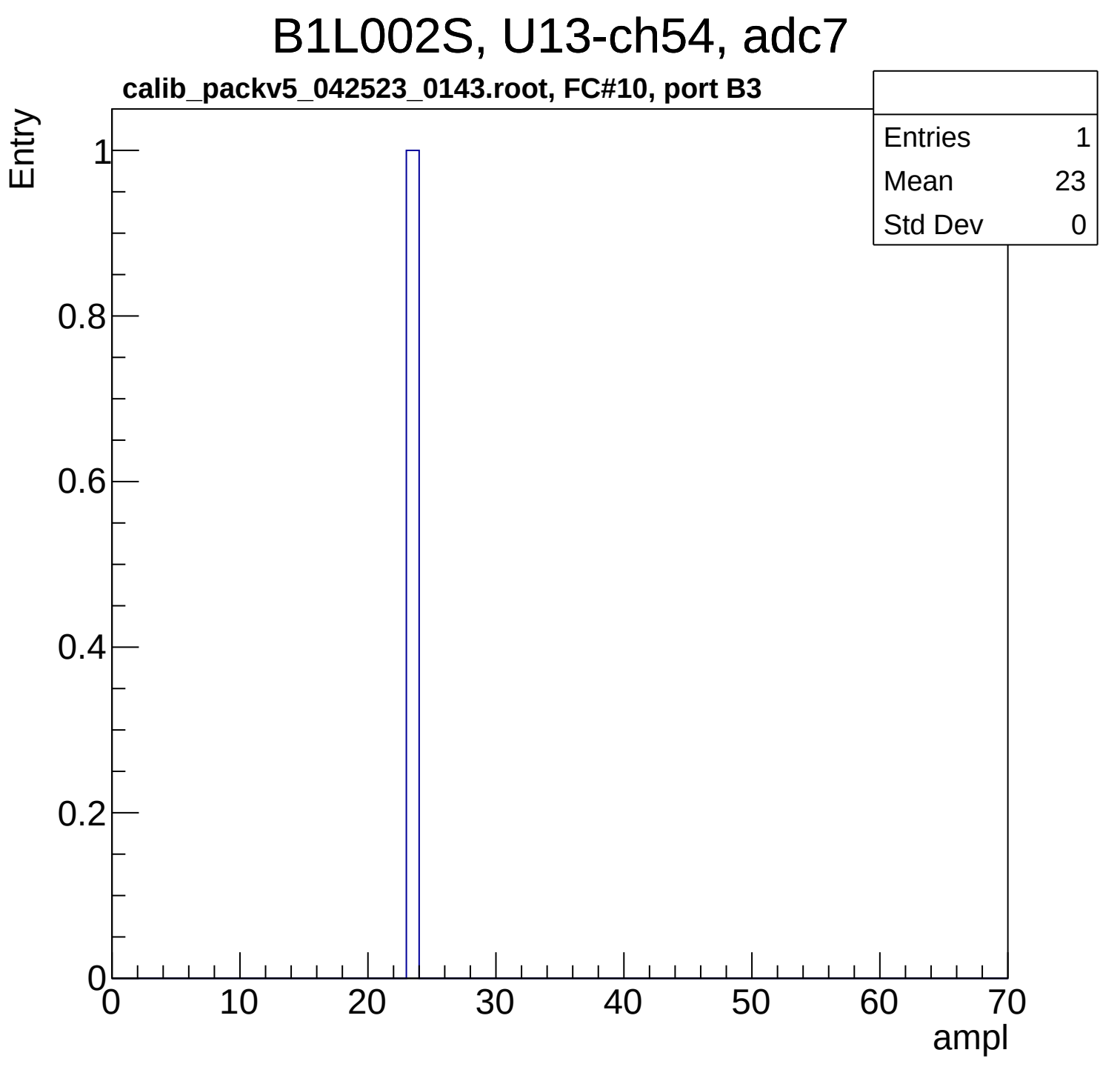

B1L002S, U13-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.25
Std Dev 13.48
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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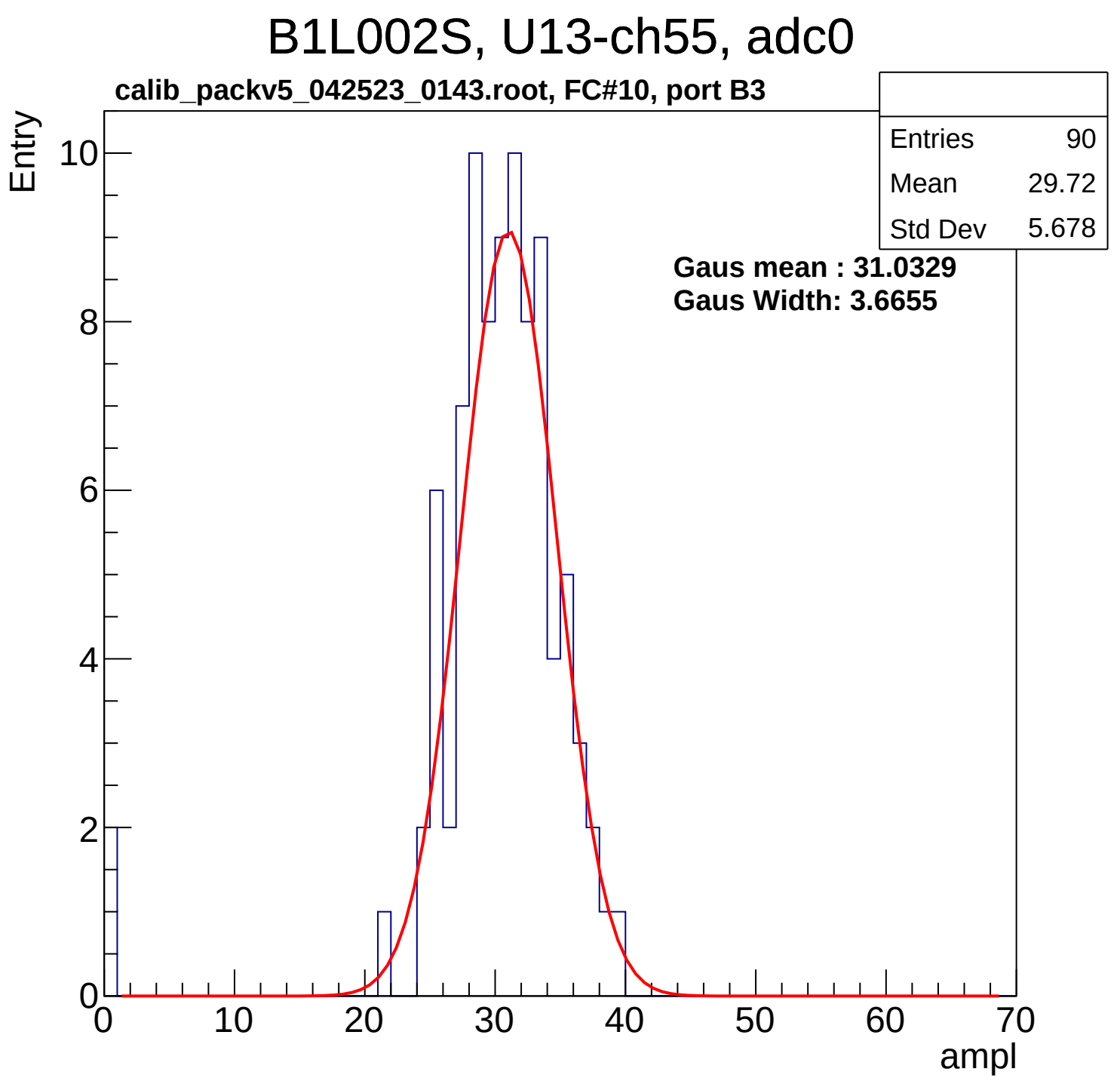
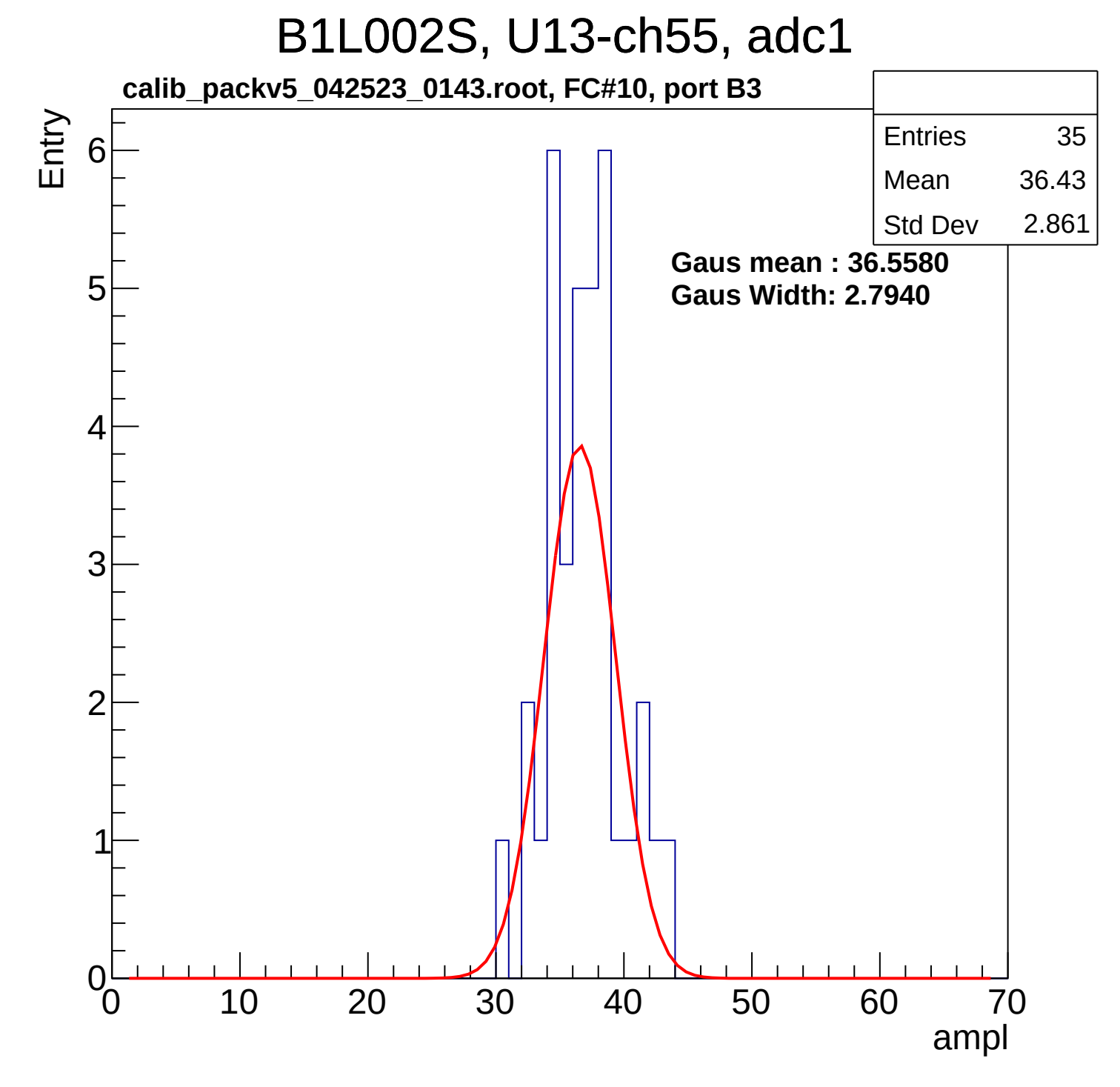
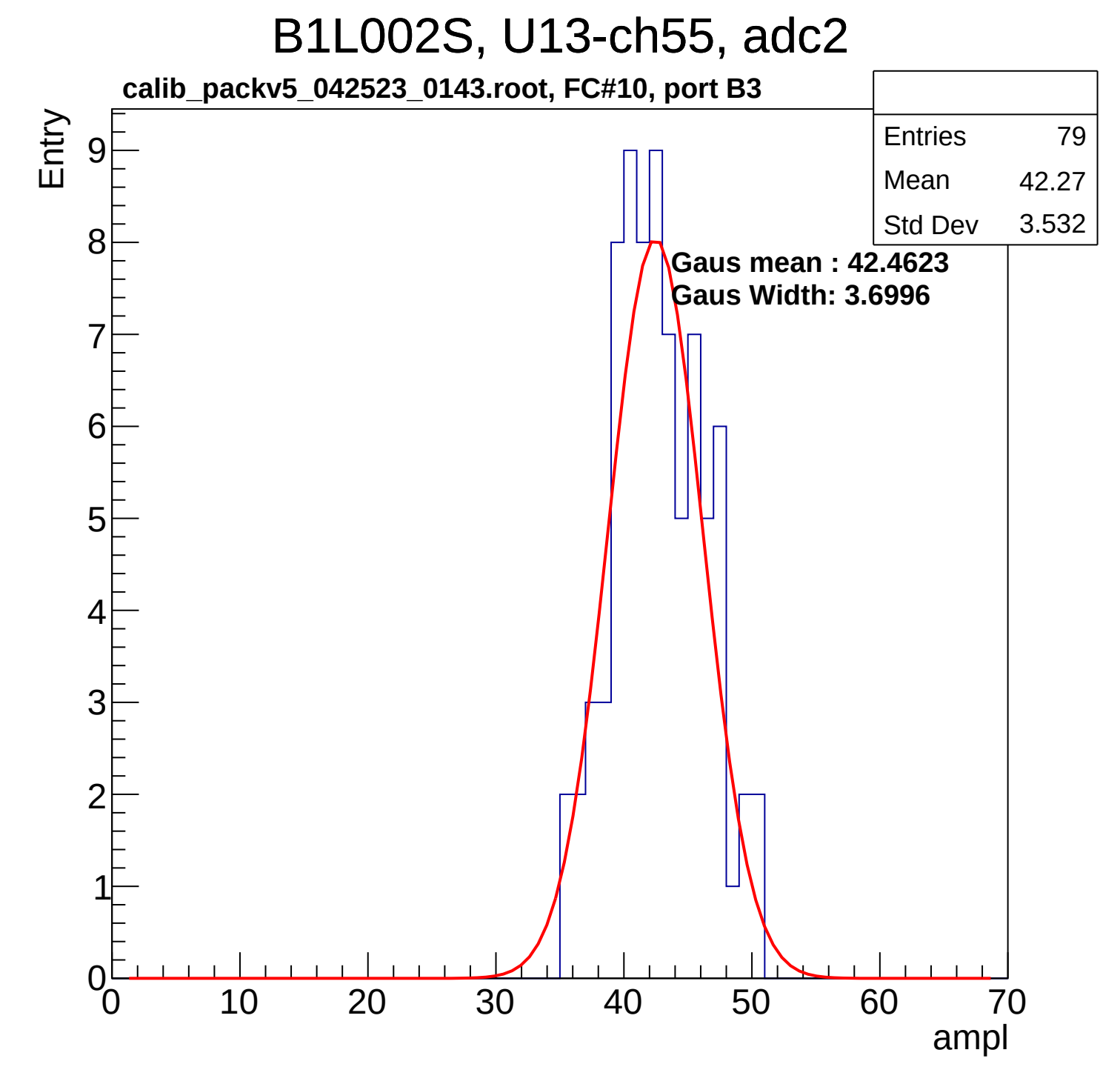
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 29.72
Std Dev 5.678
Gaus mean : 31.0329
Gaus Width: 3.6655
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 36.43
Std Dev 2.861
Gaus mean : 36.5580
Gaus Width: 2.7940
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 42.27
Std Dev 3.532
Gaus mean : 42.4623
Gaus Width: 3.6996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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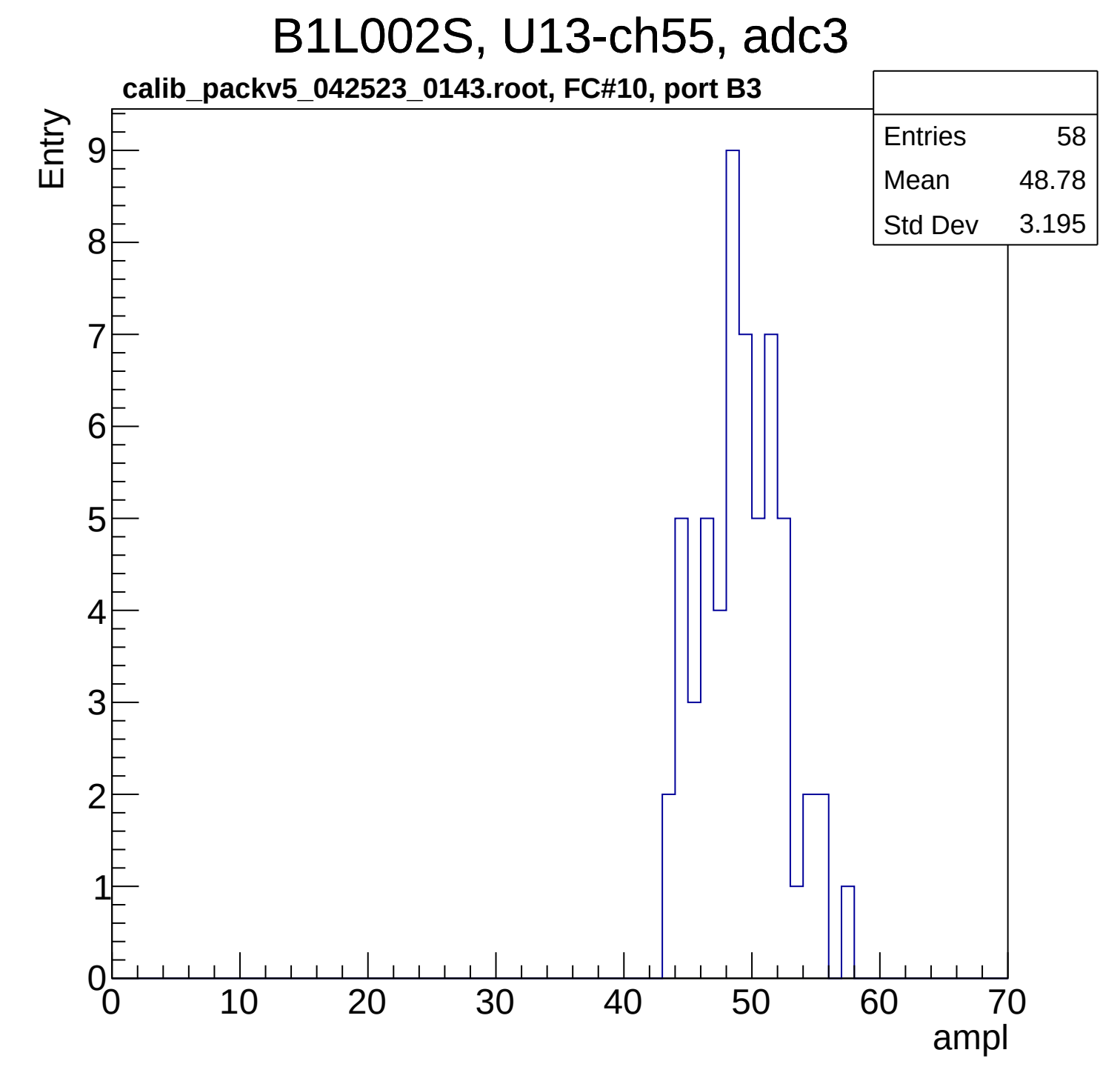
B1L002S, U13-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 48.78
Std Dev 3.195
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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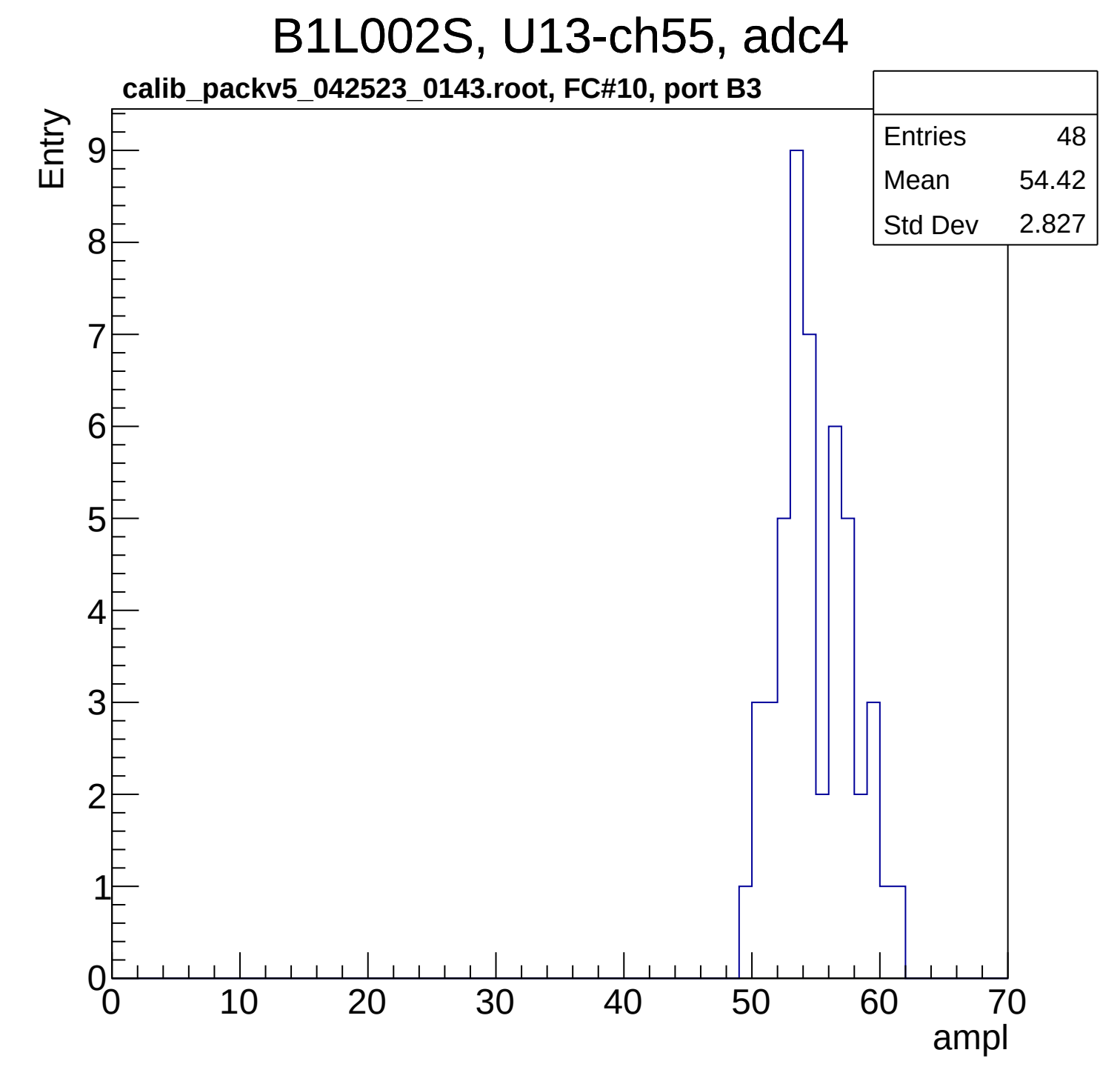
B1L002S, U13-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.42
Std Dev 2.827
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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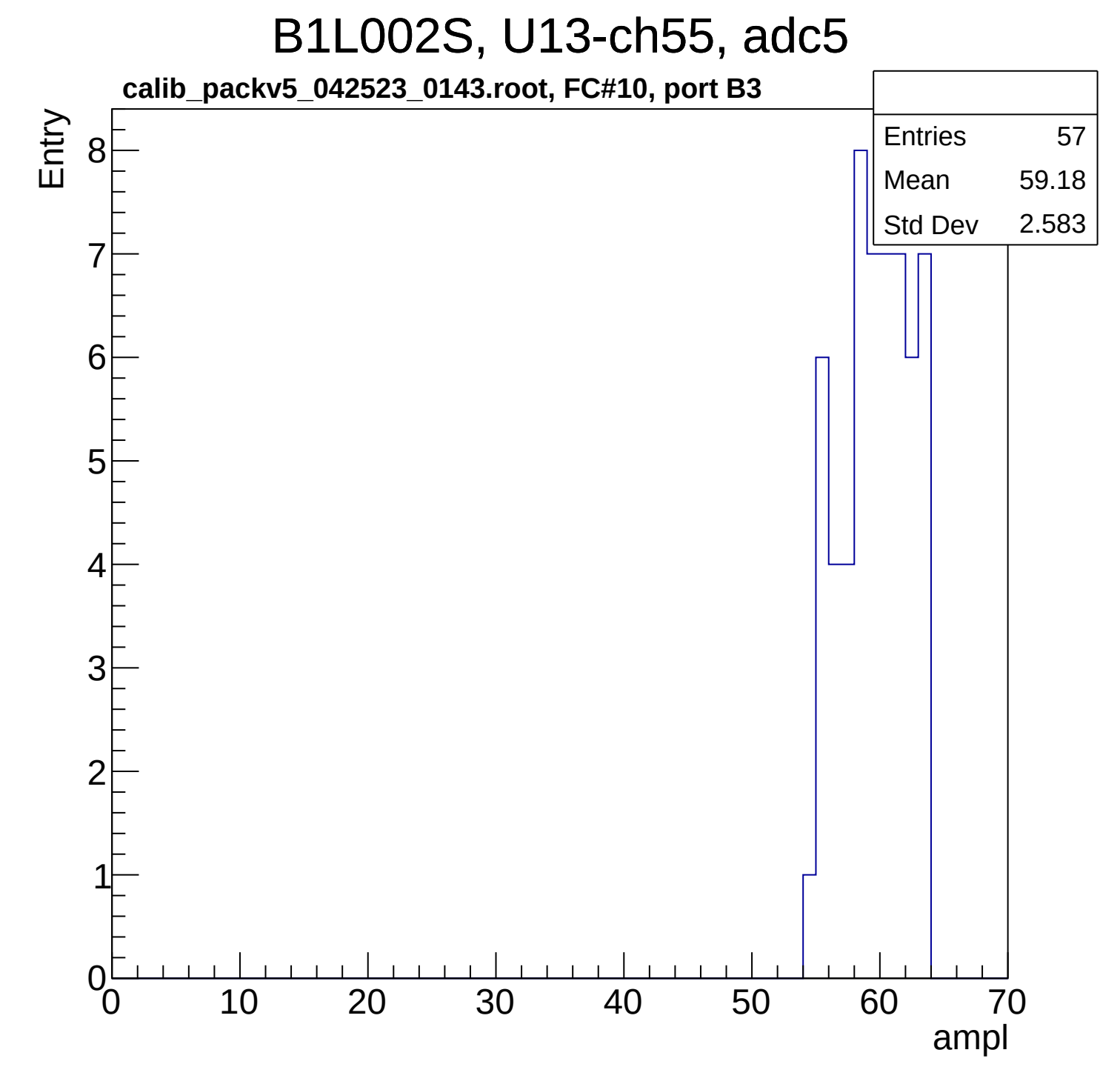
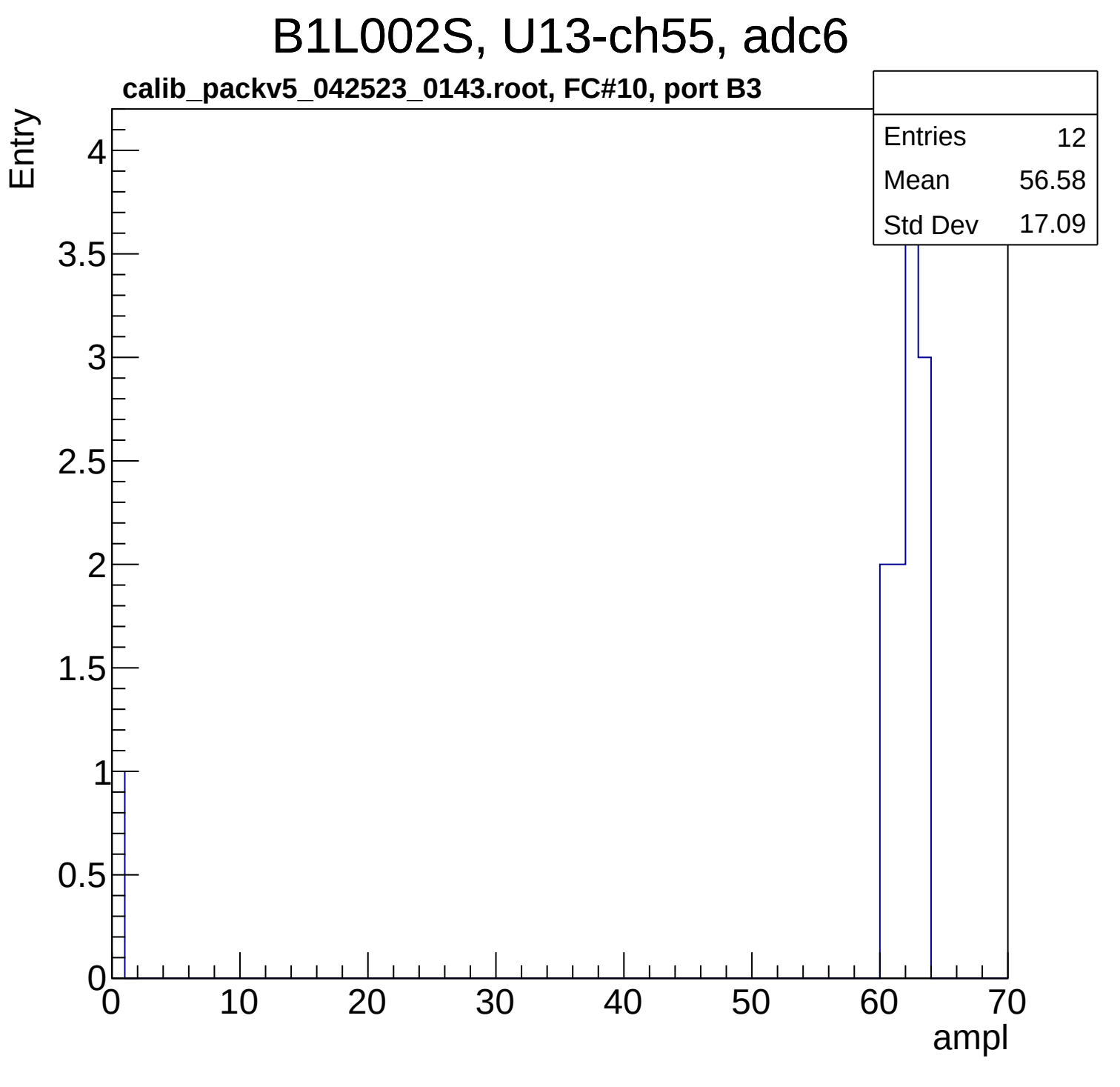
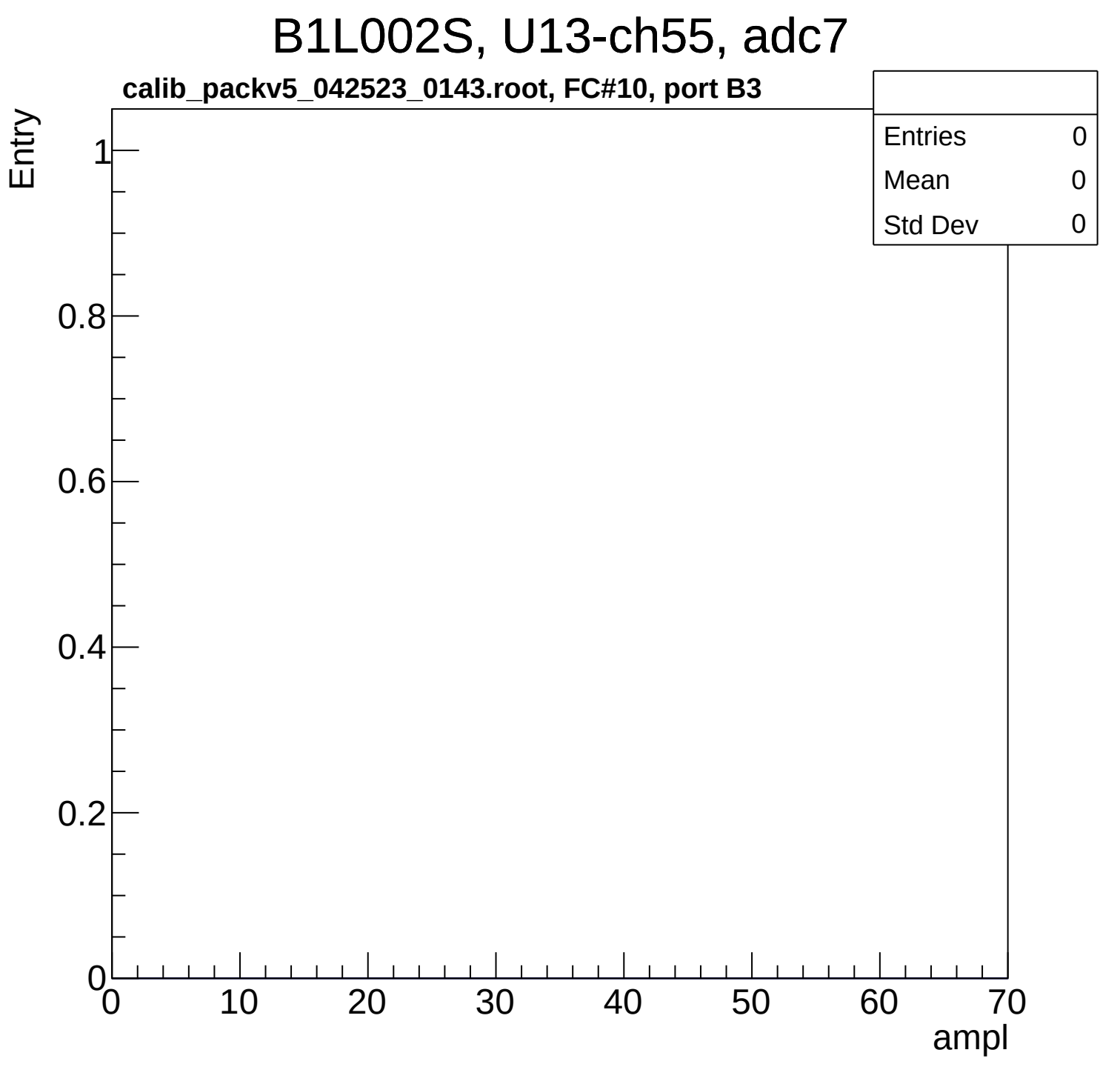

B1L002S, U13-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 59.18
Std Dev 2.583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.58
Std Dev 17.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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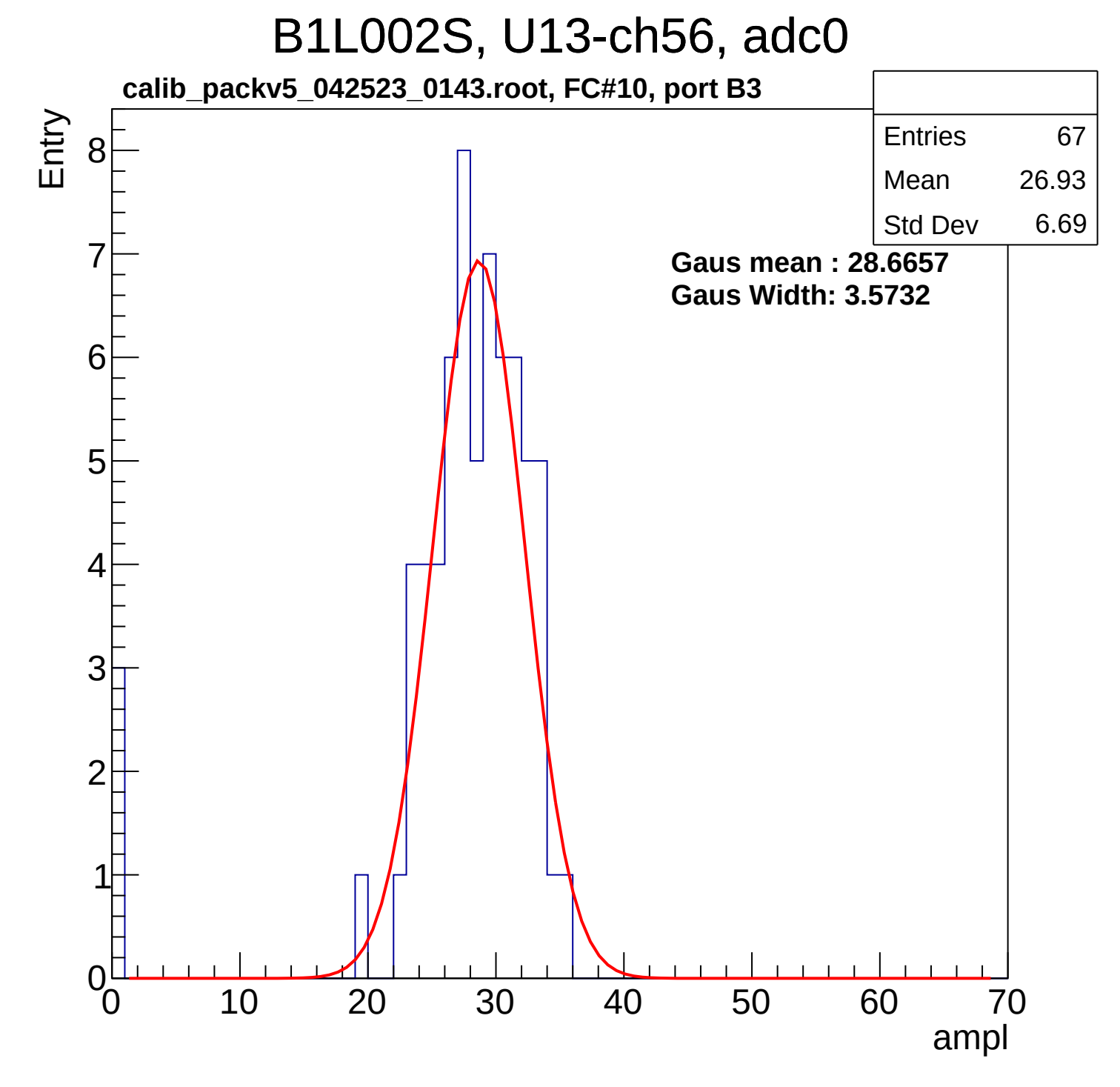
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 26.93
Std Dev 6.69
Gaus mean : 28.6657
Gaus Width: 3.5732
Entry
ampl
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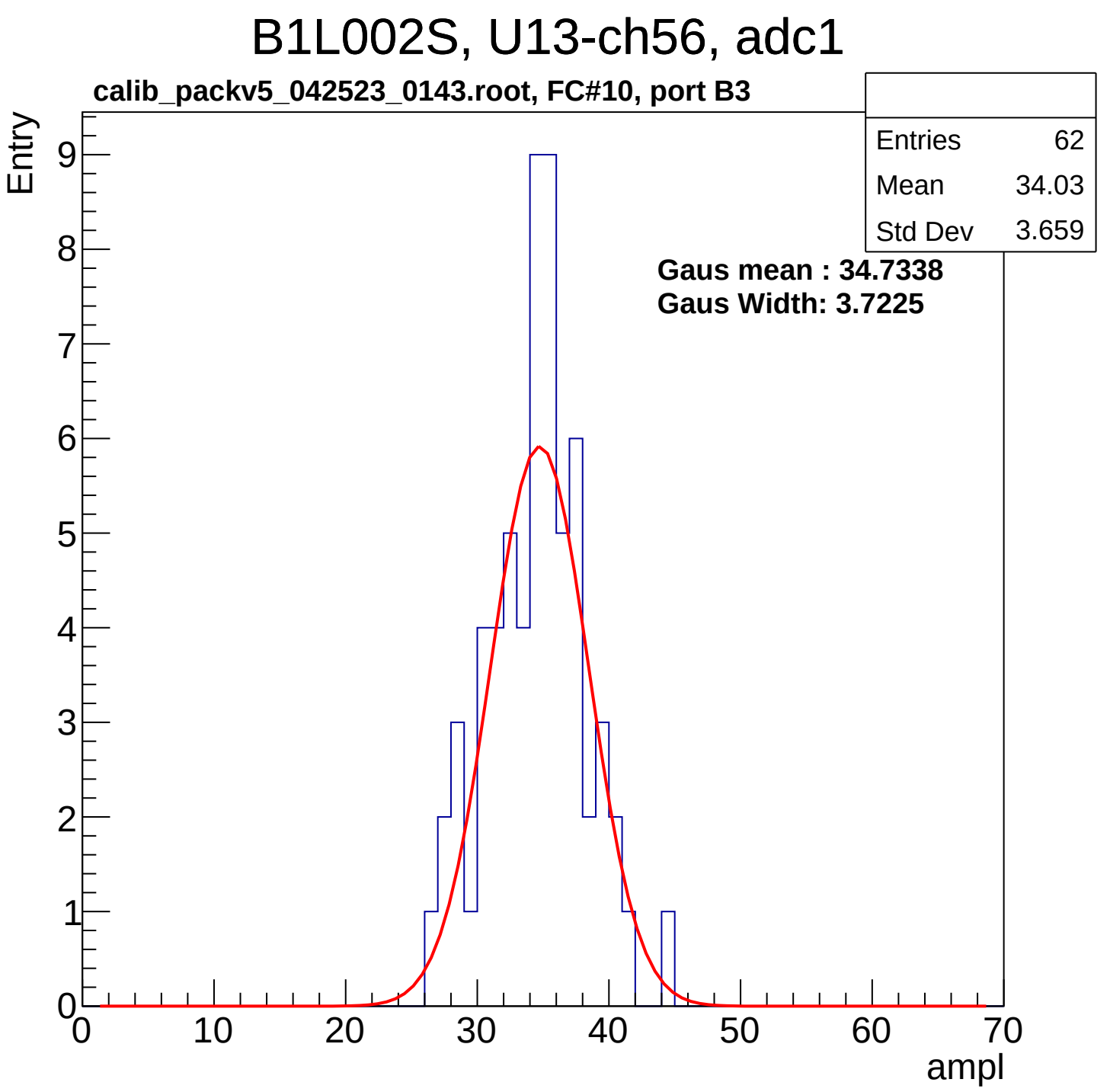
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.03
Std Dev 3.659
Gaus mean : 34.7338
Gaus Width: 3.7225
Entry
ampl
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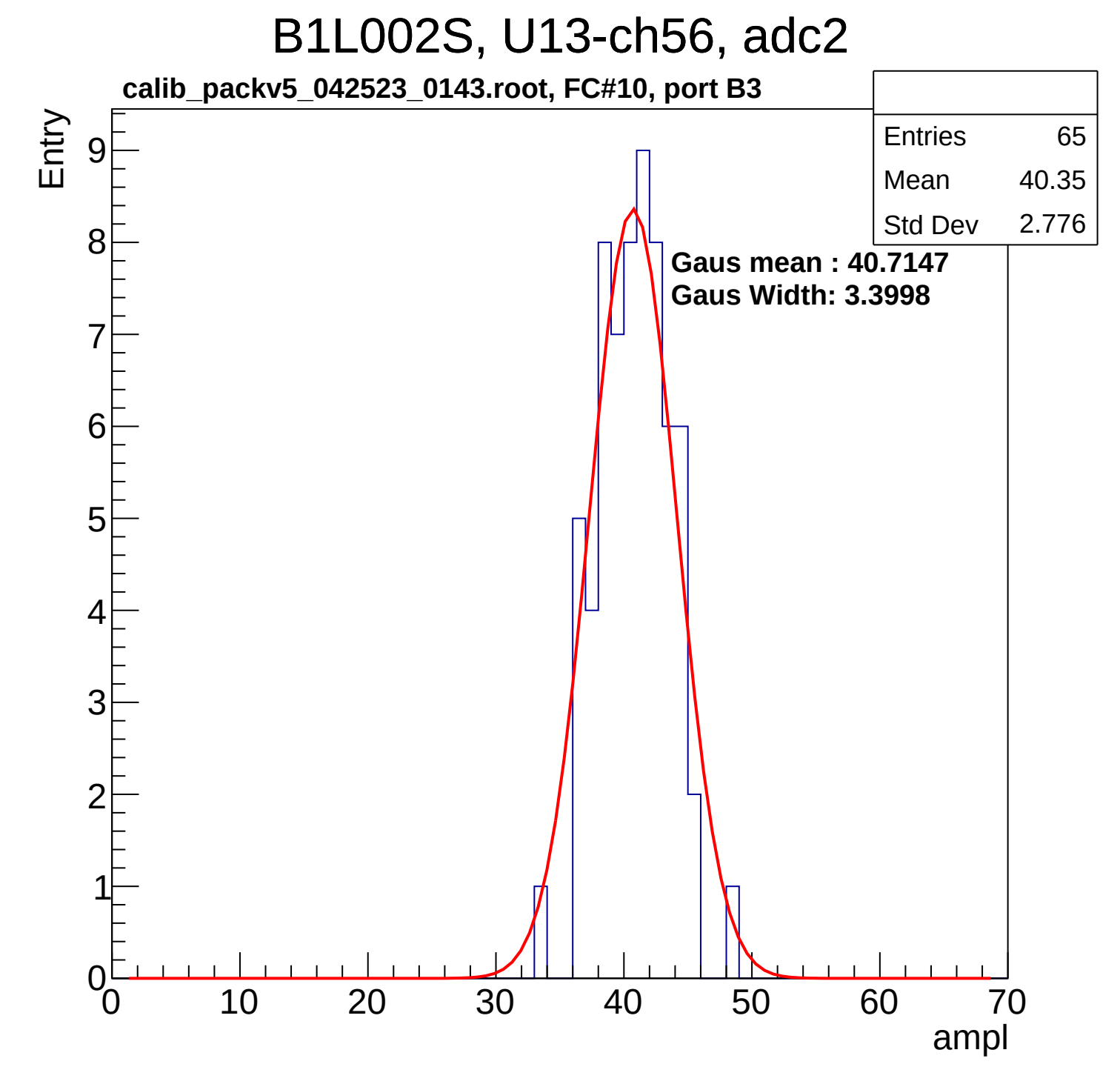
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.35
Std Dev 2.776
Gaus mean : 40.7147
Gaus Width: 3.3998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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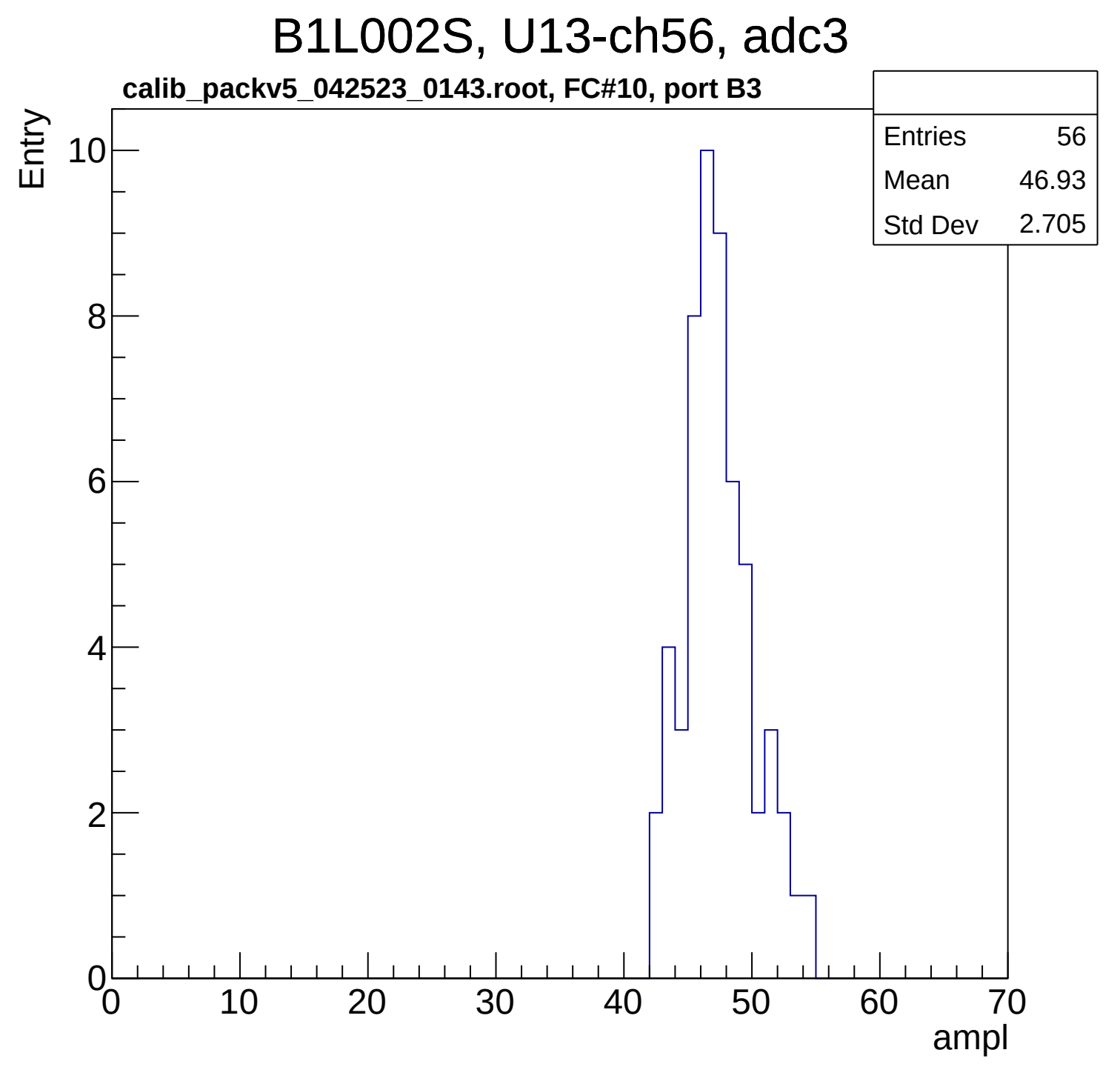
B1L002S, U13-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.93
Std Dev 2.705
Entry
ampl
0
2
4
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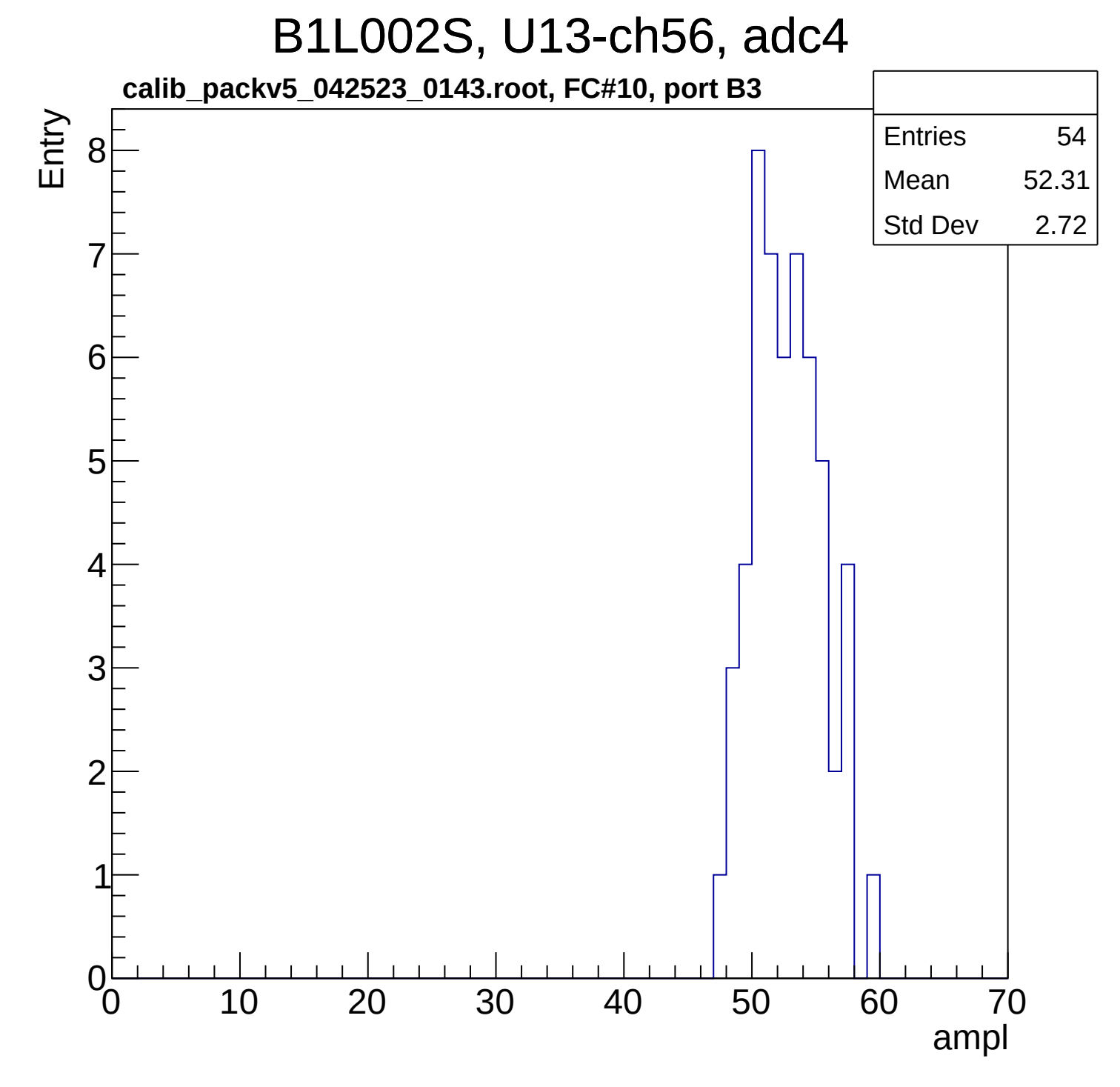
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.31
Std Dev 2.72
Entry
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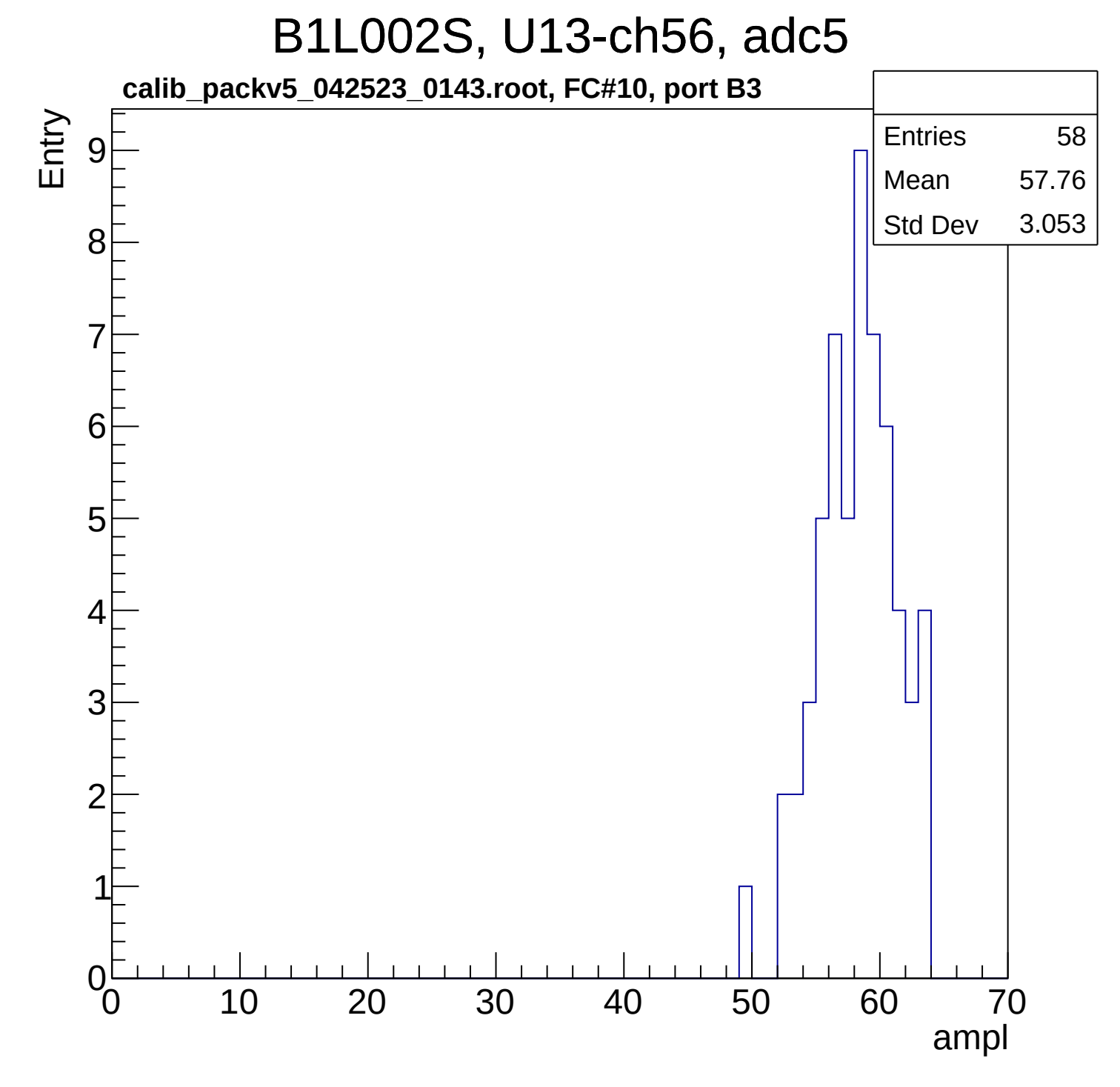
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.76
Std Dev 3.053
Entry
ampl
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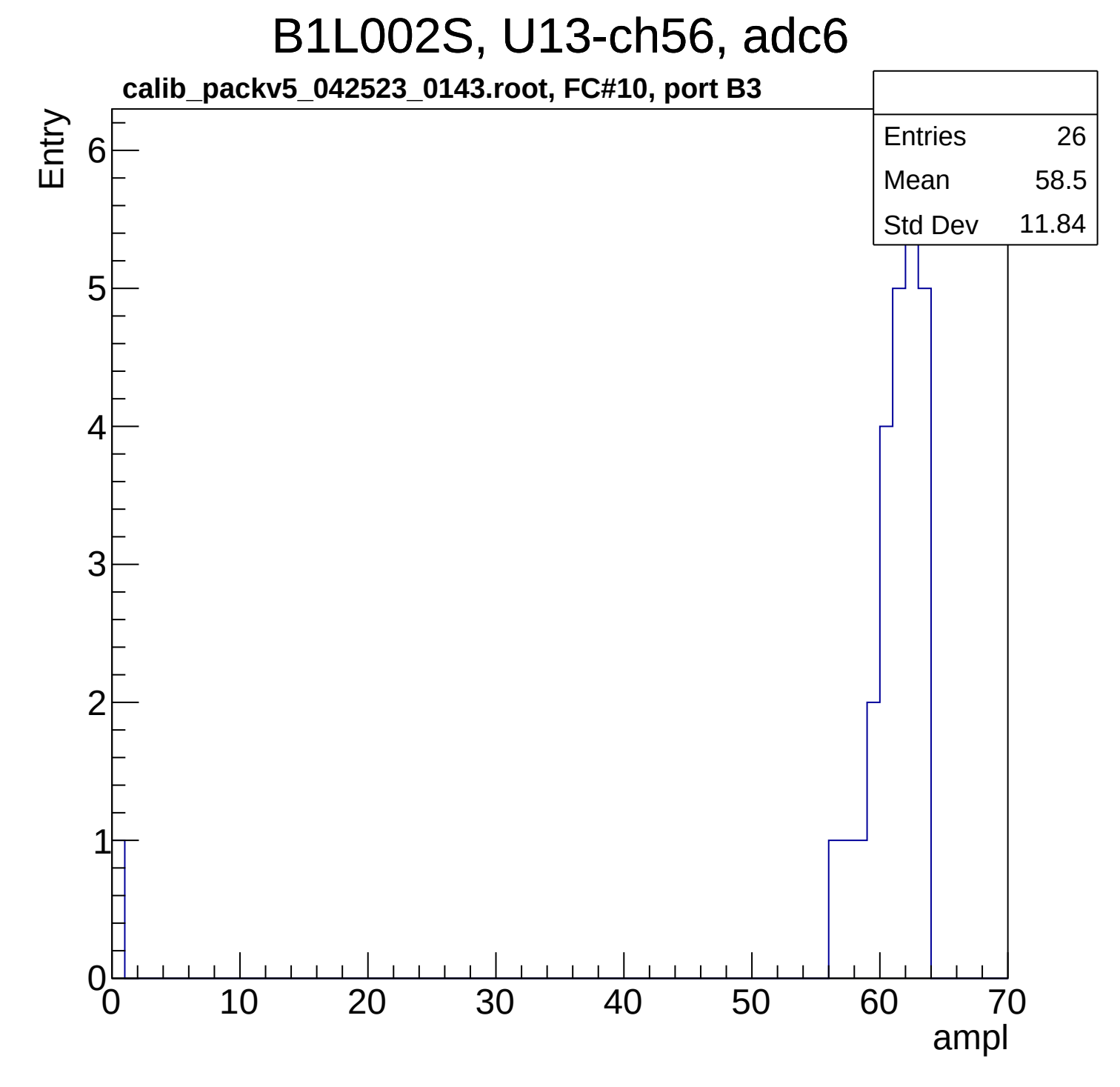
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.5
Std Dev 11.84
Entry
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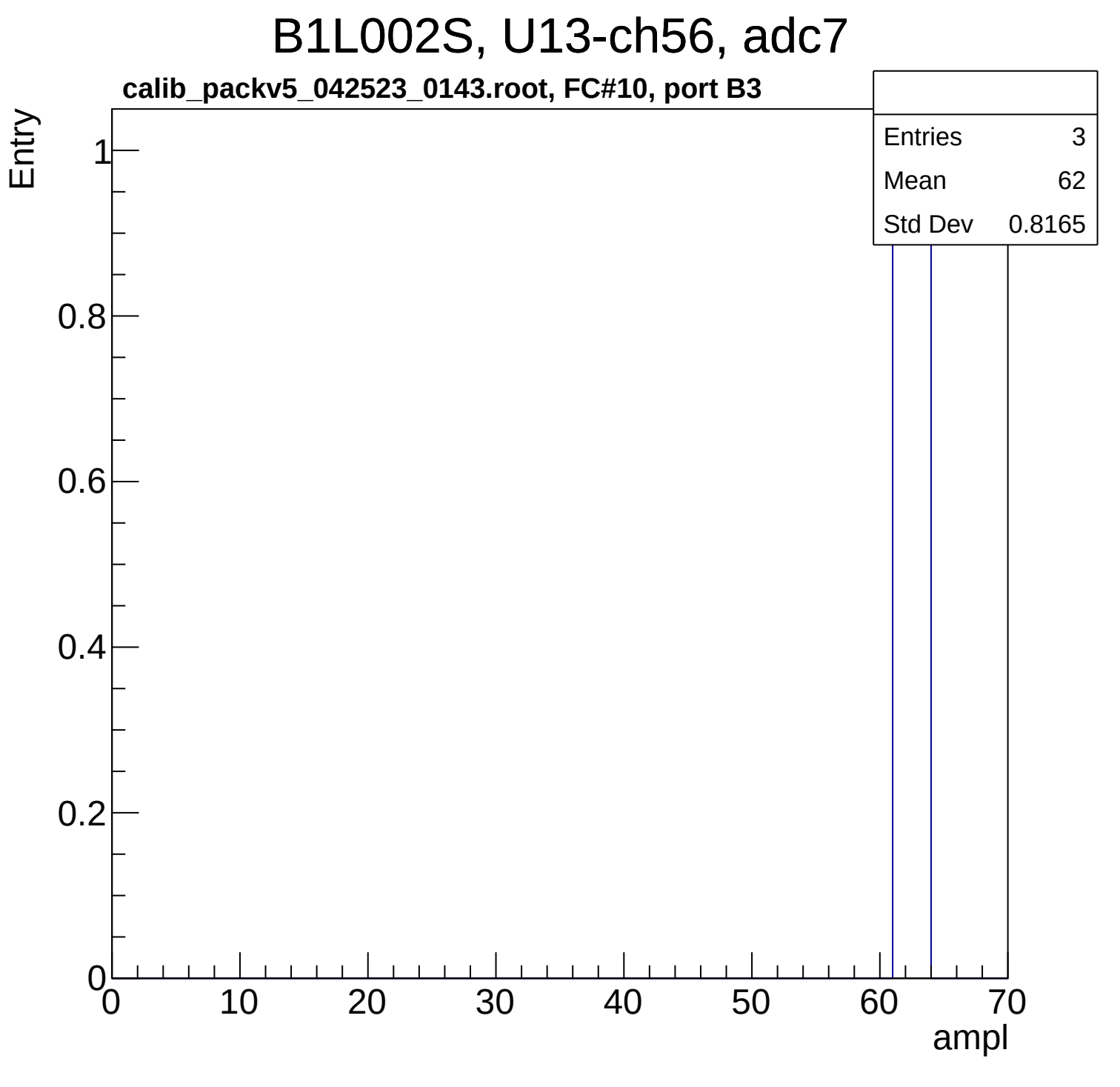
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 0.8165
Entry
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0.6
0.4
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0
10
20
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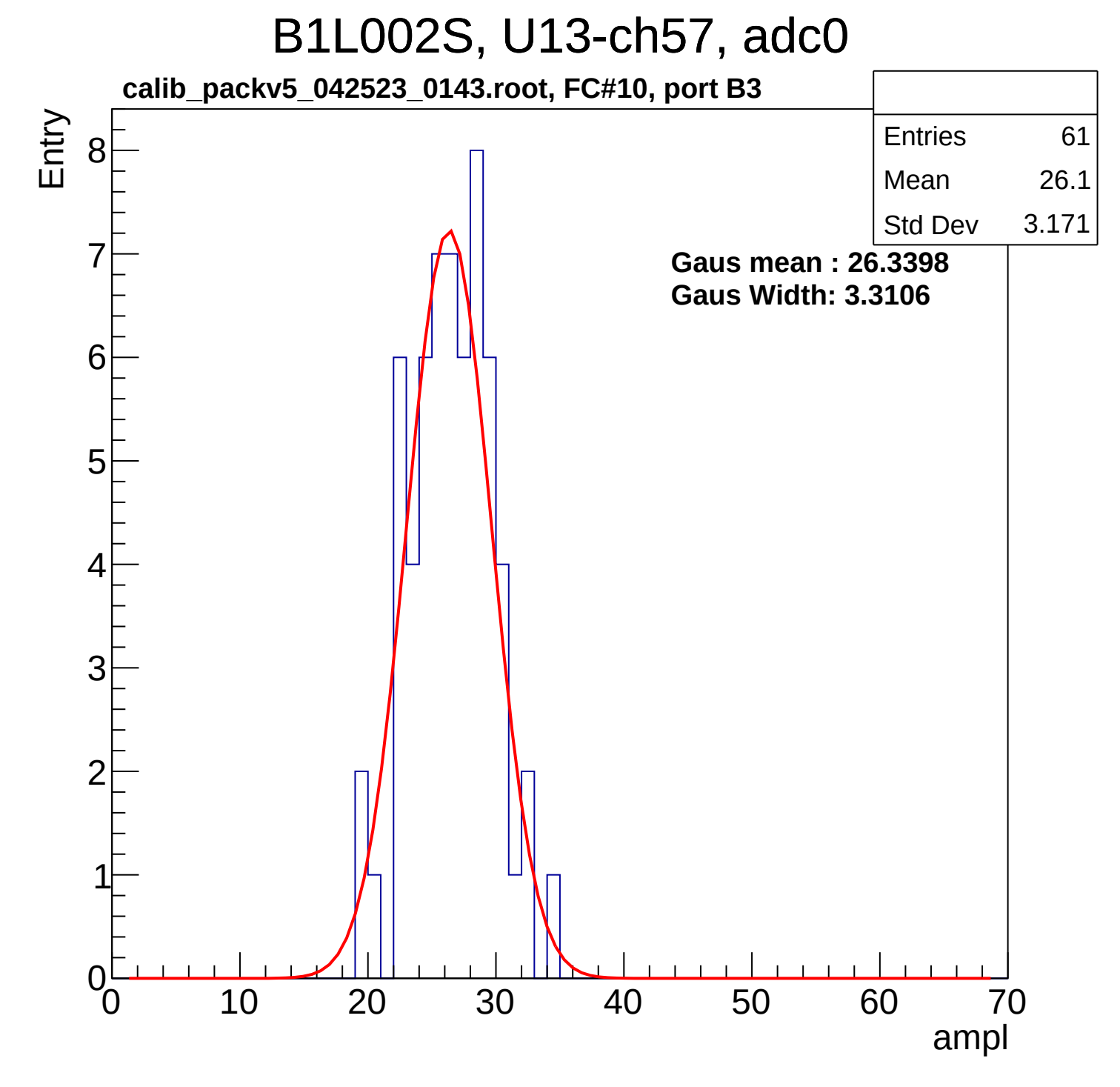
40
50
60
70
ampl


B1L002S, U13-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.1
Std Dev 3.171
Gaus mean : 26.3398
Gaus Width: 3.3106
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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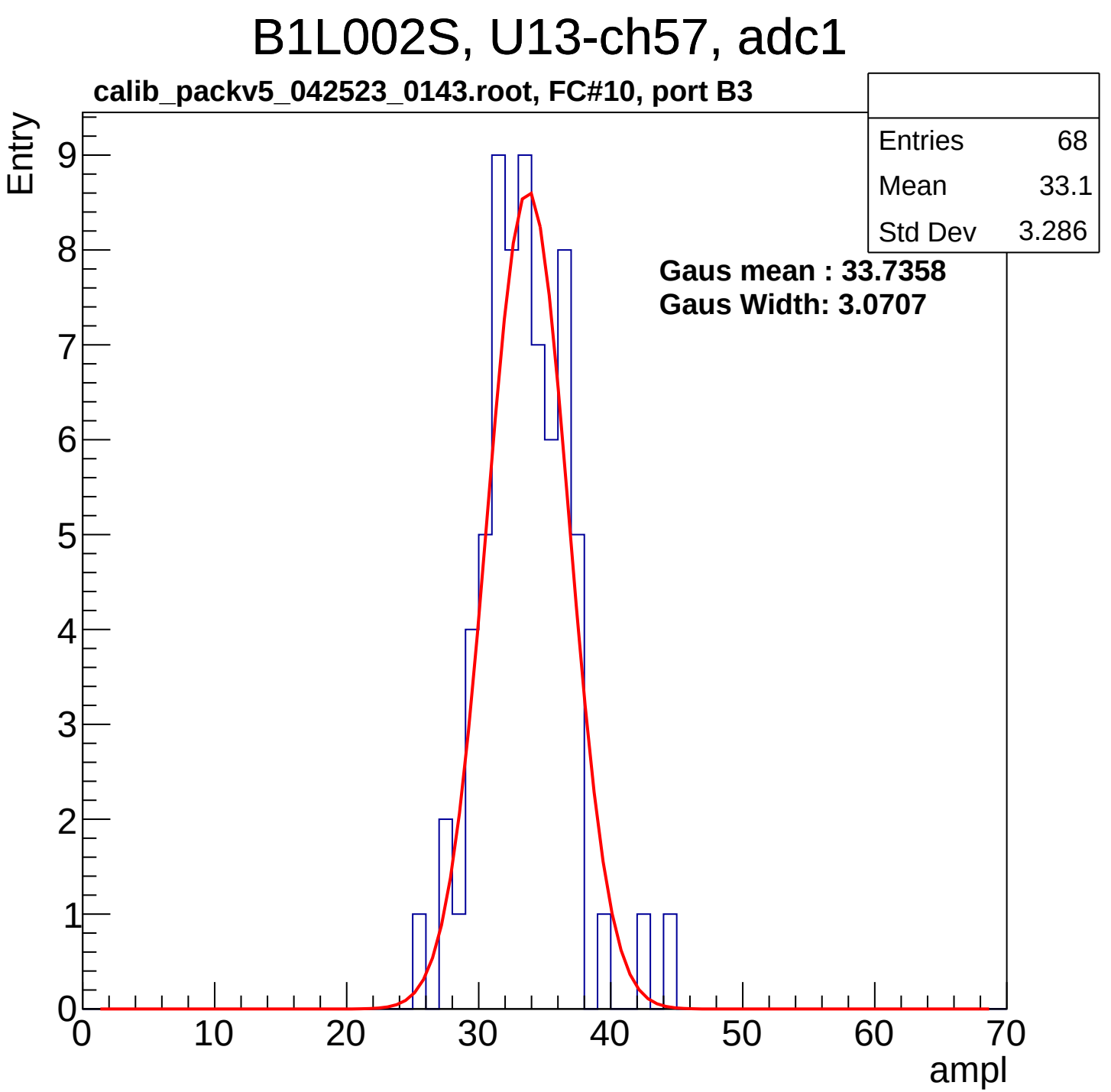
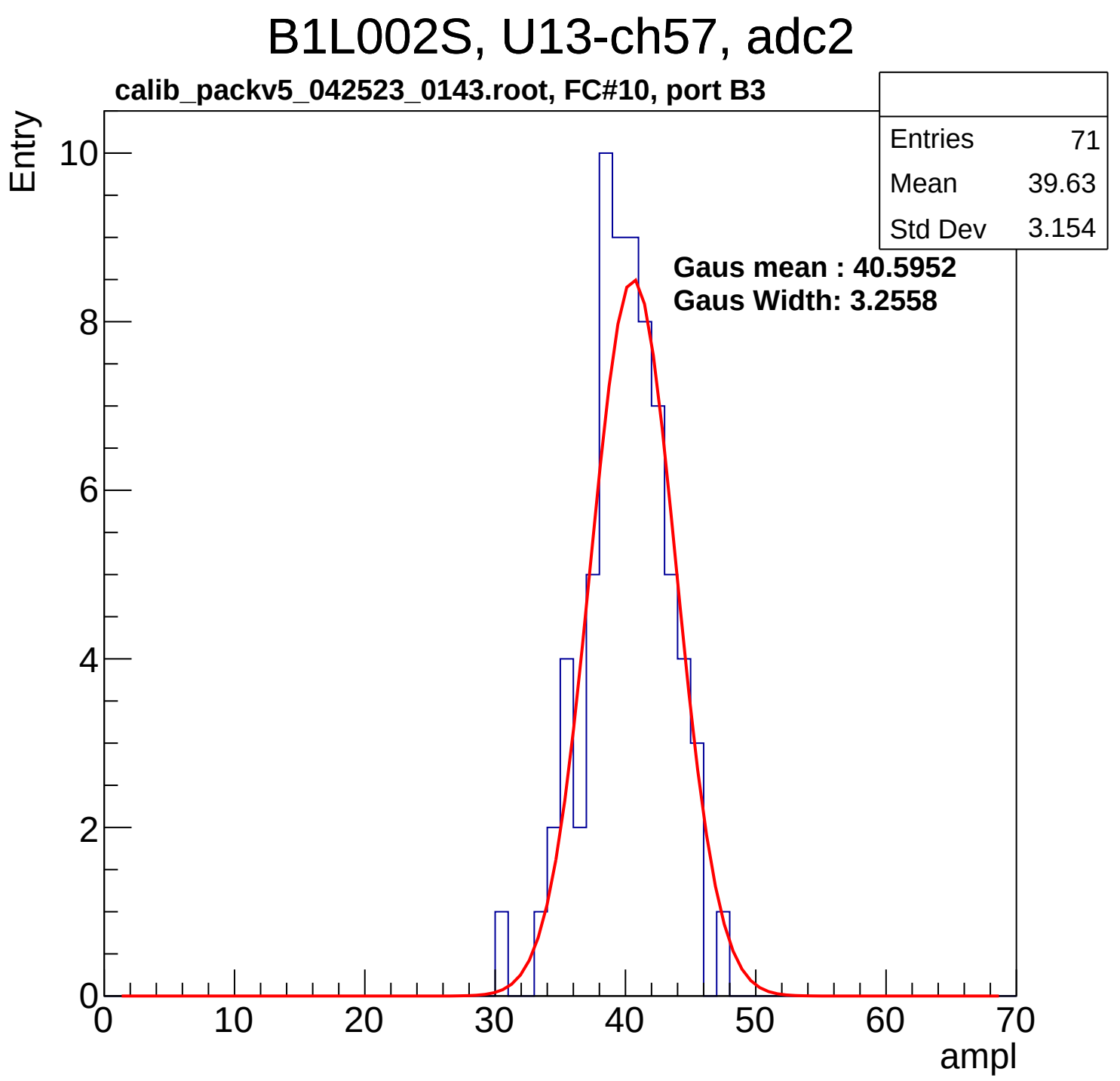

B1L002S, U13-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.1
Std Dev 3.286
Gaus mean : 33.7358
Gaus Width: 3.0707
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 39.63
Std Dev 3.154
Gaus mean : 40.5952
Gaus Width: 3.2558
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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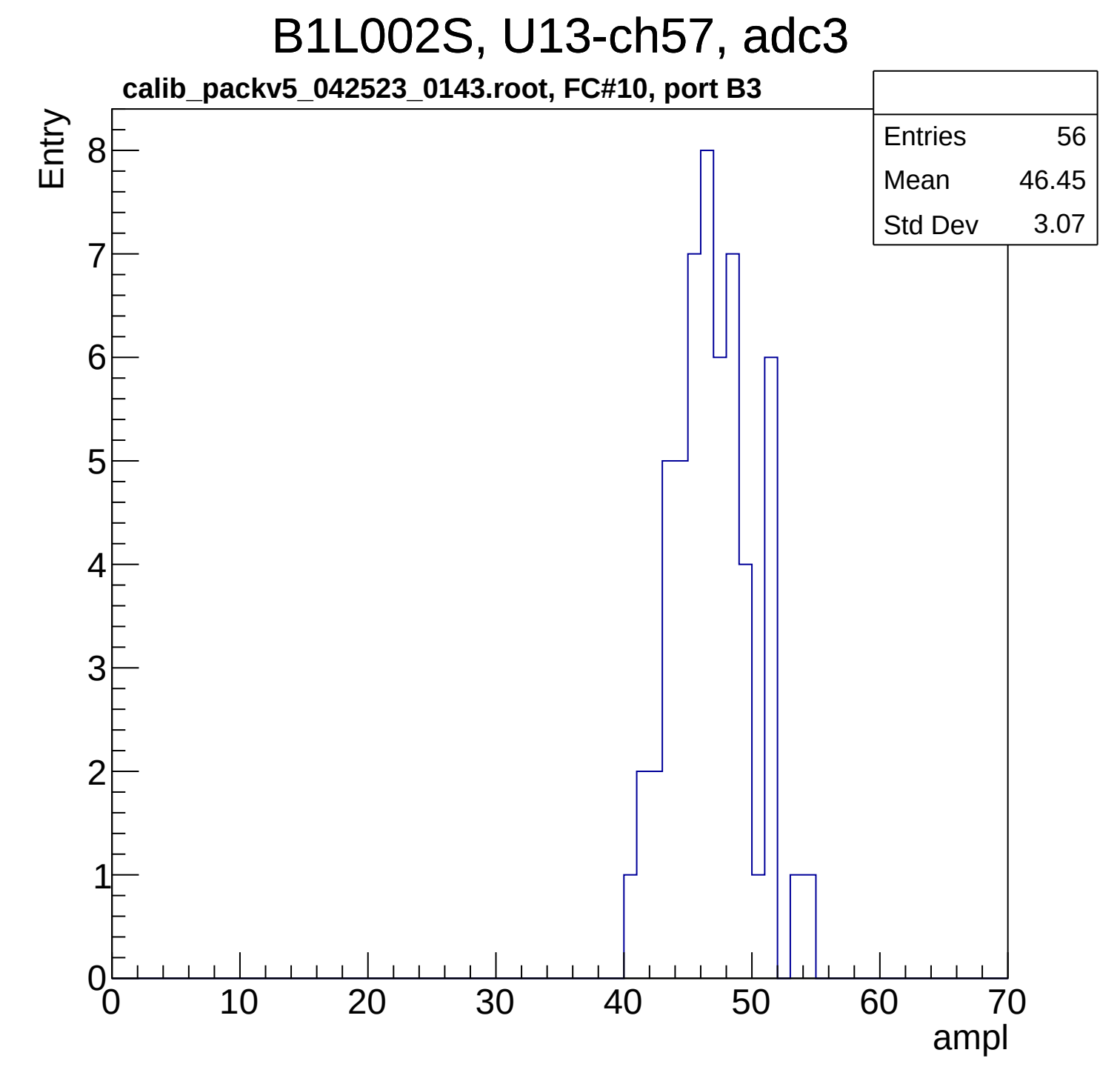
B1L002S, U13-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.45
Std Dev 3.07
Entry
ampl
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2
3
4
5
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0
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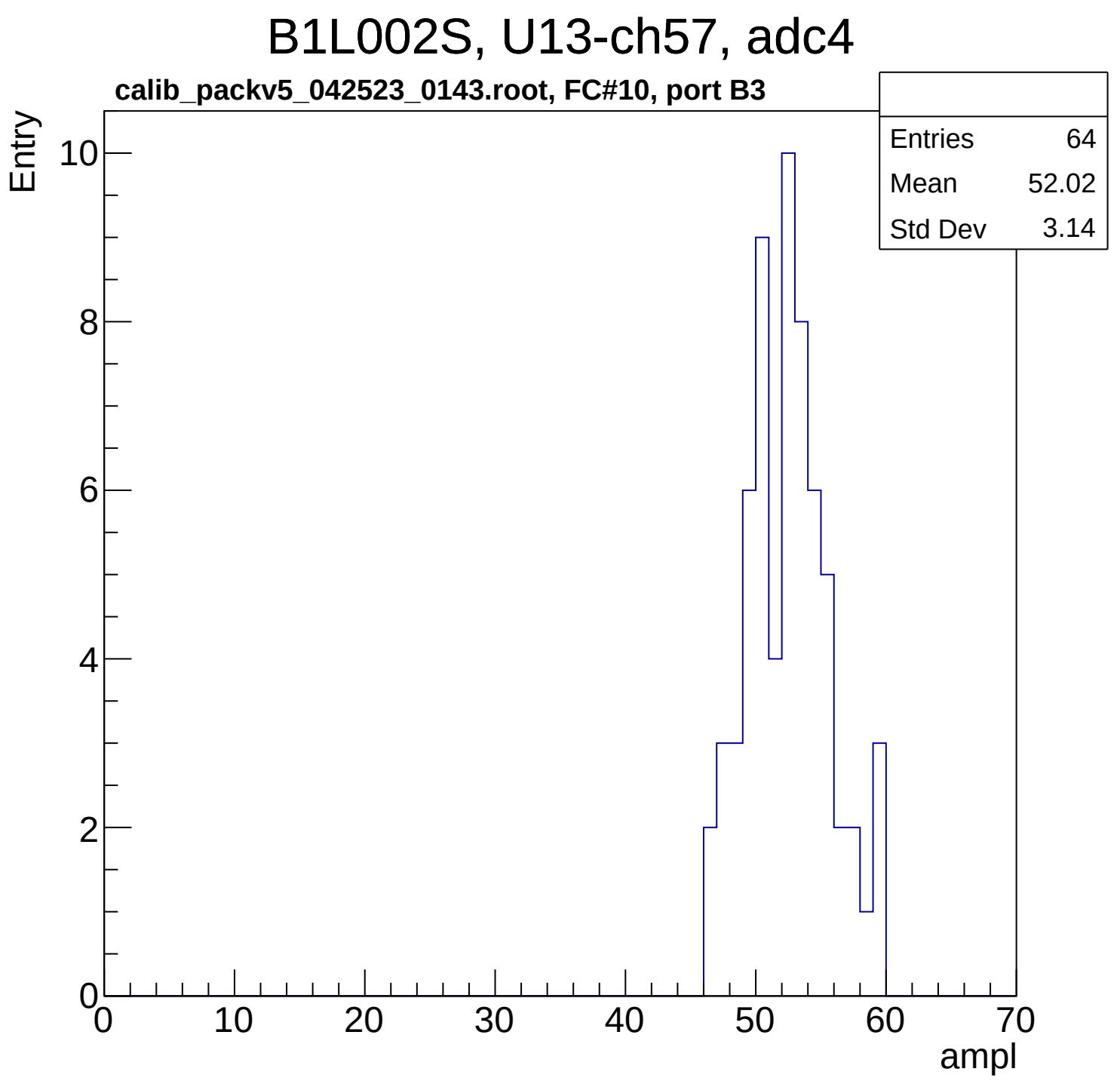
B1L002S, U13-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.02
Std Dev 3.14
Entry
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4
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8
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10
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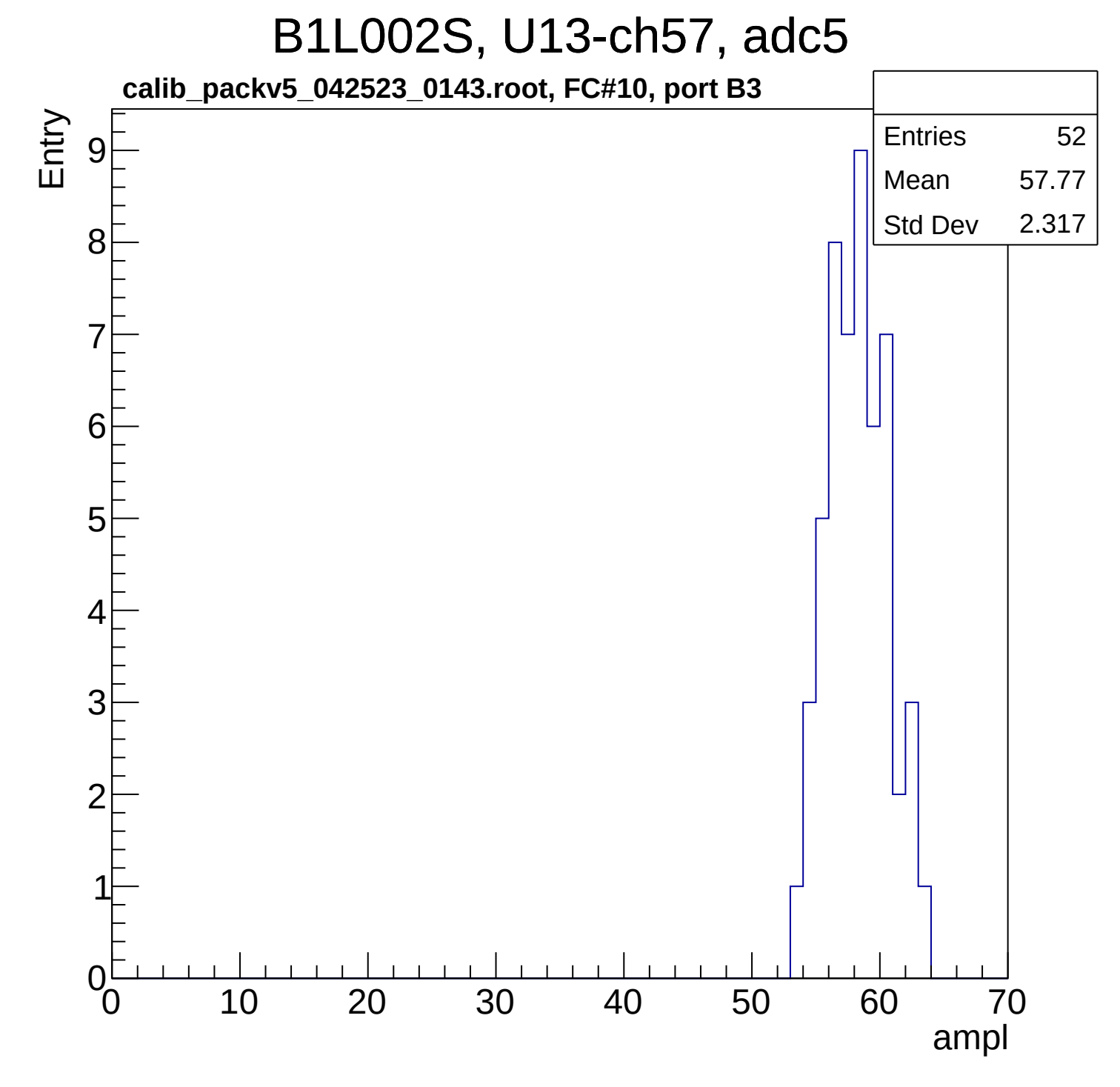
50
60
70
ampl


B1L002S, U13-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.77
Std Dev 2.317
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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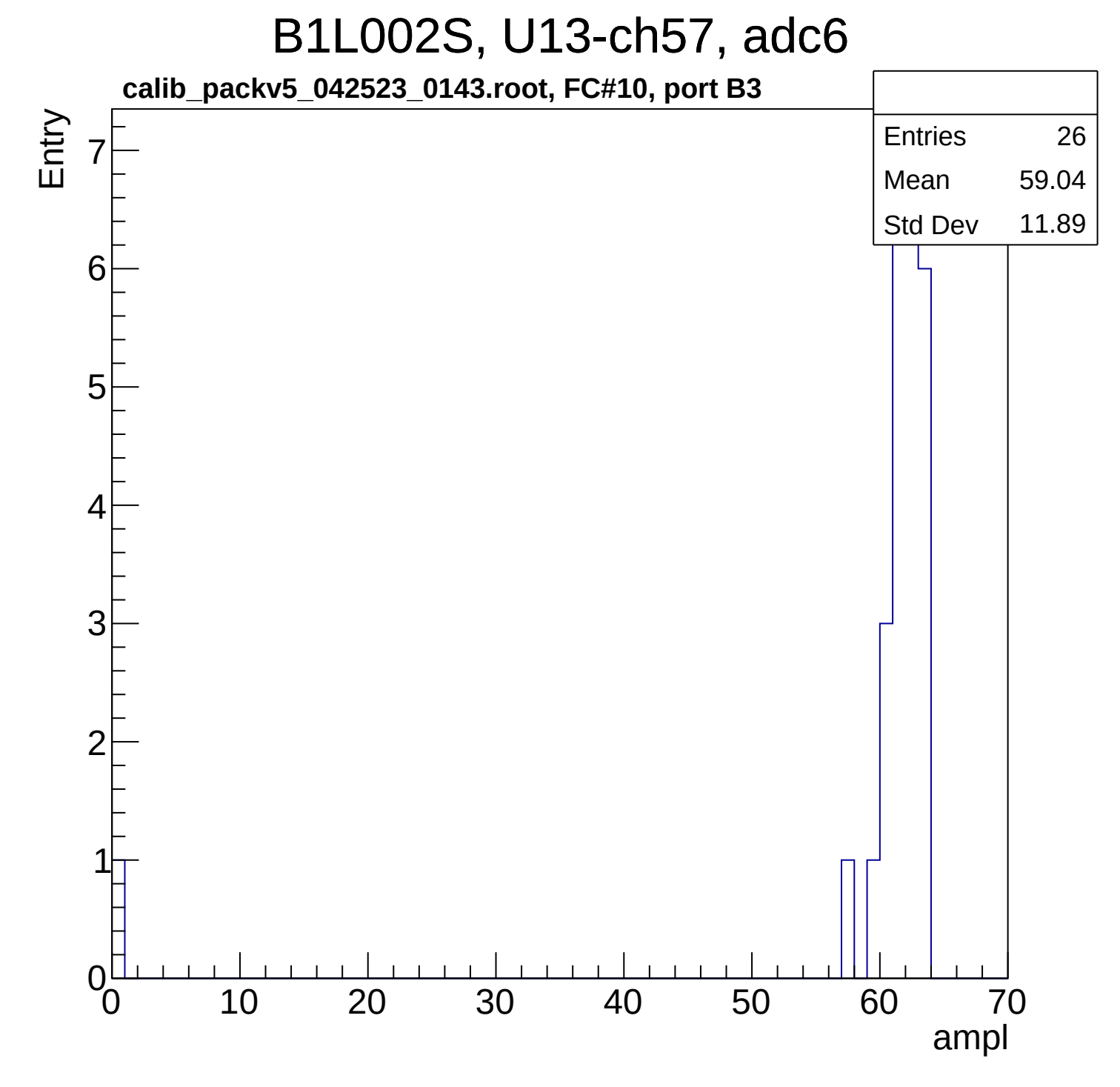
B1L002S, U13-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.04
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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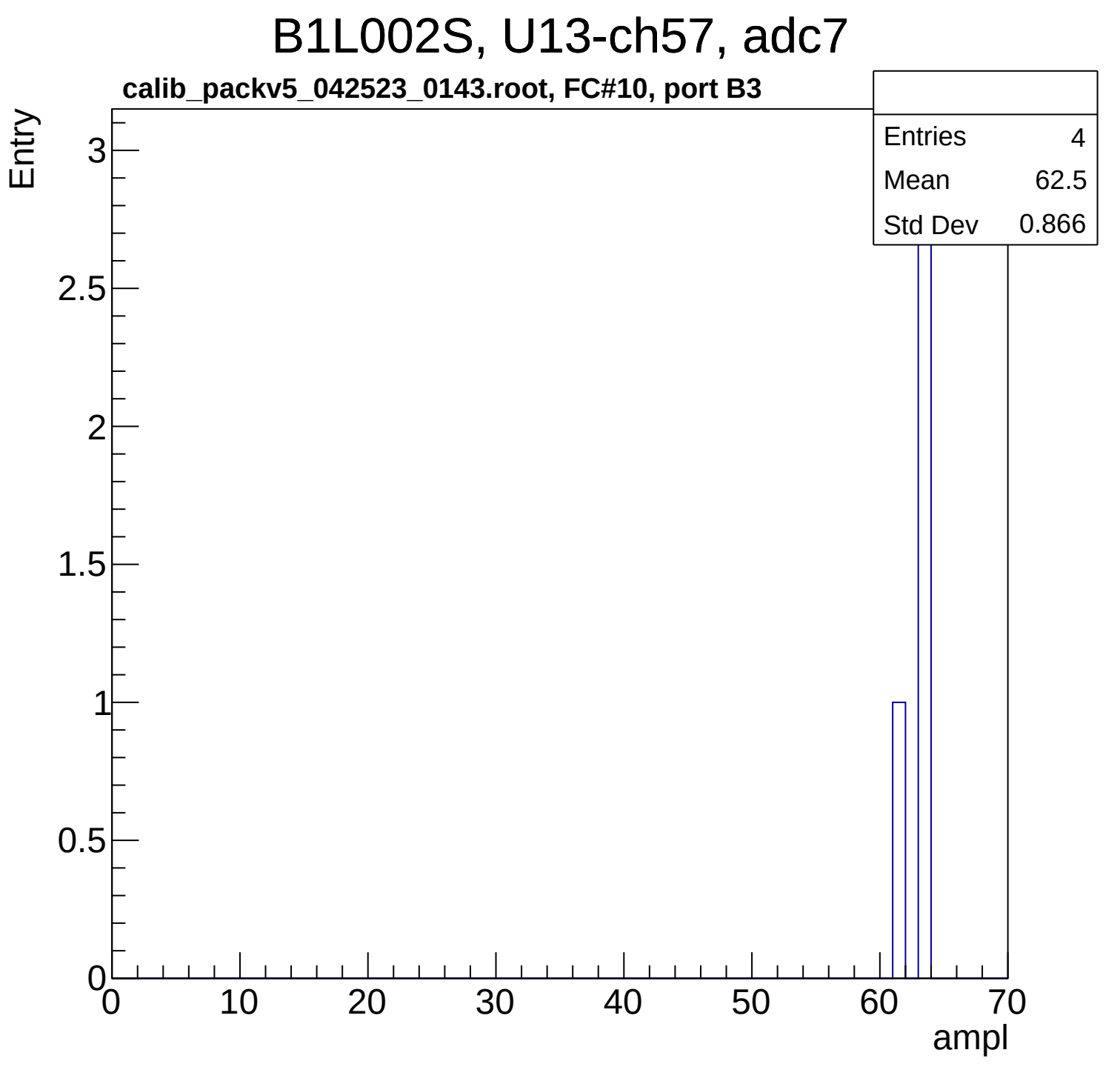
B1L002S, U13-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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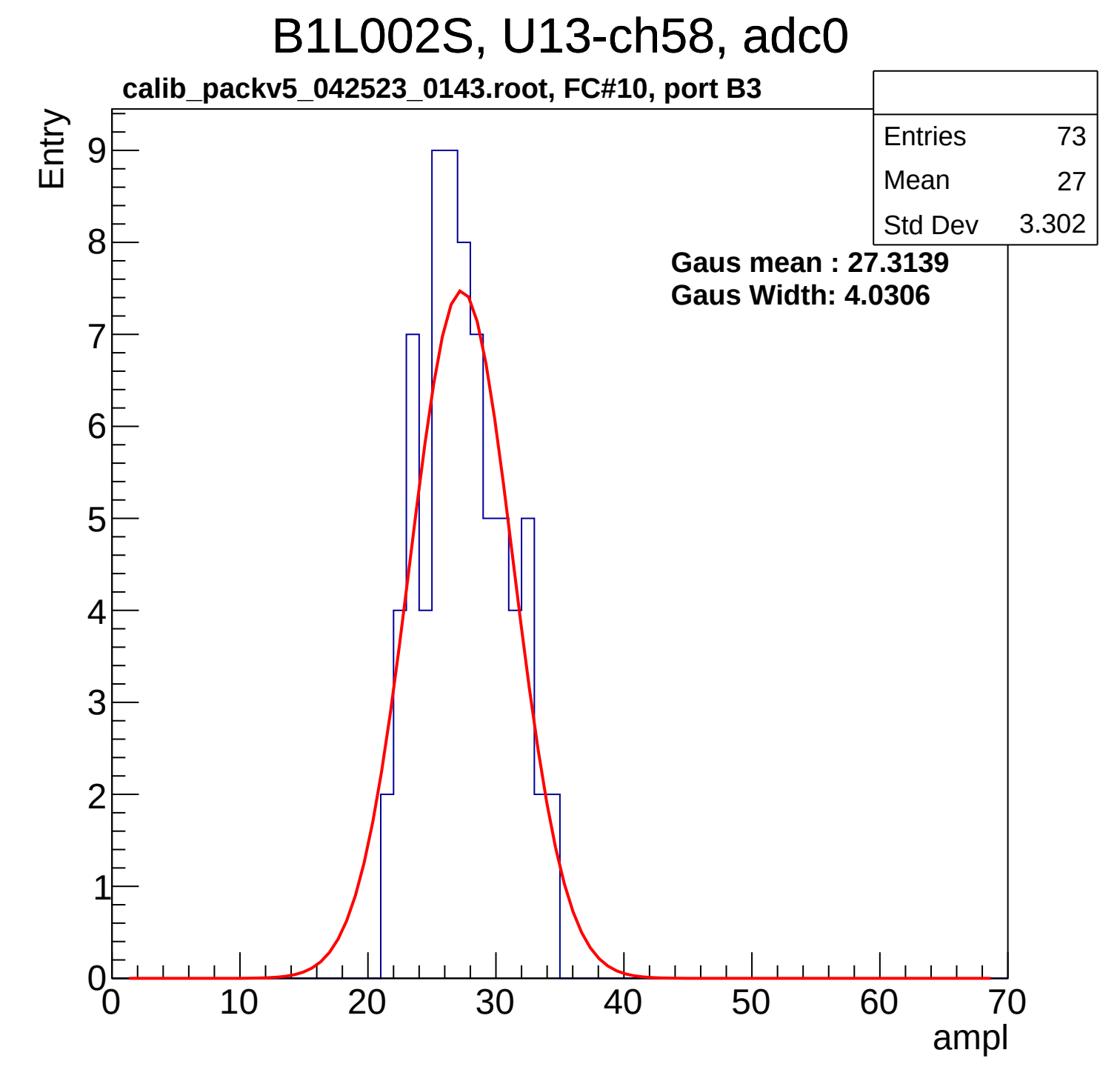
B1L002S, U13-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27
Std Dev 3.302
Gaus mean : 27.3139
Gaus Width: 4.0306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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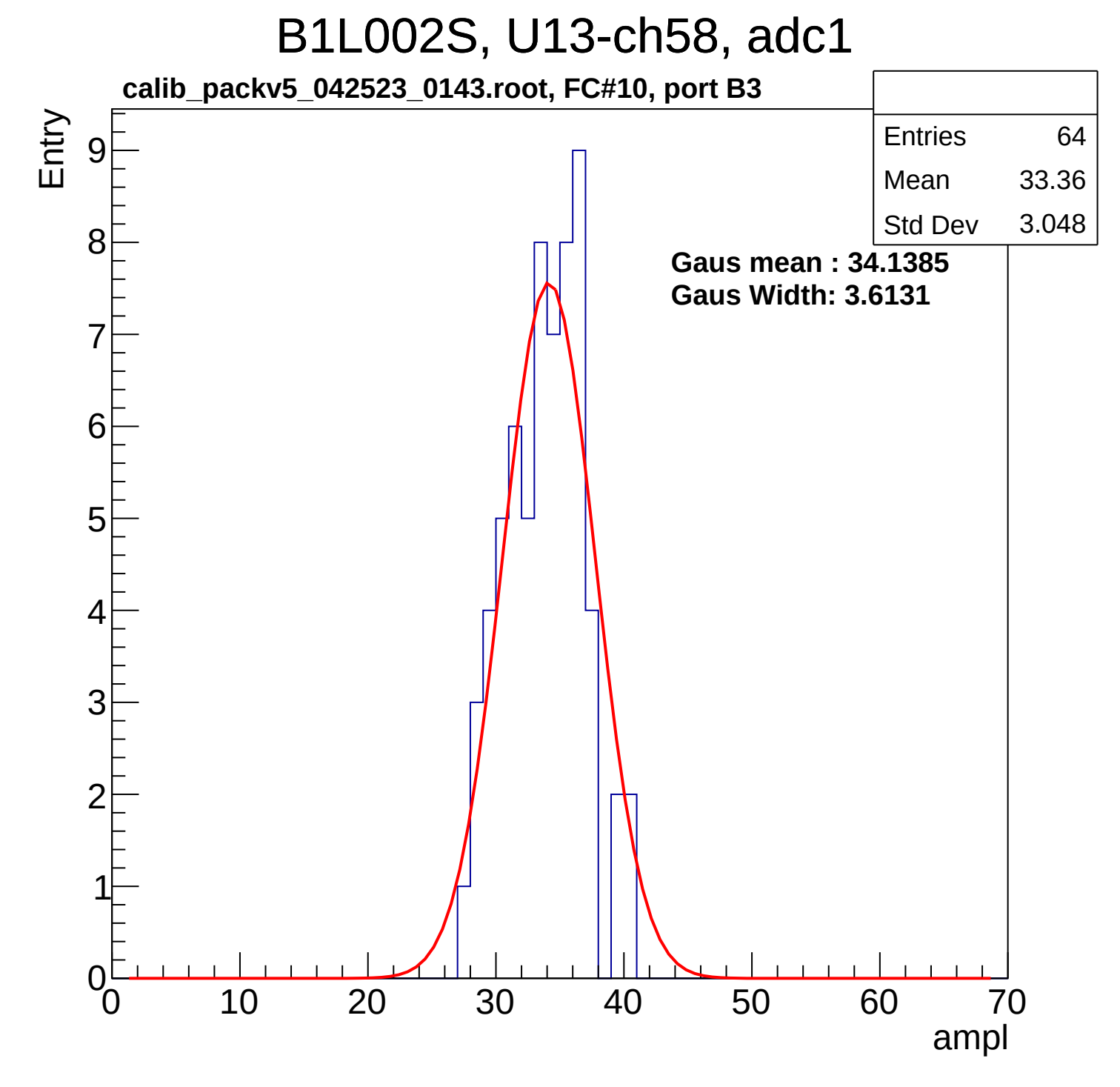
B1L002S, U13-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 33.36
Std Dev 3.048
Gaus mean : 34.1385
Gaus Width: 3.6131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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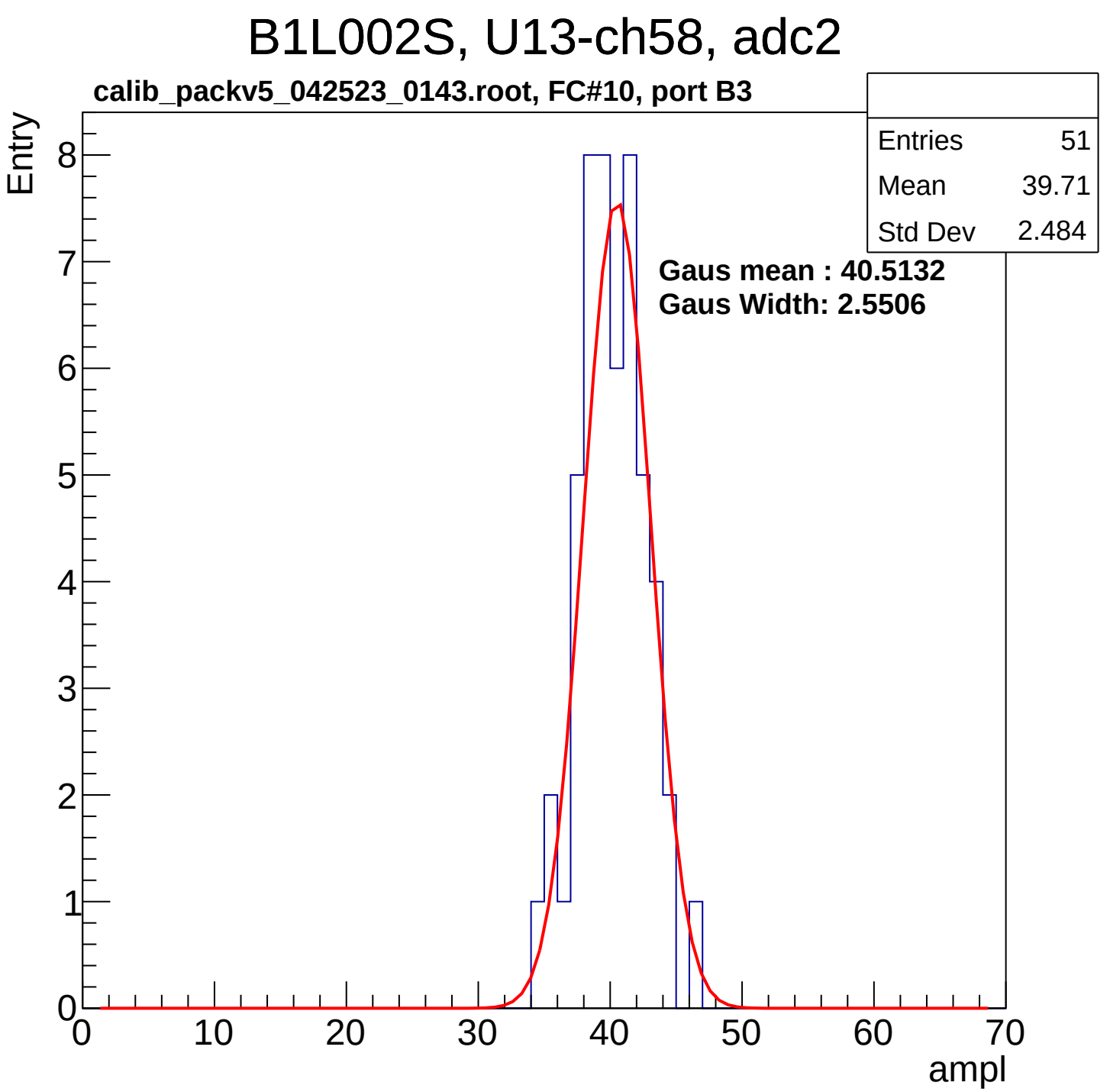
B1L002S, U13-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 39.71
Std Dev 2.484
Gaus mean : 40.5132
Gaus Width: 2.5506
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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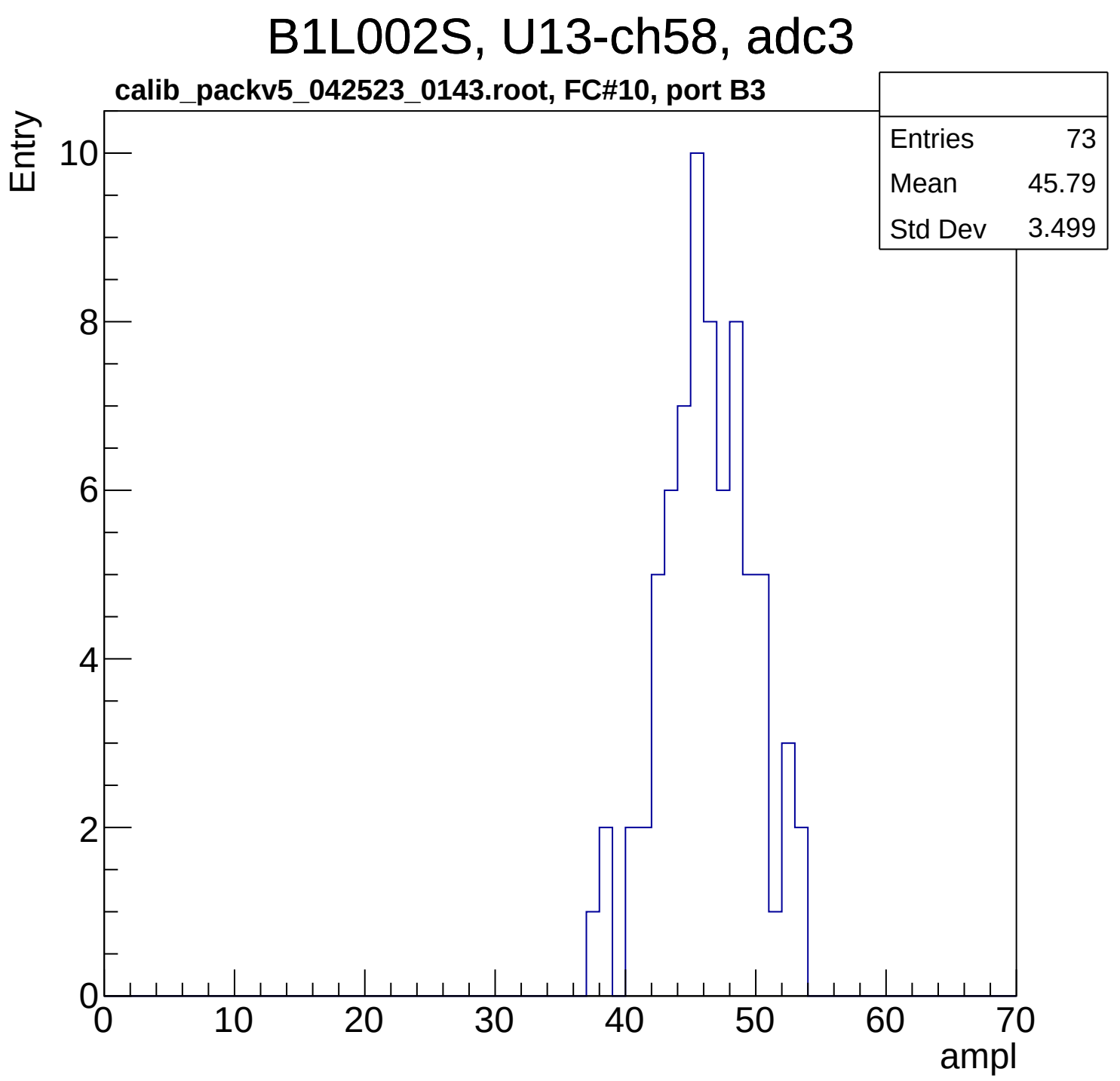
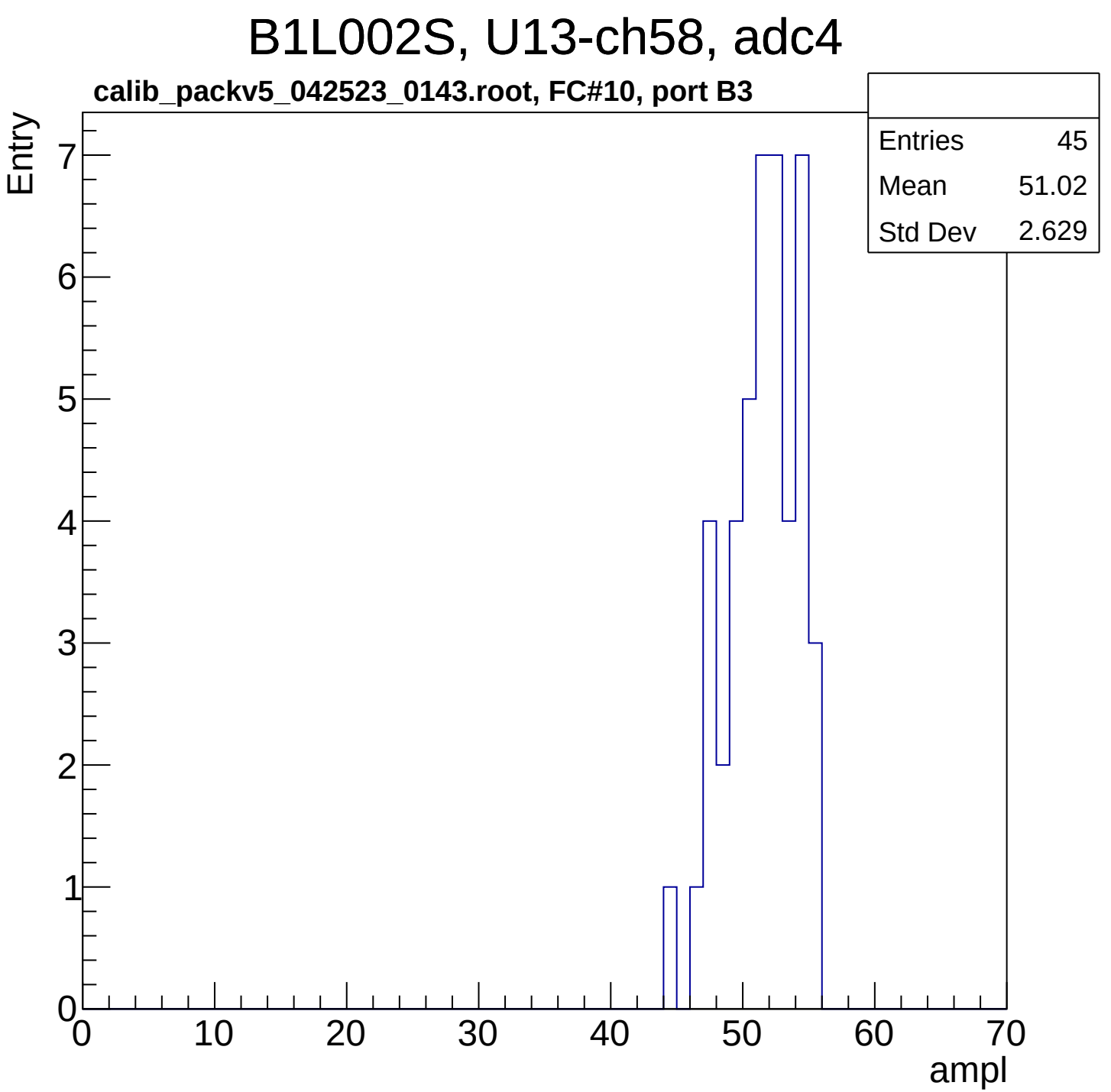

B1L002S, U13-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.79
Std Dev 3.499
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 51.02
Std Dev 2.629
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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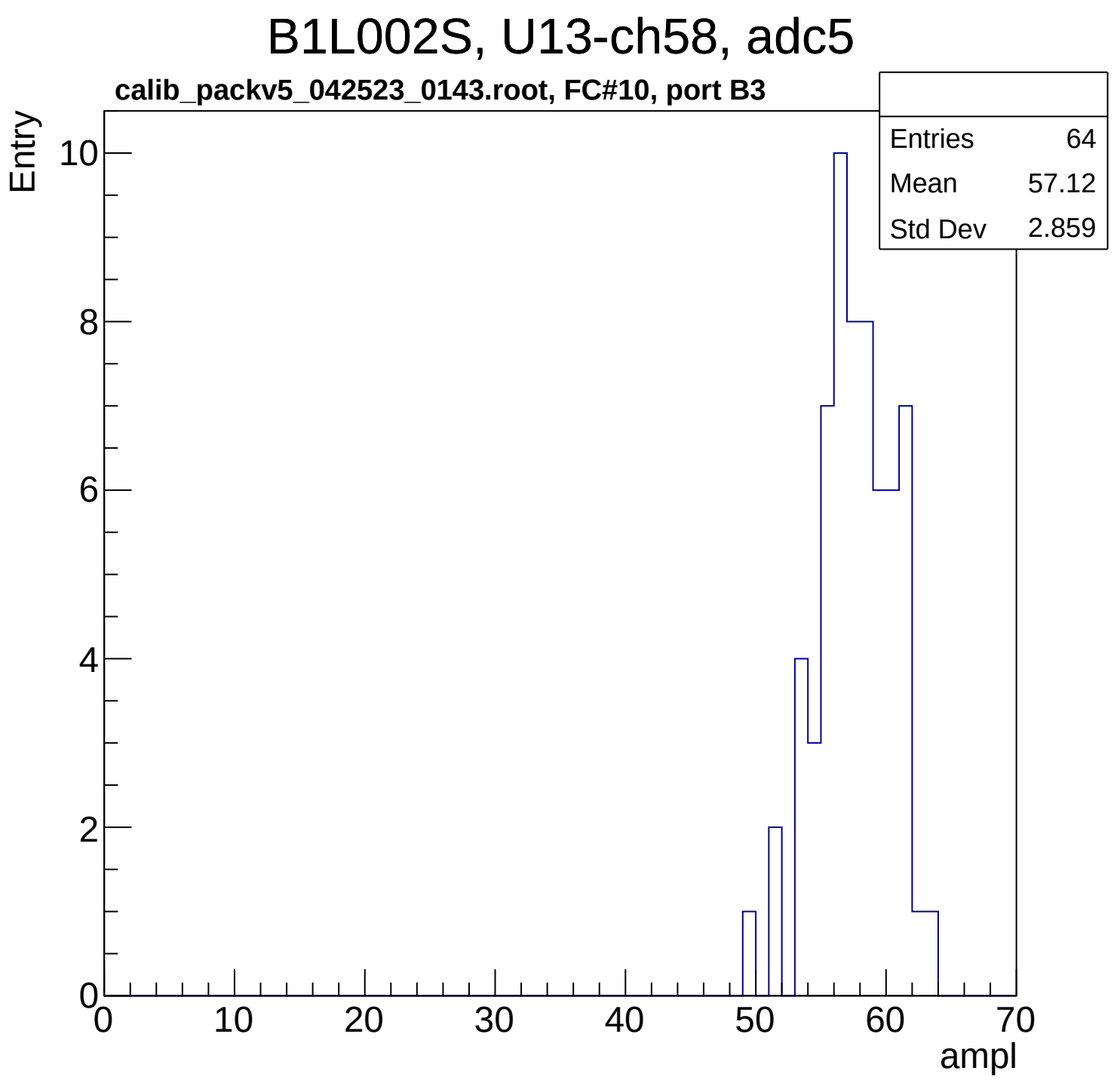
B1L002S, U13-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.12
Std Dev 2.859
Entry
ampl
0
2
4
6
8
10
0
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40
50
60
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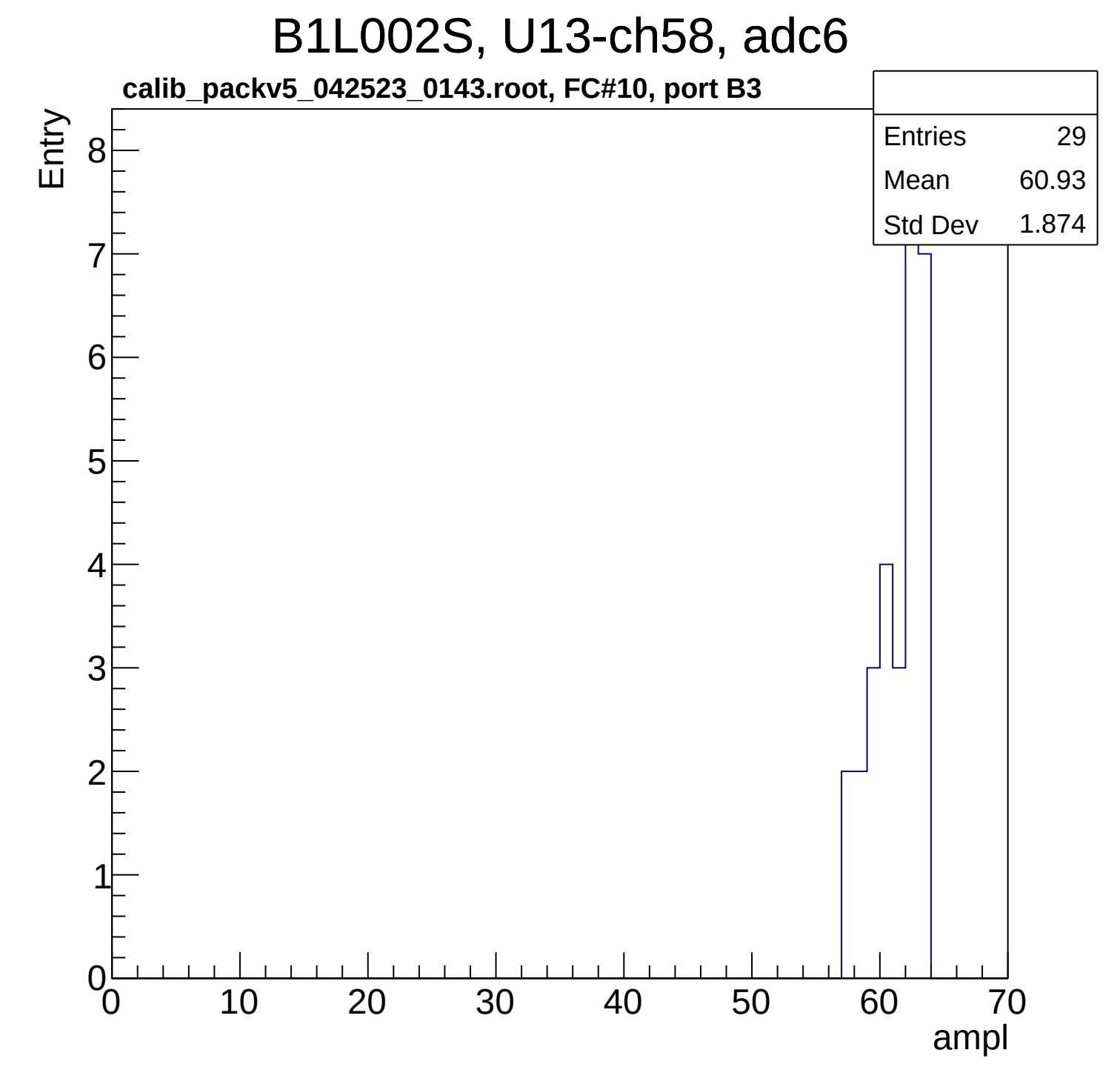
B1L002S, U13-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 60.93
Std Dev 1.874
Entry
ampl
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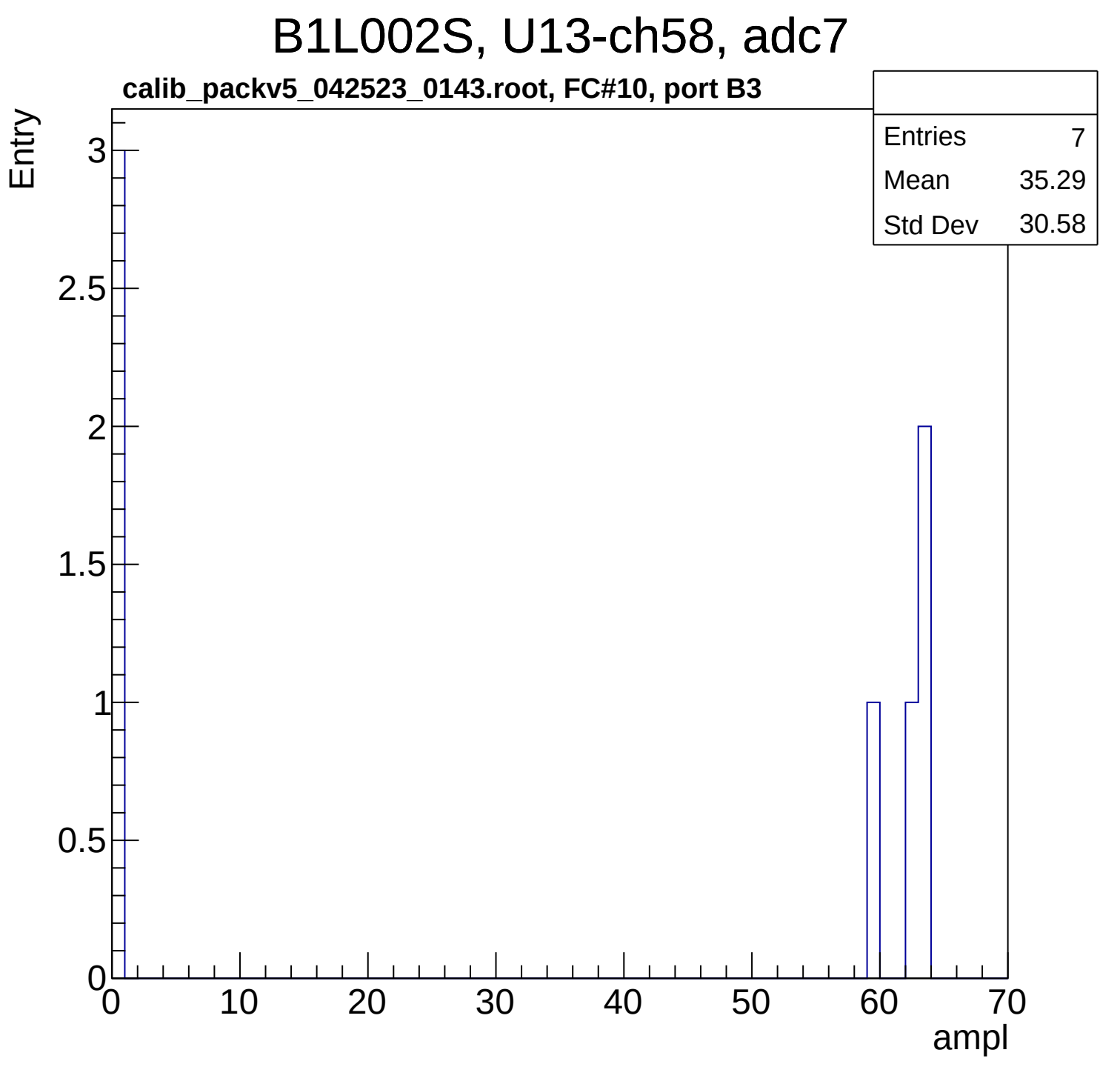
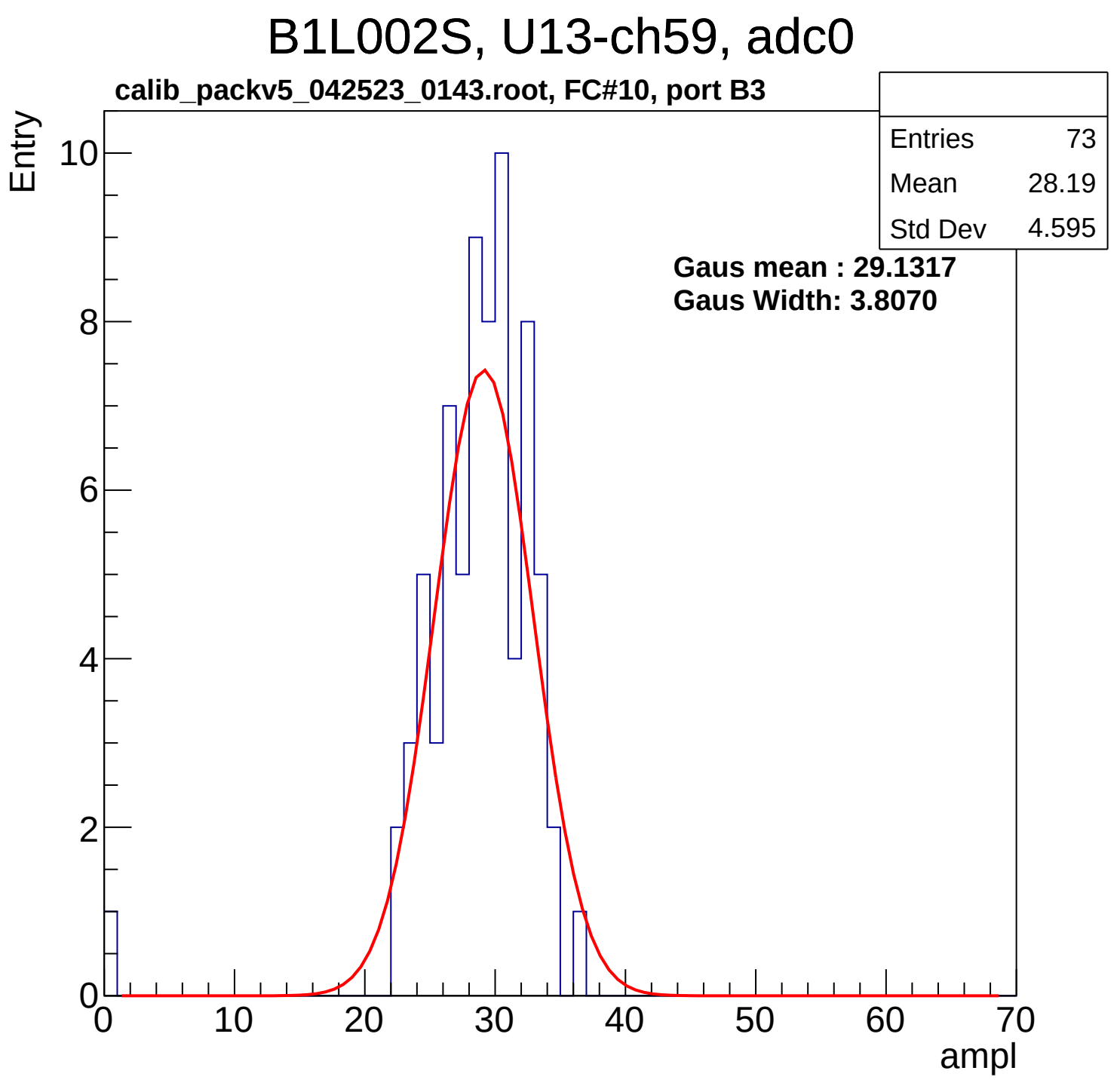
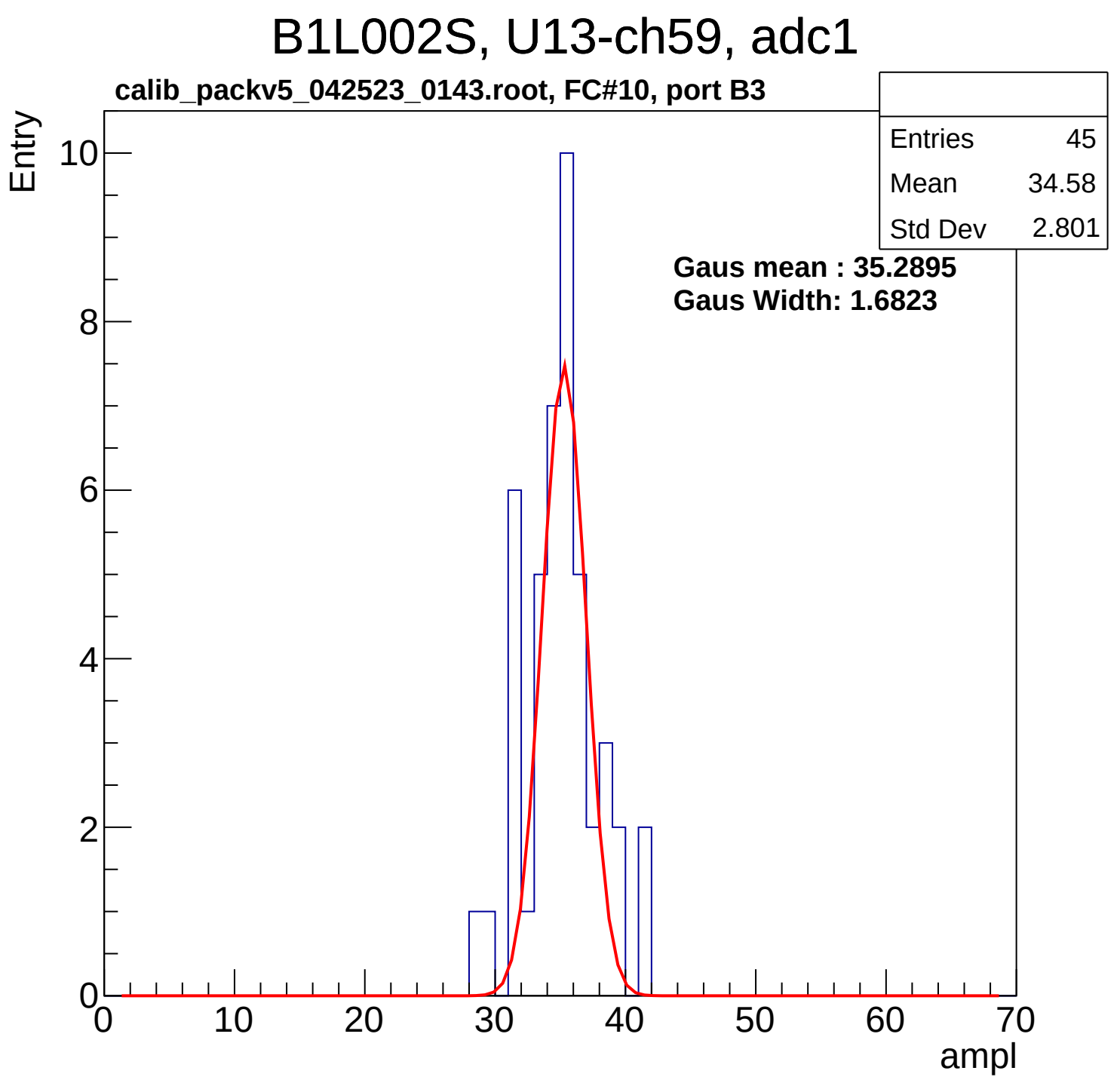
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 35.29
Std Dev 30.58
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.19
Std Dev 4.595
Gaus mean : 29.1317
Gaus Width: 3.8070
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 34.58
Std Dev 2.801
Gaus mean : 35.2895
Gaus Width: 1.6823
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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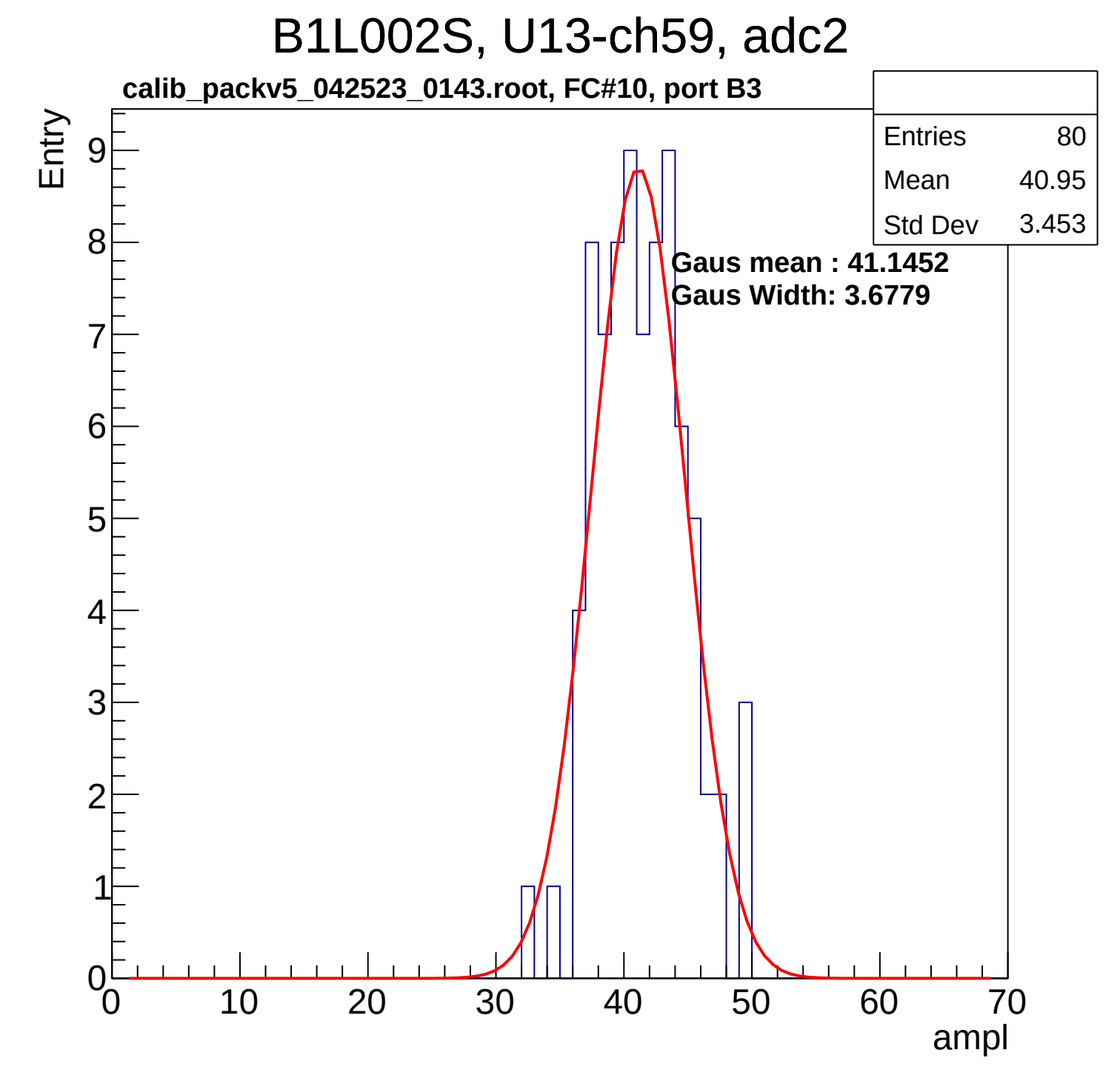
B1L002S, U13-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 40.95
Std Dev 3.453
Gaus mean : 41.1452
Gaus Width: 3.6779
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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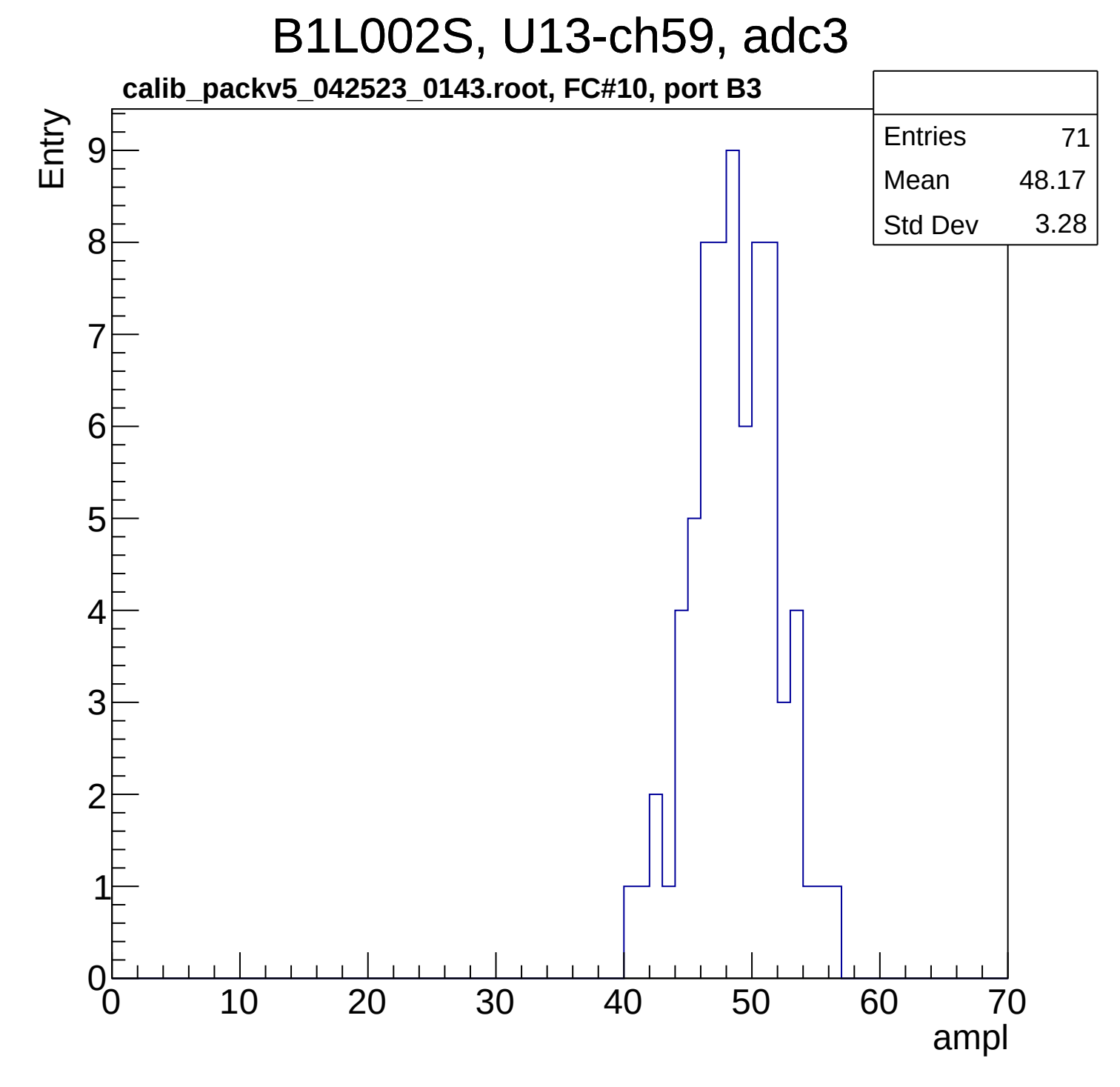
B1L002S, U13-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 48.17
Std Dev 3.28
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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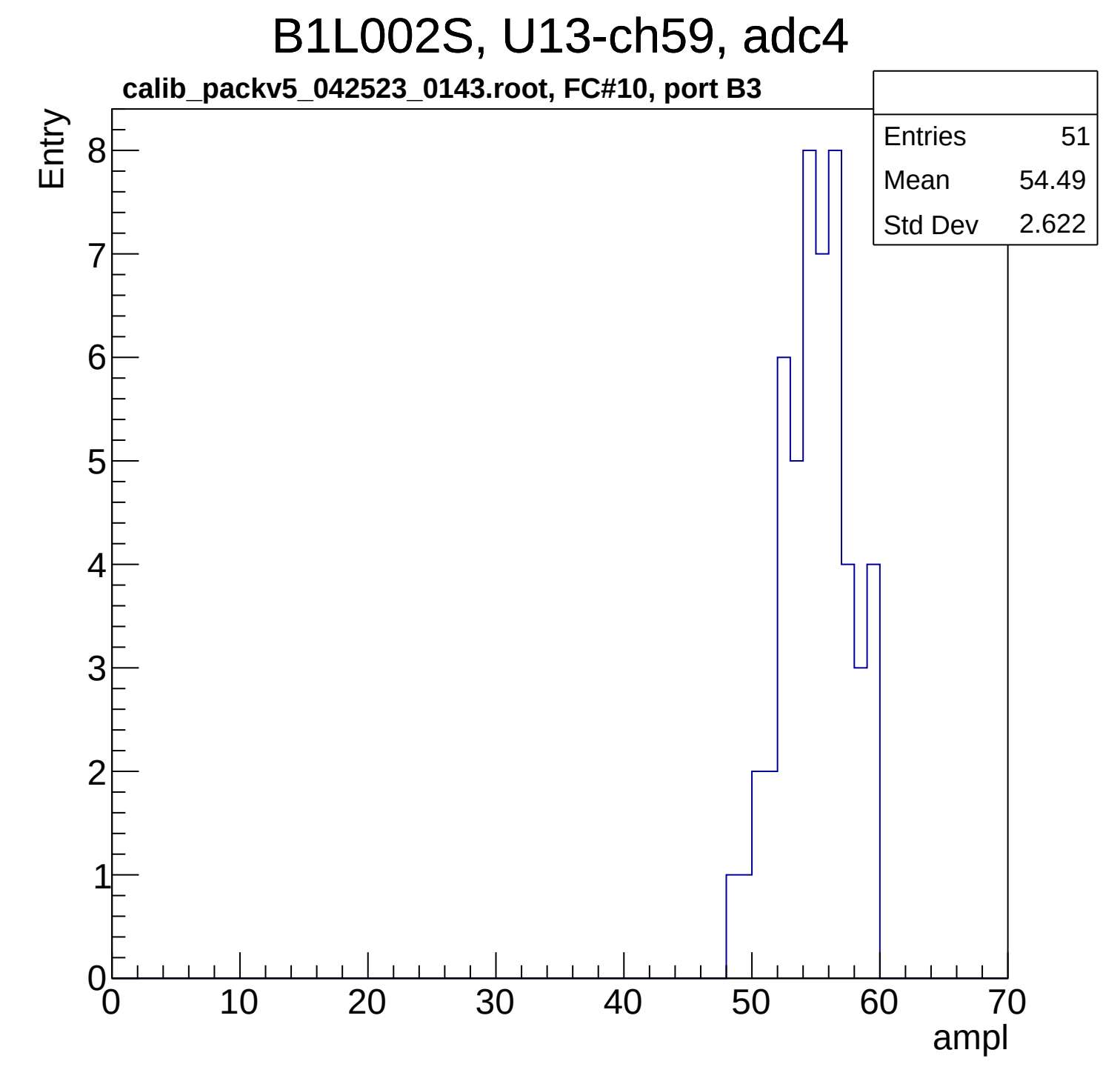
B1L002S, U13-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.49
Std Dev 2.622
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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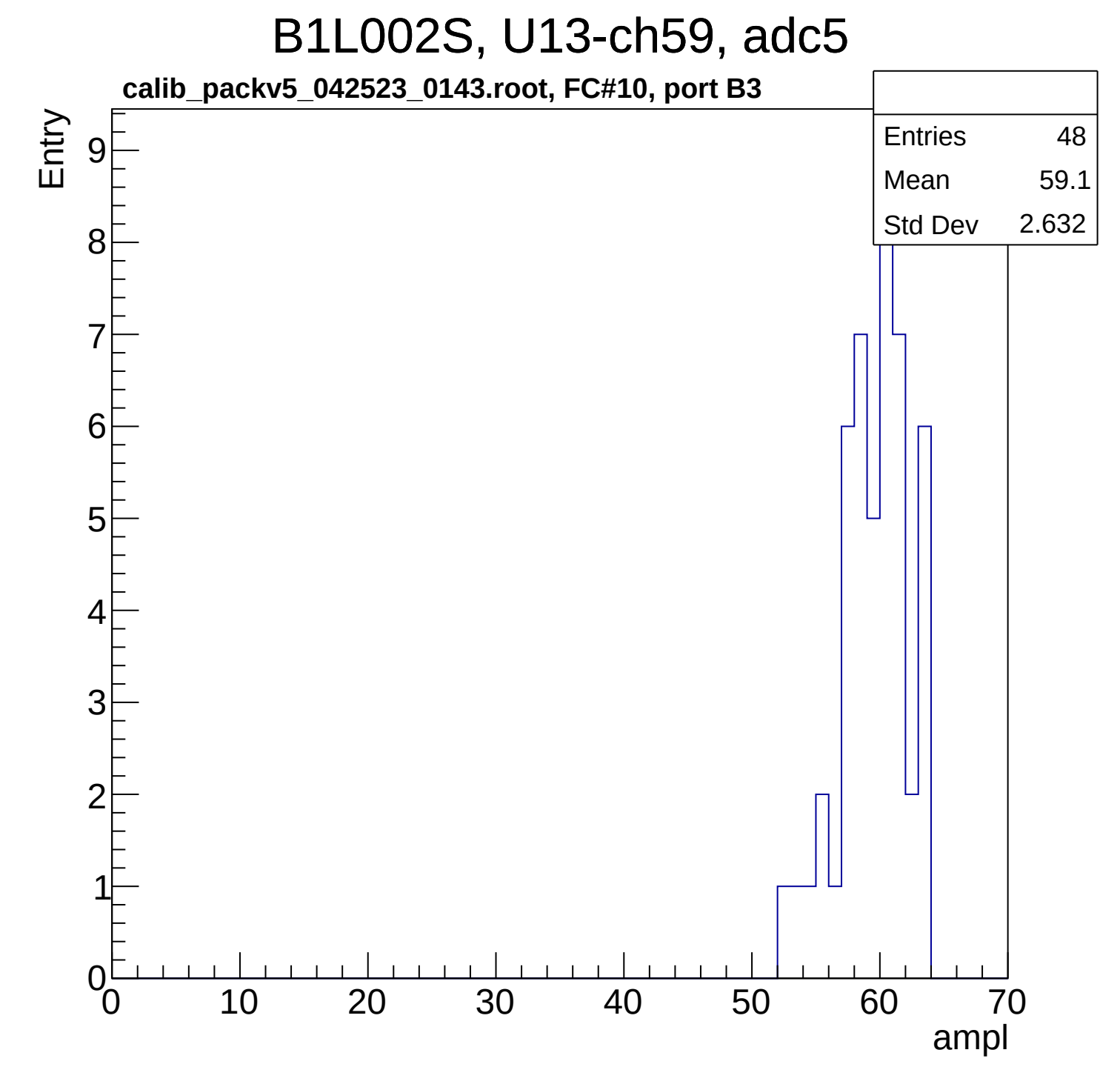
B1L002S, U13-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.1
Std Dev 2.632
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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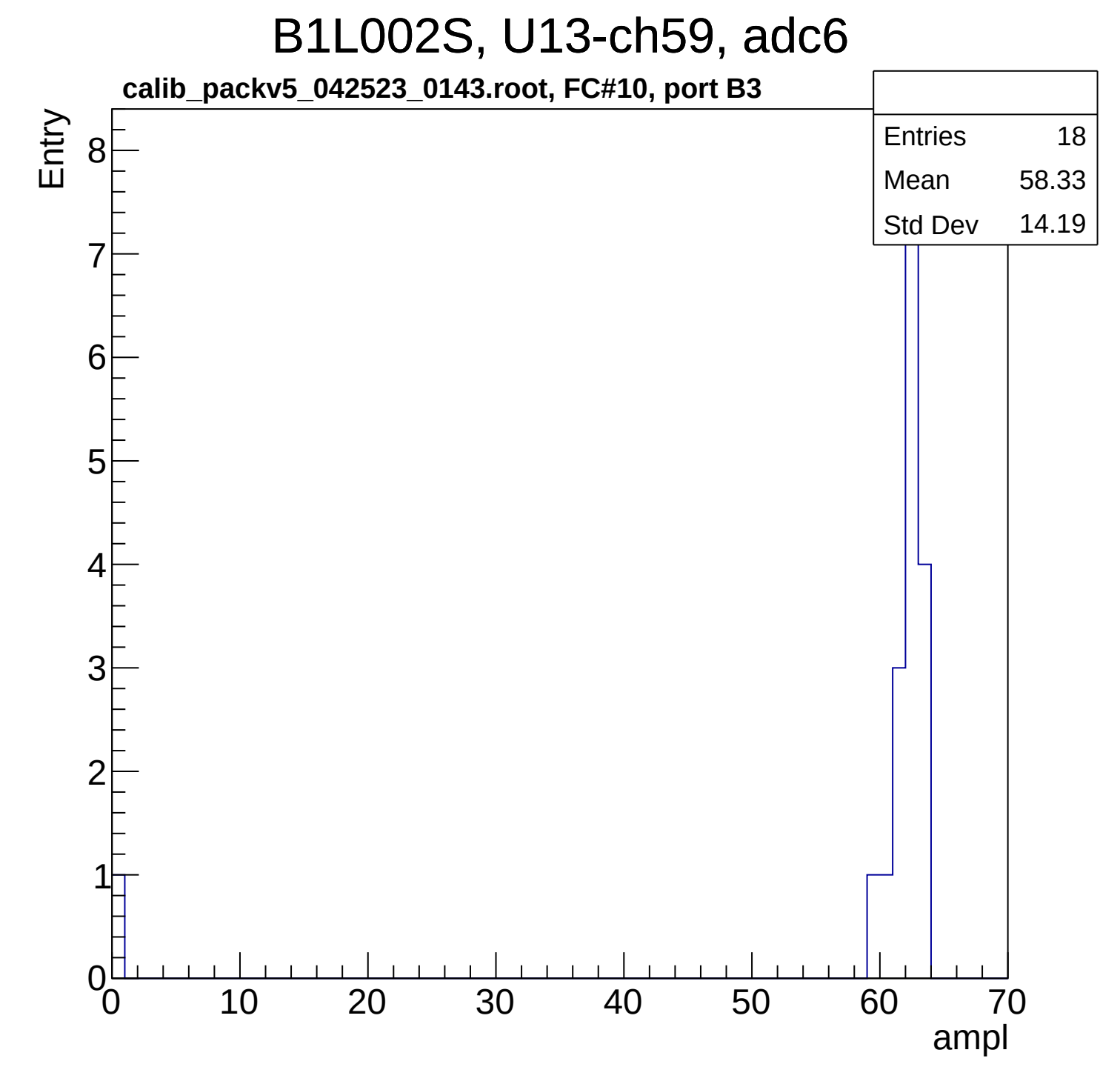
40
50
60
70


B1L002S, U13-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.33
Std Dev 14.19
Entry
ampl
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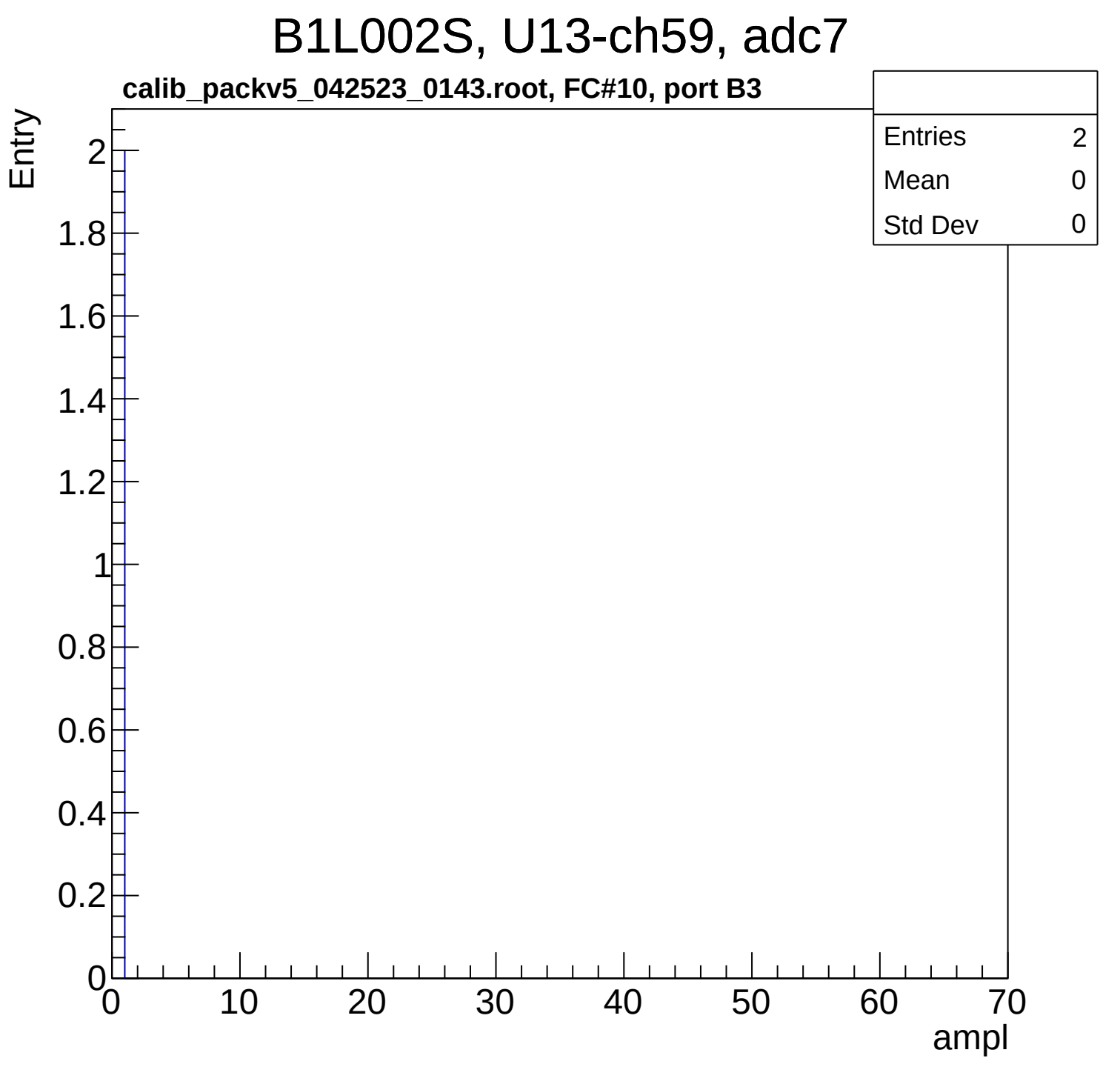
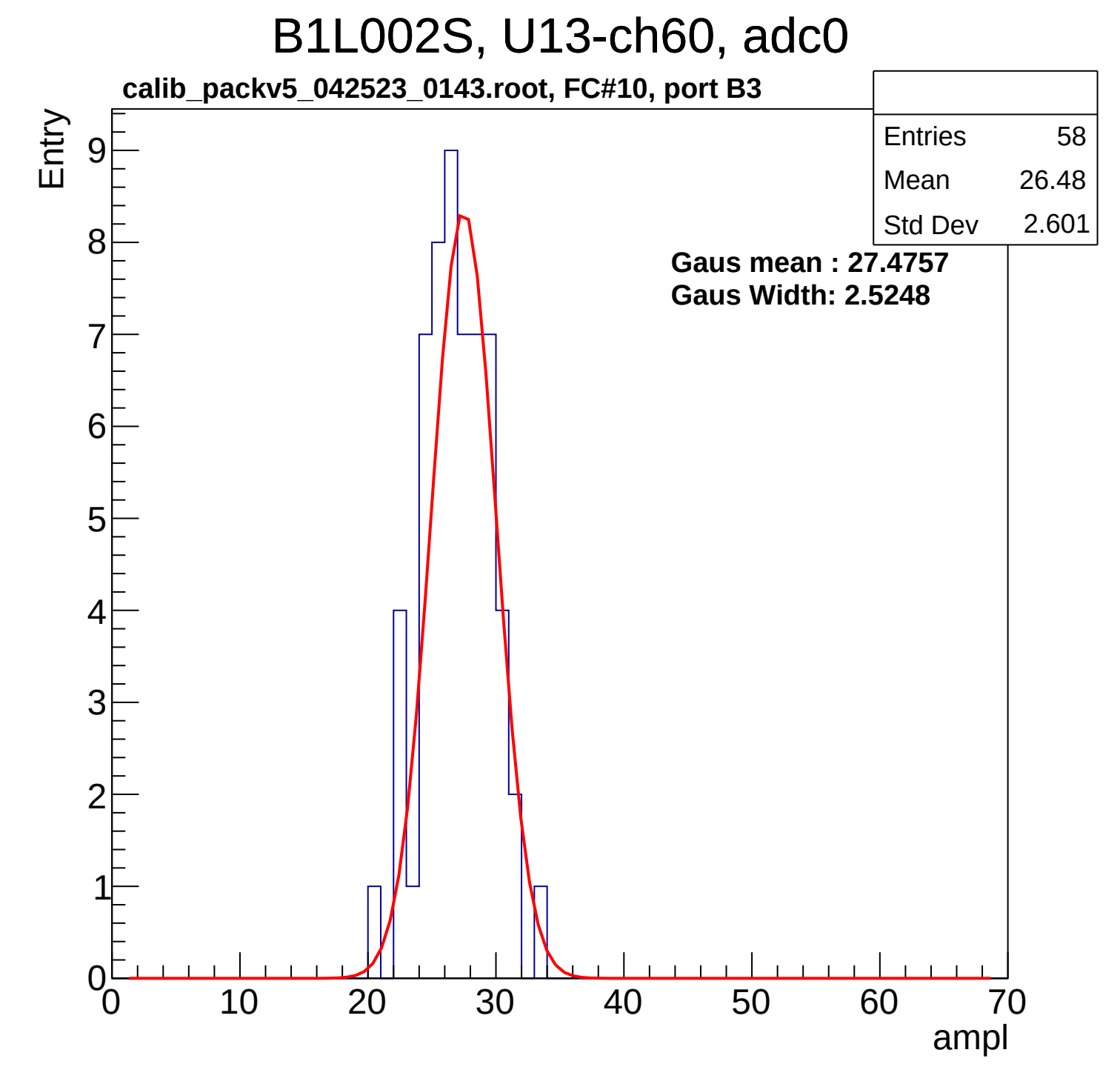
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.48
Std Dev 2.601
Gaus mean : 27.4757
Gaus Width: 2.5248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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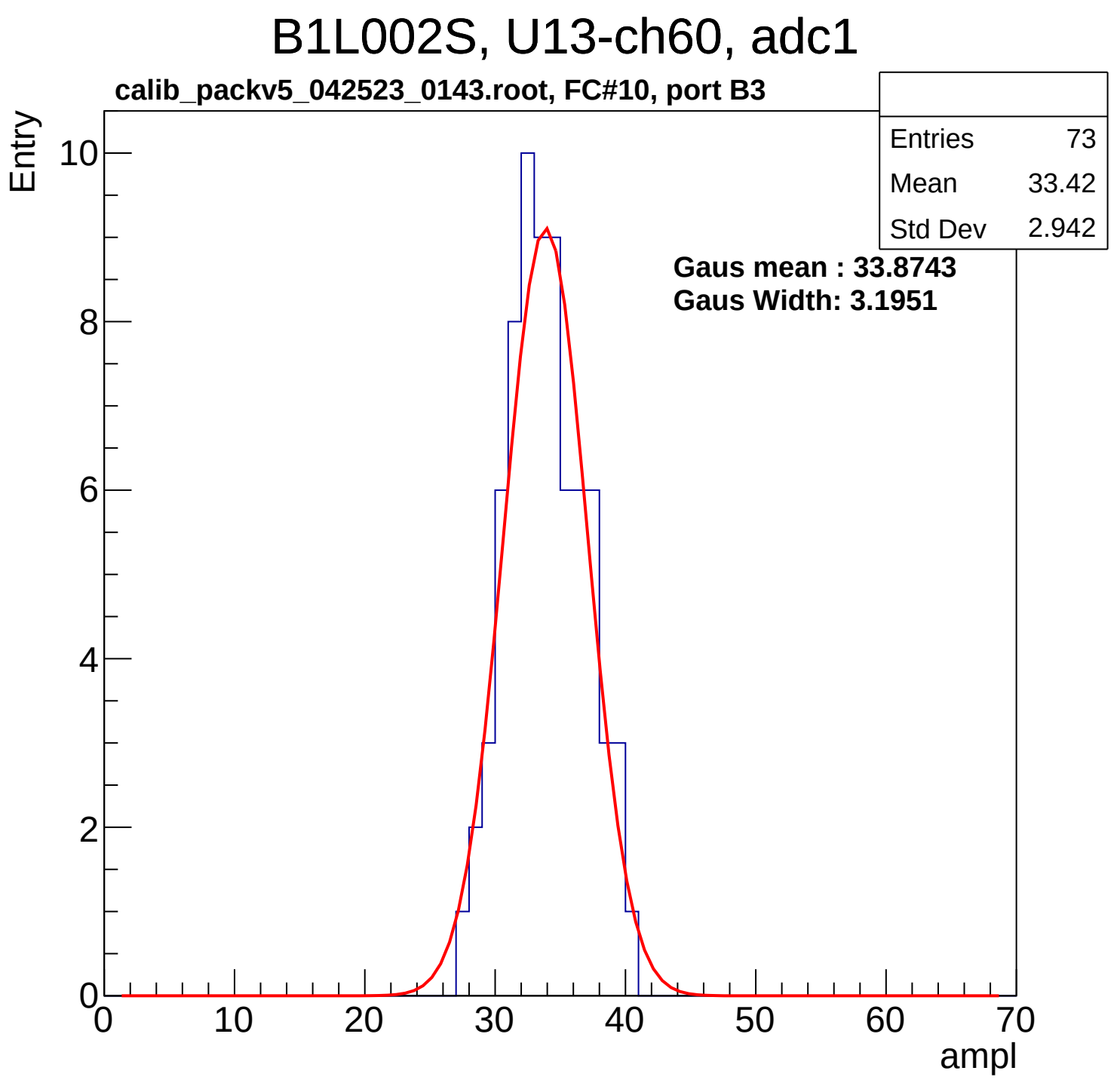
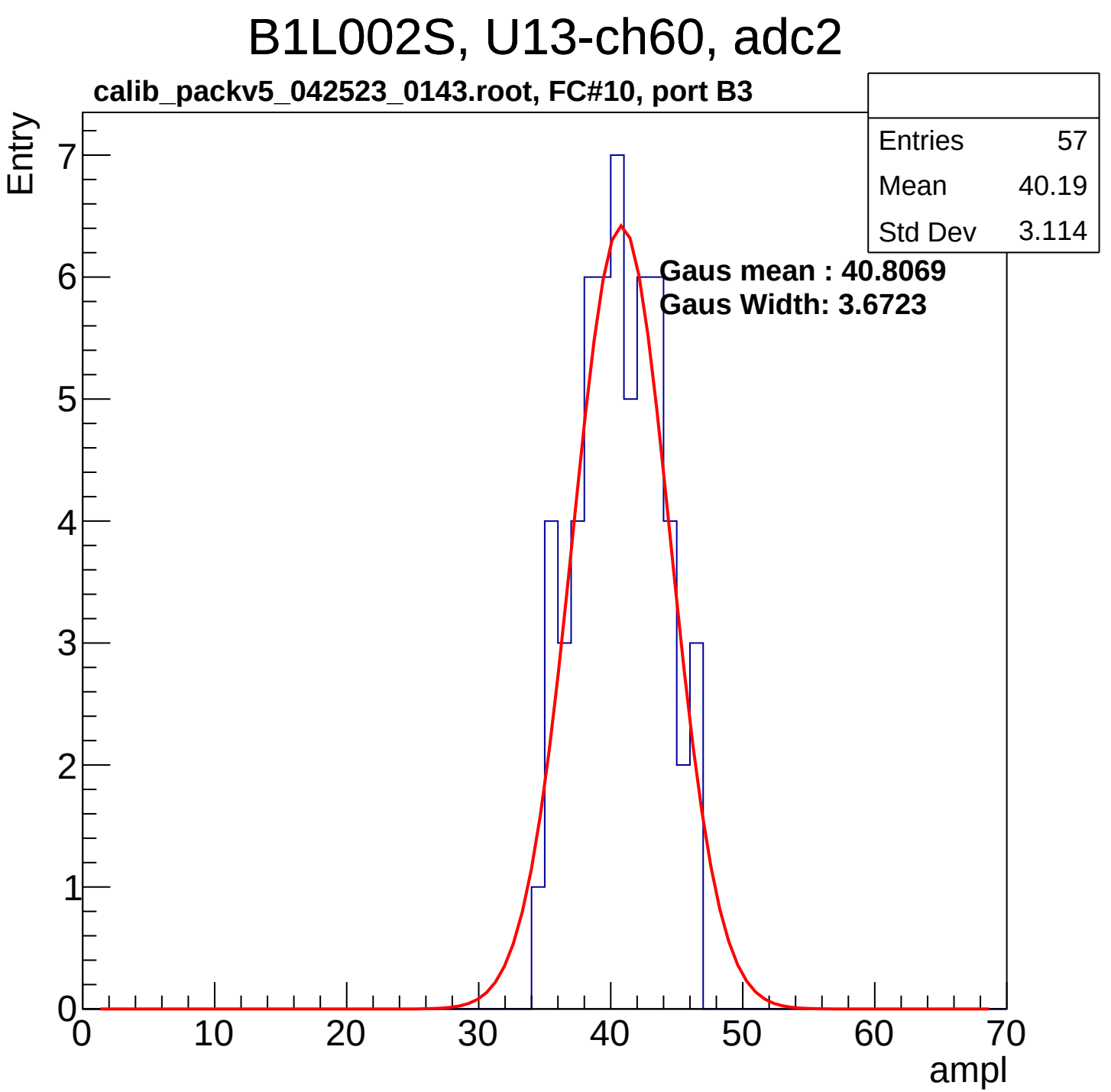

B1L002S, U13-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 33.42
Std Dev 2.942
Gaus mean : 33.8743
Gaus Width: 3.1951
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.19
Std Dev 3.114
Gaus mean : 40.8069
Gaus Width: 3.6723
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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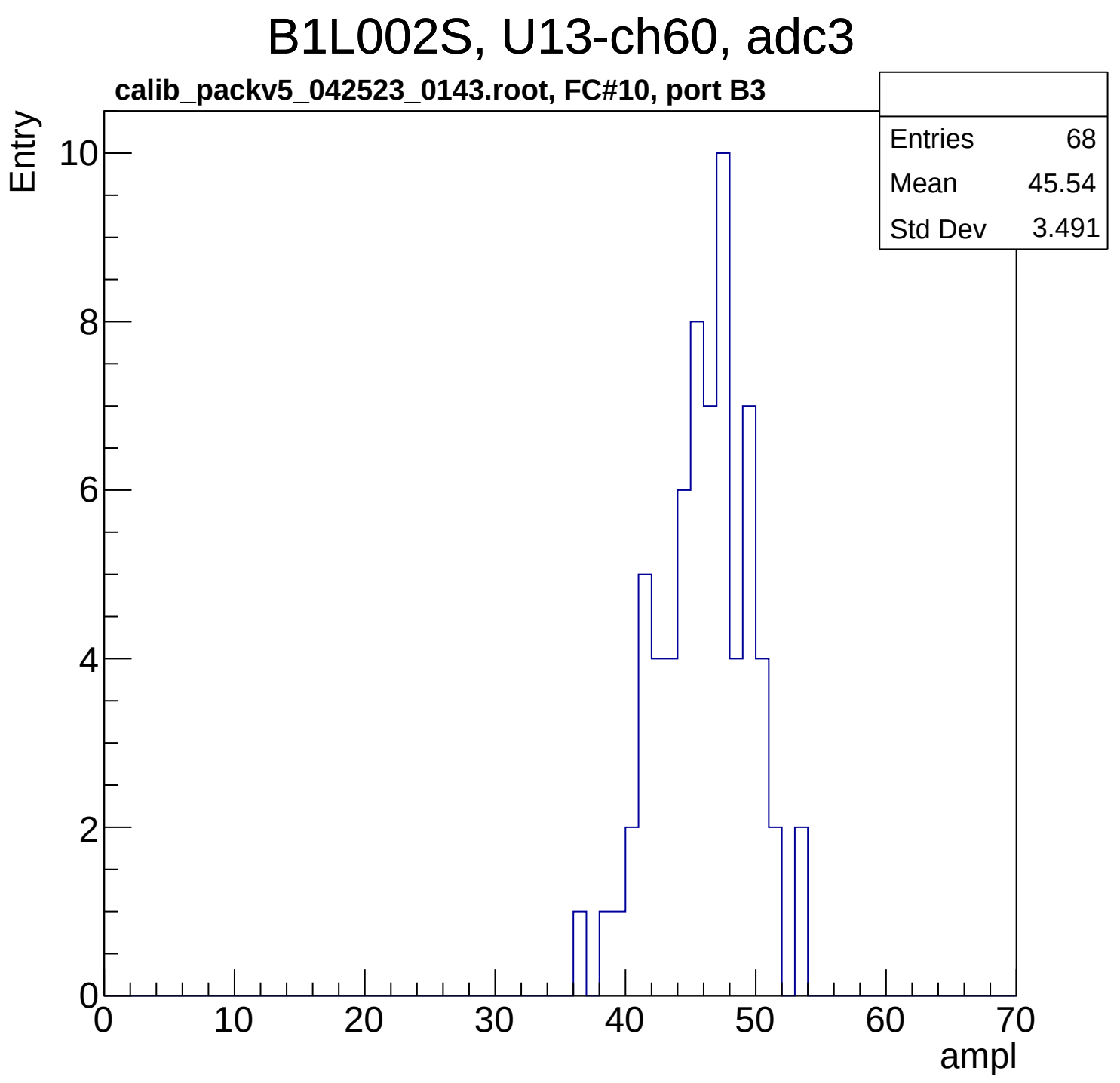
B1L002S, U13-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.54
Std Dev 3.491
Entry
ampl
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2
4
6
8
10
0
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20
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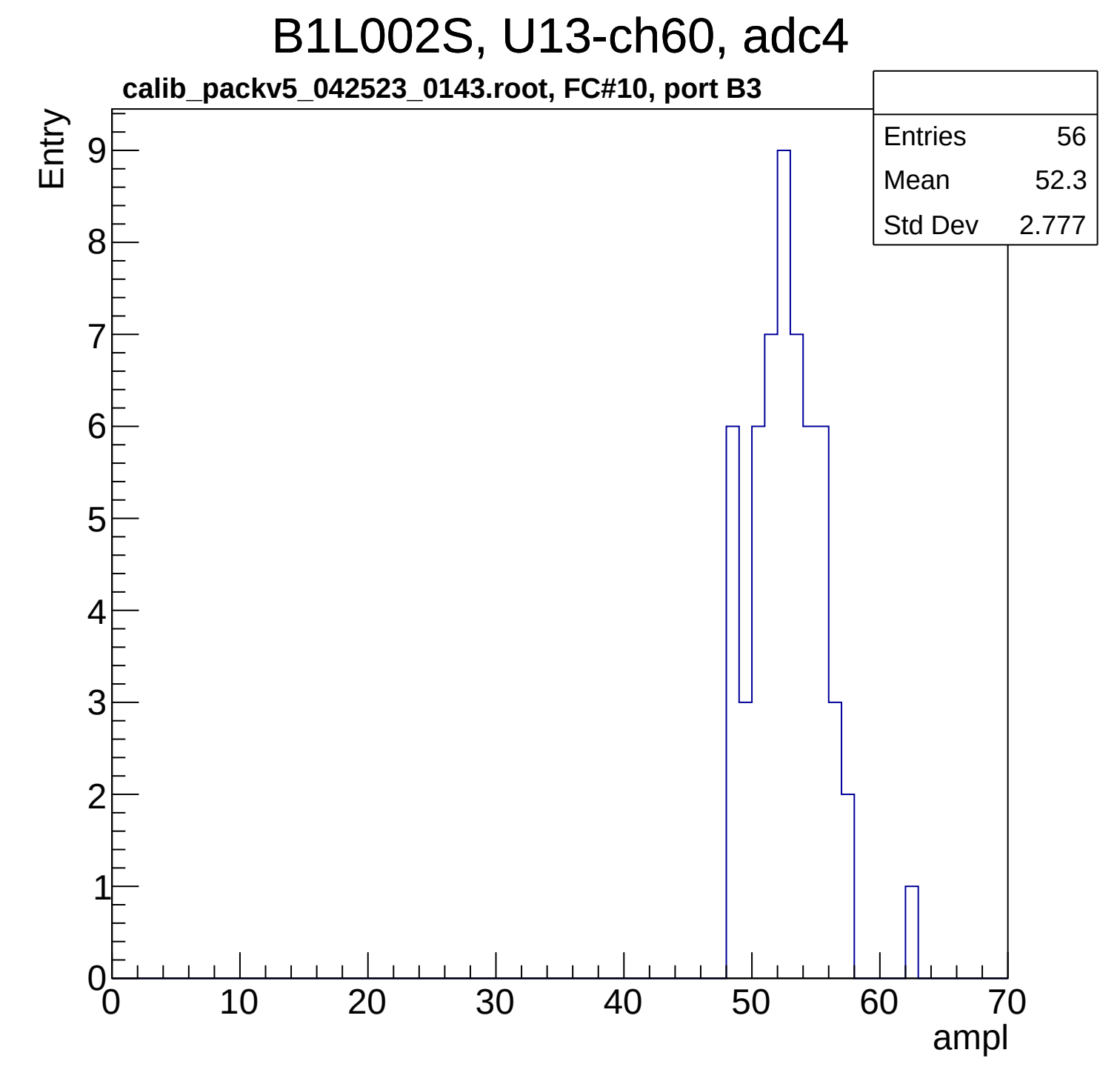
40
50
60
70


B1L002S, U13-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.3
Std Dev 2.777
Entry
ampl
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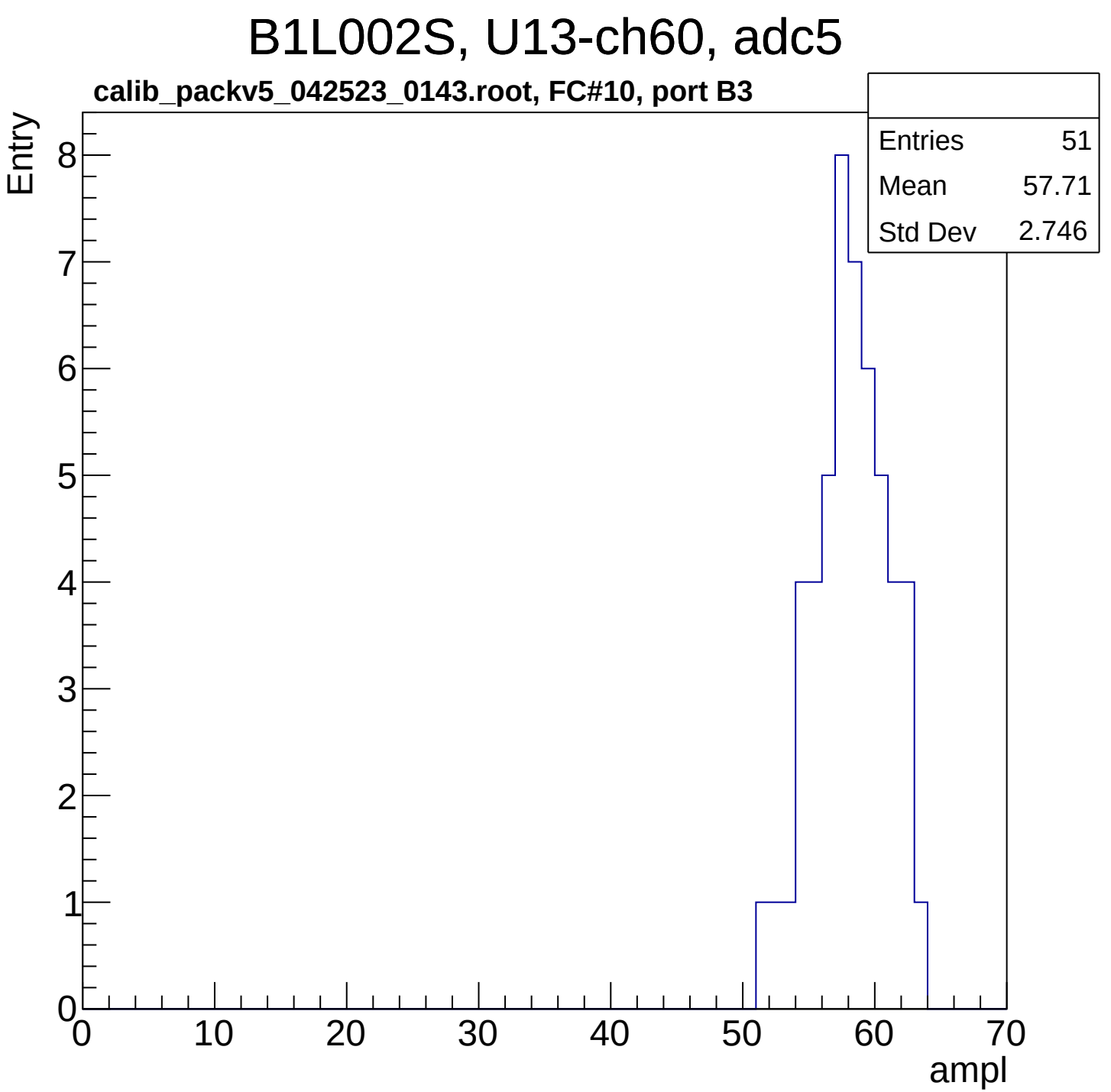
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.71
Std Dev 2.746
Entry
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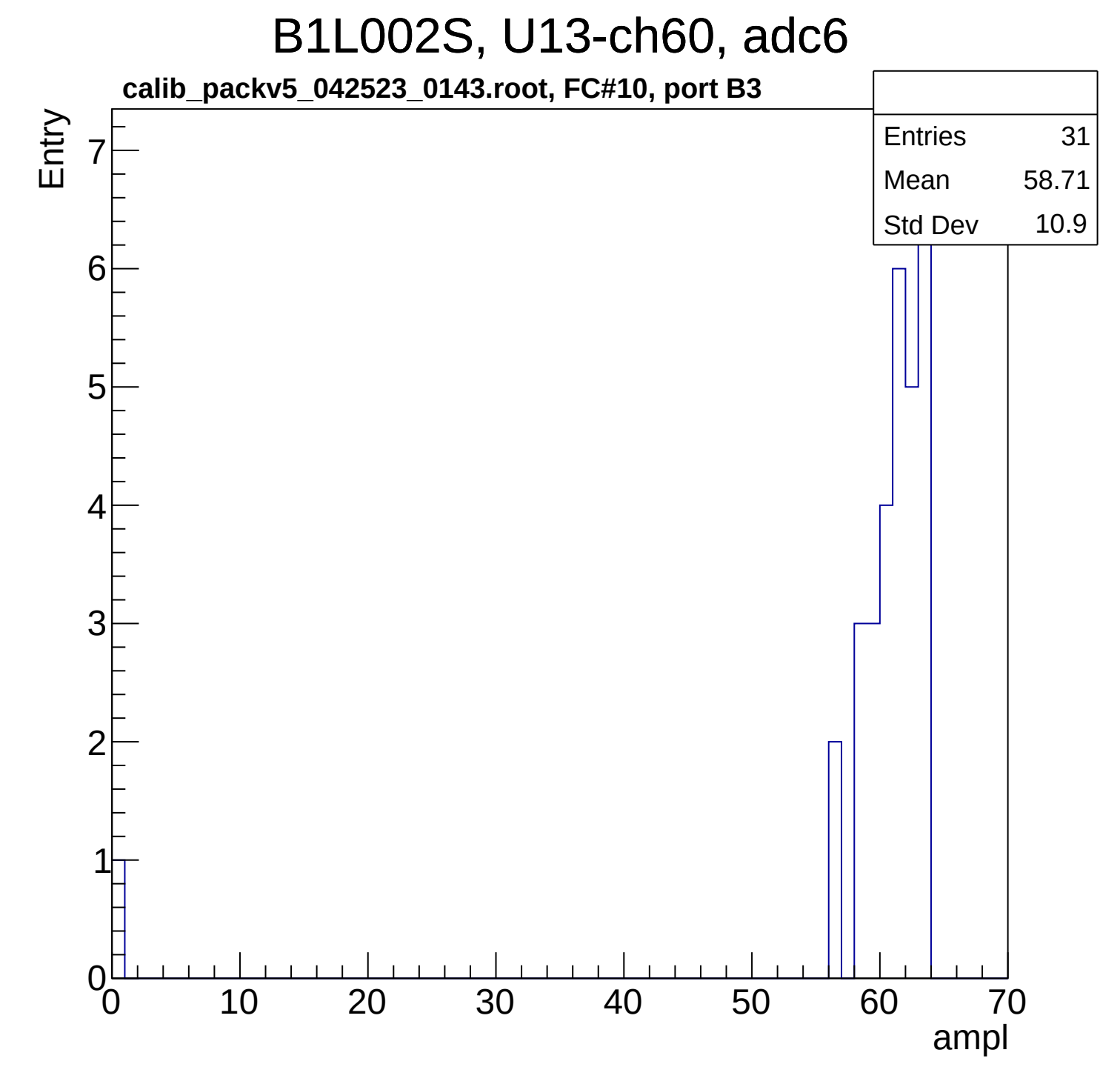
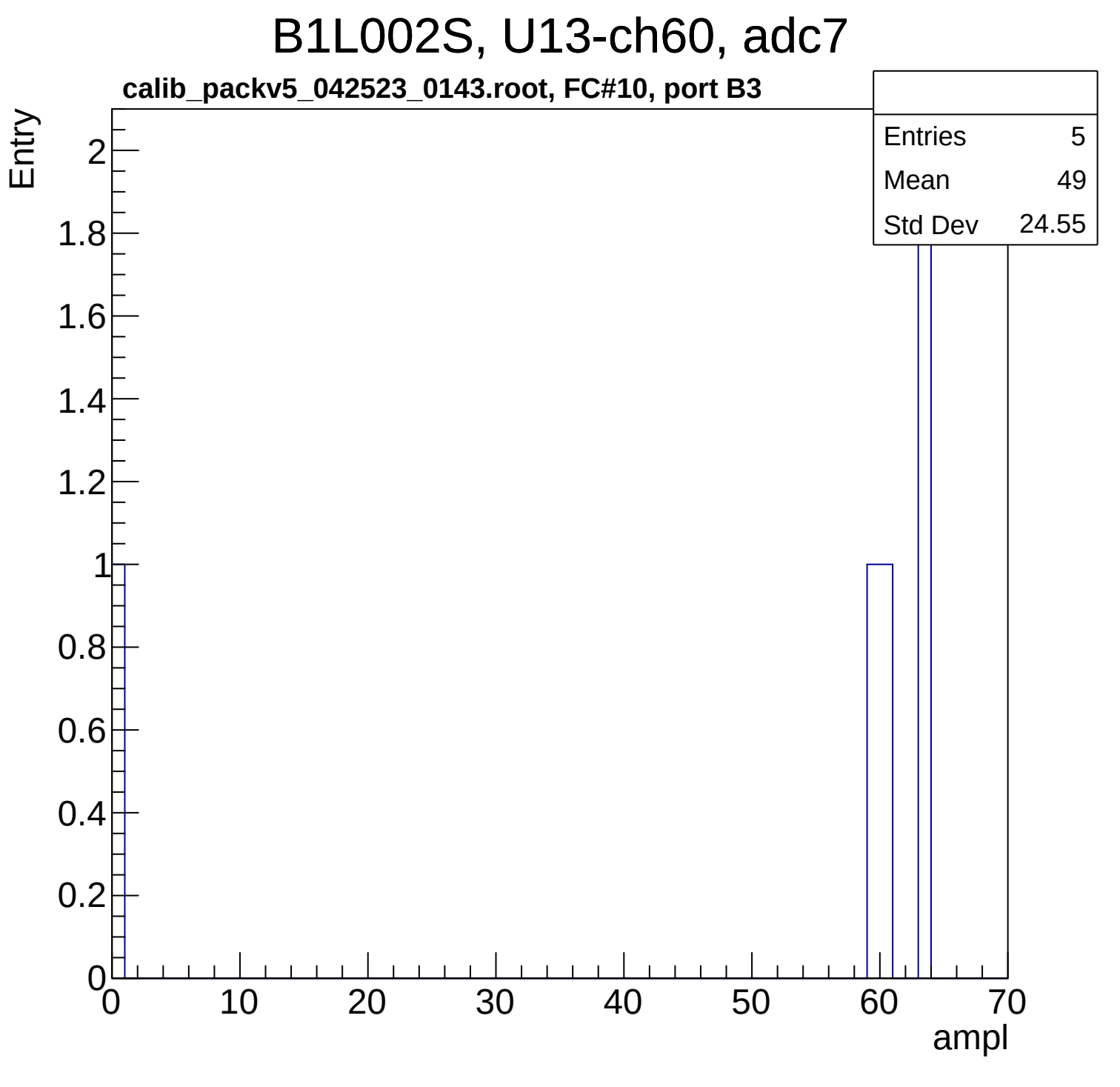
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.71
Std Dev 10.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49
Std Dev 24.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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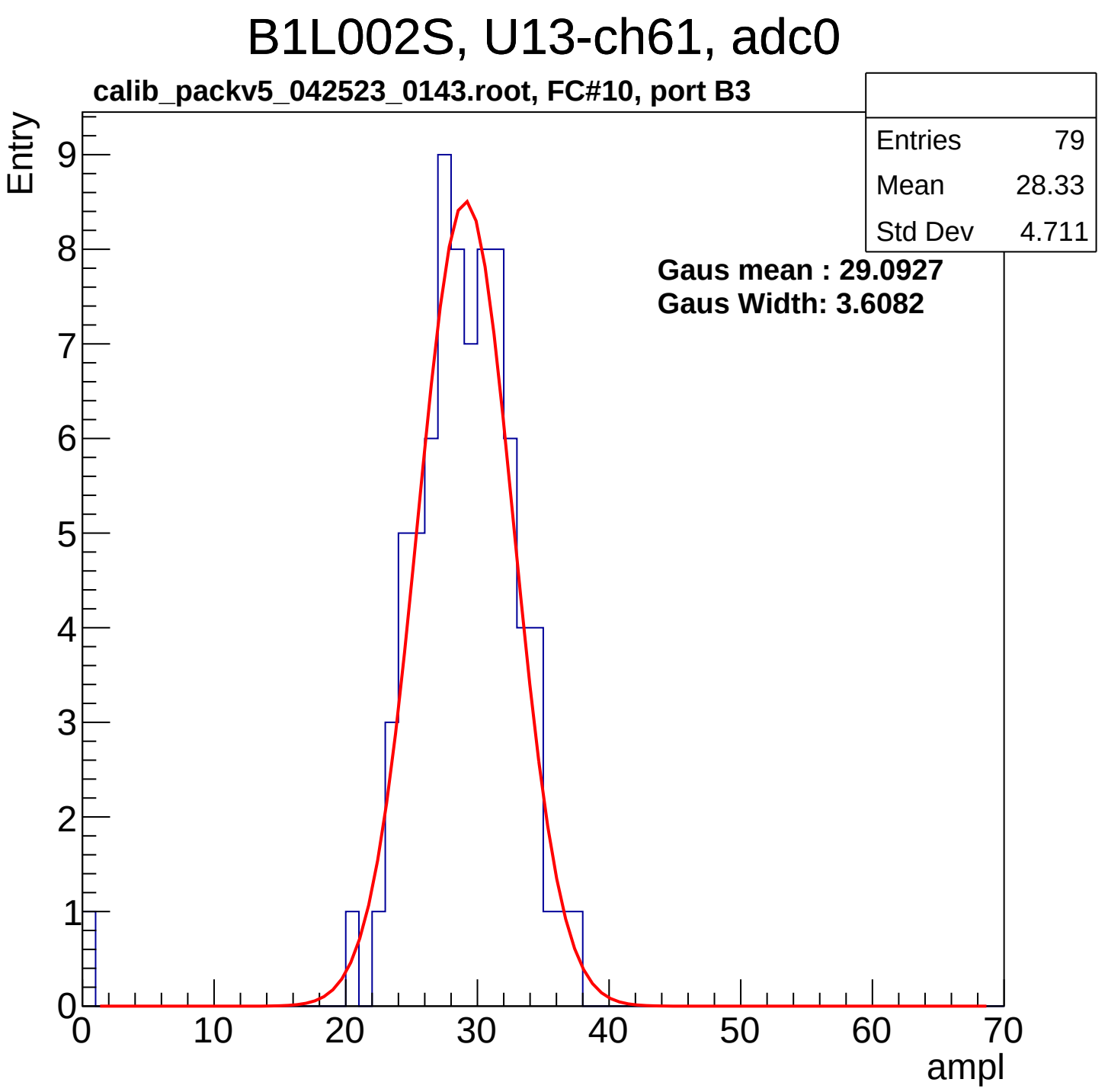
B1L002S, U13-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 28.33
Std Dev 4.711
Gaus mean : 29.0927
Gaus Width: 3.6082
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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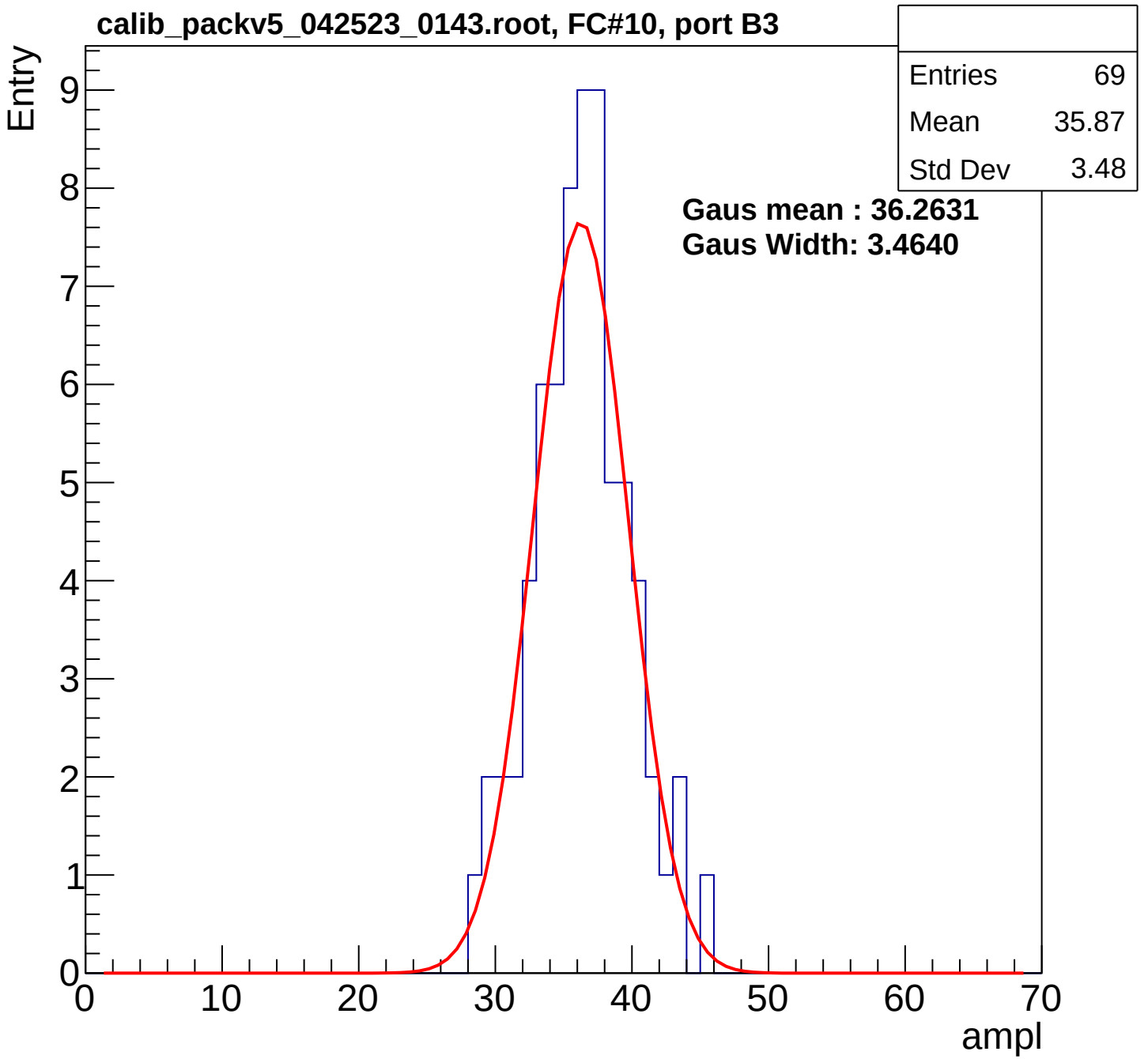
B1L002S, U13-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.87
Std Dev 3.48
Gaus mean : 36.2631
Gaus Width: 3.4640
Entry
ampl
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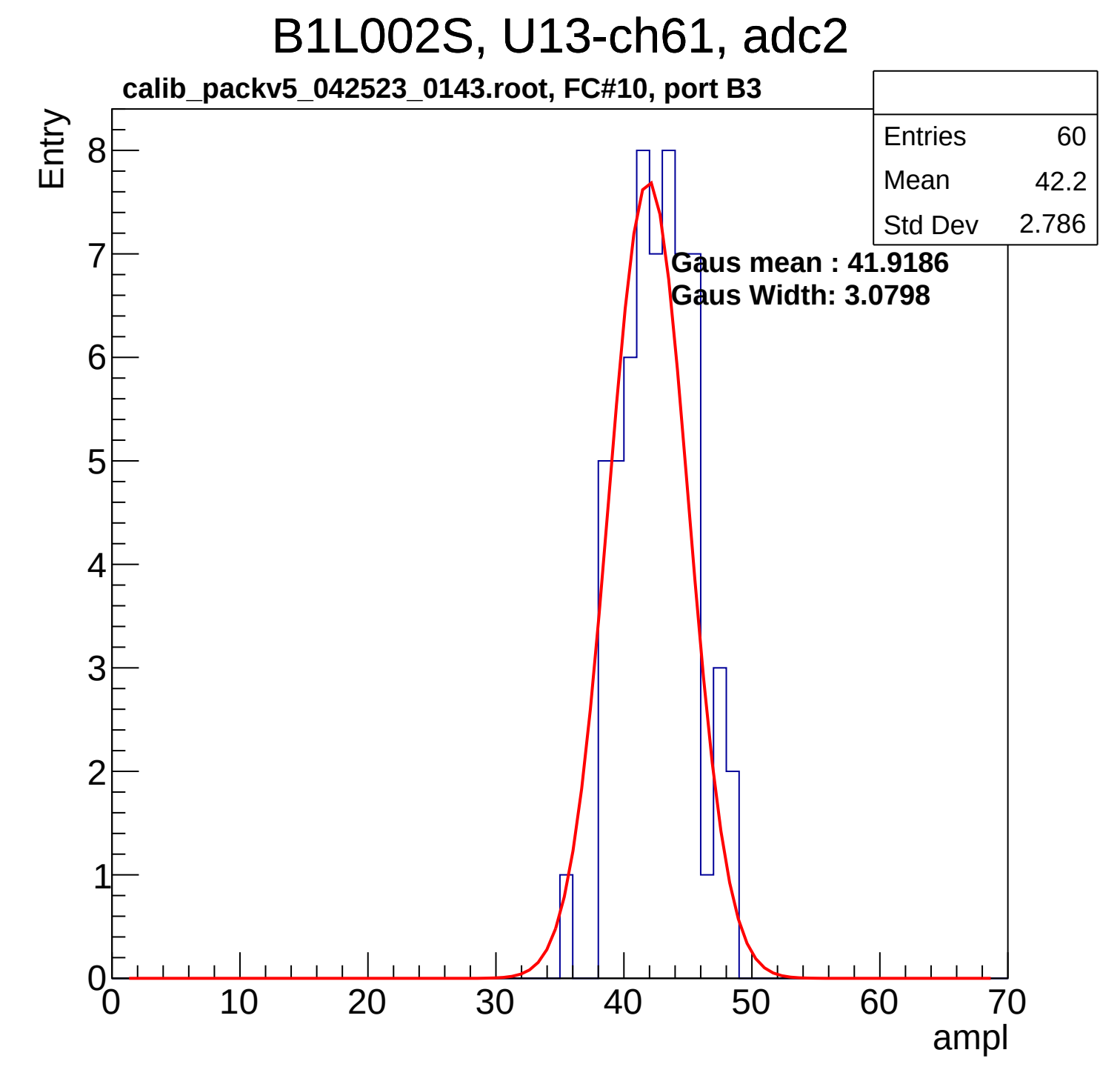
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.2
Std Dev 2.786
Gaus mean : 41.9186
Gaus Width: 3.0798
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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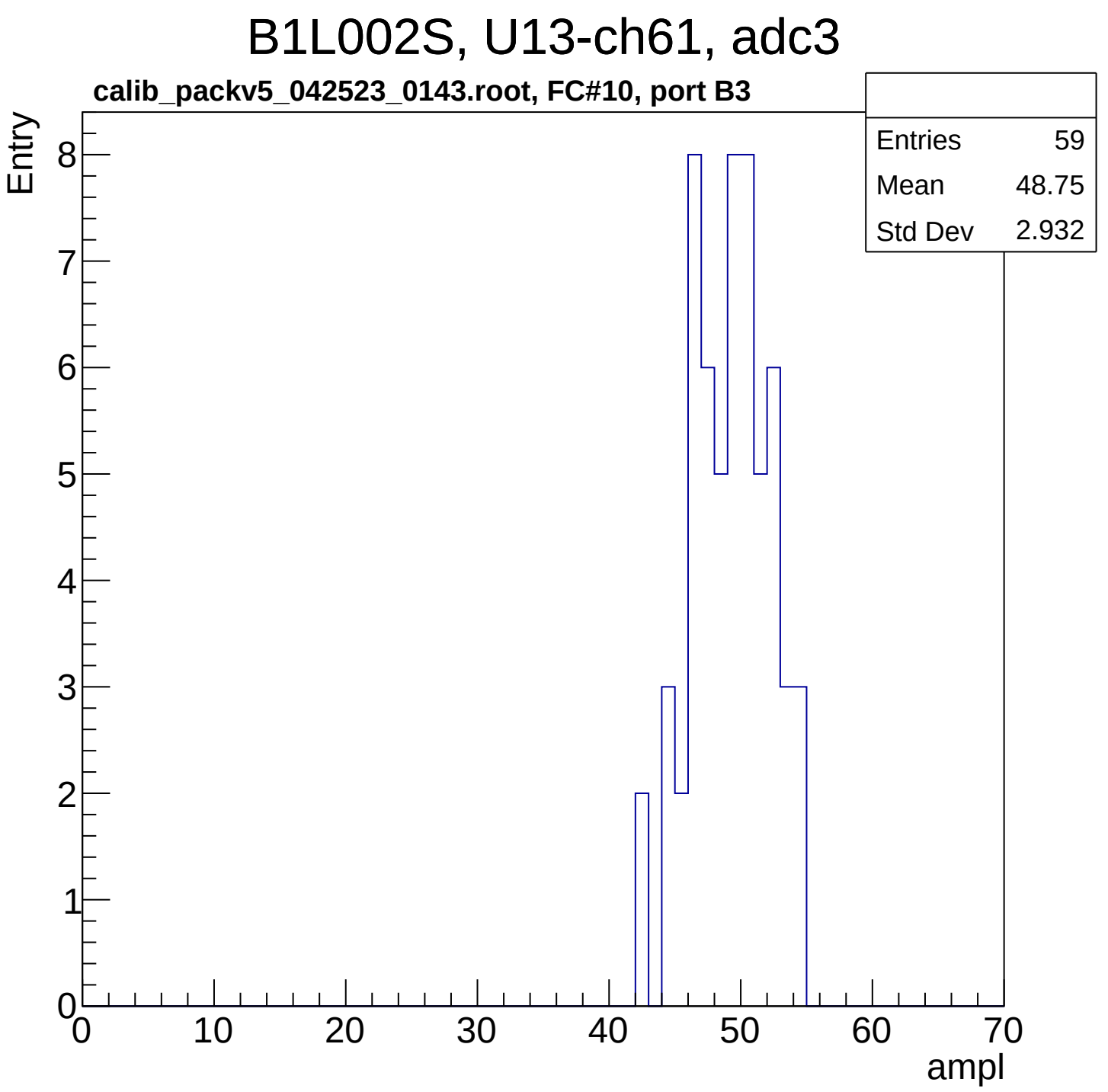
B1L002S, U13-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 48.75
Std Dev 2.932
Entry
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2
3
4
5
6
7
8
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10
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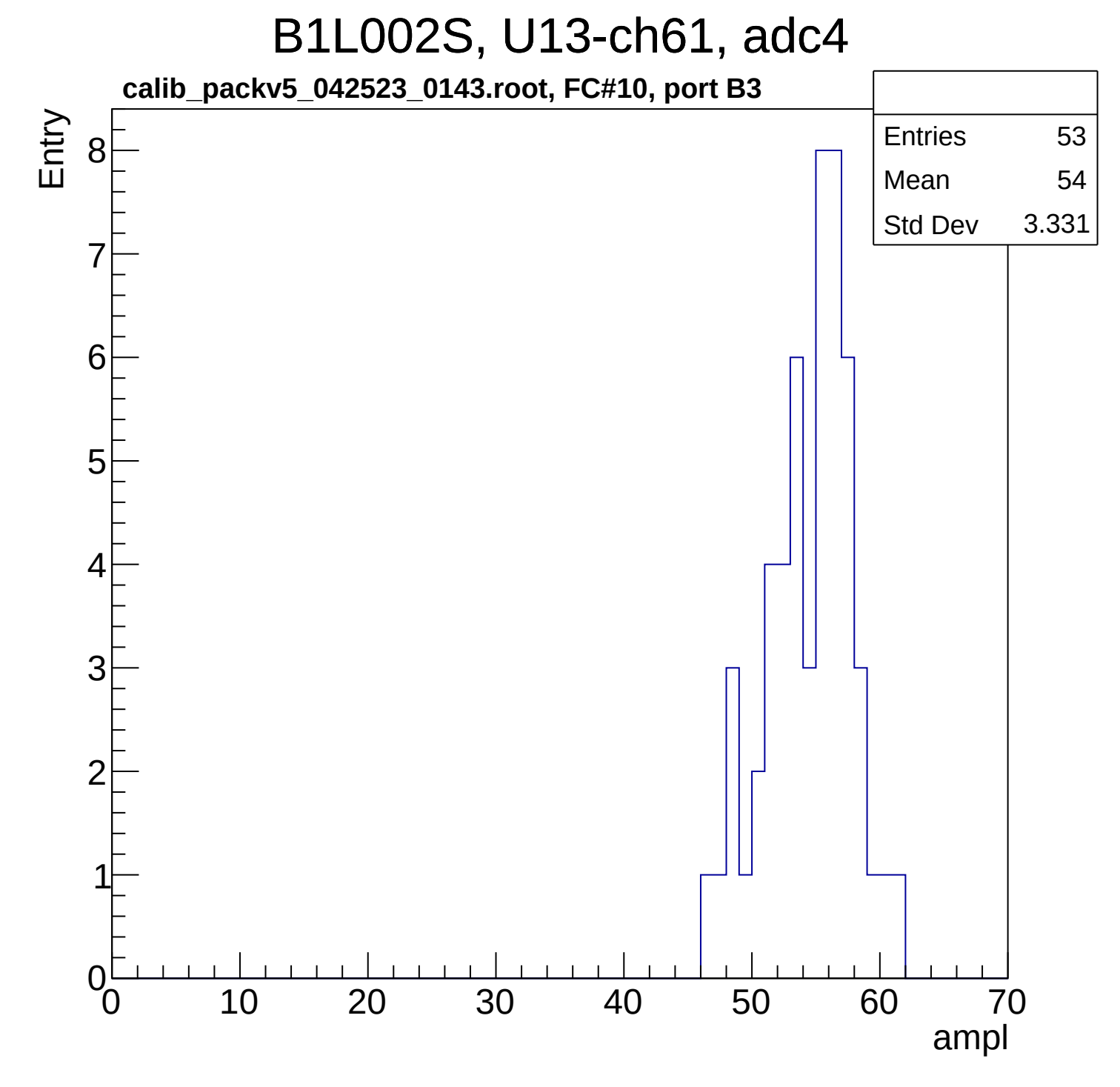
30
40
50
60
70
ampl


B1L002S, U13-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54
Std Dev 3.331
Entry
ampl
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5
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40
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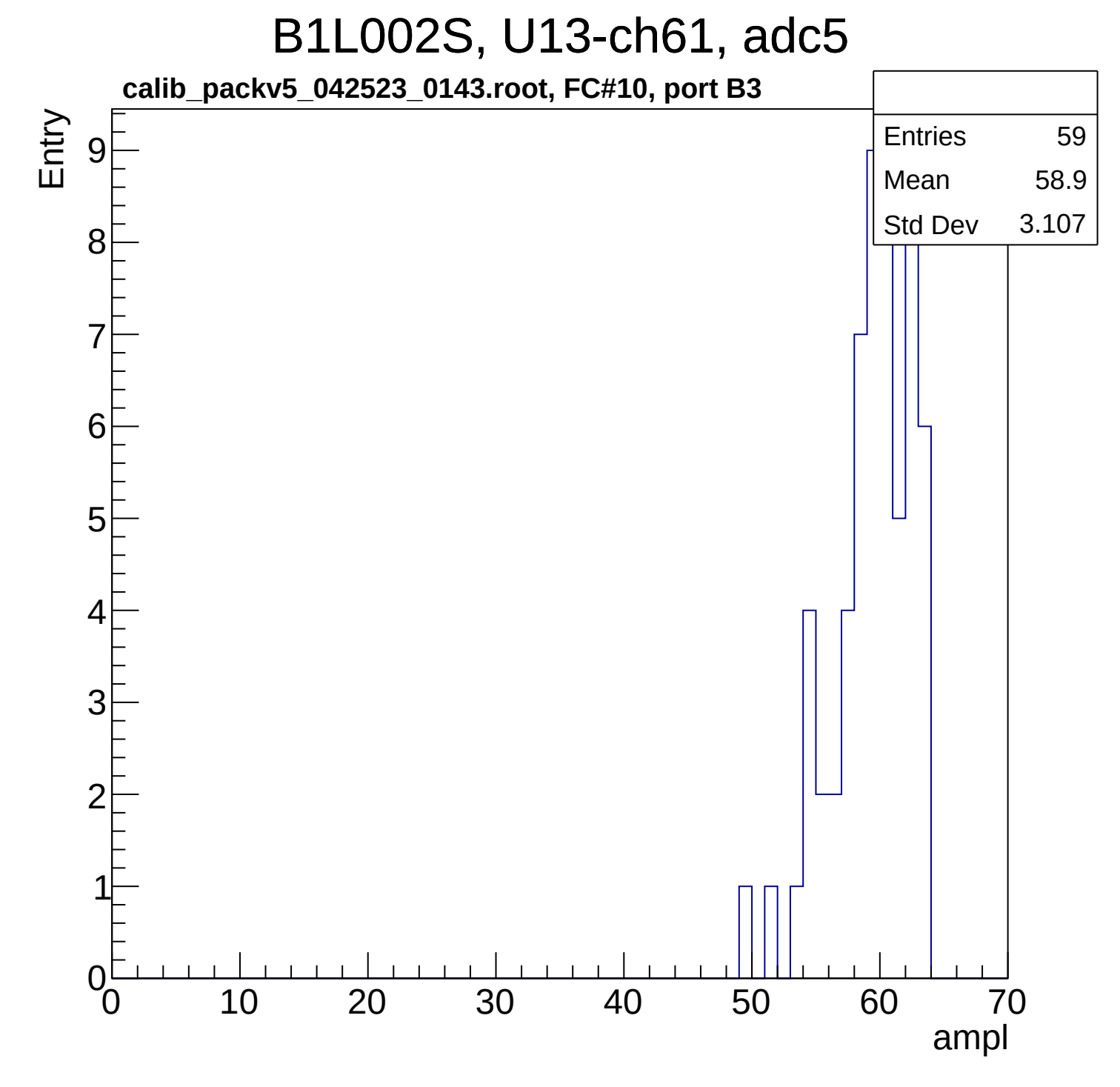
B1L002S, U13-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.9
Std Dev 3.107
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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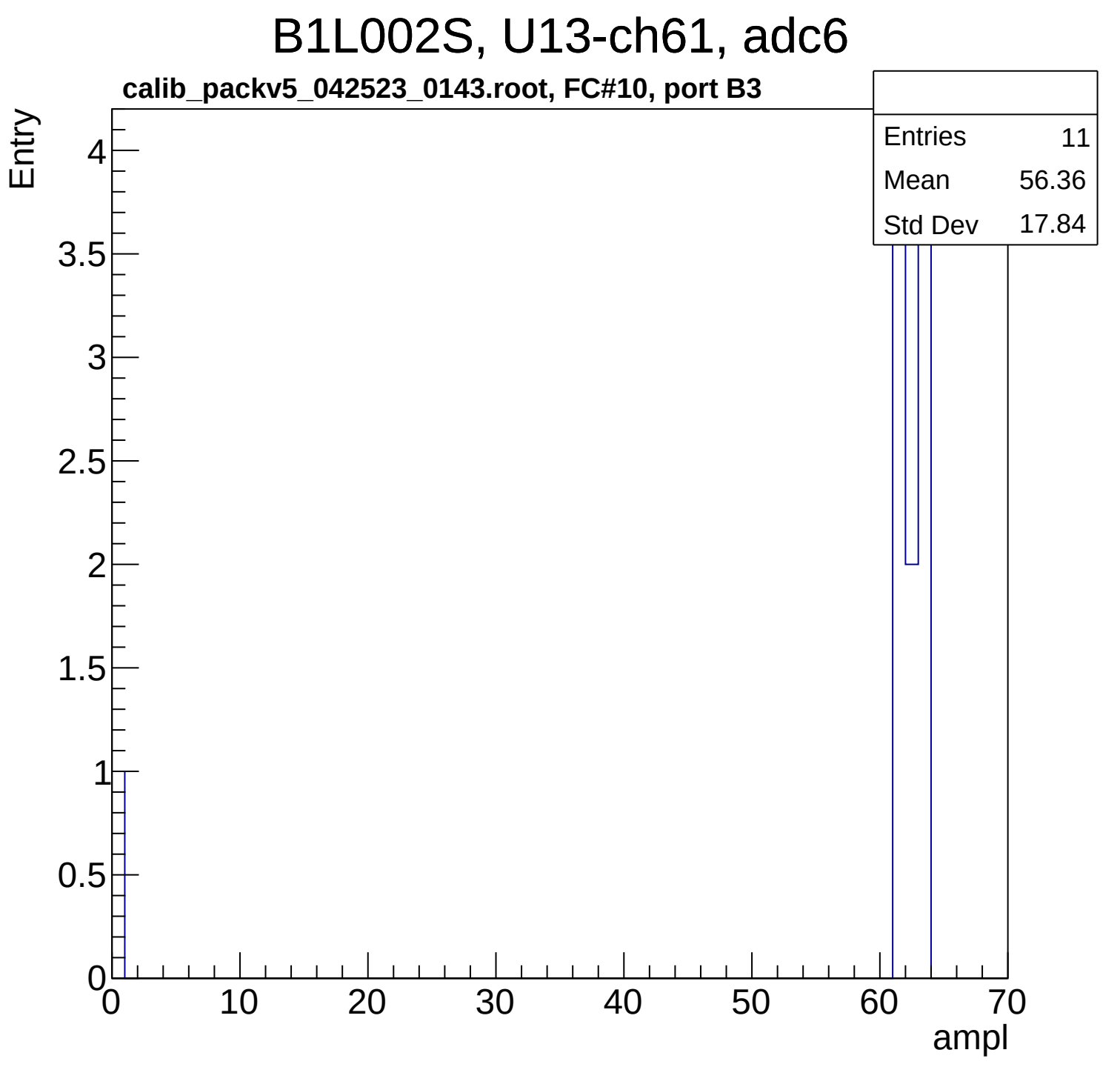
40
50
60
70


B1L002S, U13-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.36
Std Dev 17.84
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
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10
20
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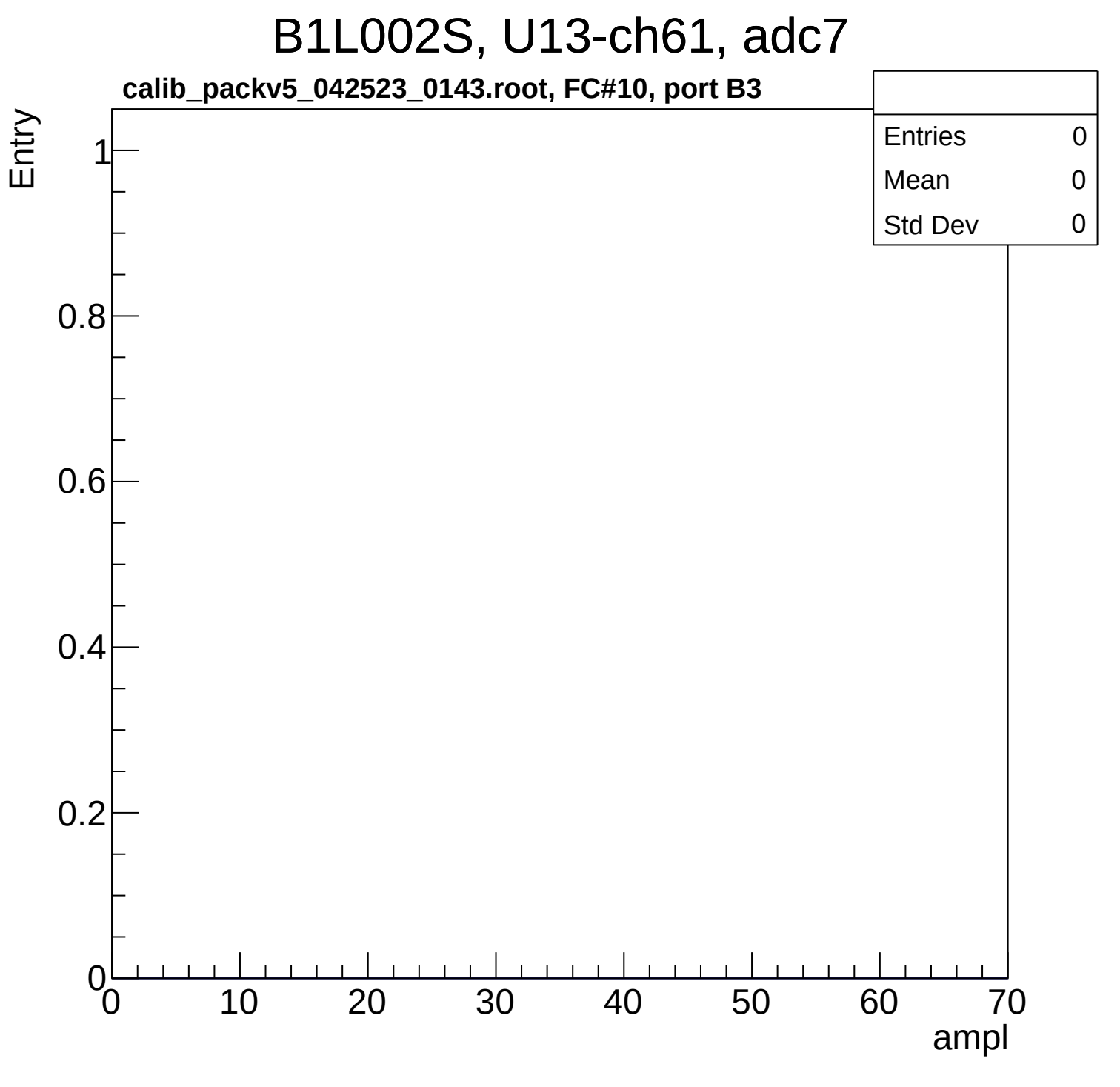
40
50
60
70
ampl


B1L002S, U13-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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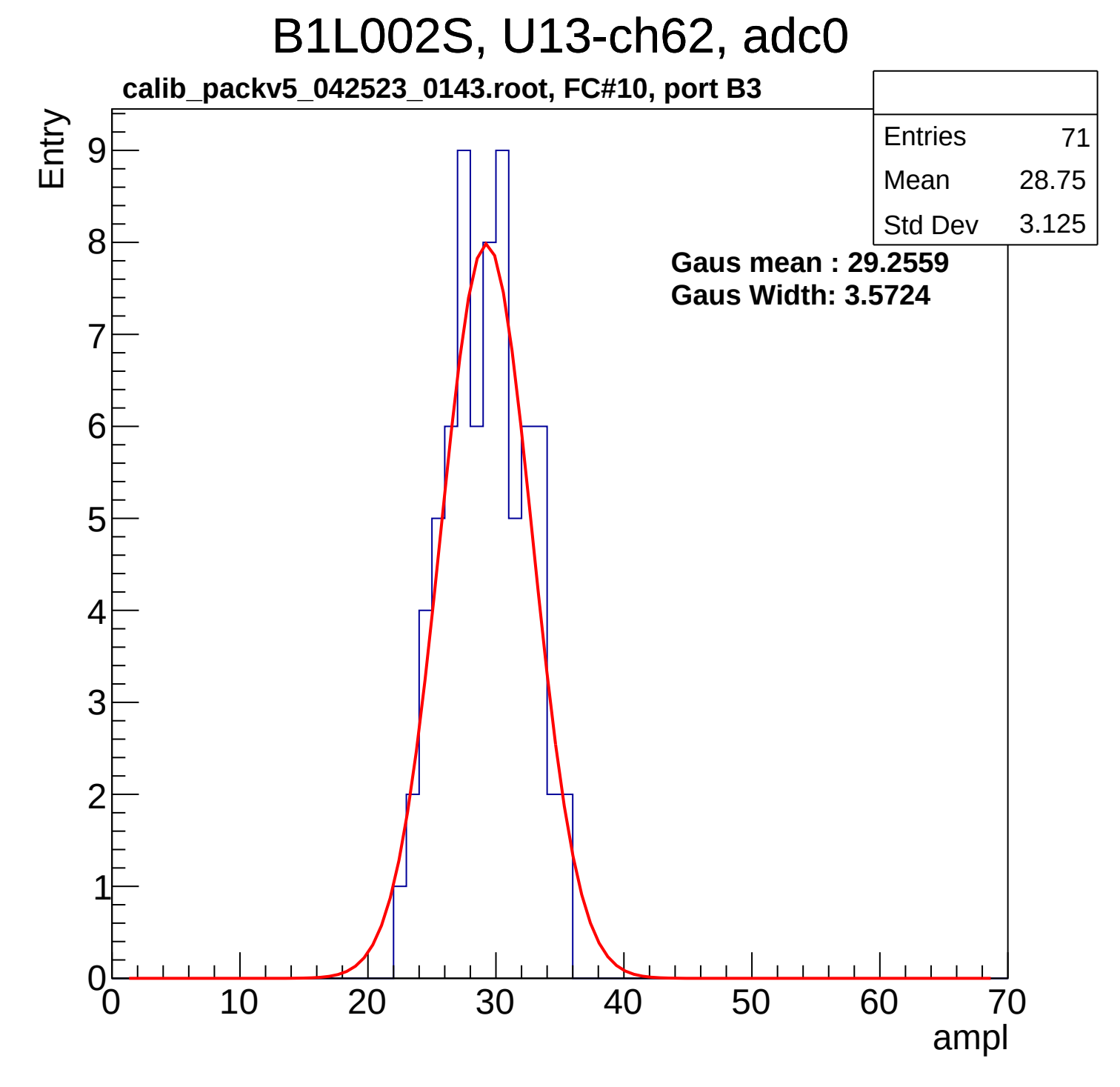
50
60
70
ampl


B1L002S, U13-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.75
Std Dev 3.125
Gaus mean : 29.2559
Gaus Width: 3.5724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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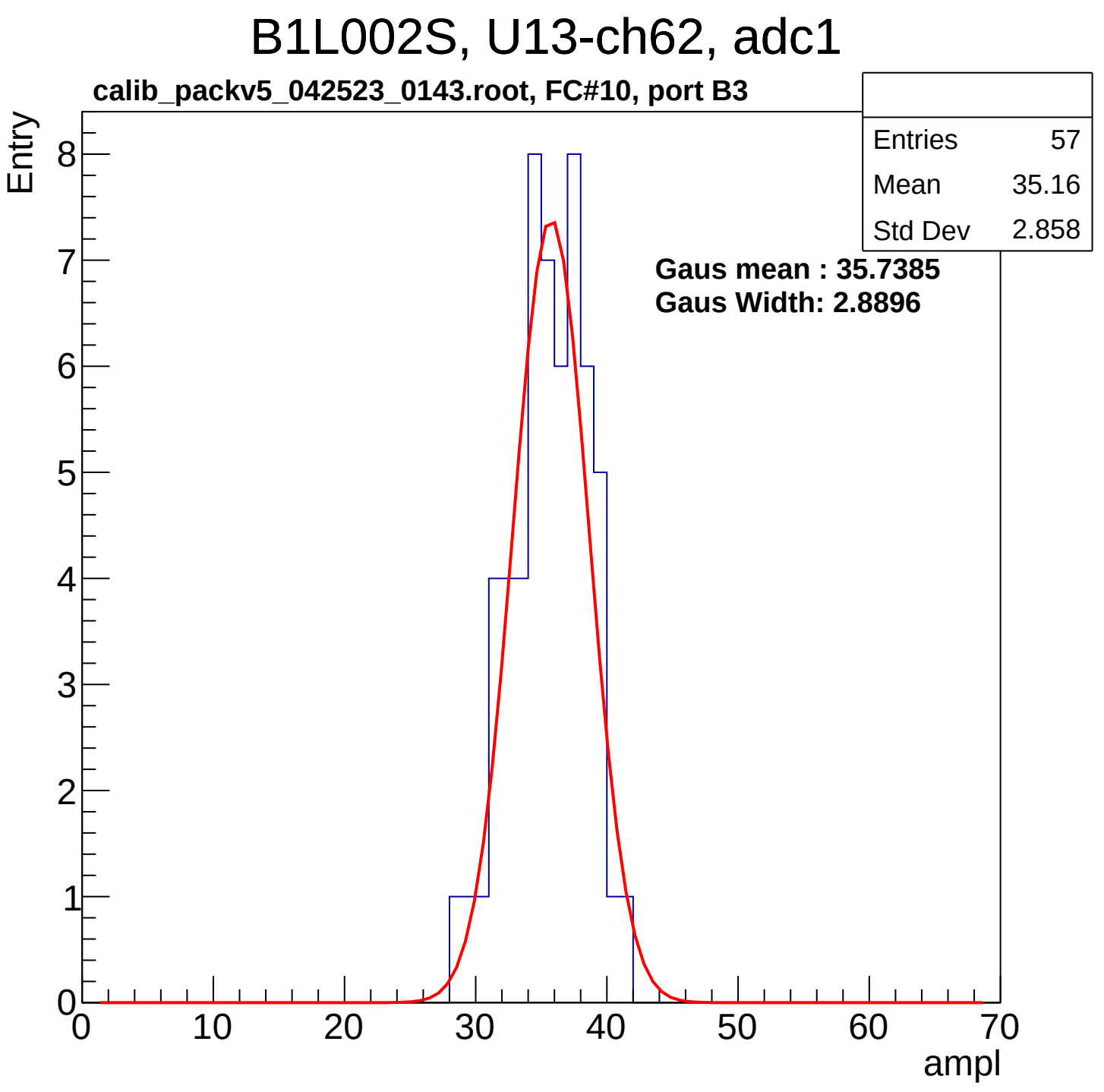
B1L002S, U13-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.16
Std Dev 2.858
Gaus mean : 35.7385
Gaus Width: 2.8896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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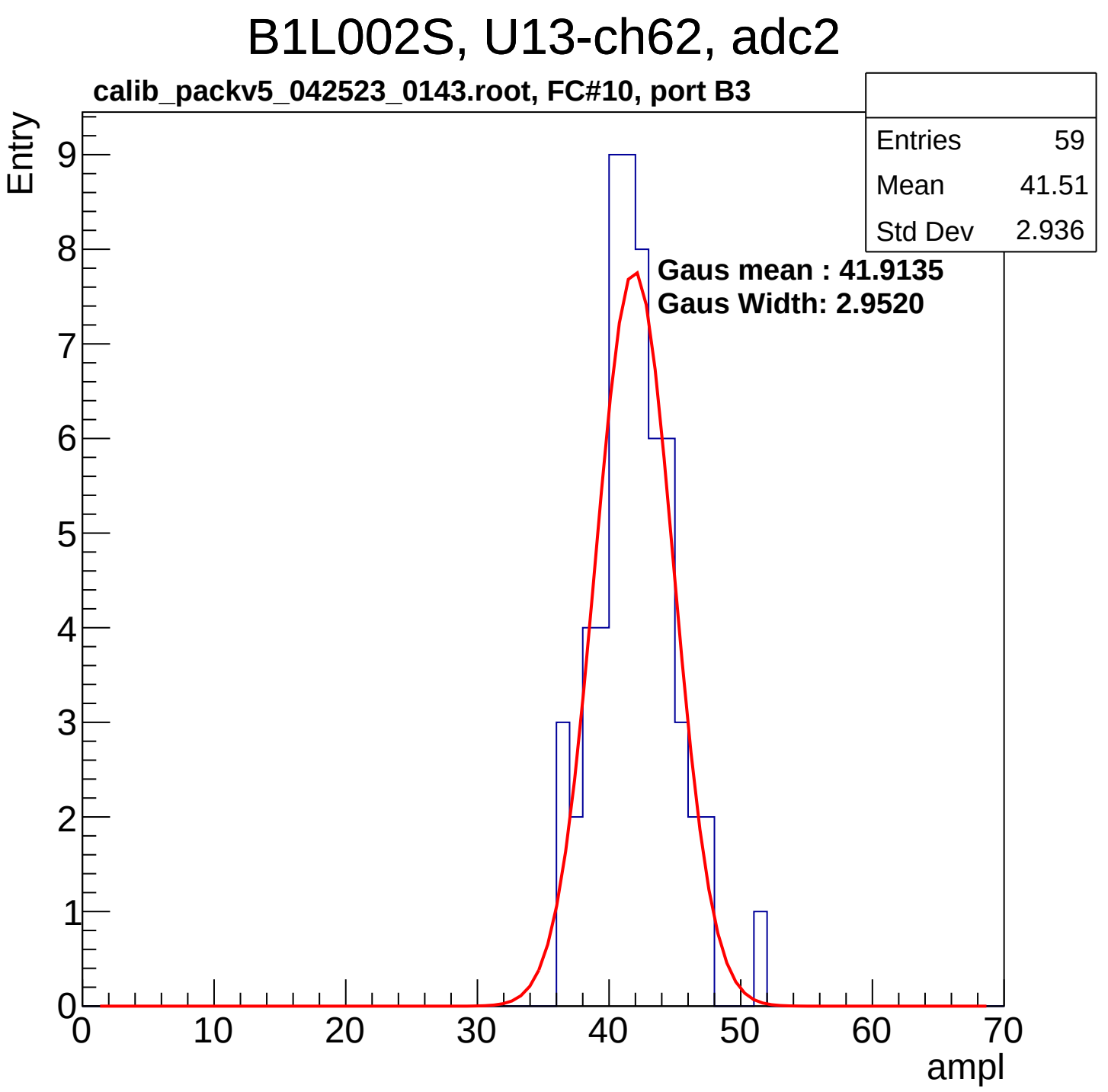
B1L002S, U13-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.51
Std Dev 2.936
Gaus mean : 41.9135
Gaus Width: 2.9520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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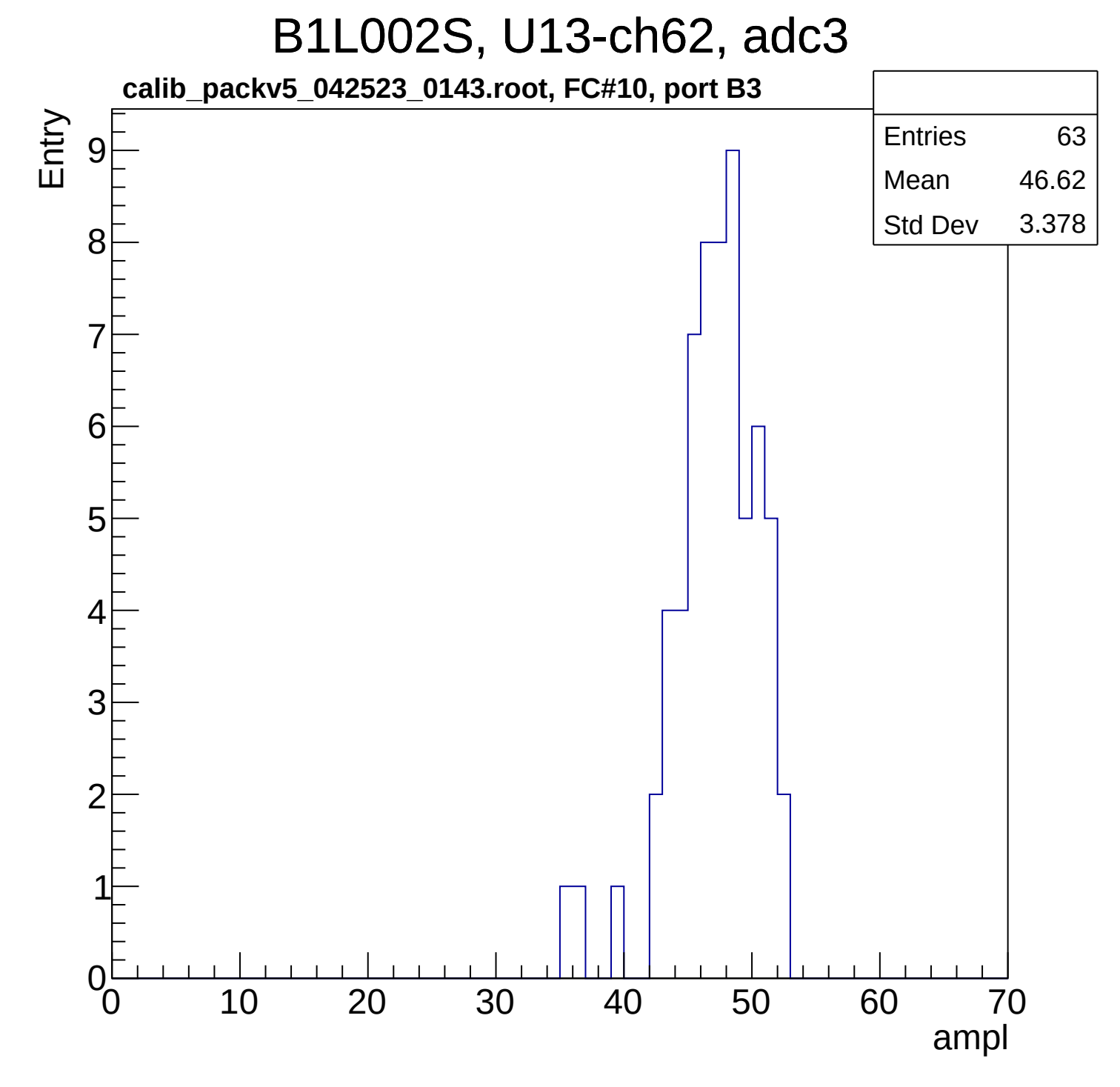
B1L002S, U13-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.62
Std Dev 3.378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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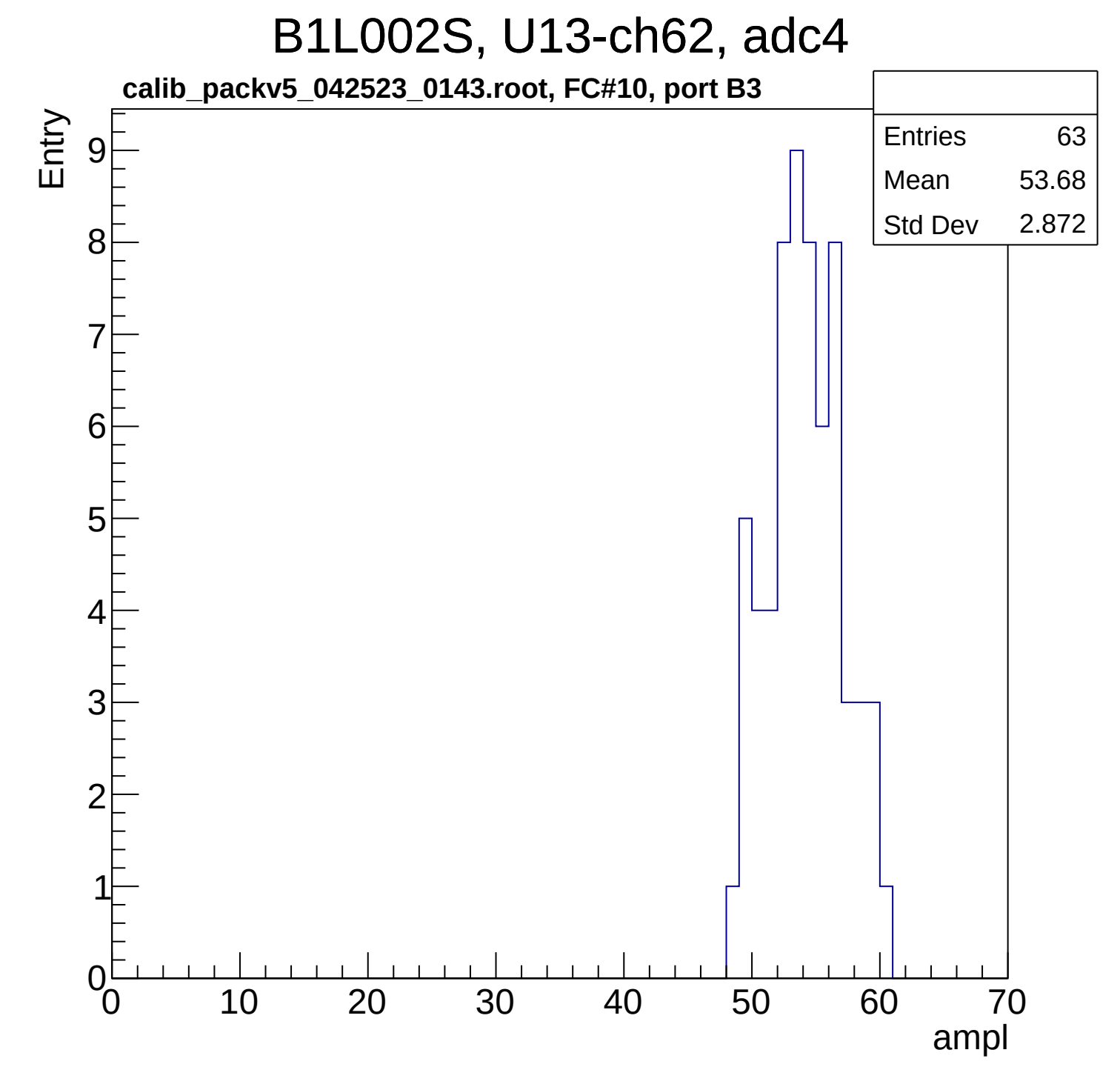
B1L002S, U13-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.68
Std Dev 2.872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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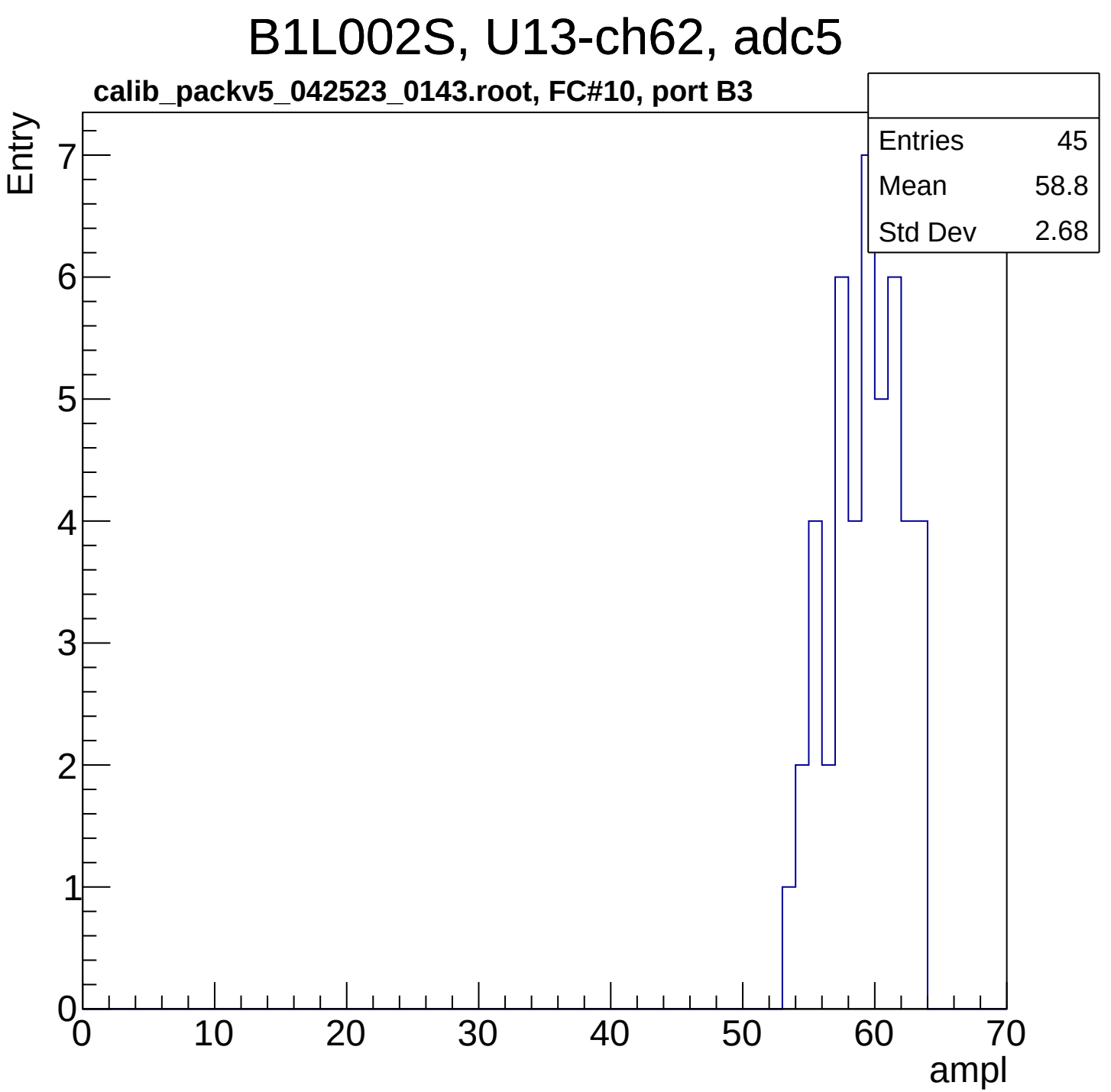
B1L002S, U13-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.8
Std Dev 2.68
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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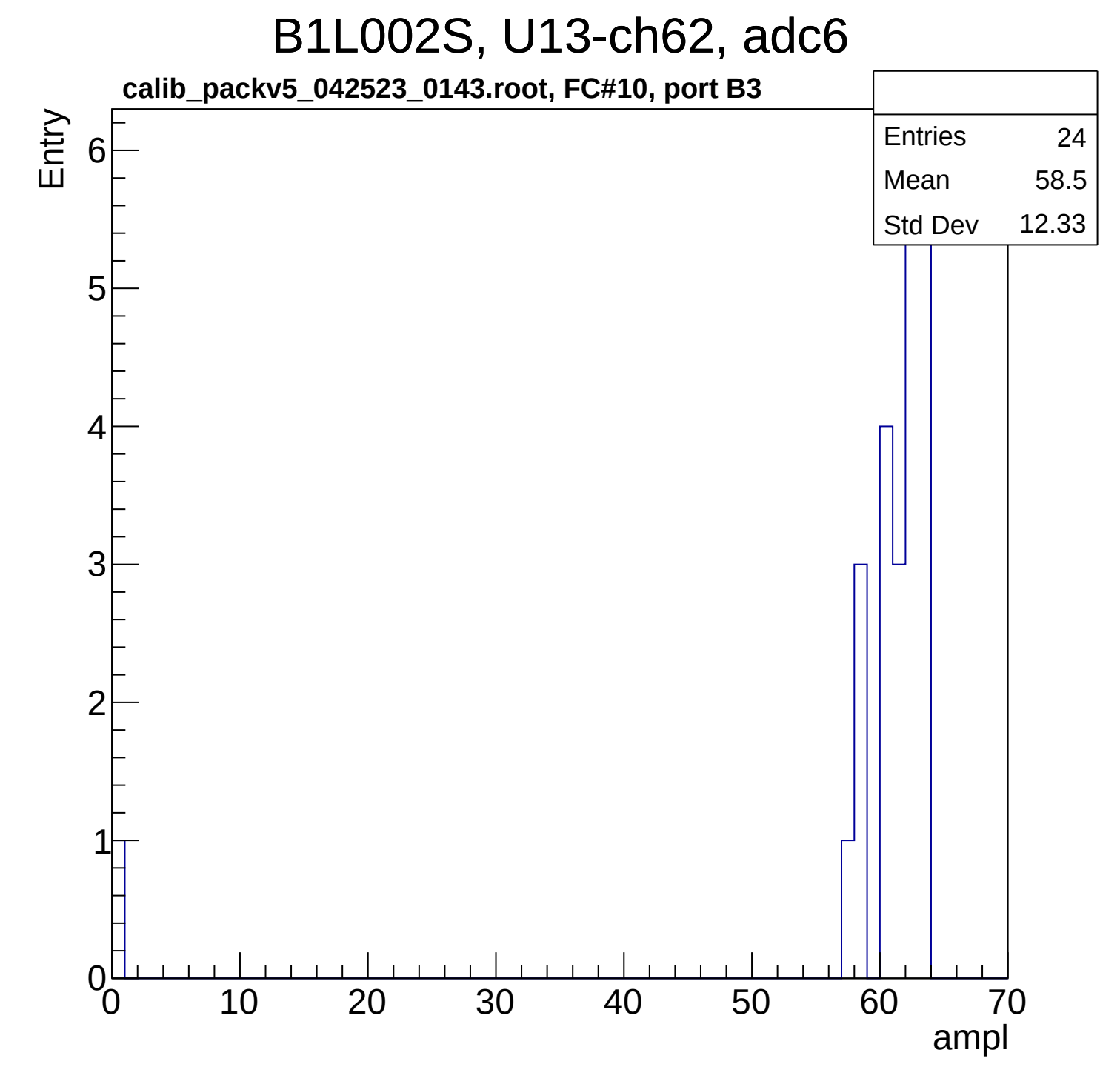
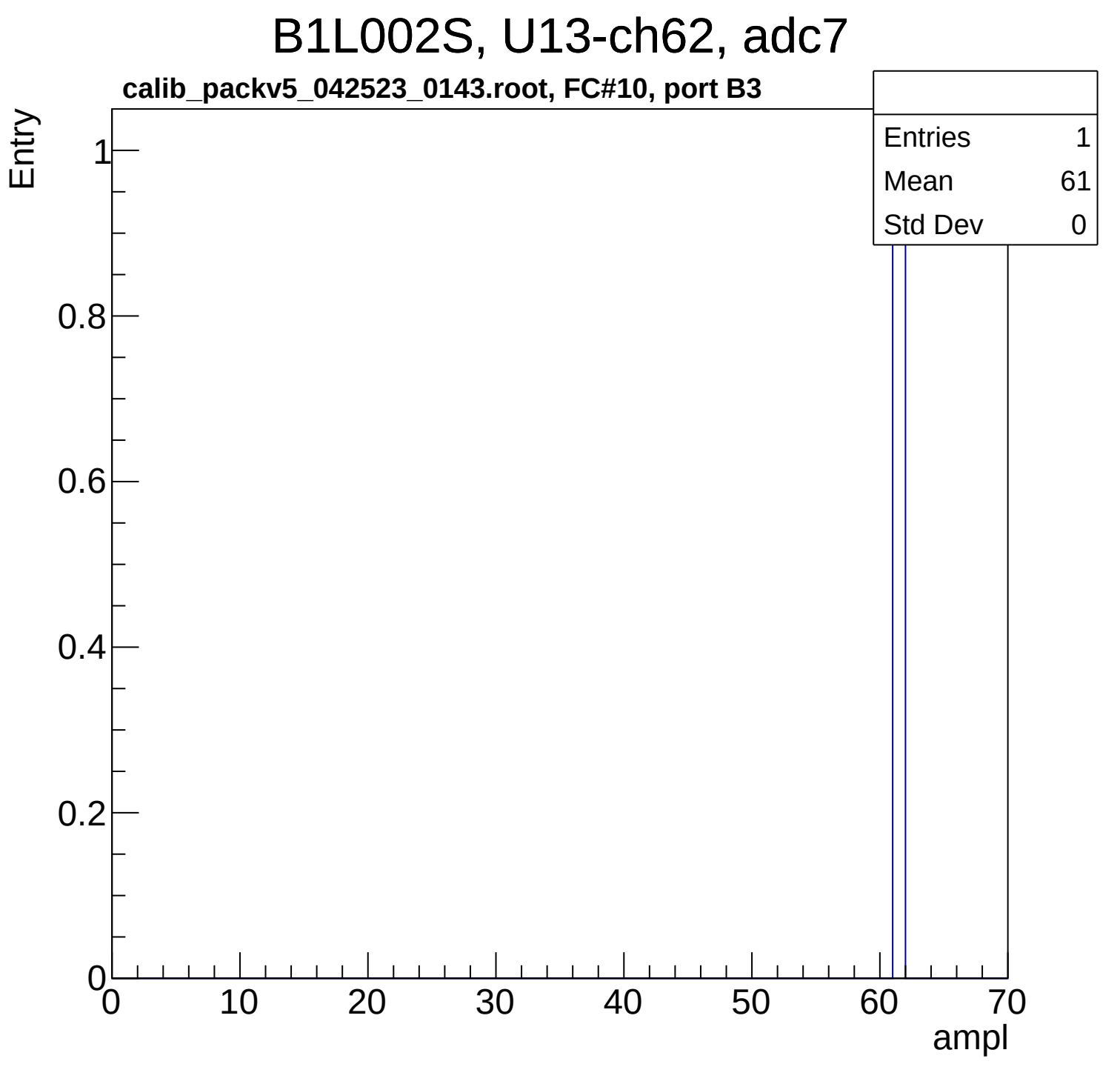
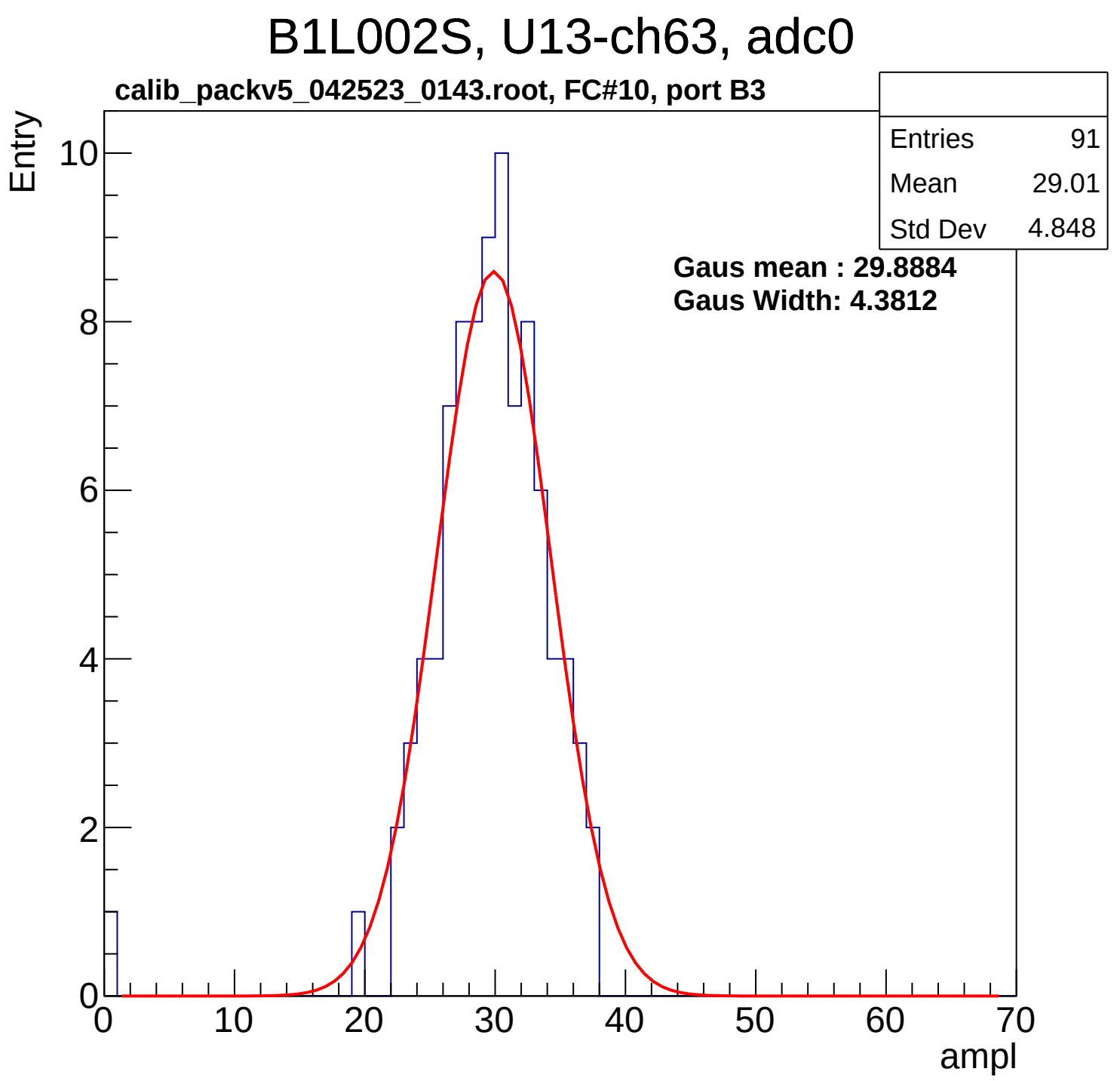
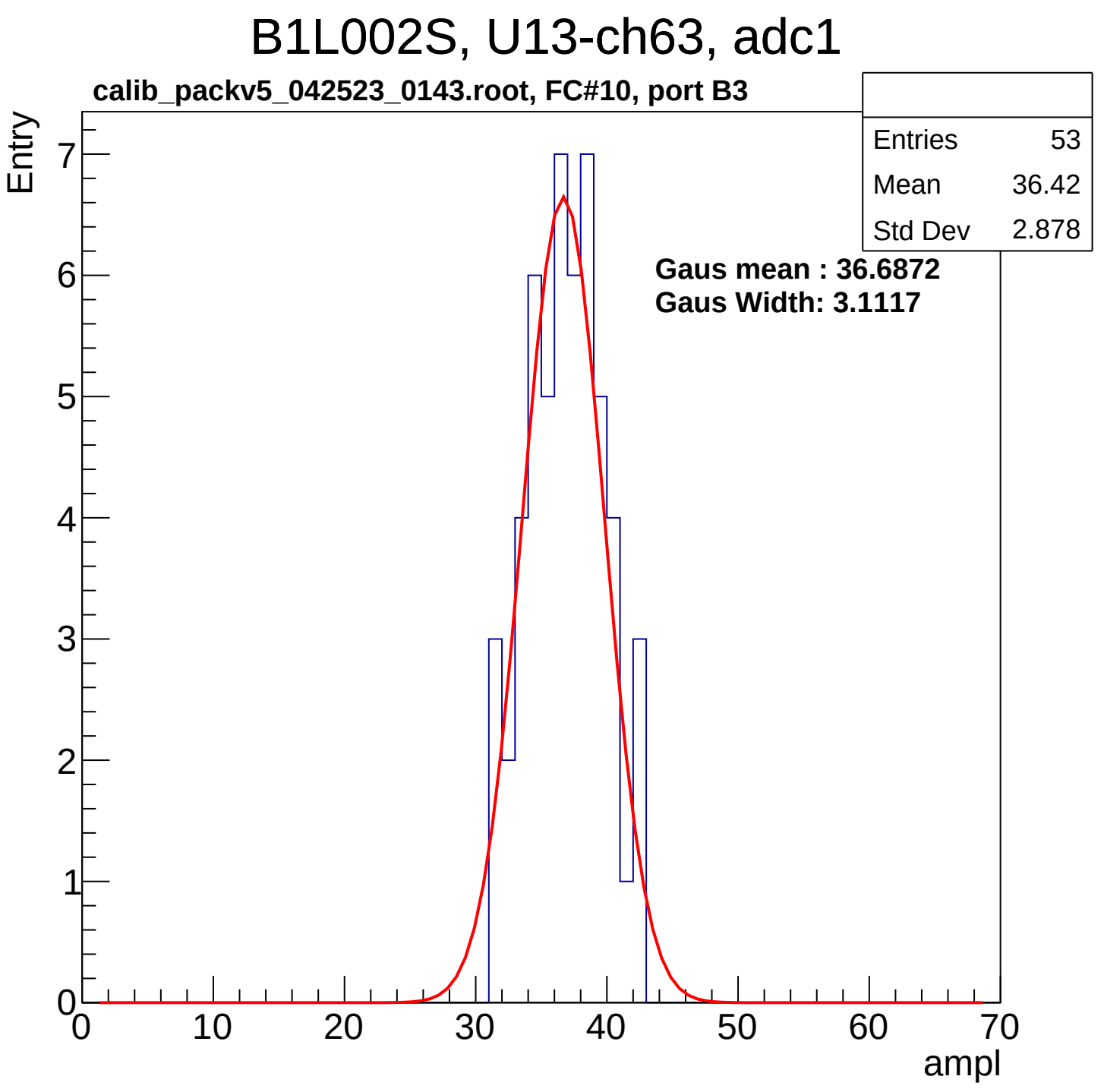

B1L002S, U13-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.5
Std Dev 12.33
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 91
Mean 29.01
Std Dev 4.848
Gaus mean : 29.8884
Gaus Width: 4.3812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 36.42
Std Dev 2.878
Gaus mean : 36.6872
Gaus Width: 3.1117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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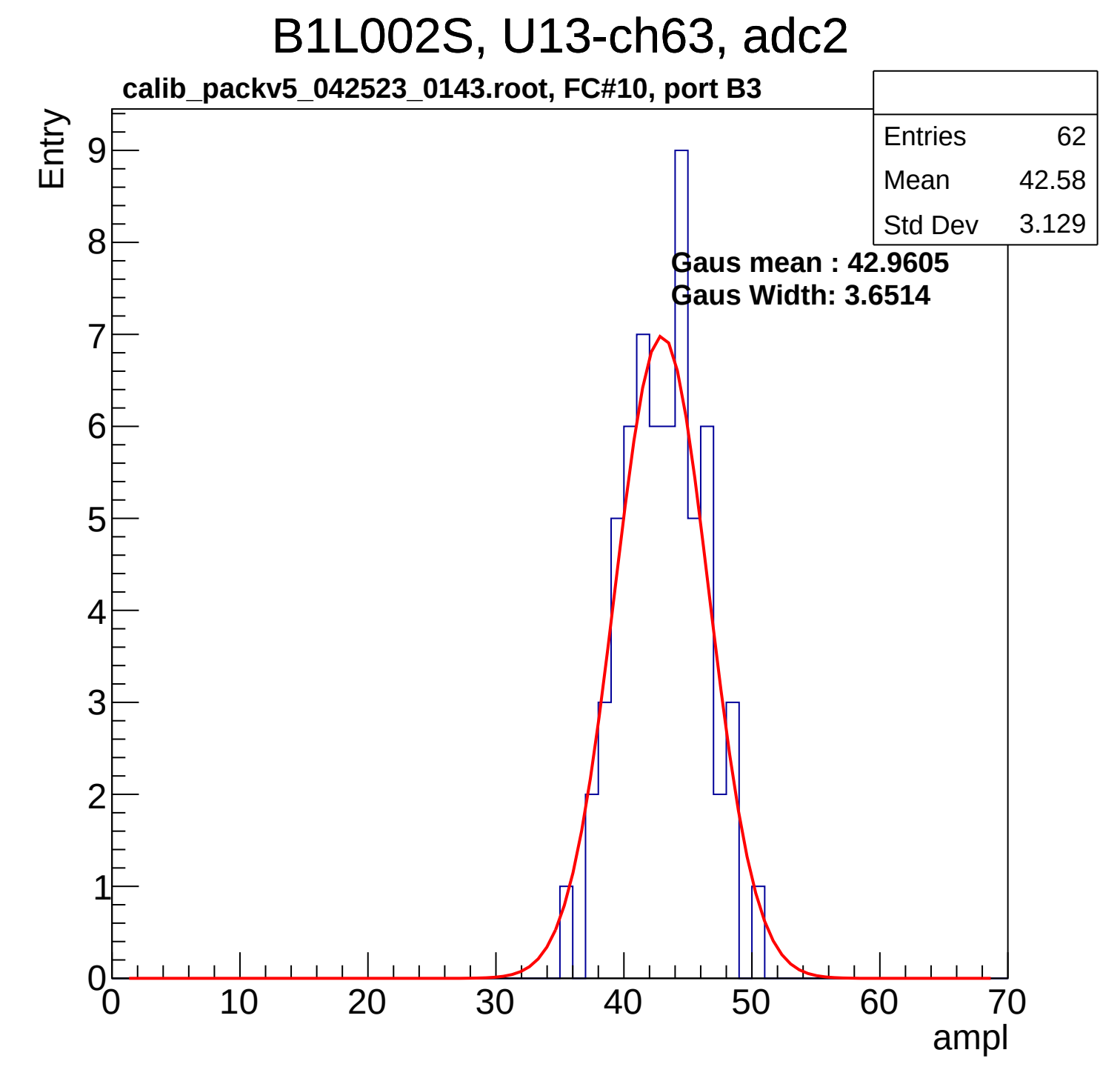
B1L002S, U13-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.58
Std Dev 3.129
Gaus mean : 42.9605
Gaus Width: 3.6514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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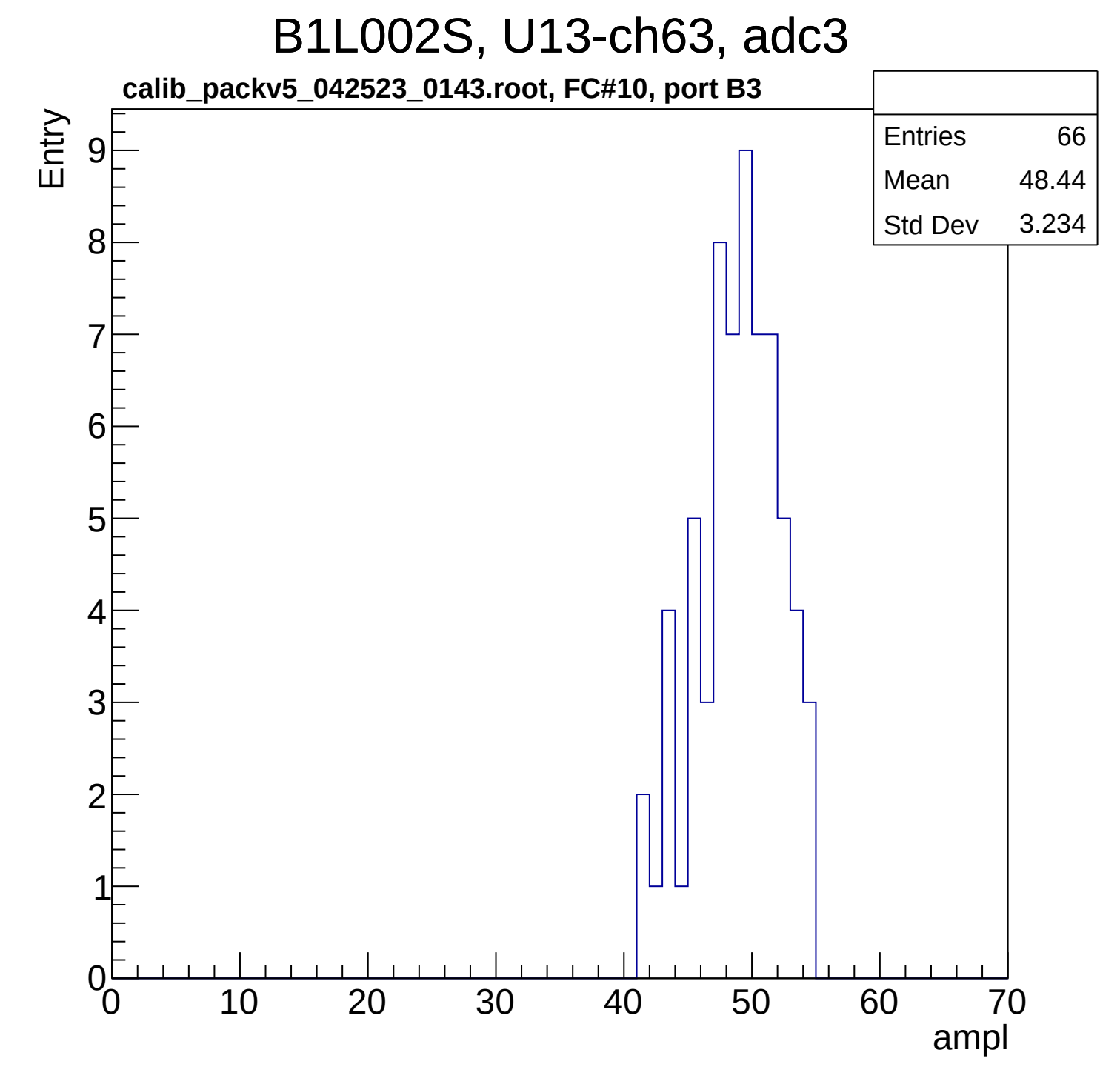
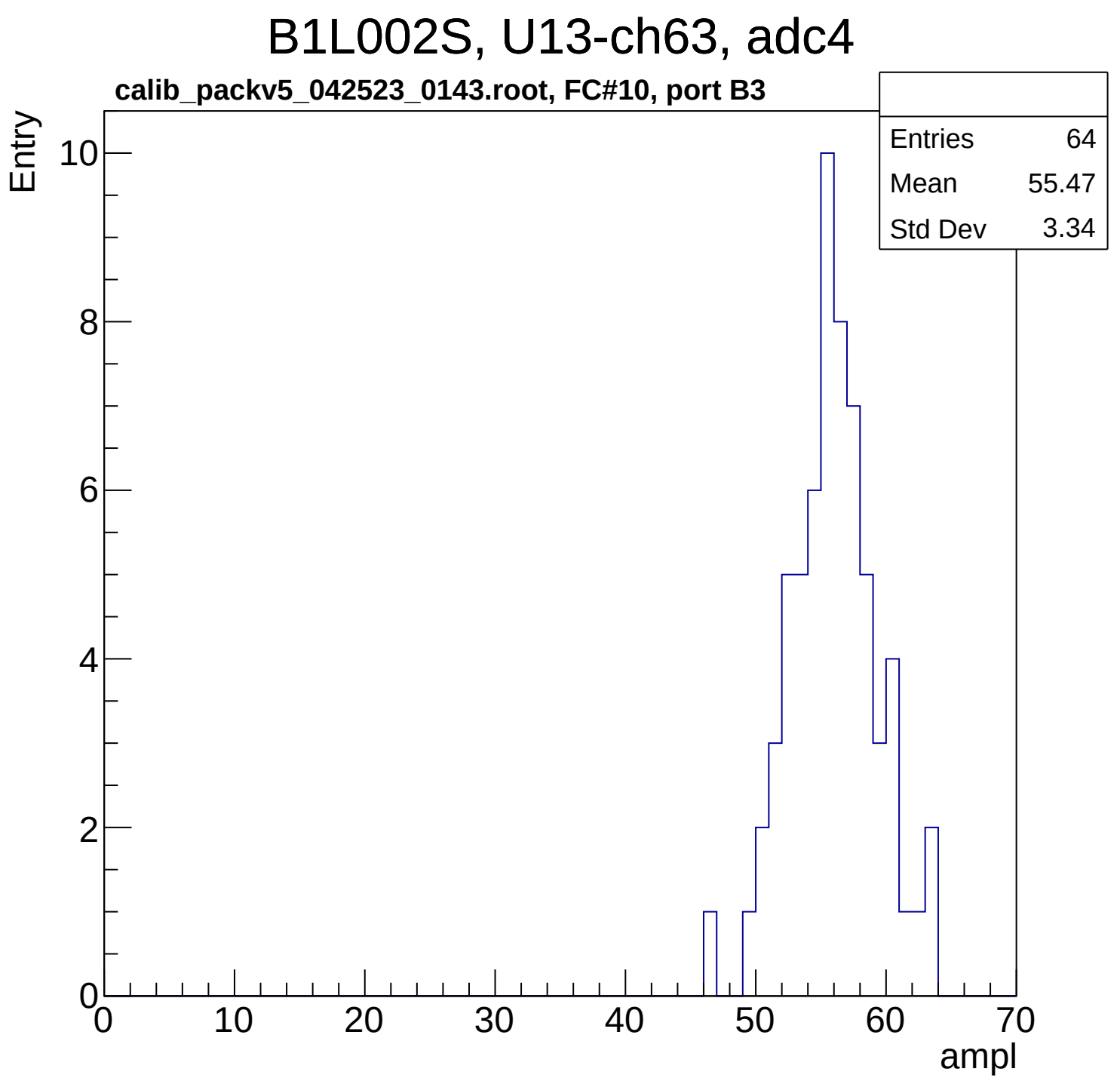
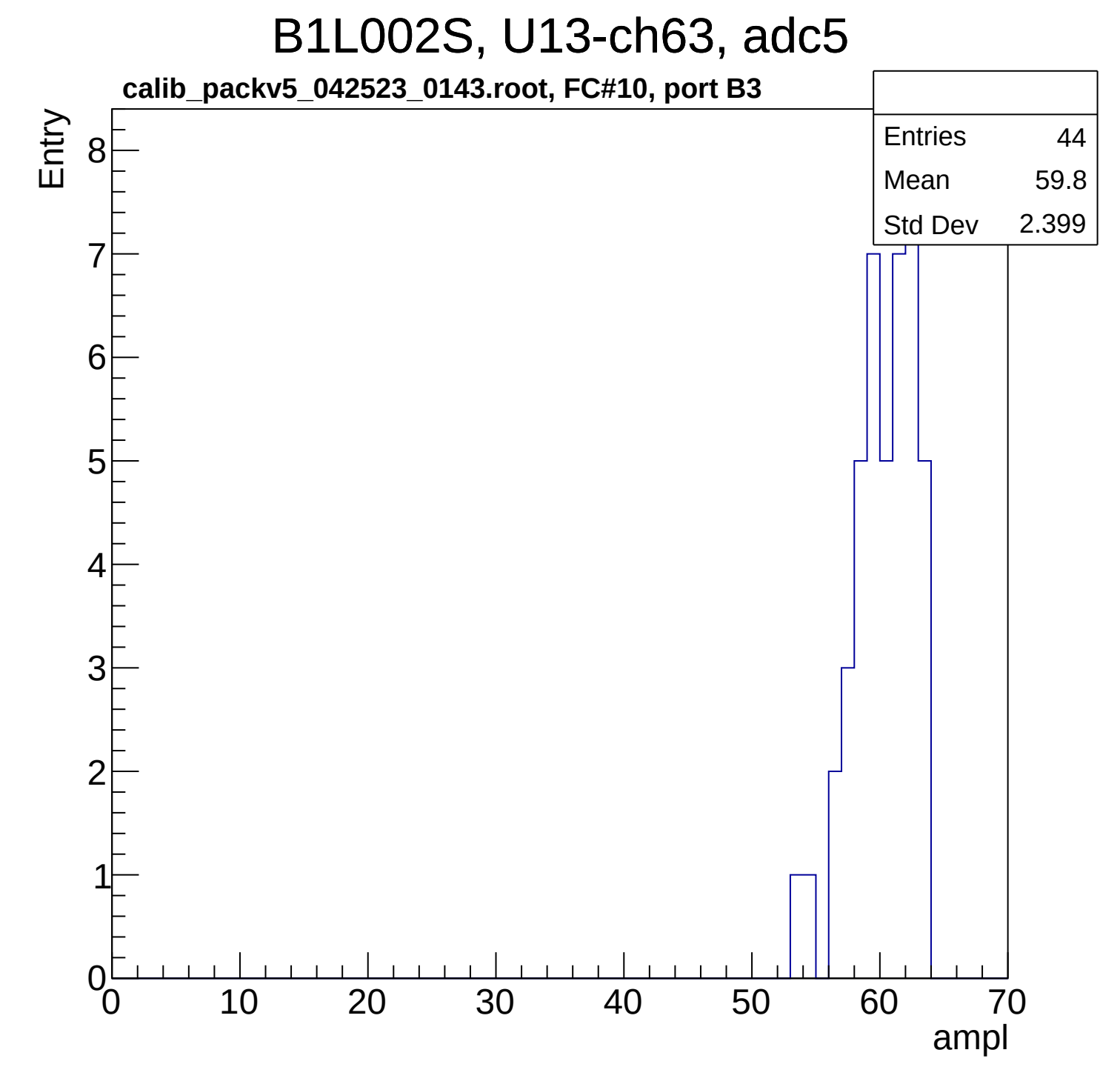
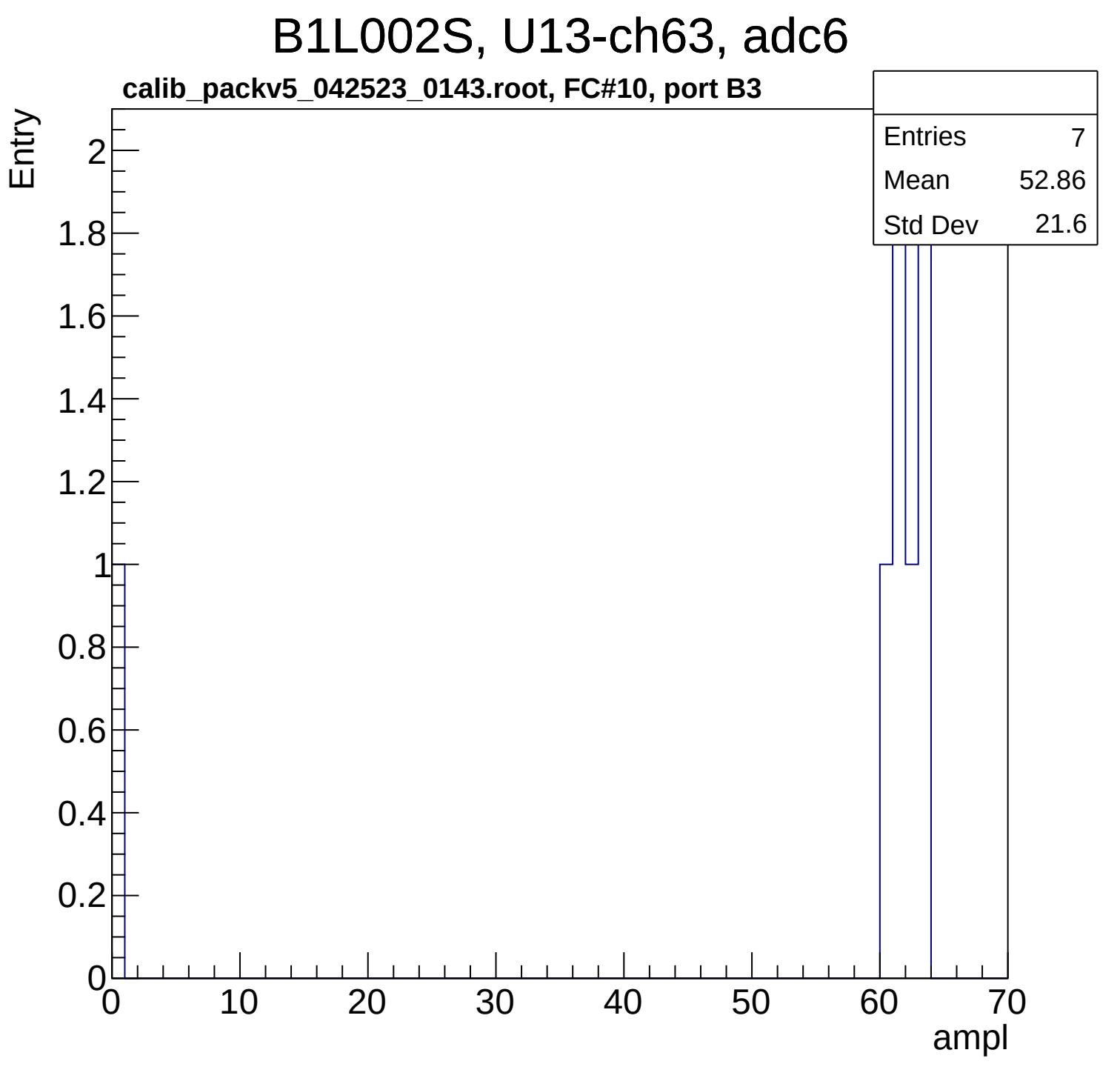
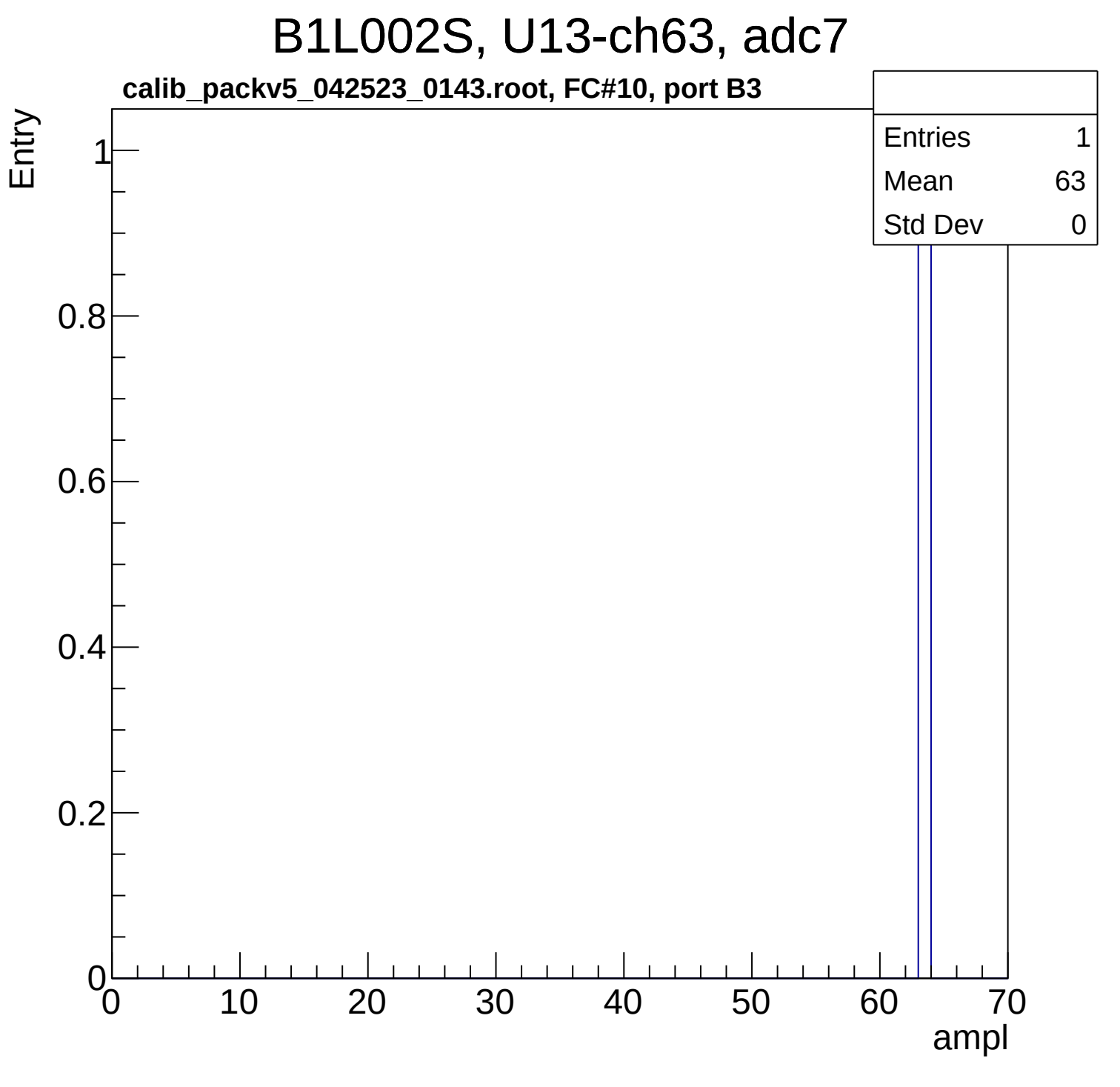
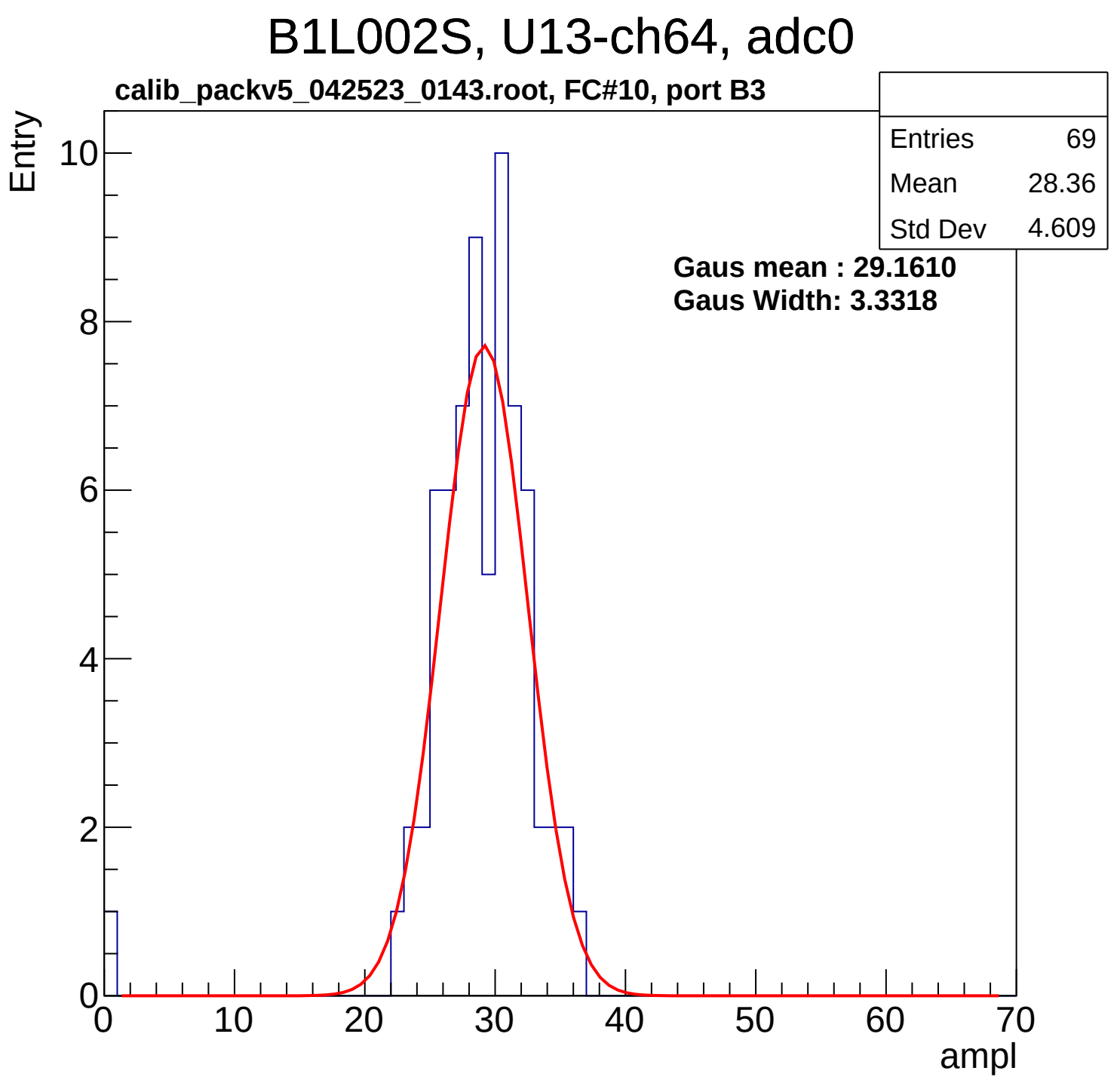

B1L002S, U13-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.44
Std Dev 3.234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

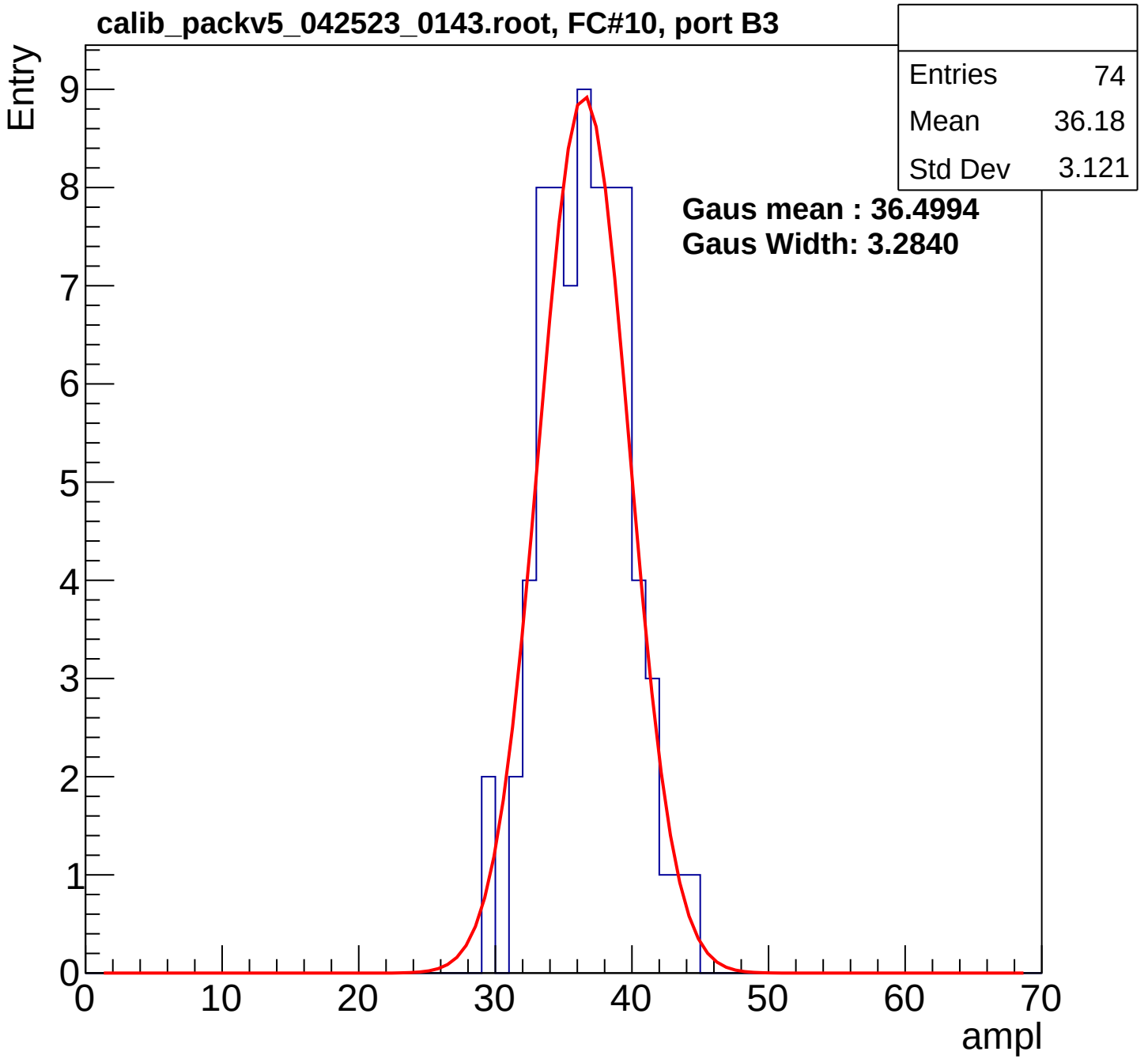

B1L002S, U13-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 55.47
Std Dev 3.34
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

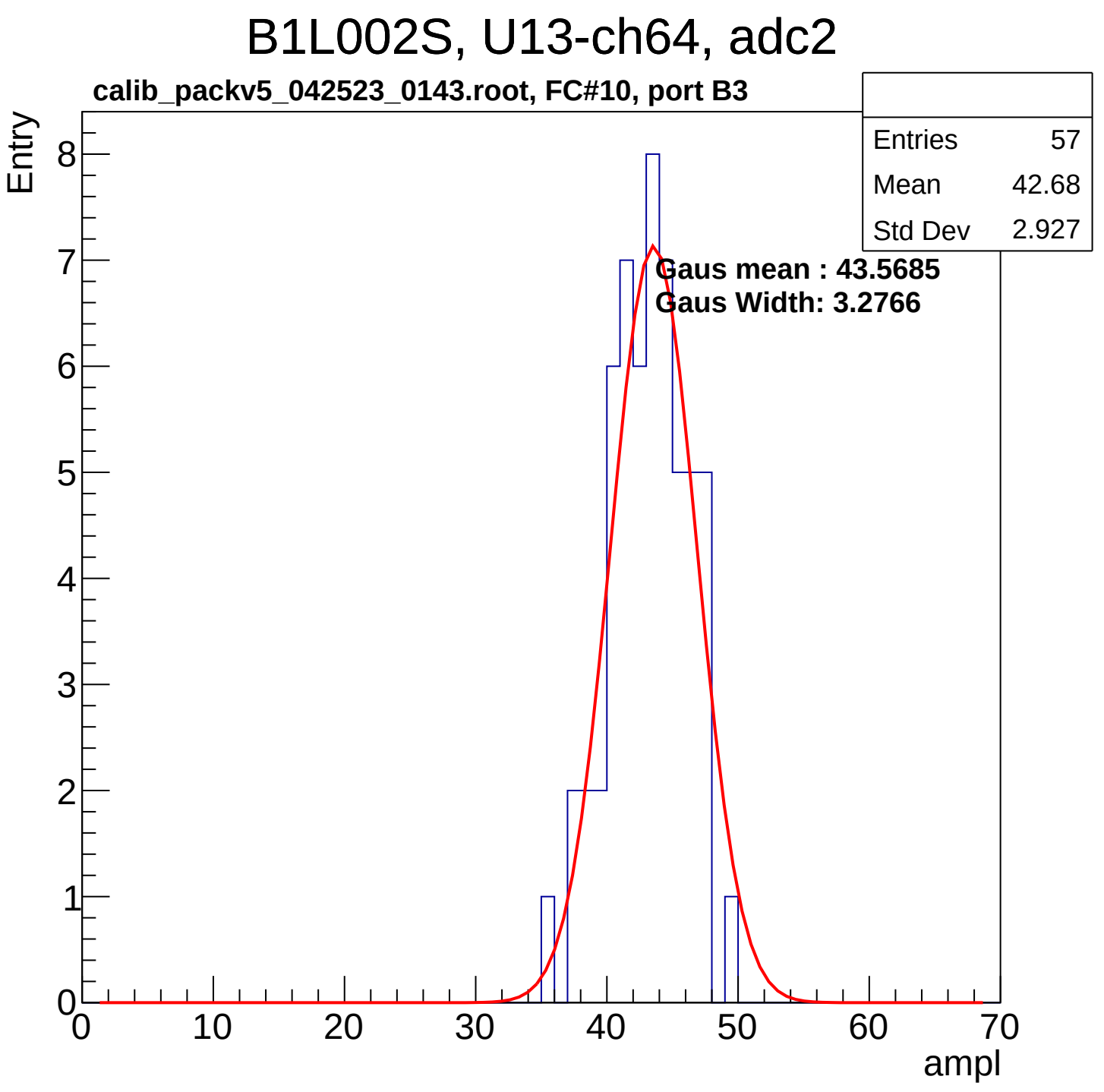

B1L002S, U13-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.8
Std Dev 2.399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 52.86
Std Dev 21.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.36
Std Dev 4.609
Gaus mean : 29.1610
Gaus Width: 3.3318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 36.18
Std Dev 3.121
Gaus mean : 36.4994
Gaus Width: 3.2840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.68
Std Dev 2.927
Gaus mean : 43.5685
Gaus Width: 3.2766
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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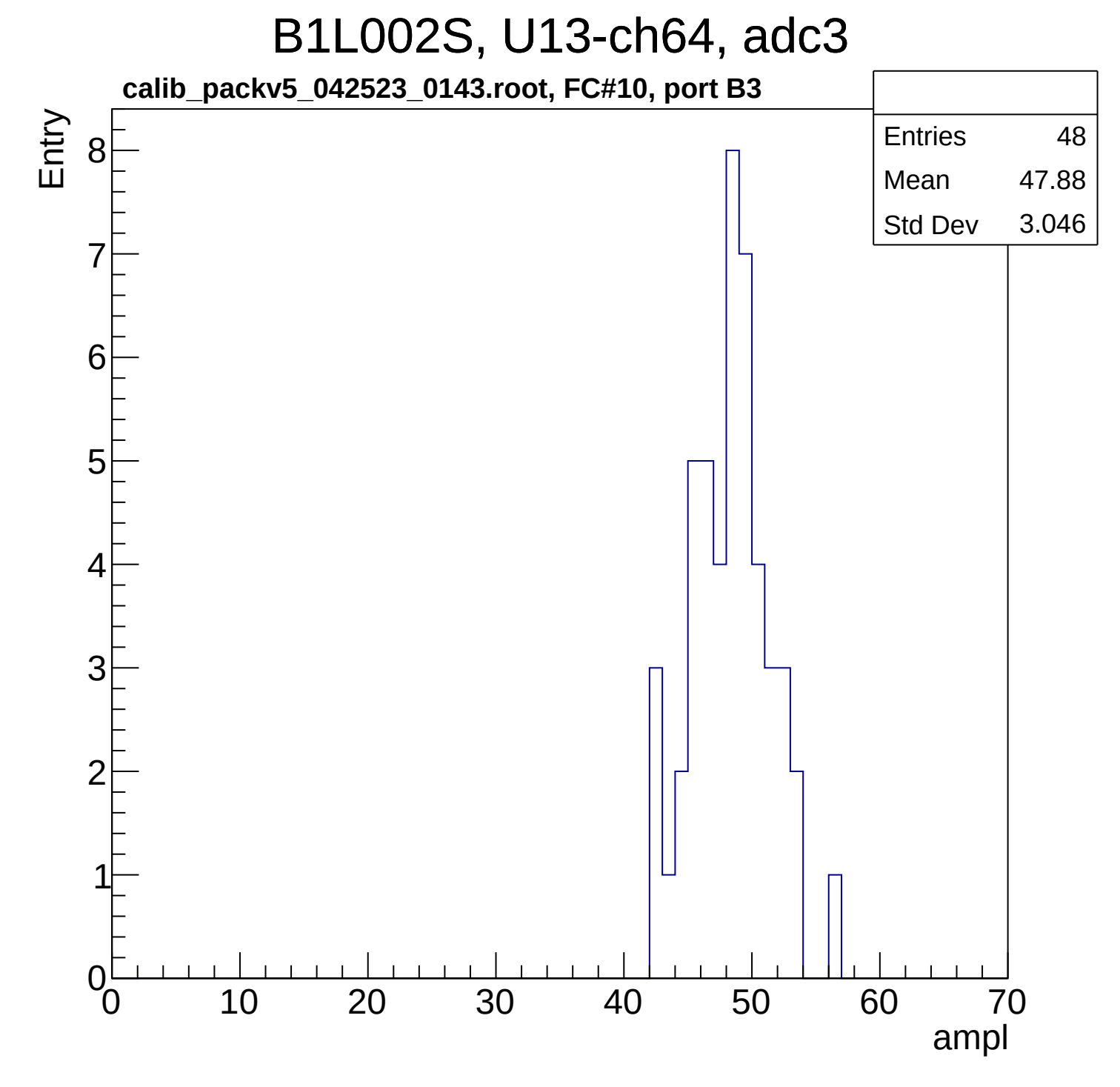
60
70


B1L002S, U13-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 47.88
Std Dev 3.046
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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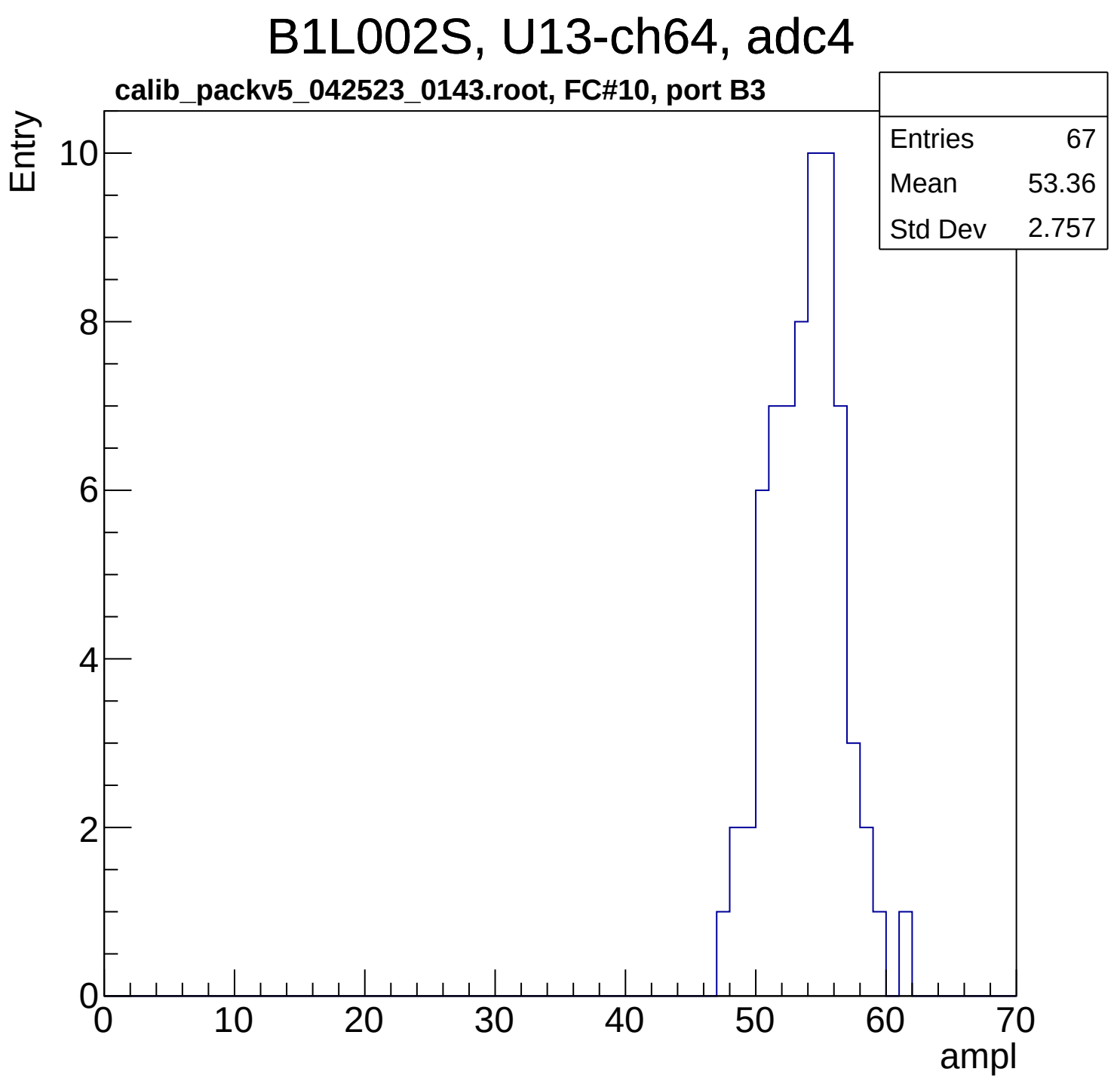
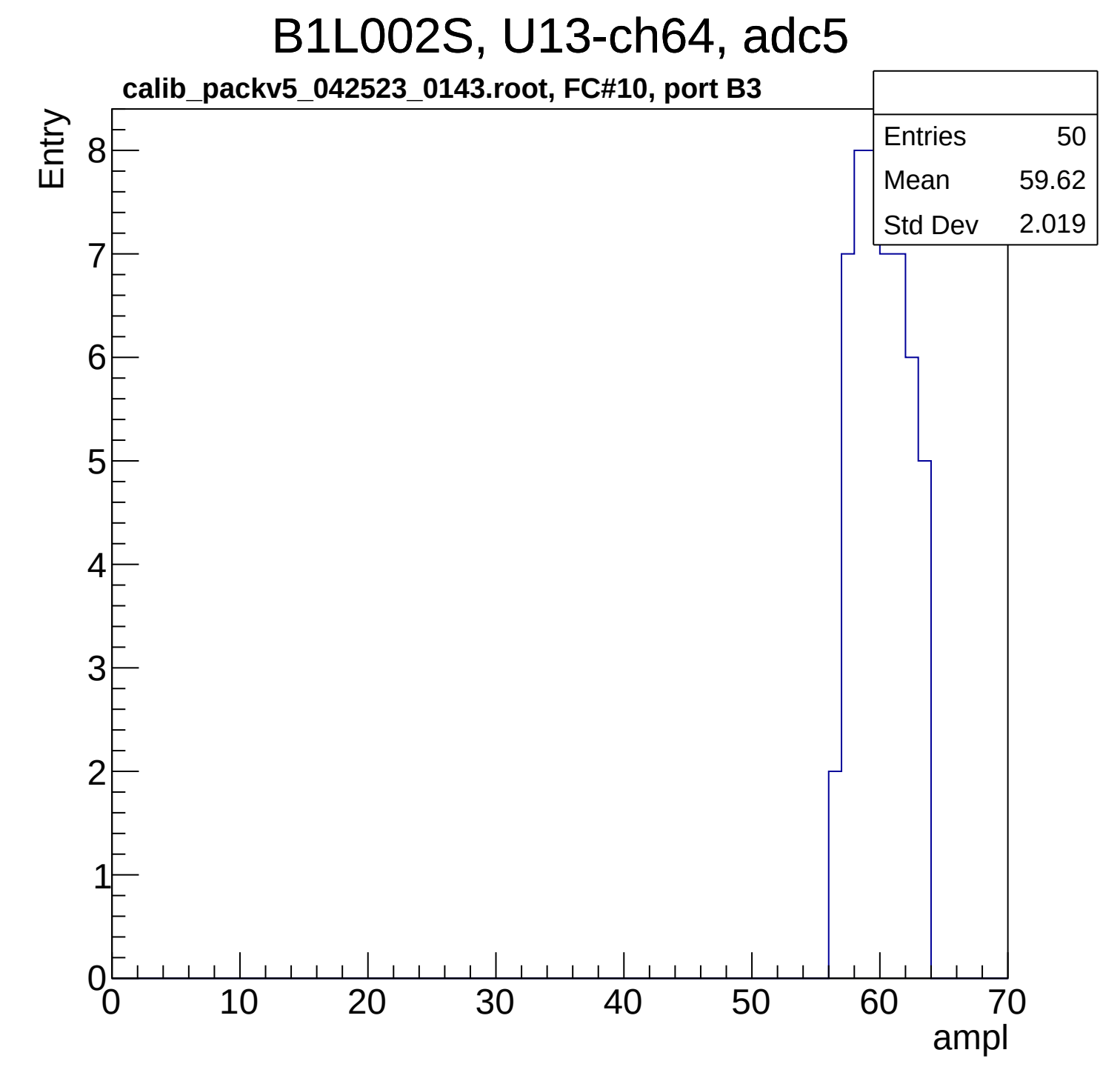
50
60
70


B1L002S, U13-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.36
Std Dev 2.757
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.62
Std Dev 2.019
Entry
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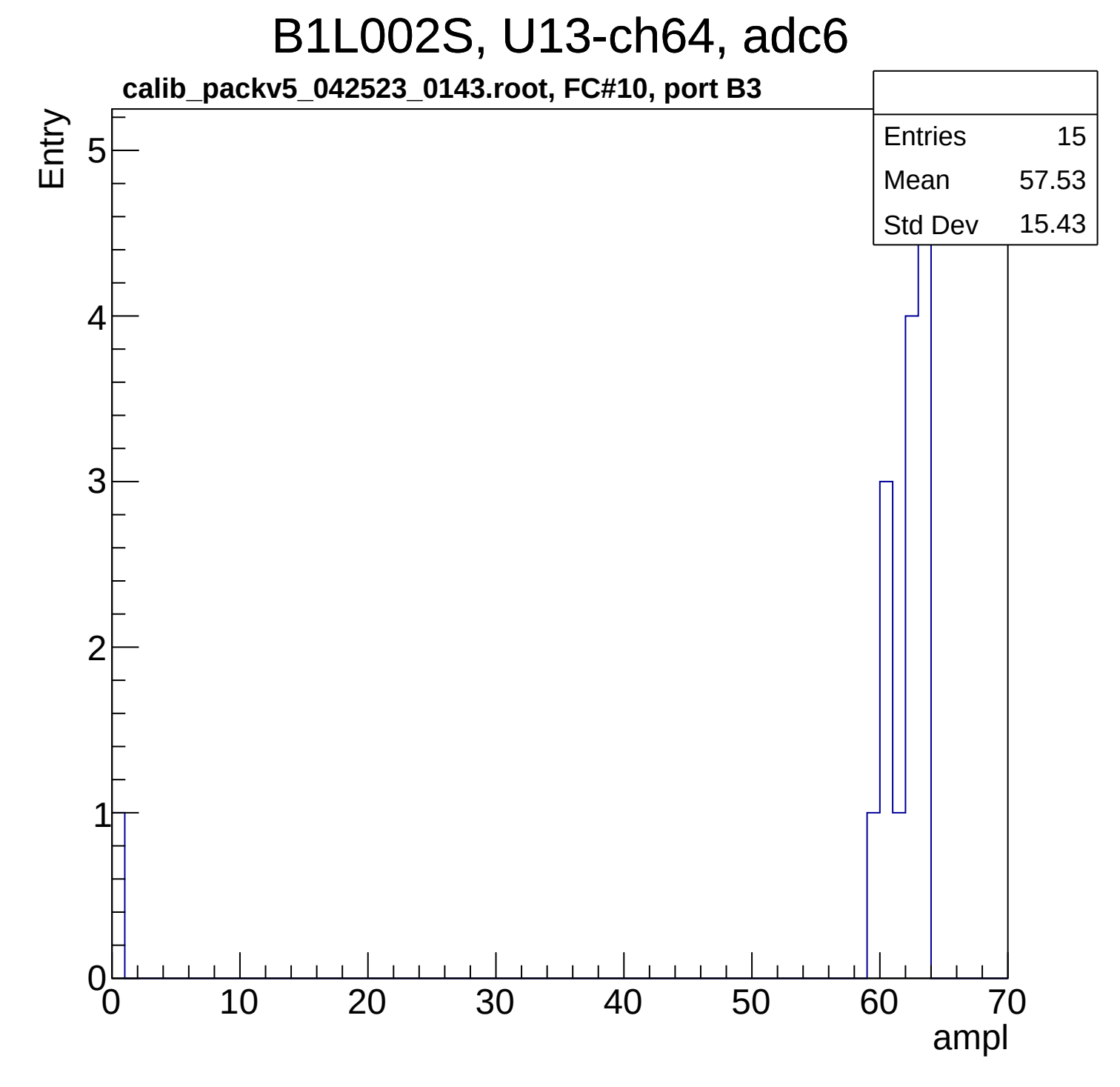
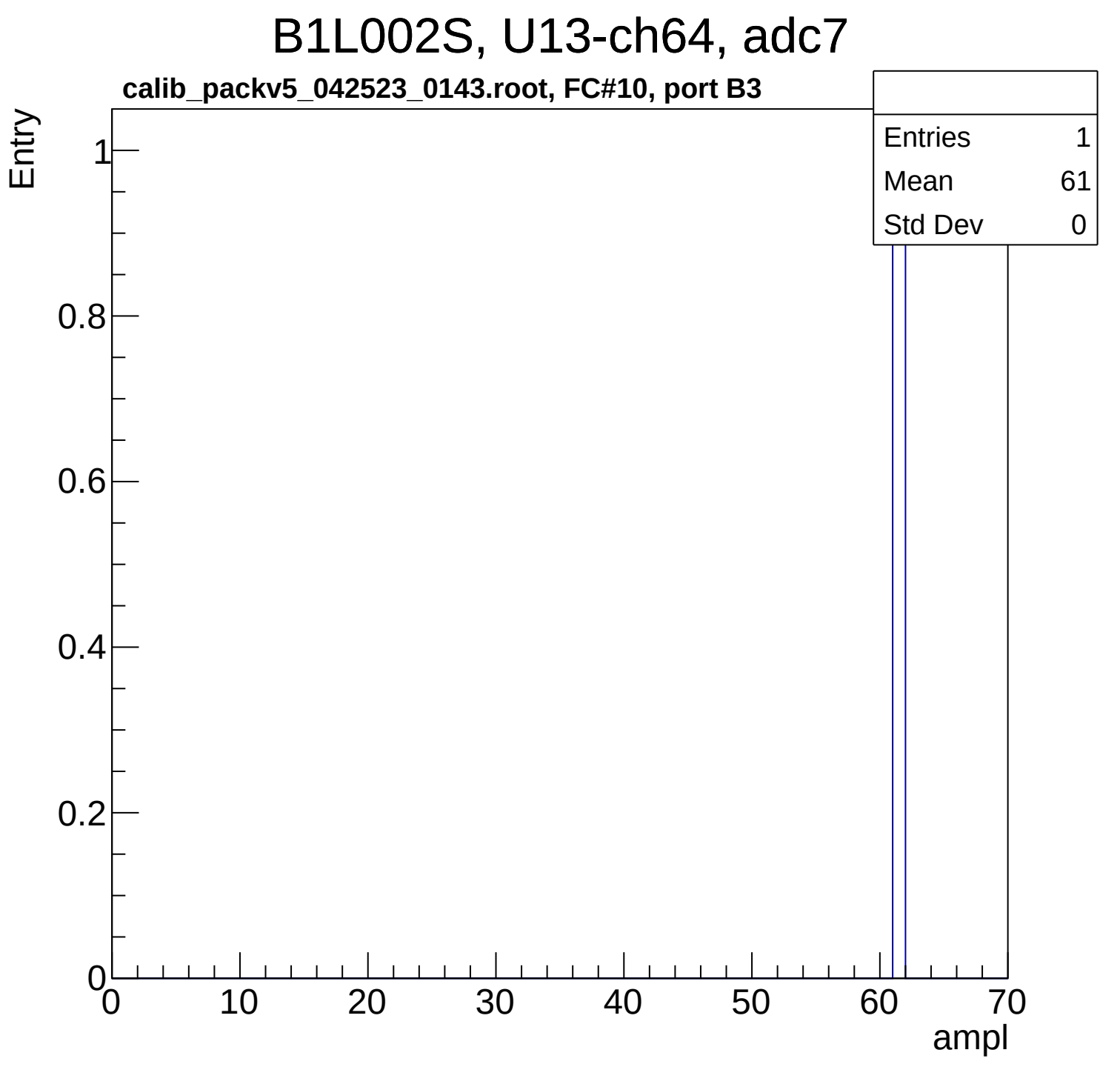
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.53
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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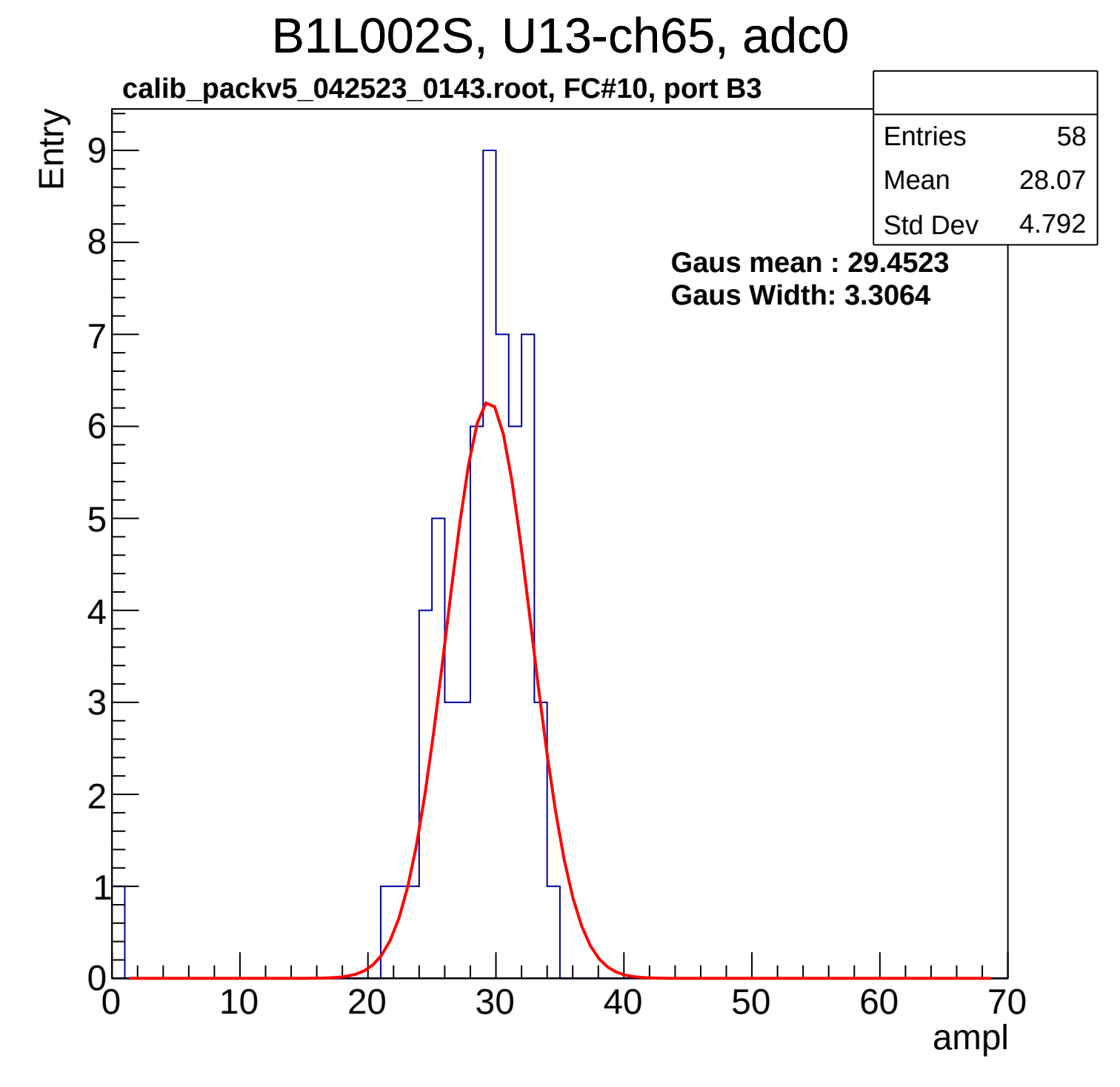
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.07
Std Dev 4.792
Gaus mean : 29.4523
Gaus Width: 3.3064
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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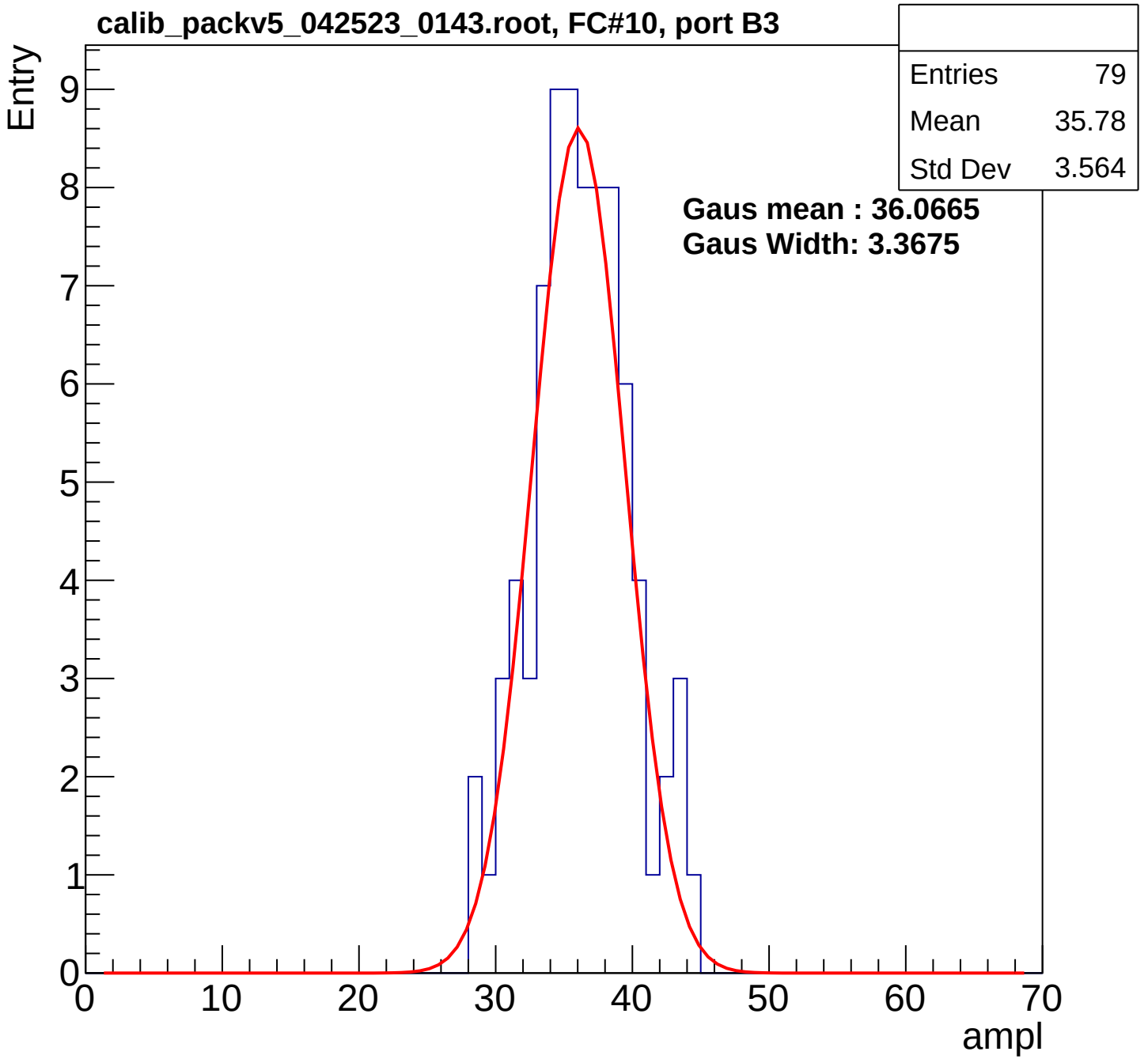
30
40
50
60
70


B1L002S, U13-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.78
Std Dev 3.564
Gaus mean : 36.0665
Gaus Width: 3.3675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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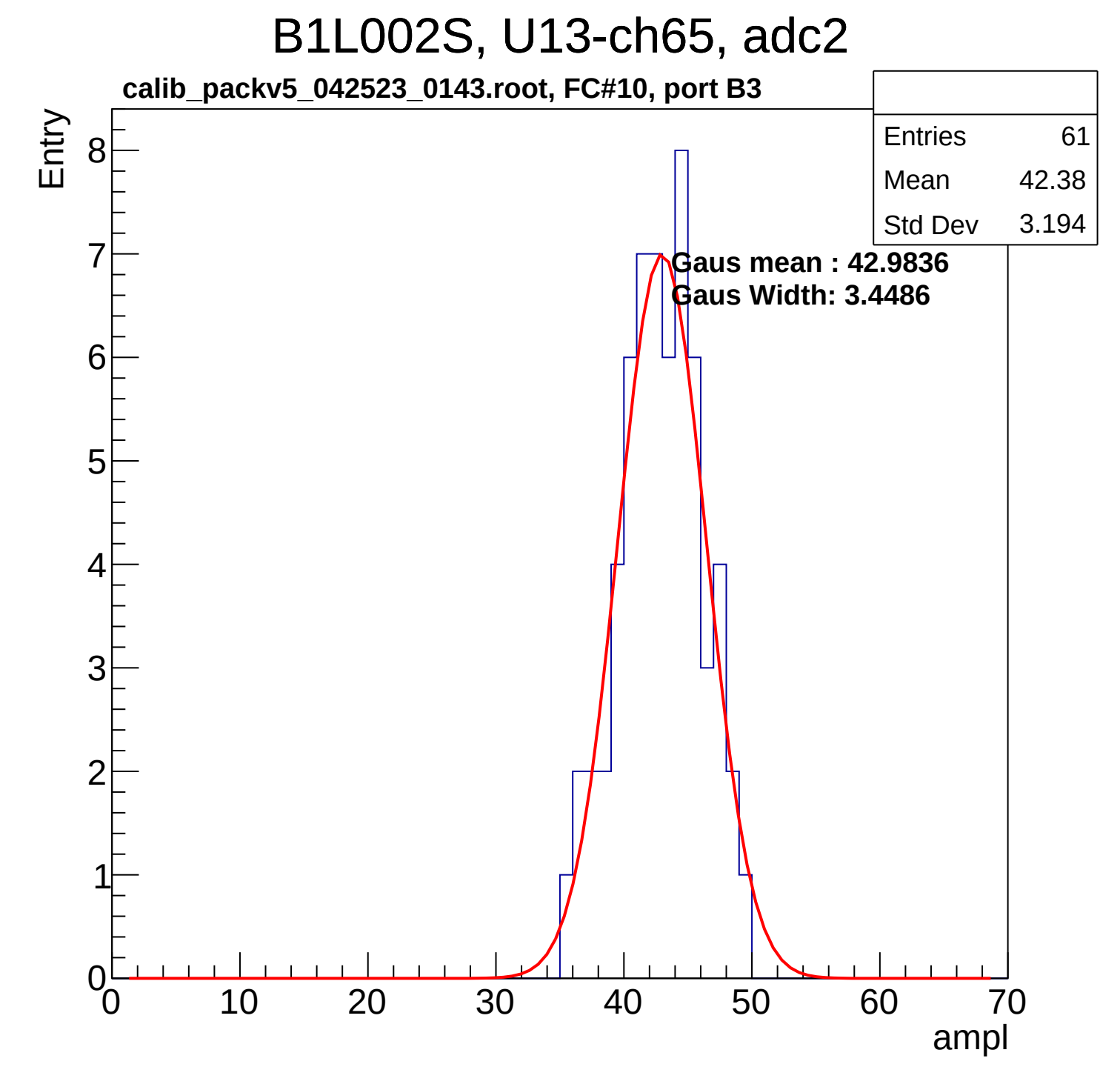
B1L002S, U13-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 42.38
Std Dev 3.194
Gaus mean : 42.9836
Gaus Width: 3.4486
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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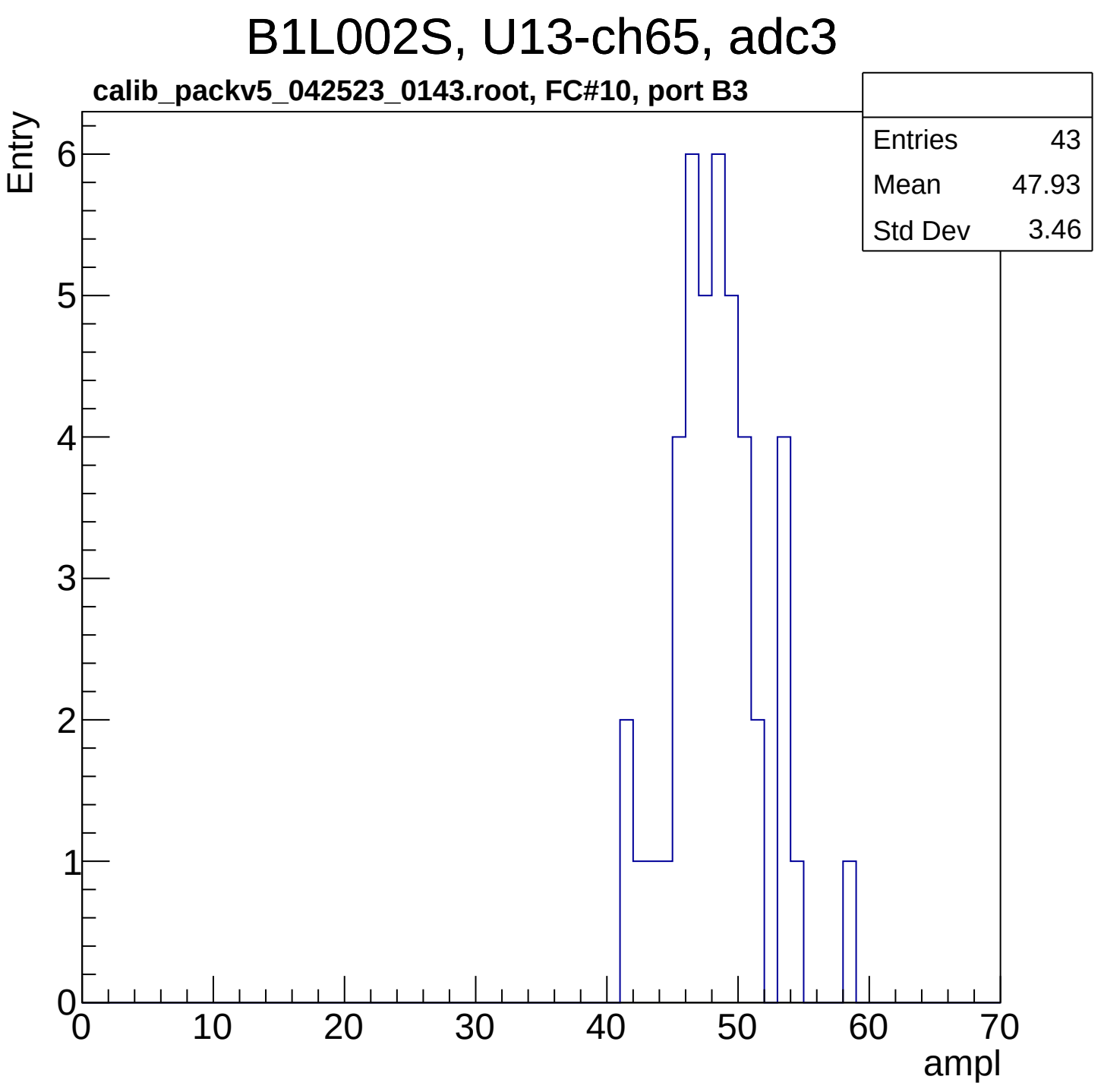
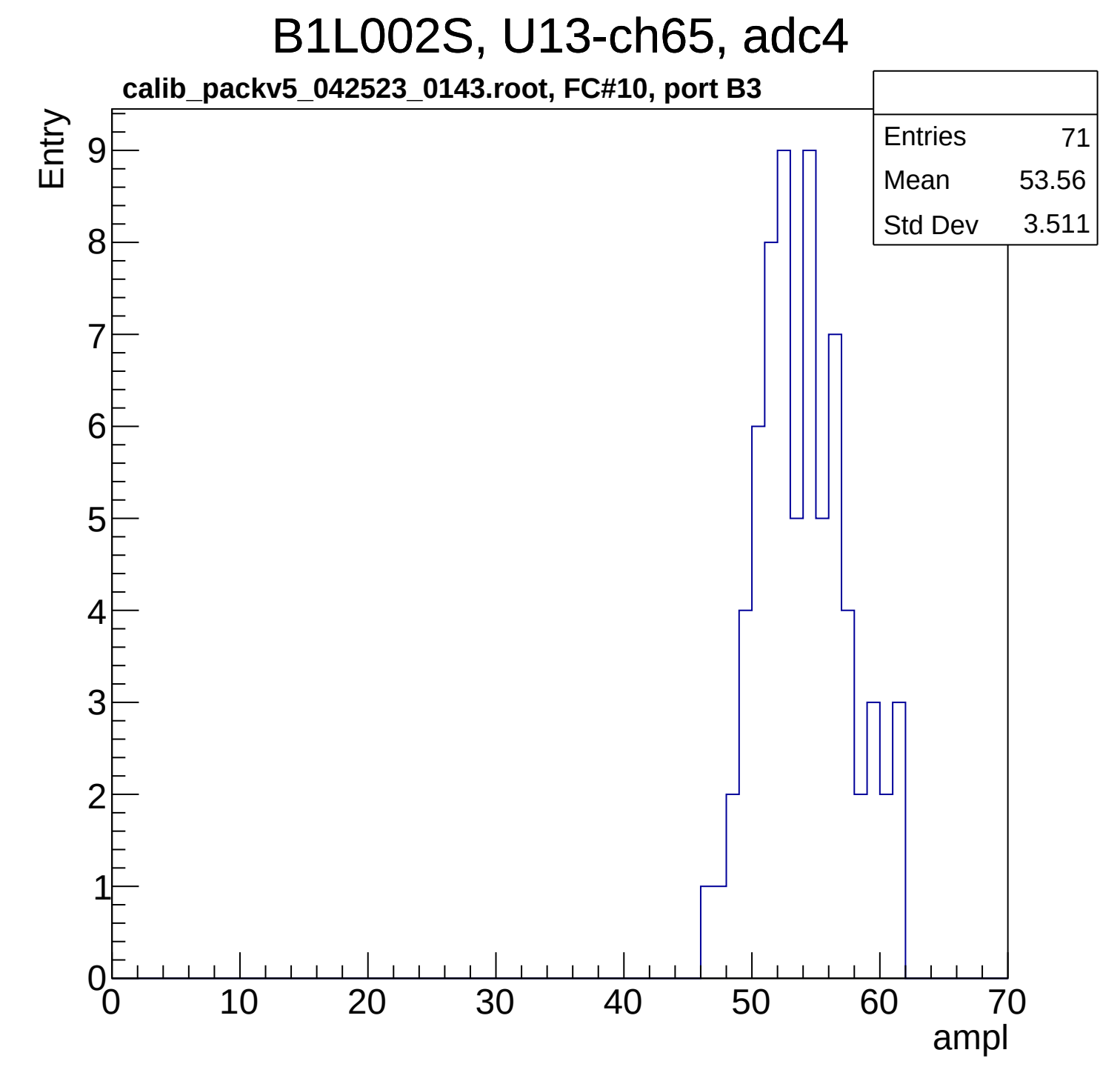

B1L002S, U13-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 47.93
Std Dev 3.46
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 53.56
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
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9
0
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60
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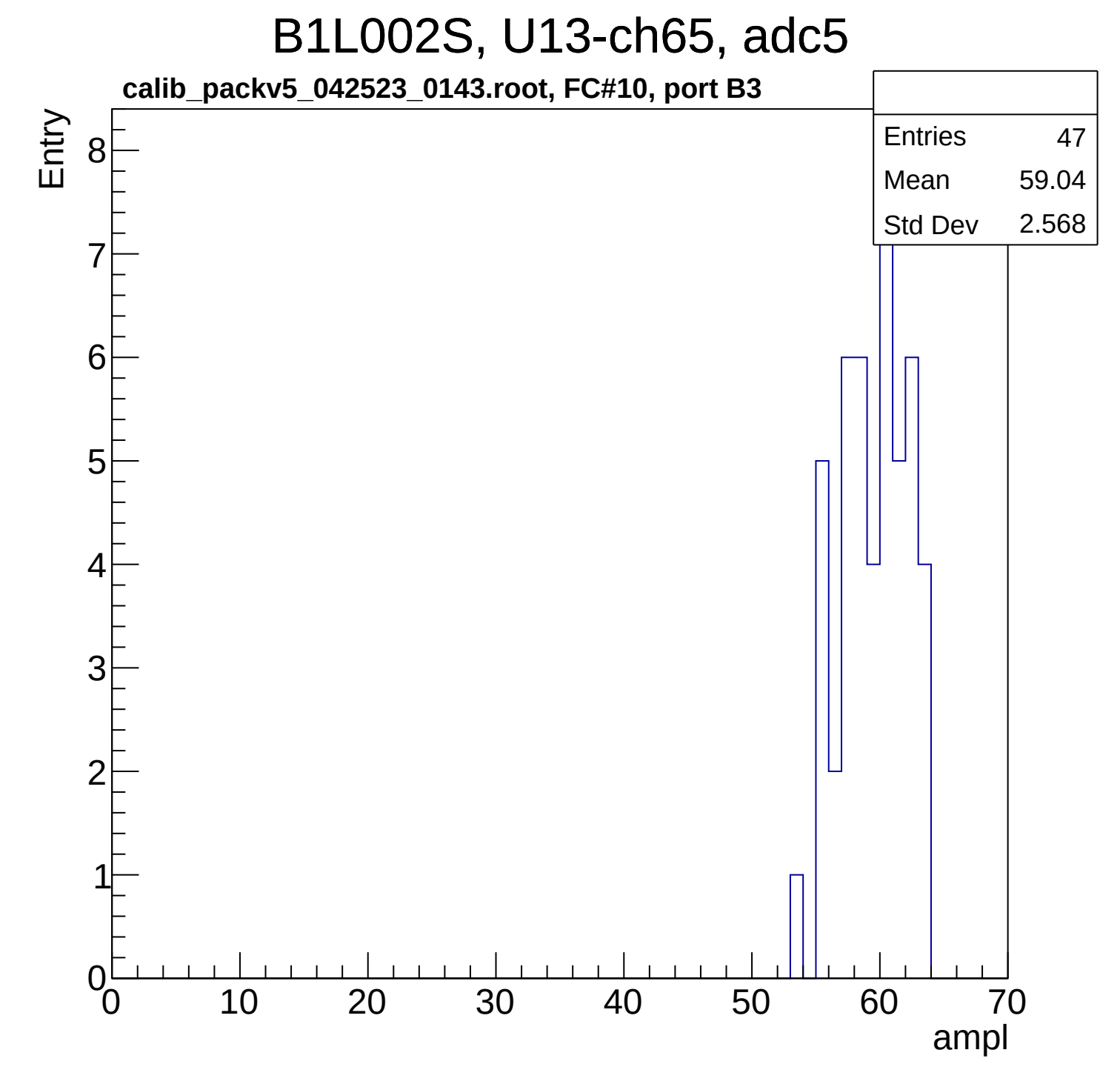
B1L002S, U13-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.04
Std Dev 2.568
Entry
ampl
0
1
2
3
4
5
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7
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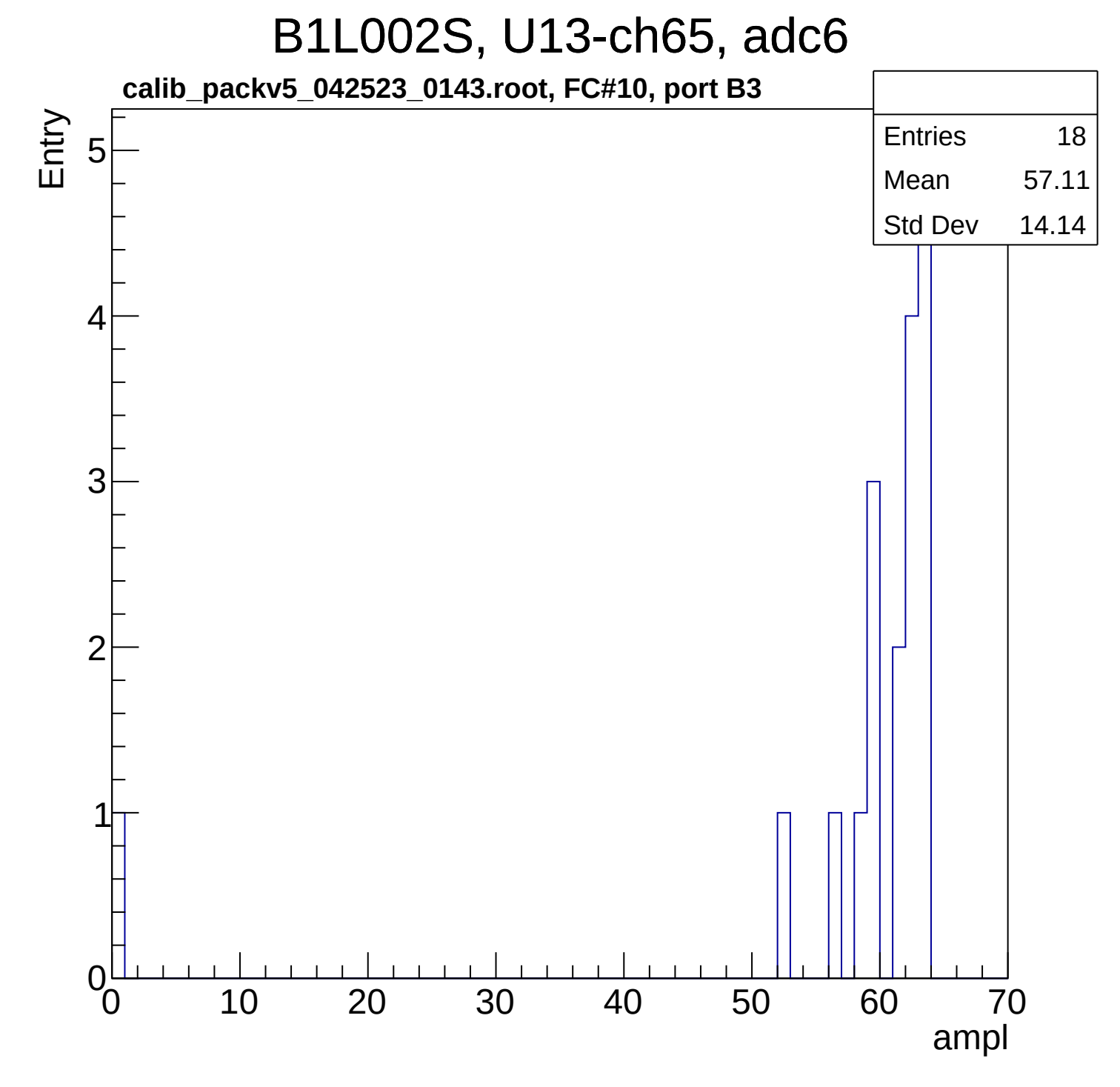
B1L002S, U13-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.11
Std Dev 14.14
Entry
0
1
2
3
4
5
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10
20
30
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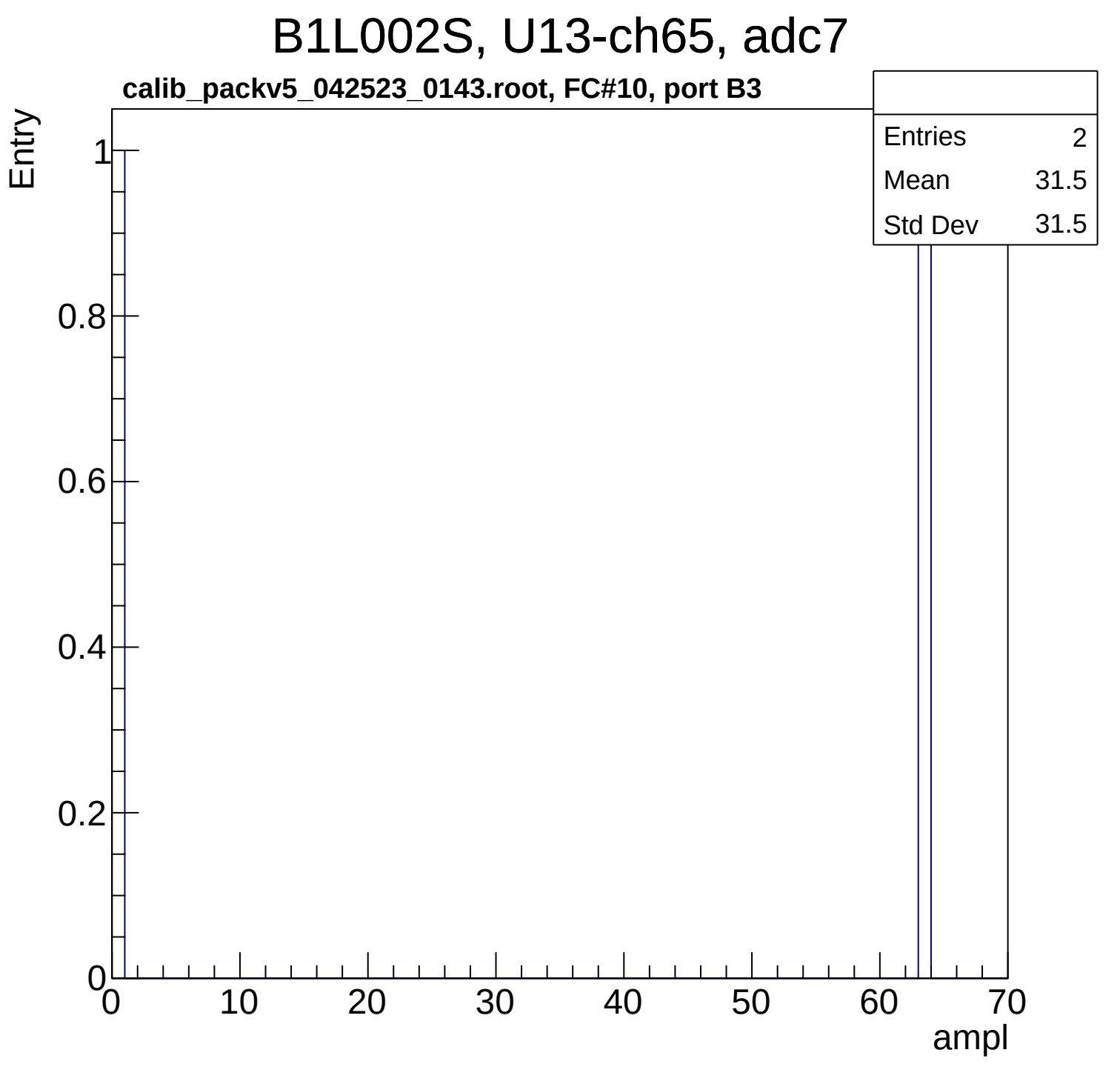
50
60
70
ampl


B1L002S, U13-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
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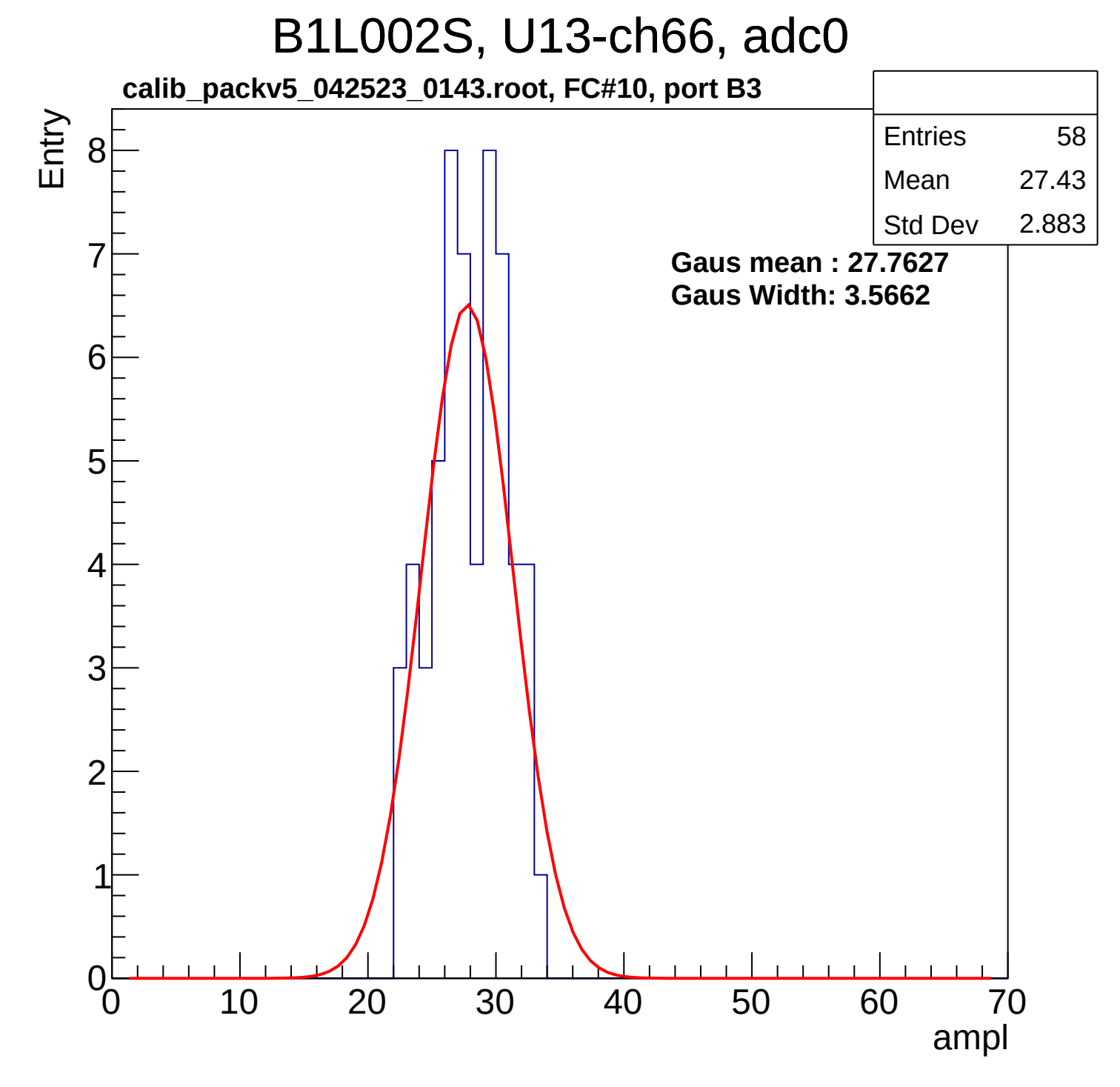
50
60
70
ampl


B1L002S, U13-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.43
Std Dev 2.883
Gaus mean : 27.7627
Gaus Width: 3.5662
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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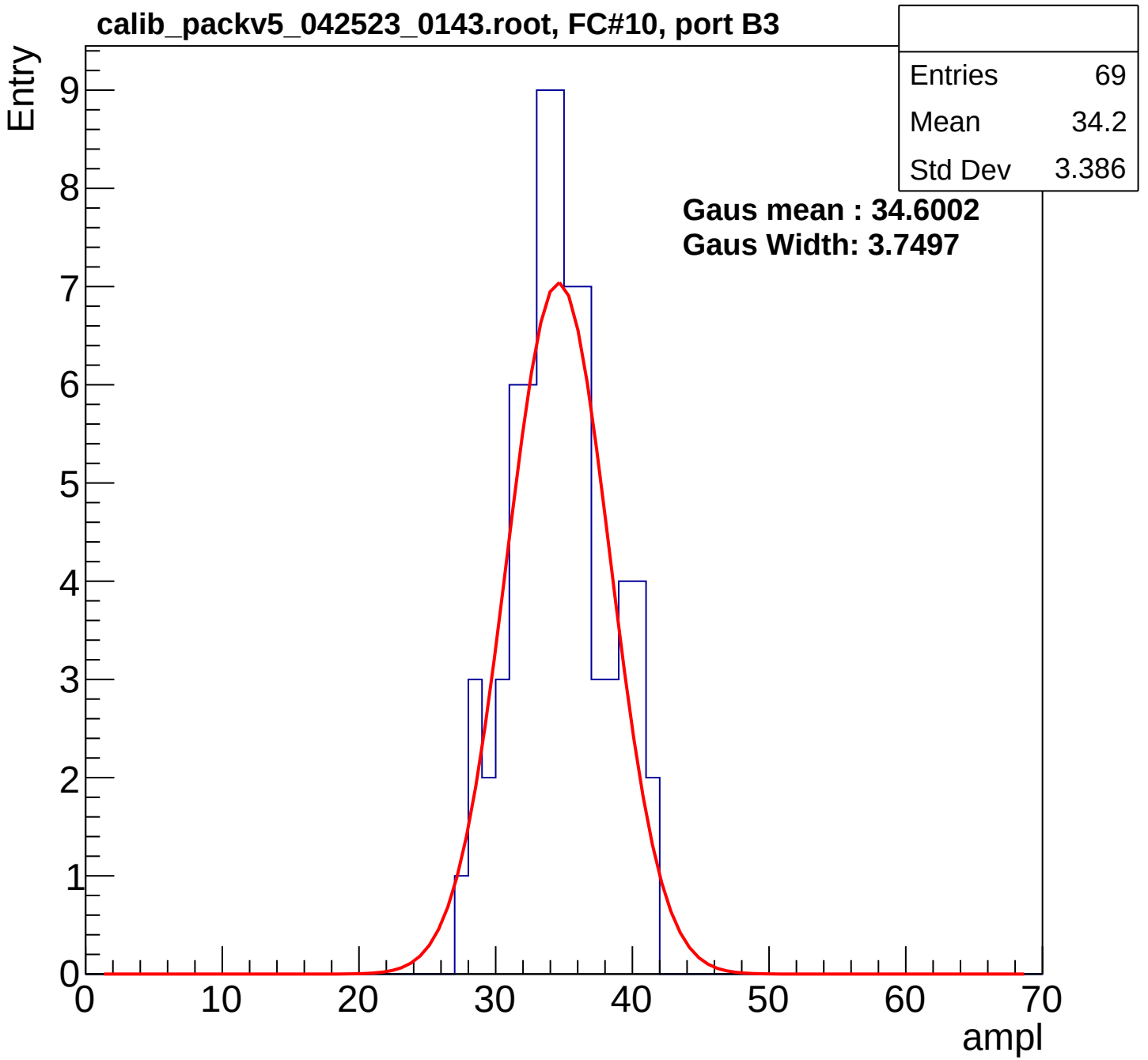
B1L002S, U13-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.2
Std Dev 3.386
Gaus mean : 34.6002
Gaus Width: 3.7497
Entry
ampl
0
10
20
30
40
50
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1
2
3
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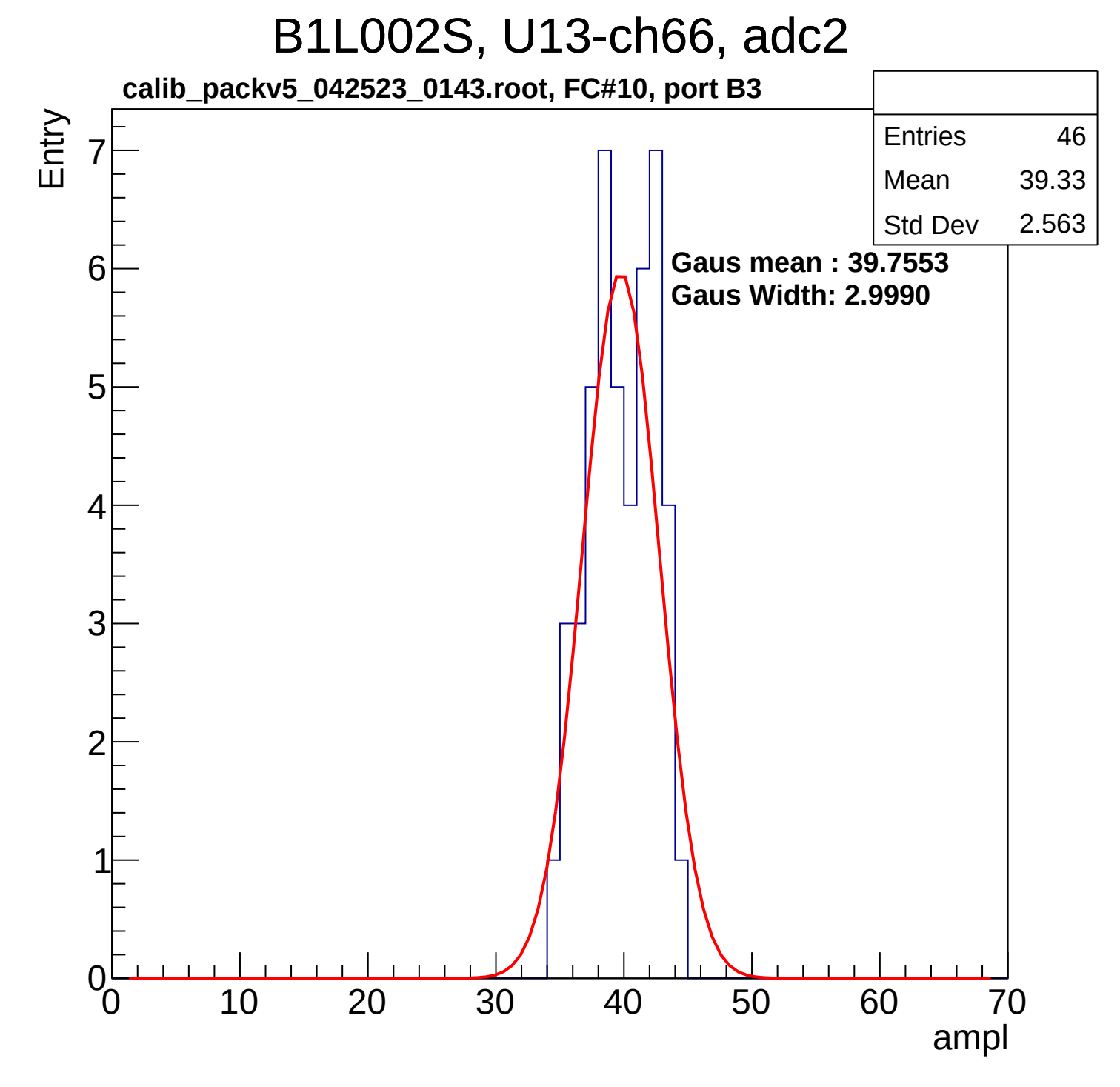
6
7
8
9


B1L002S, U13-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 39.33
Std Dev 2.563
Gaus mean : 39.7553
Gaus Width: 2.9990
Entry
ampl
0
1
2
3
4
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6
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0
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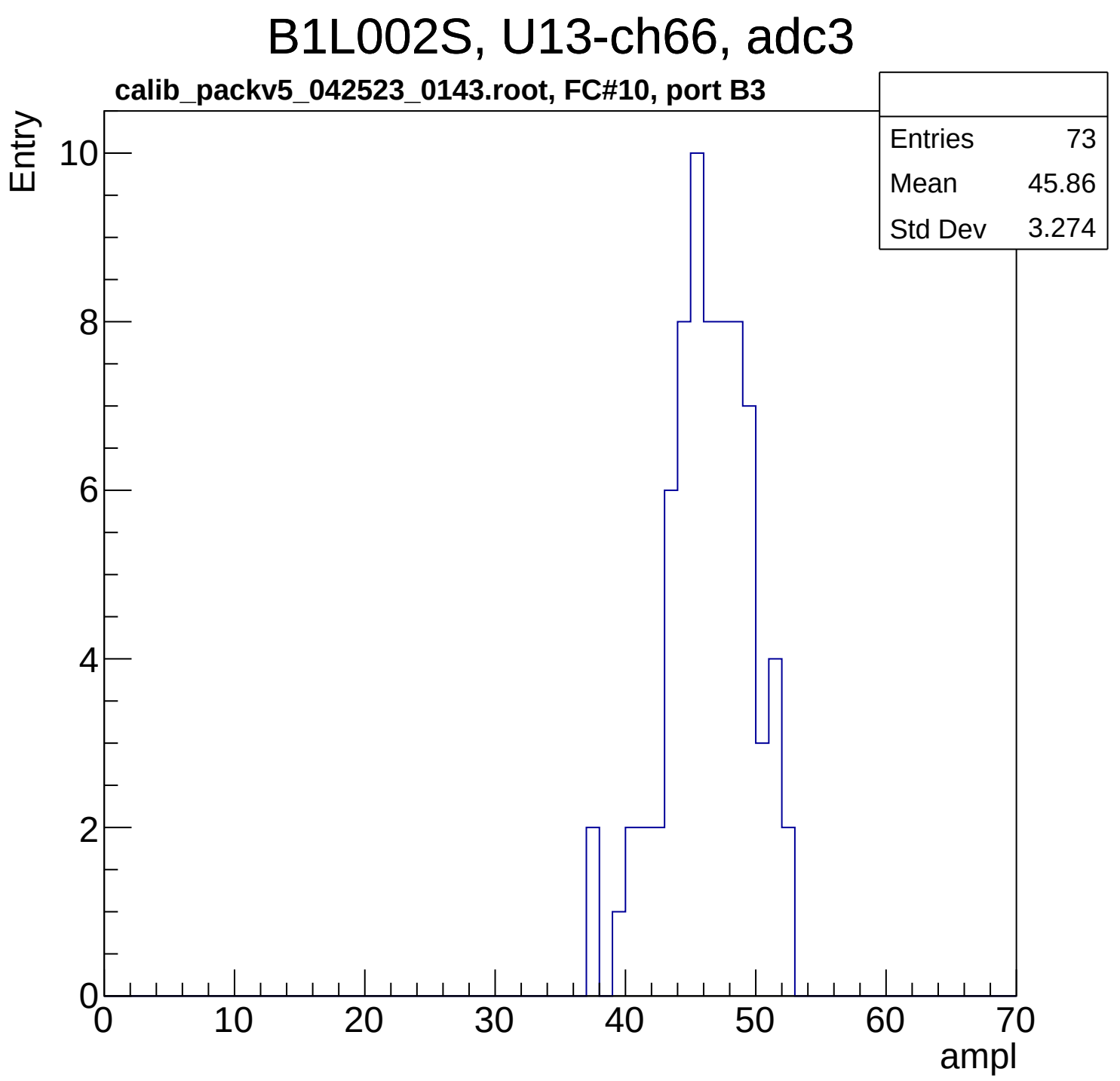
B1L002S, U13-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.86
Std Dev 3.274
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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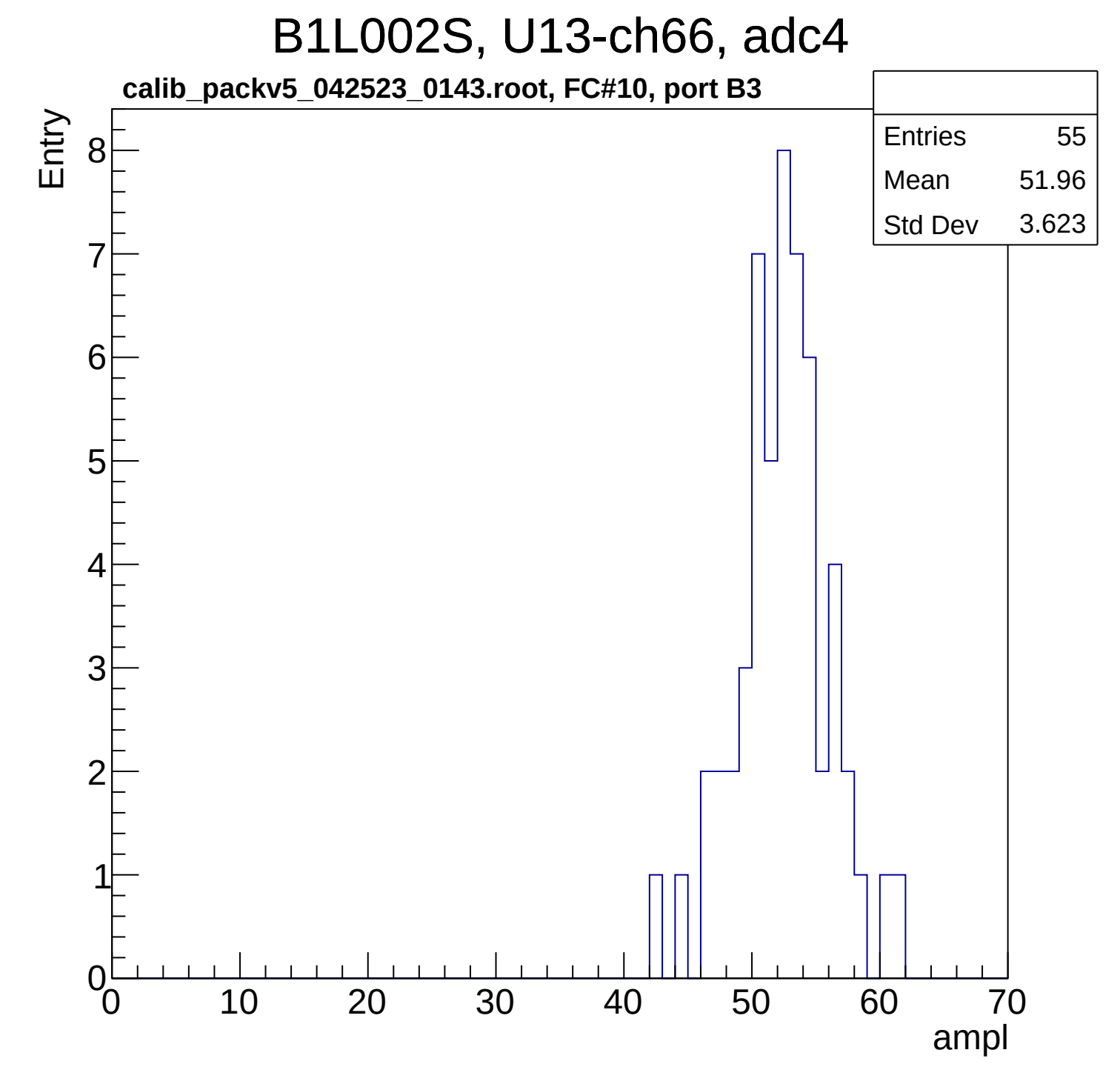
B1L002S, U13-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.96
Std Dev 3.623
Entry
ampl
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1
2
3
4
5
6
7
8
0
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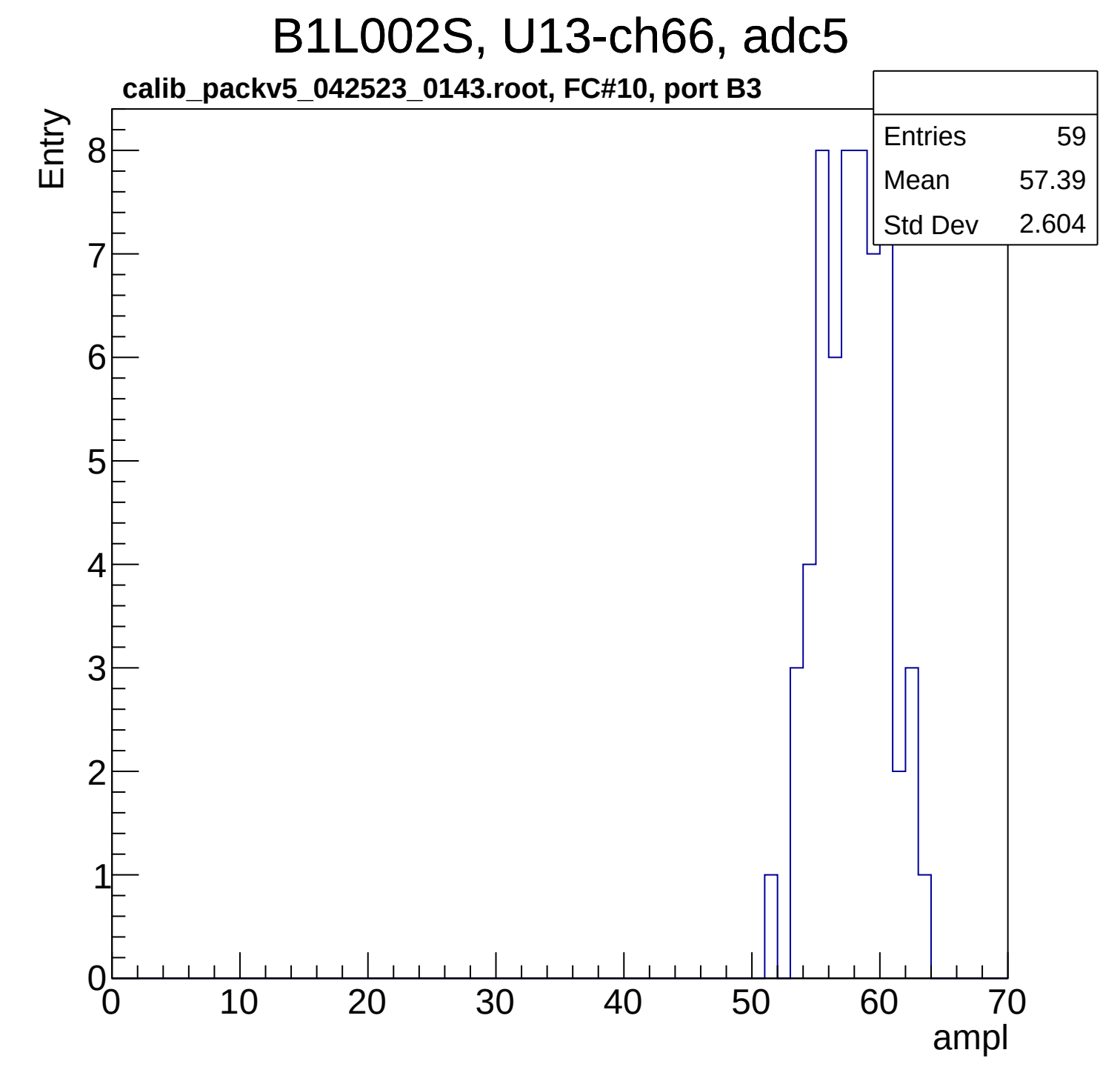
B1L002S, U13-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.39
Std Dev 2.604
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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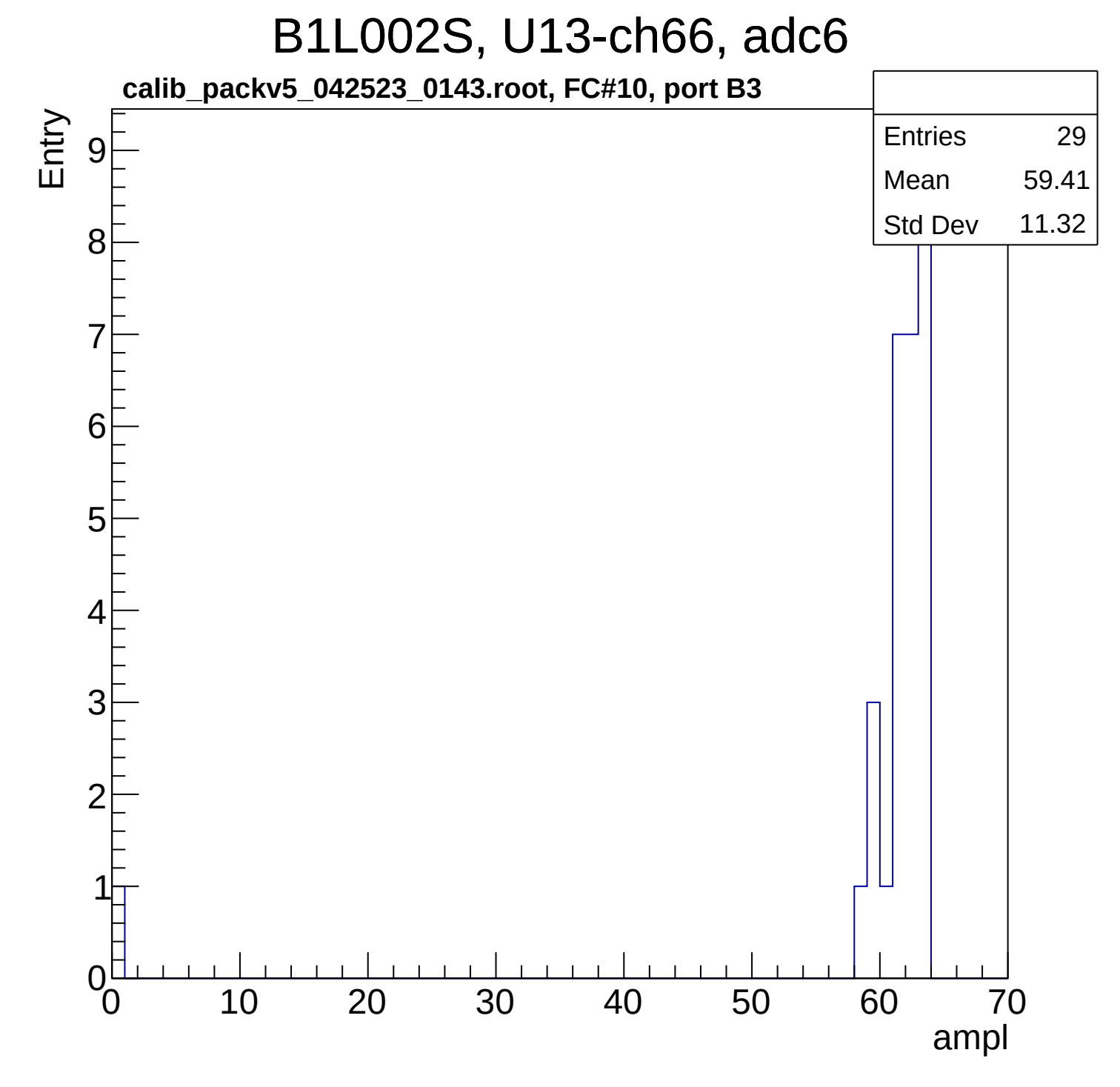
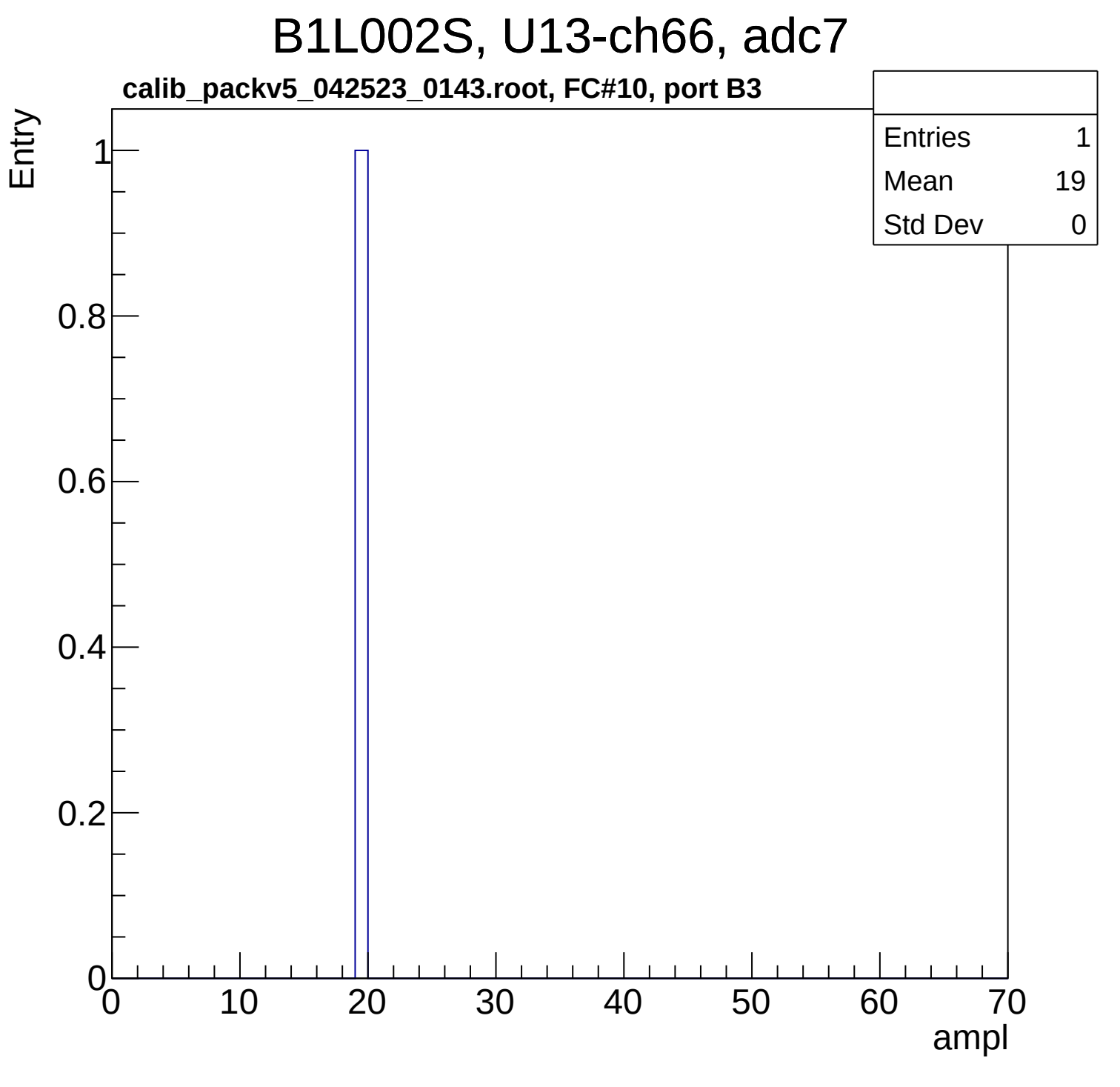

B1L002S, U13-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.41
Std Dev 11.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
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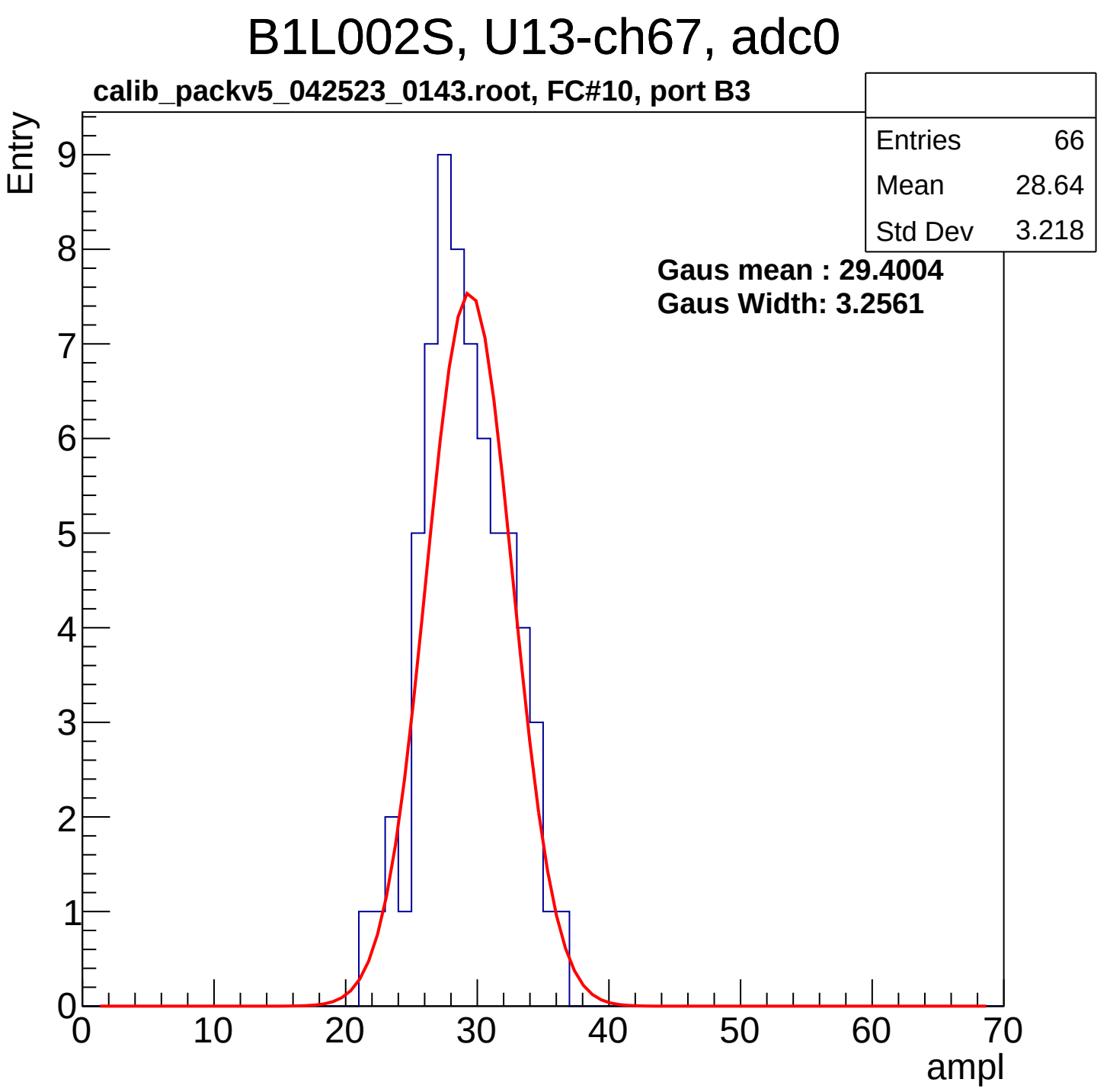
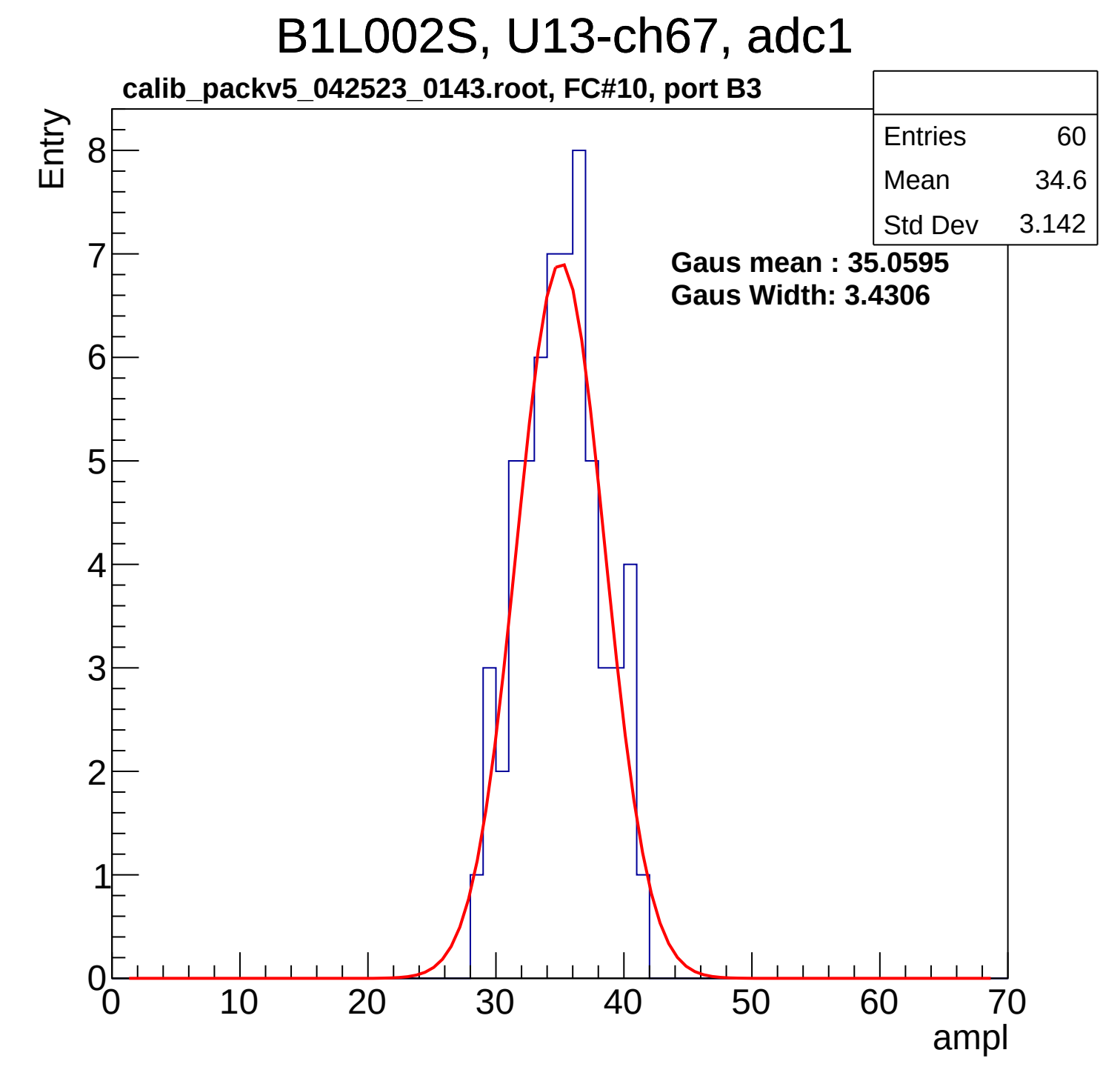
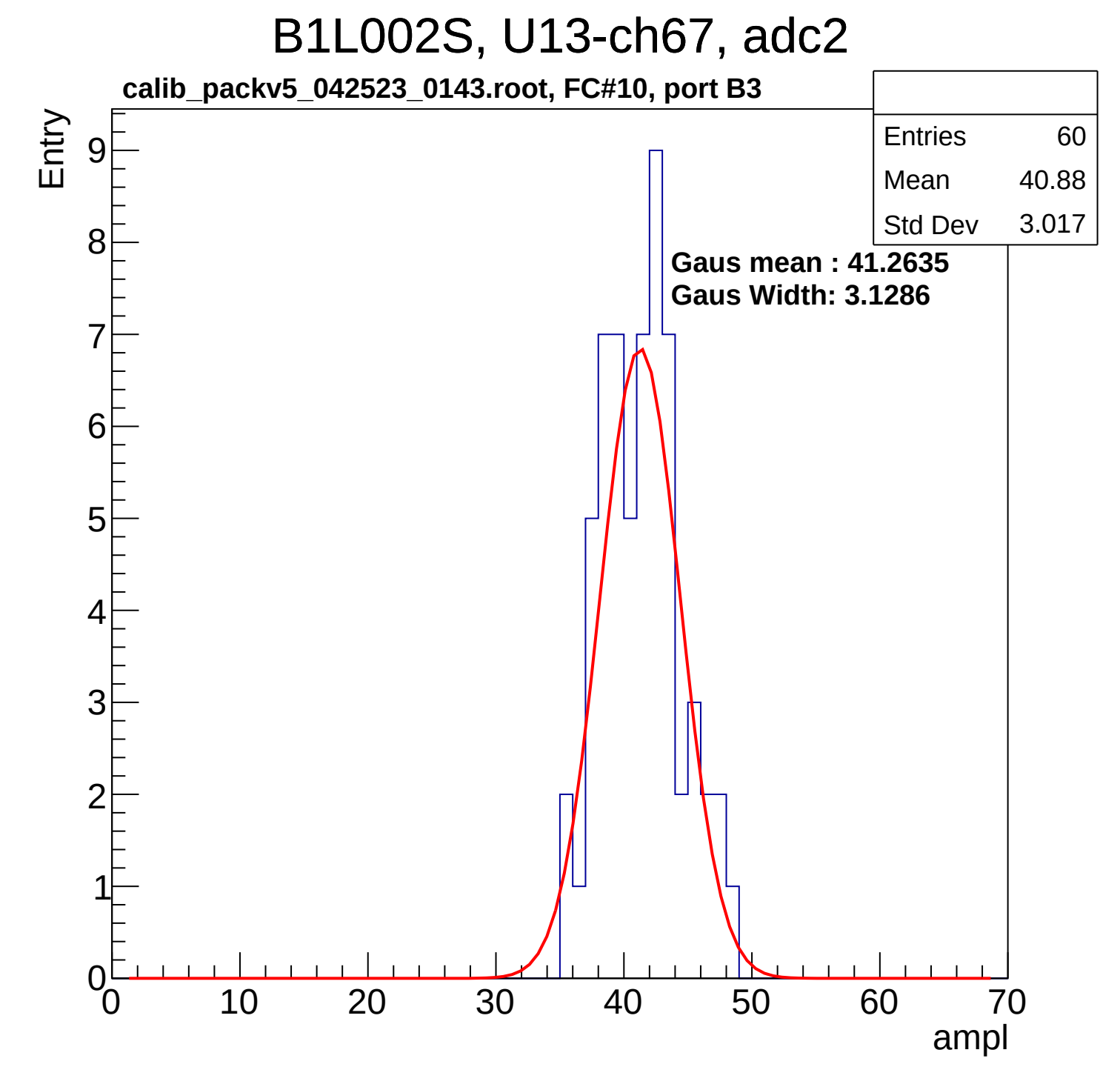
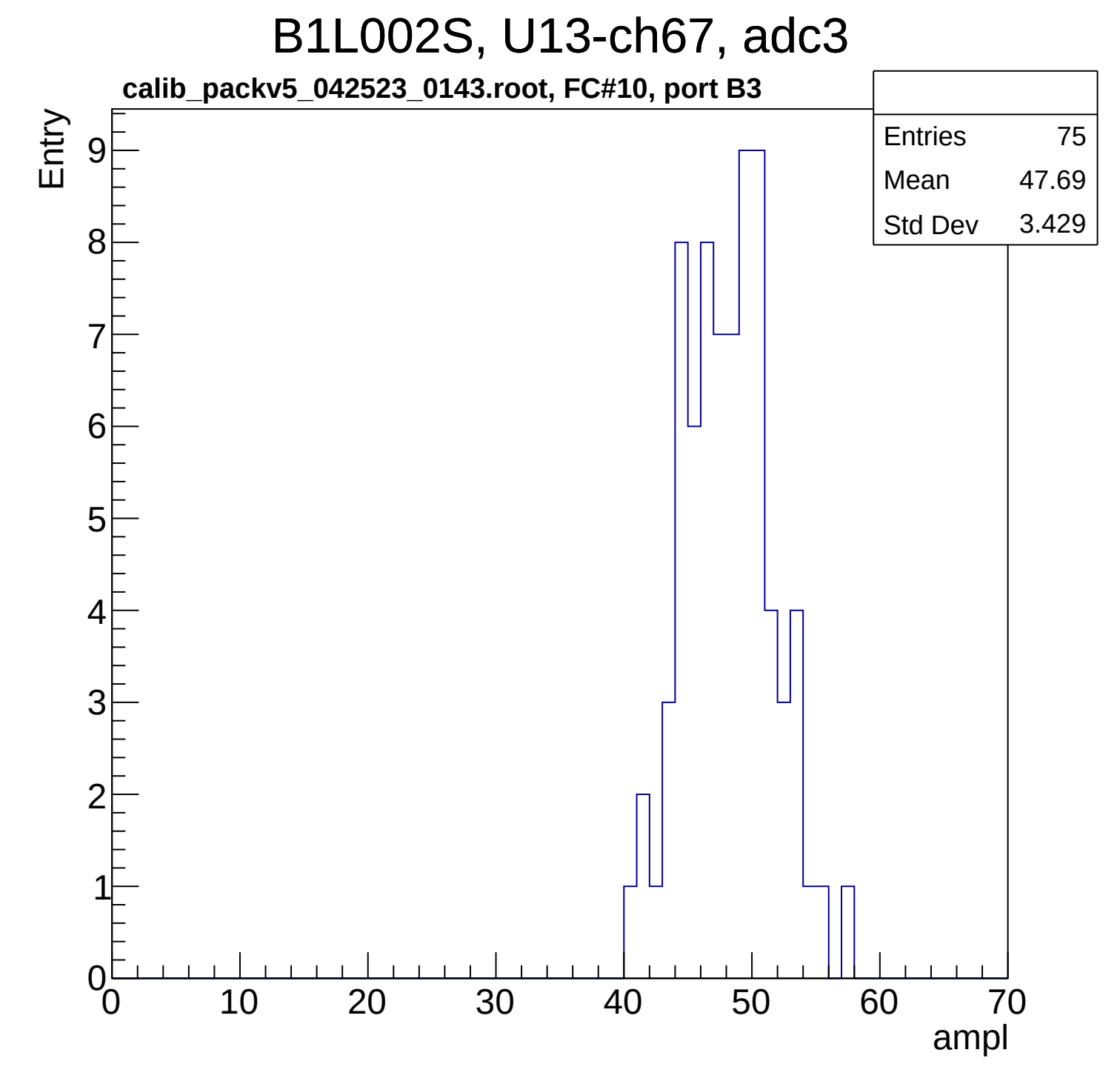
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.64
Std Dev 3.218
Gaus mean : 29.4004
Gaus Width: 3.2561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.6
Std Dev 3.142
Gaus mean : 35.0595
Gaus Width: 3.4306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.88
Std Dev 3.017
Gaus mean : 41.2635
Gaus Width: 3.1286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 47.69
Std Dev 3.429
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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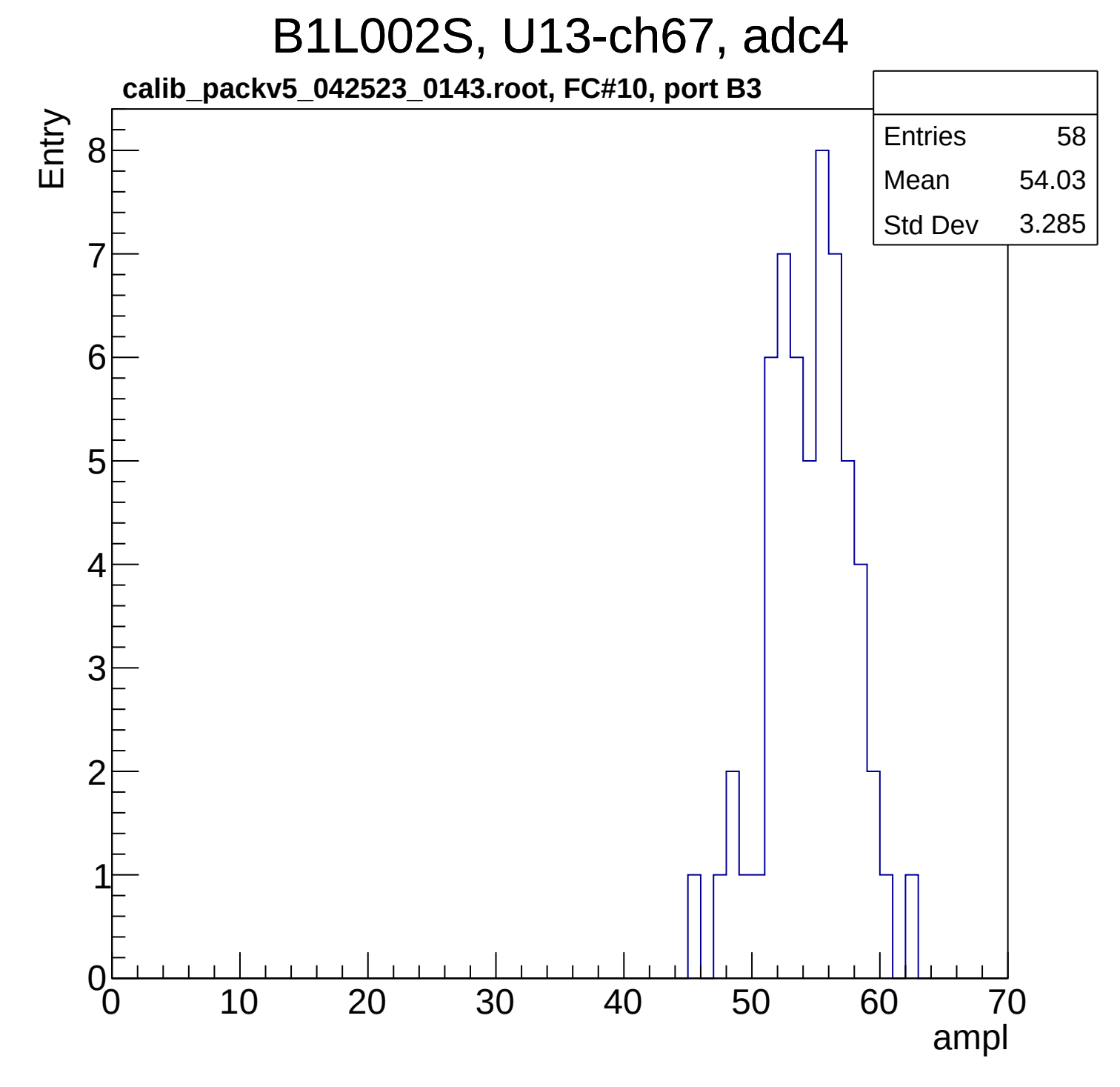
B1L002S, U13-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.03
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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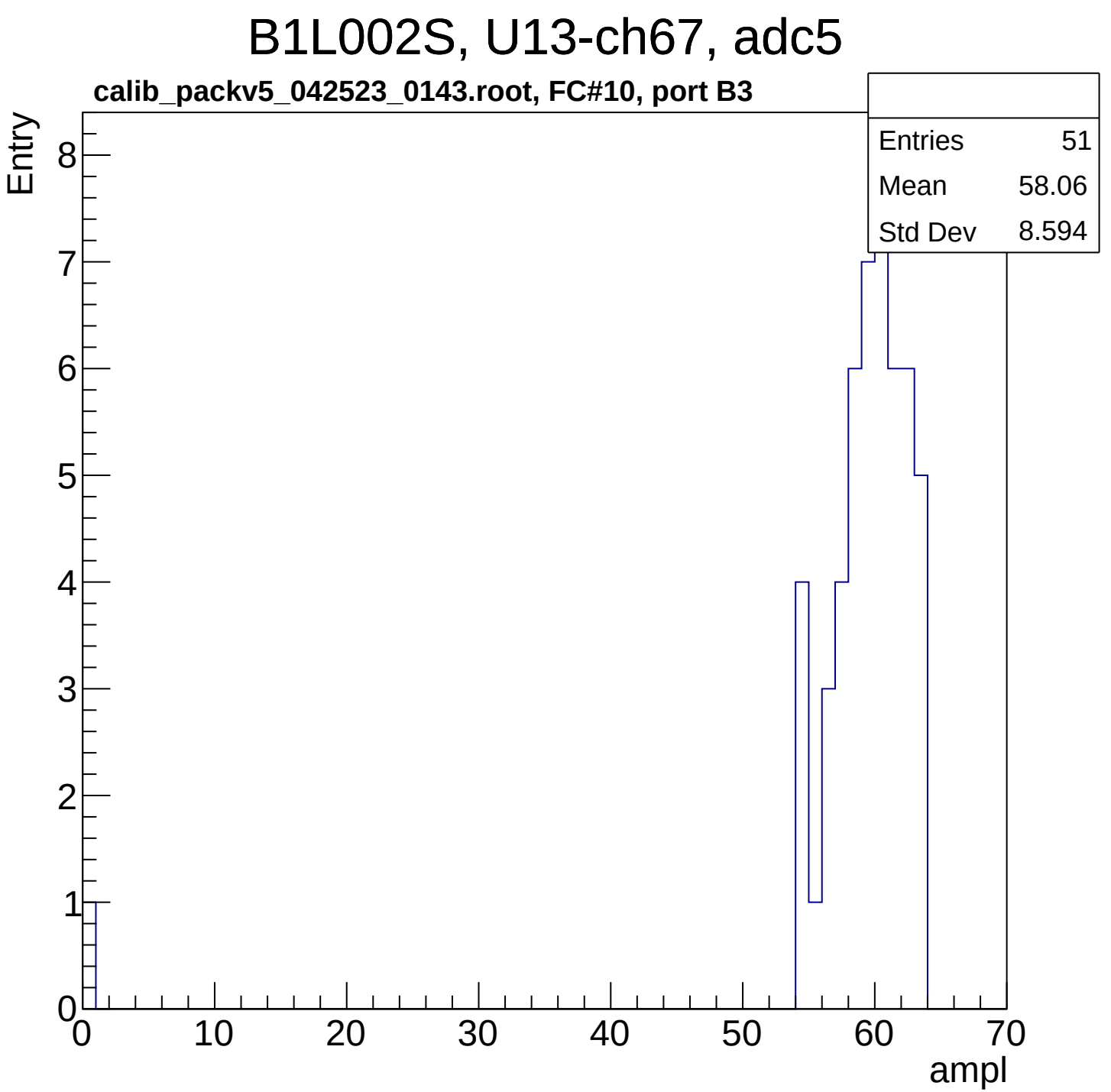
B1L002S, U13-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.06
Std Dev 8.594
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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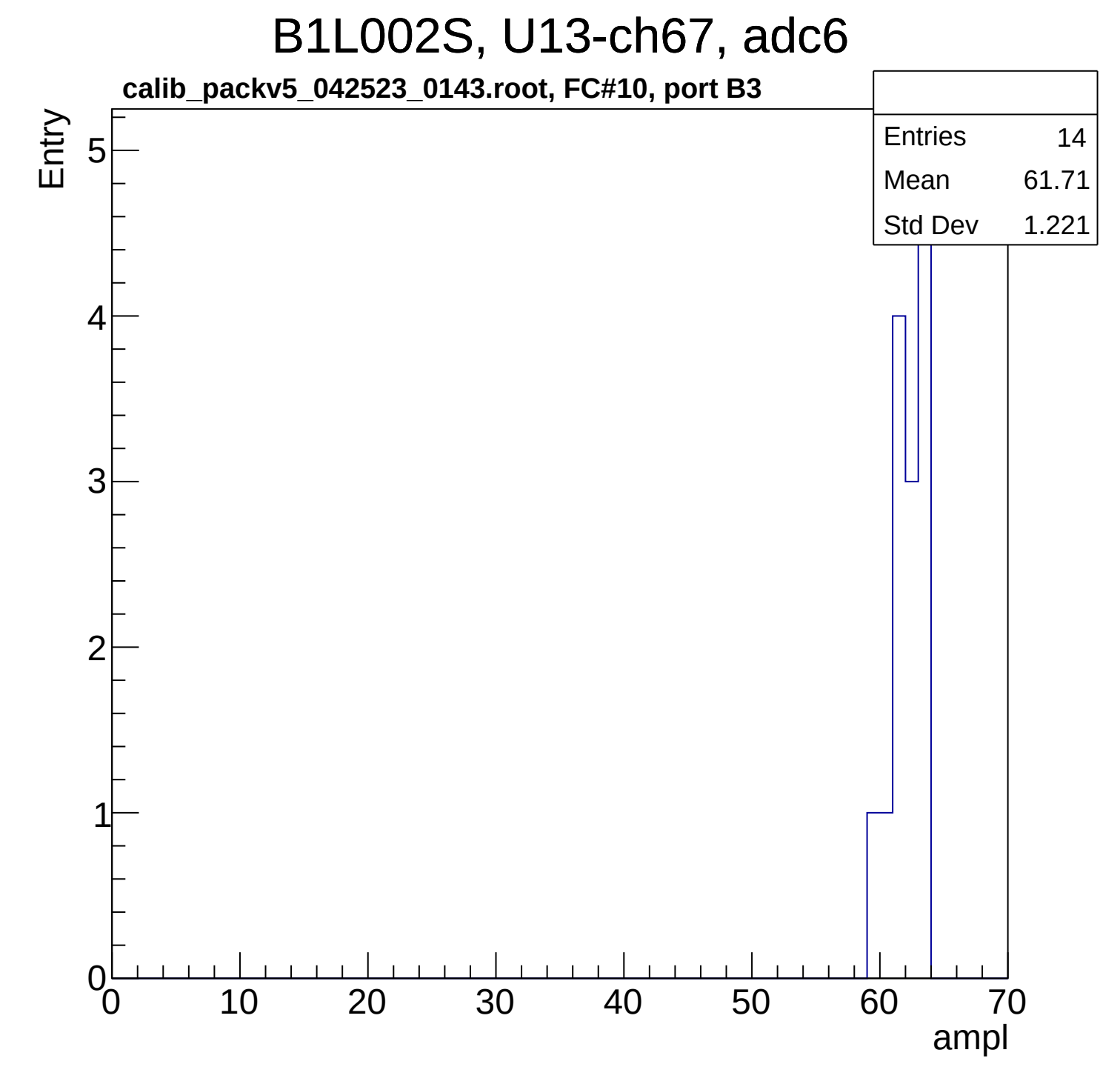
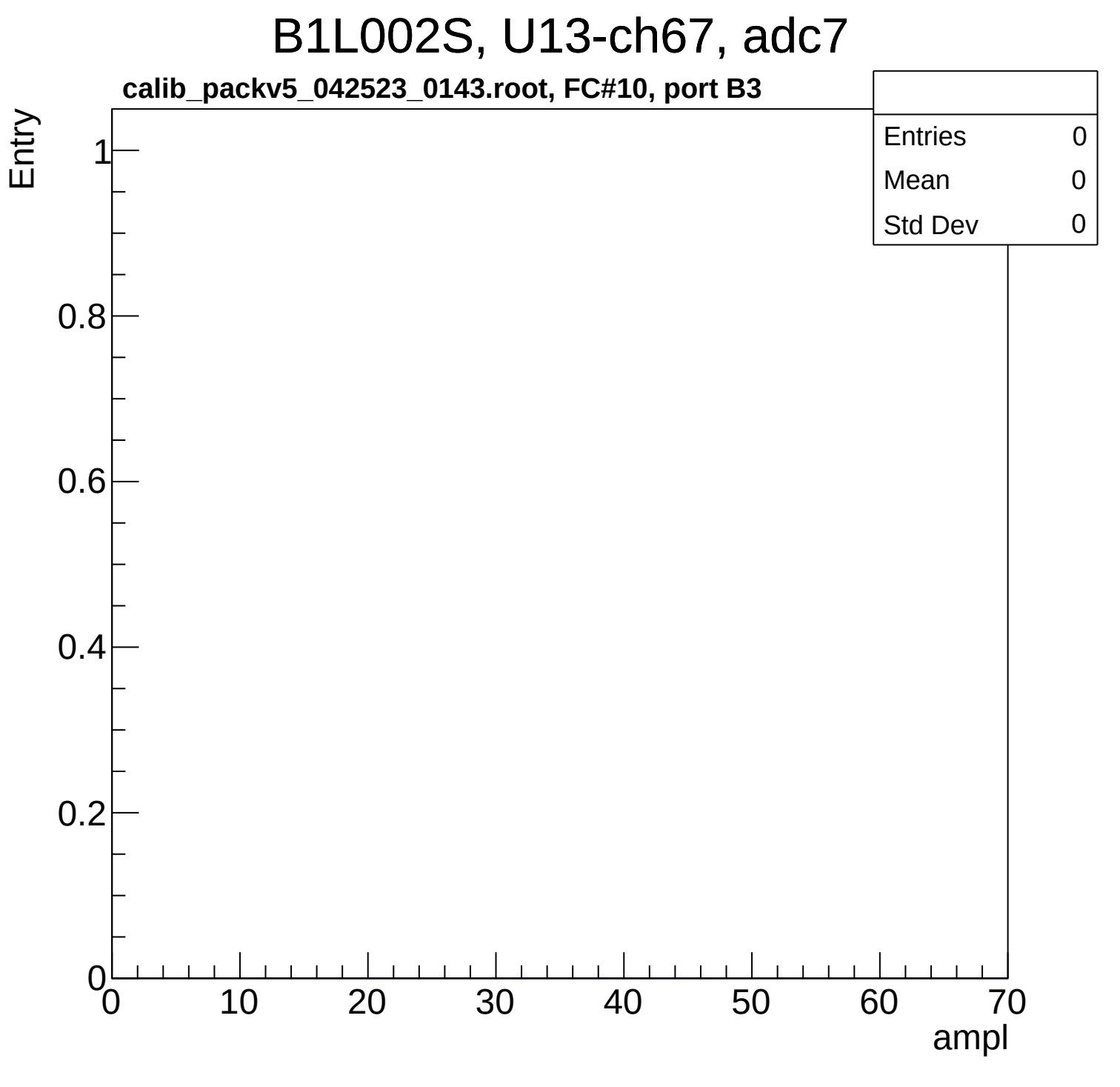
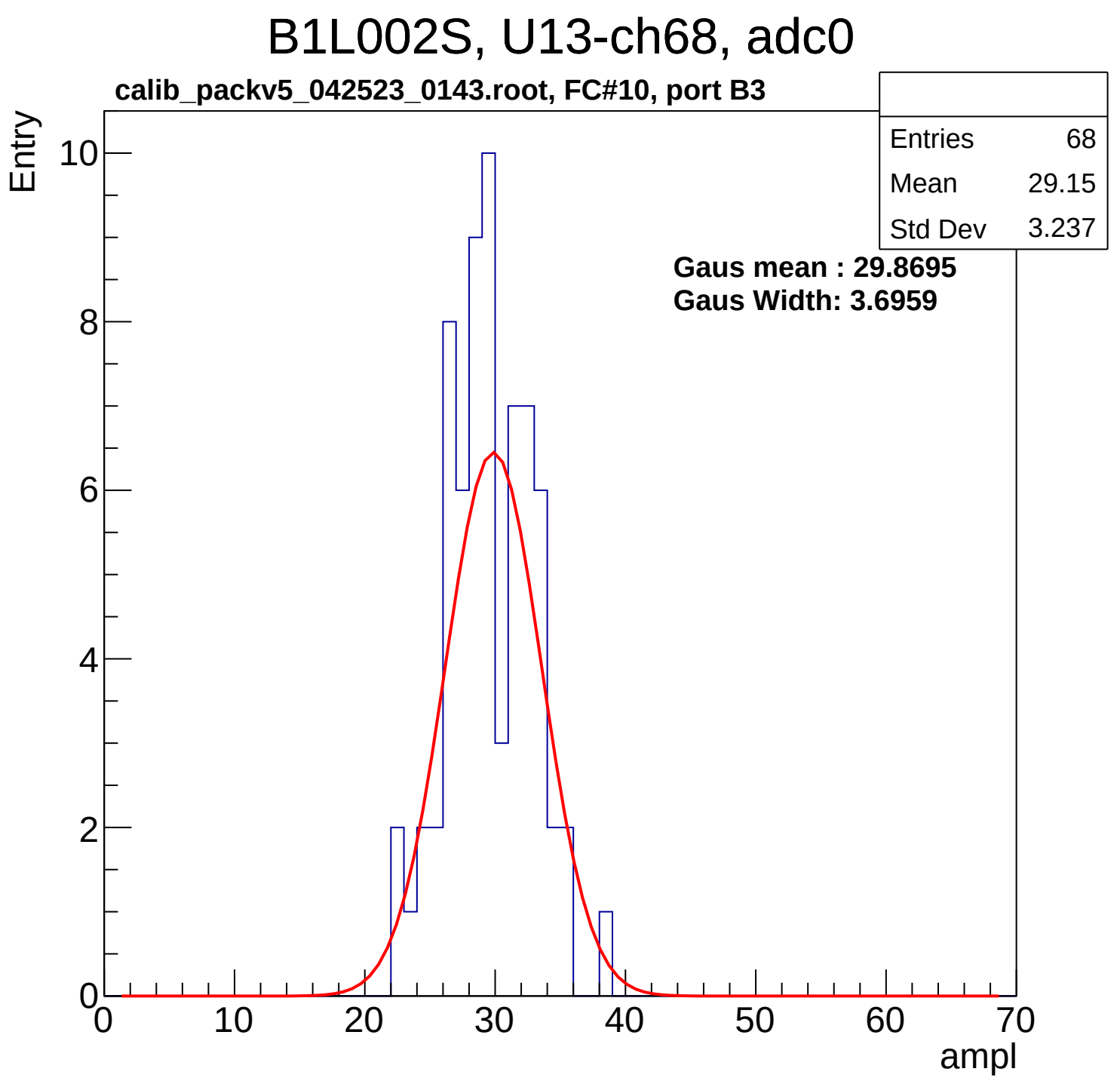
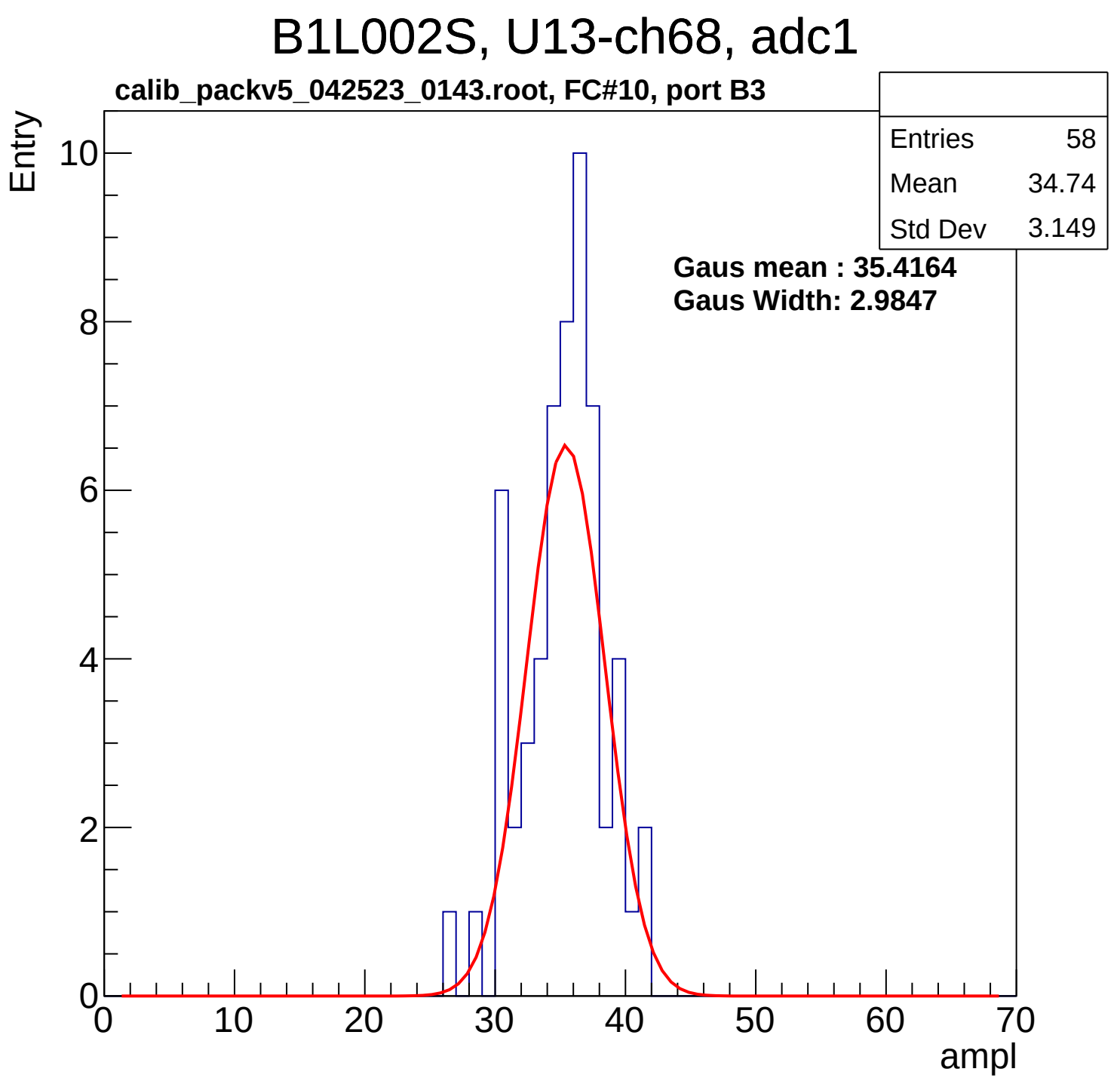

B1L002S, U13-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.71
Std Dev 1.221
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U13-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.15
Std Dev 3.237
Gaus mean : 29.8695
Gaus Width: 3.6959
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.74
Std Dev 3.149
Gaus mean : 35.4164
Gaus Width: 2.9847
Entry
ampl
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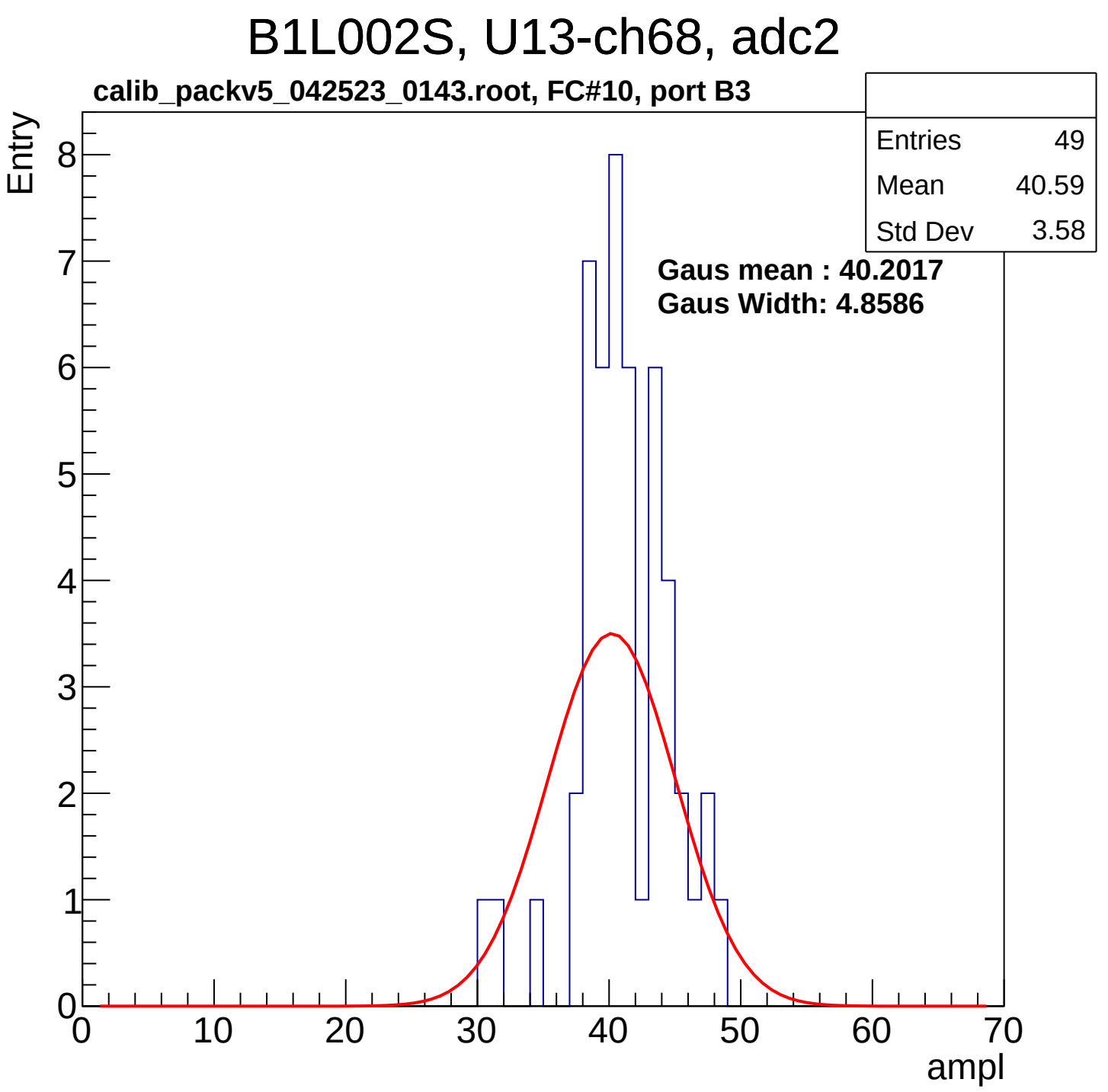
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.59
Std Dev 3.58
Gaus mean : 40.2017
Gaus Width: 4.8586
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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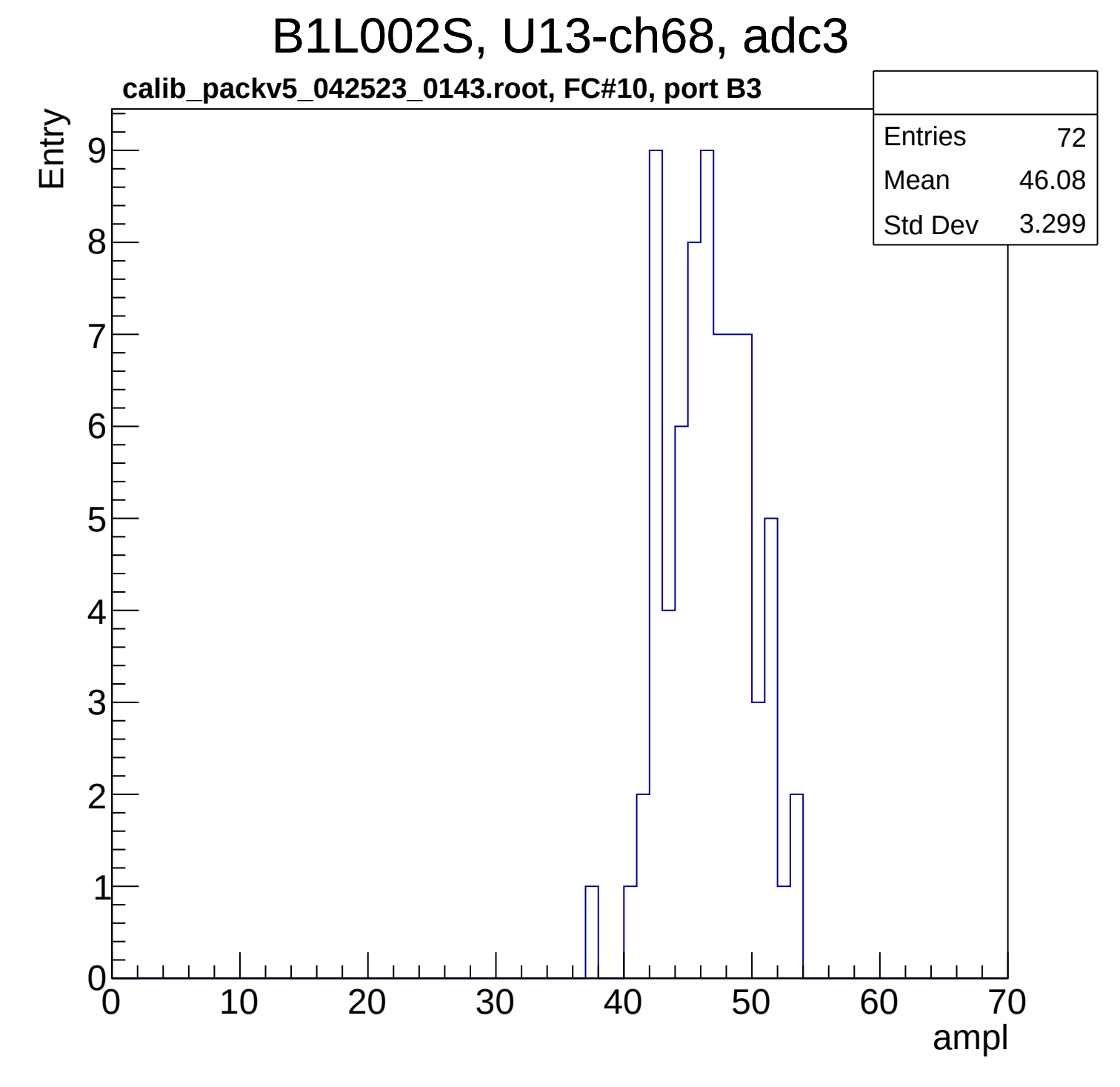
B1L002S, U13-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.08
Std Dev 3.299
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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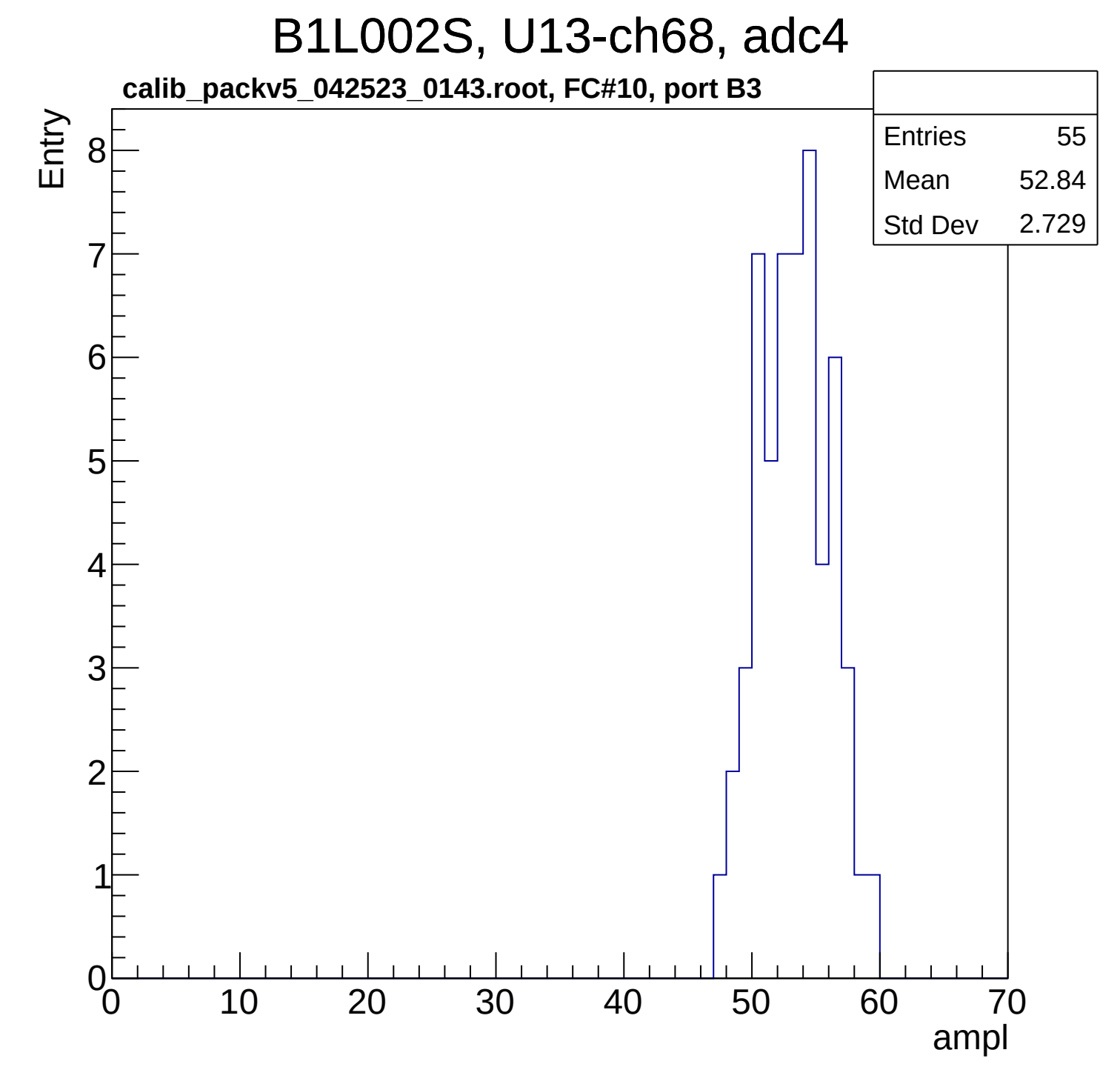
B1L002S, U13-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.84
Std Dev 2.729
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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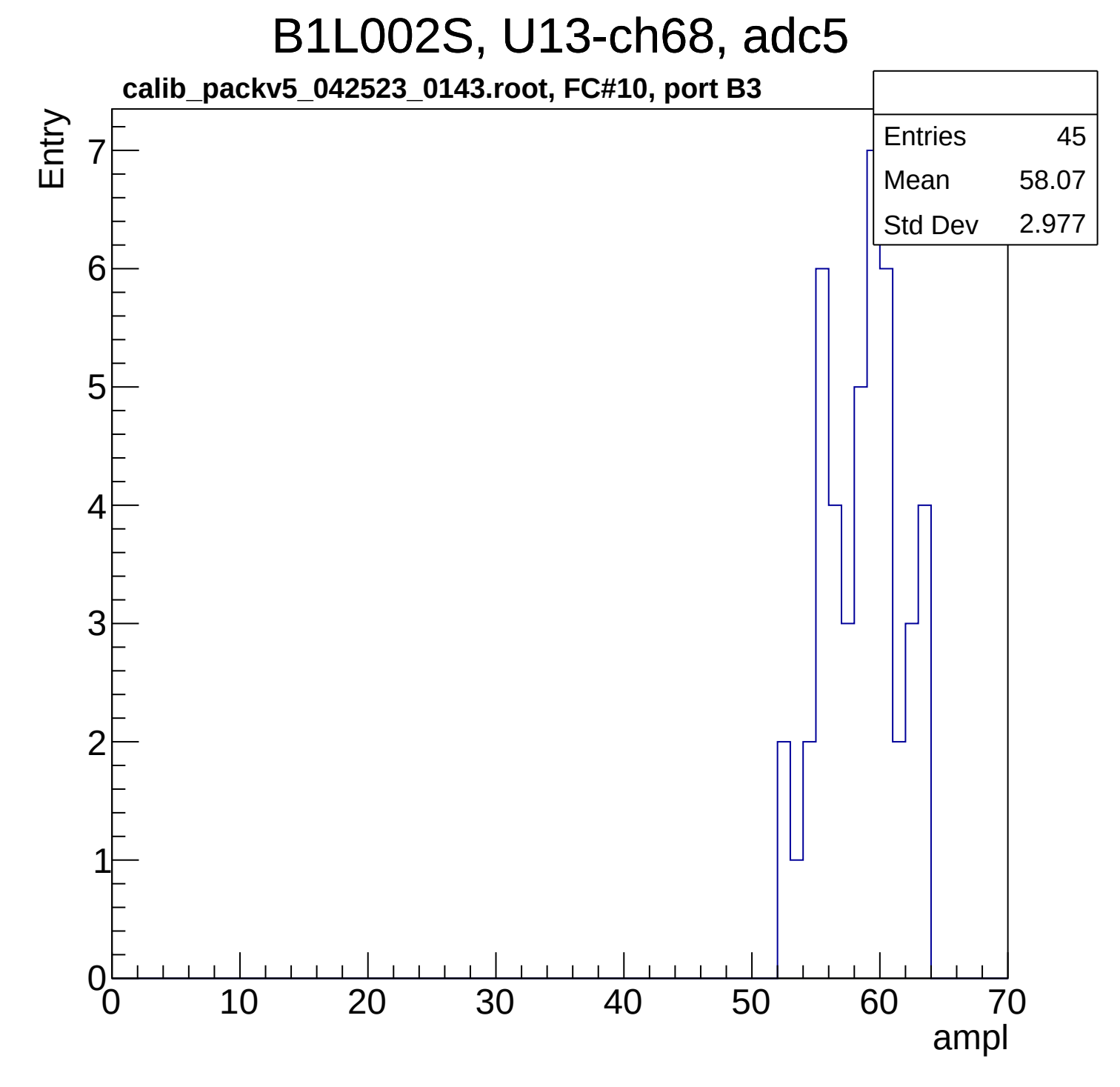
B1L002S, U13-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.07
Std Dev 2.977
Entry
ampl
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6
7
0
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20
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40
50
60
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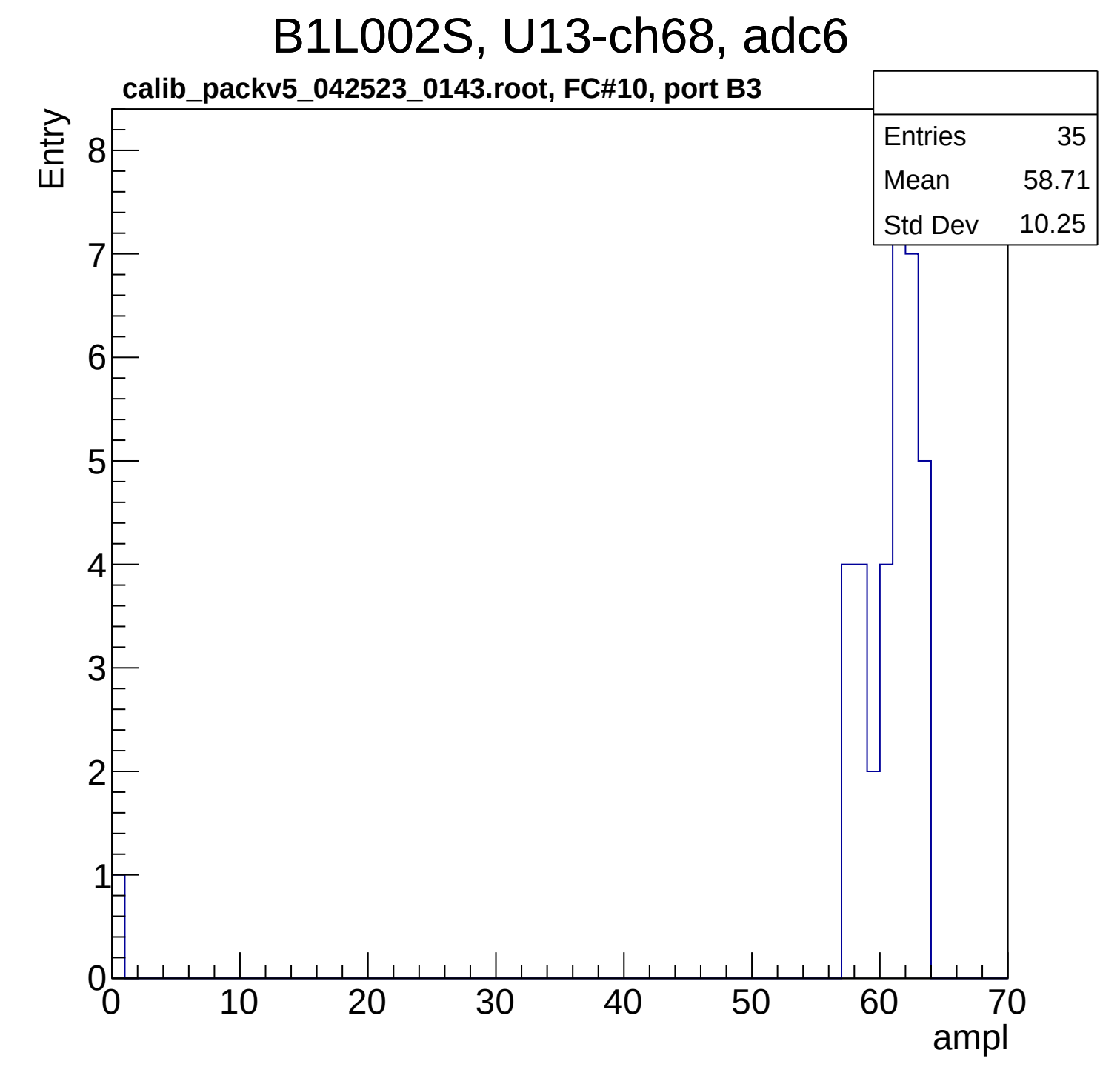
B1L002S, U13-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.71
Std Dev 10.25
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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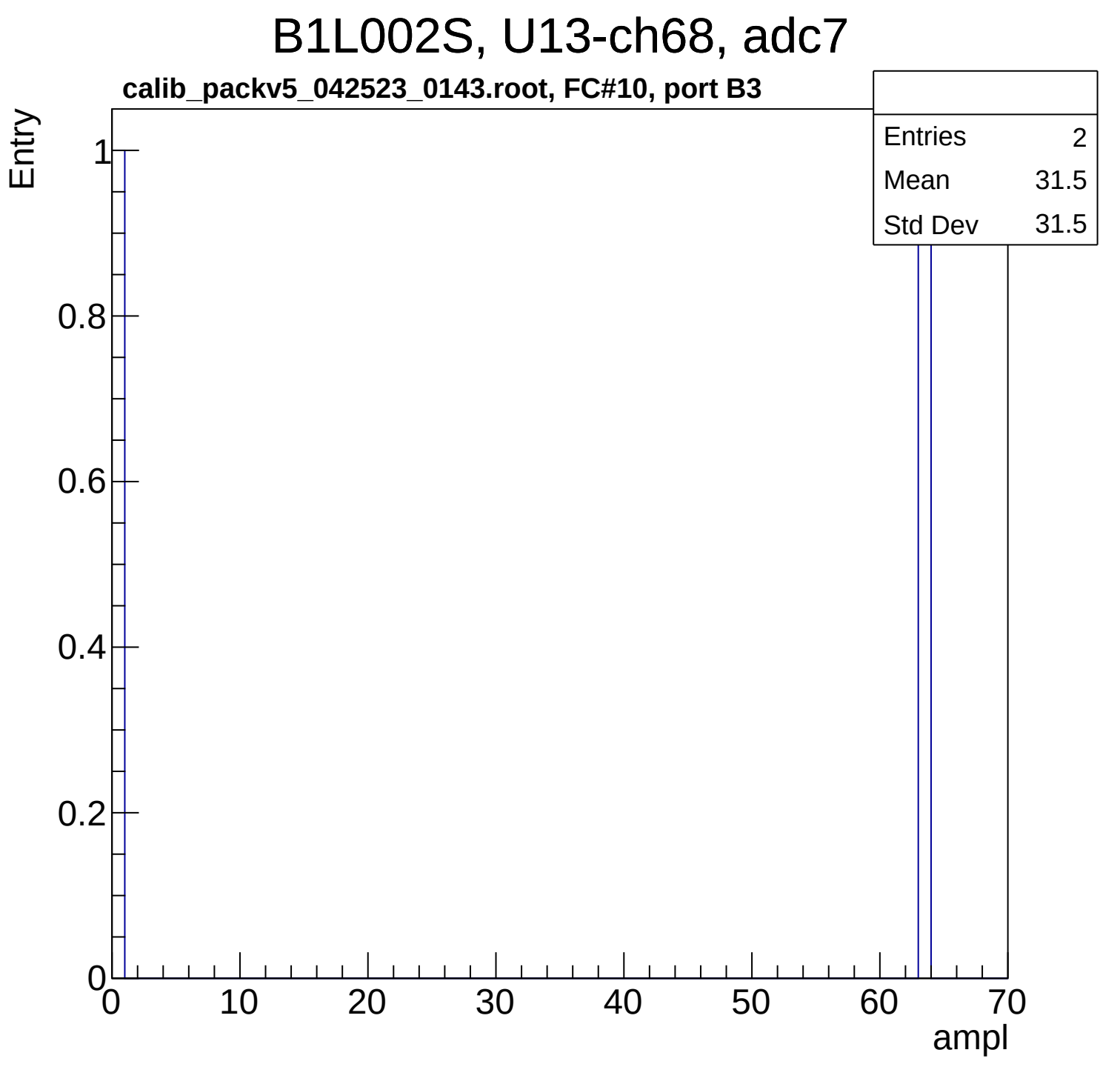
B1L002S, U13-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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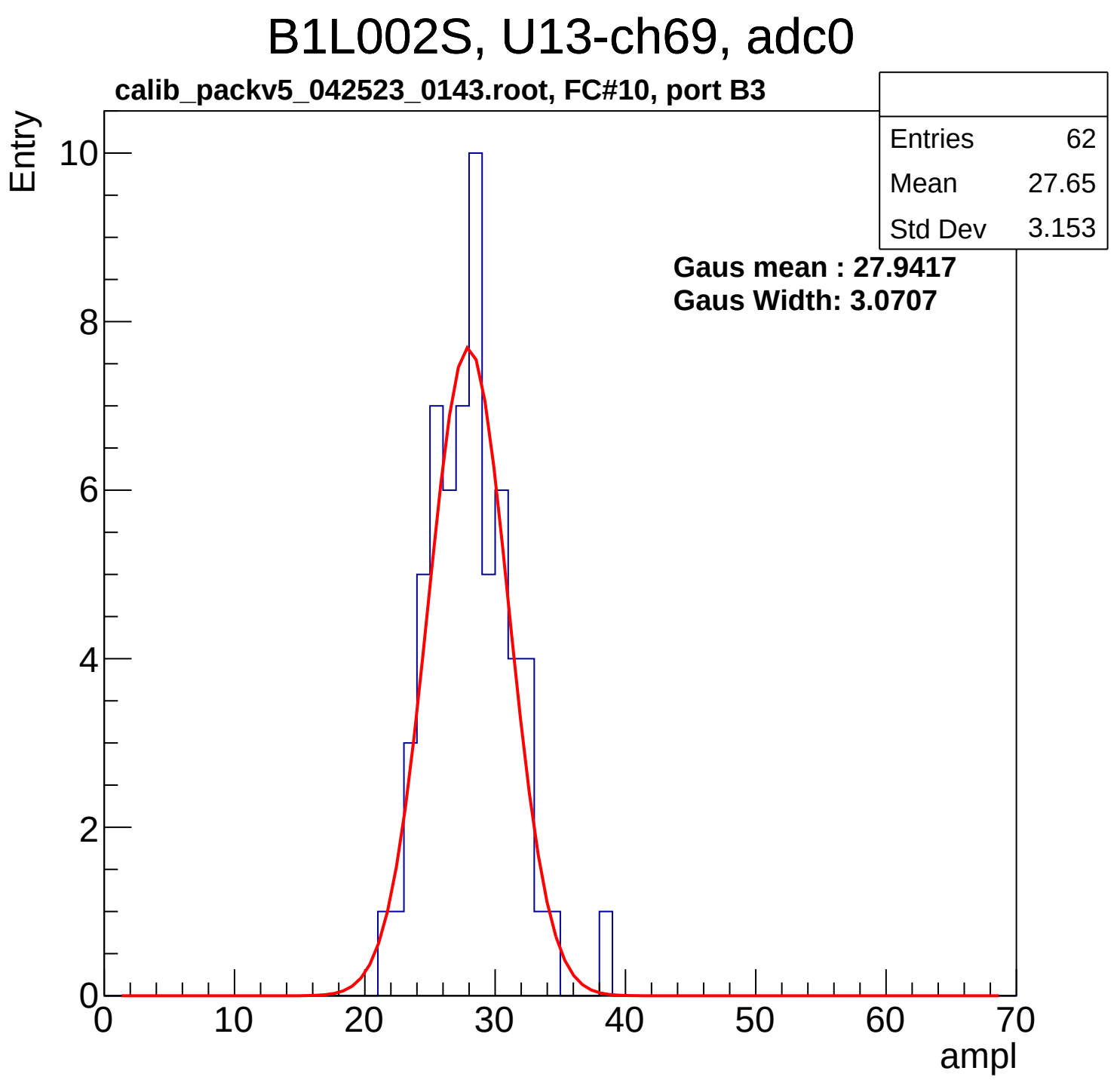
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.65
Std Dev 3.153
Gaus mean : 27.9417
Gaus Width: 3.0707
Entry
ampl
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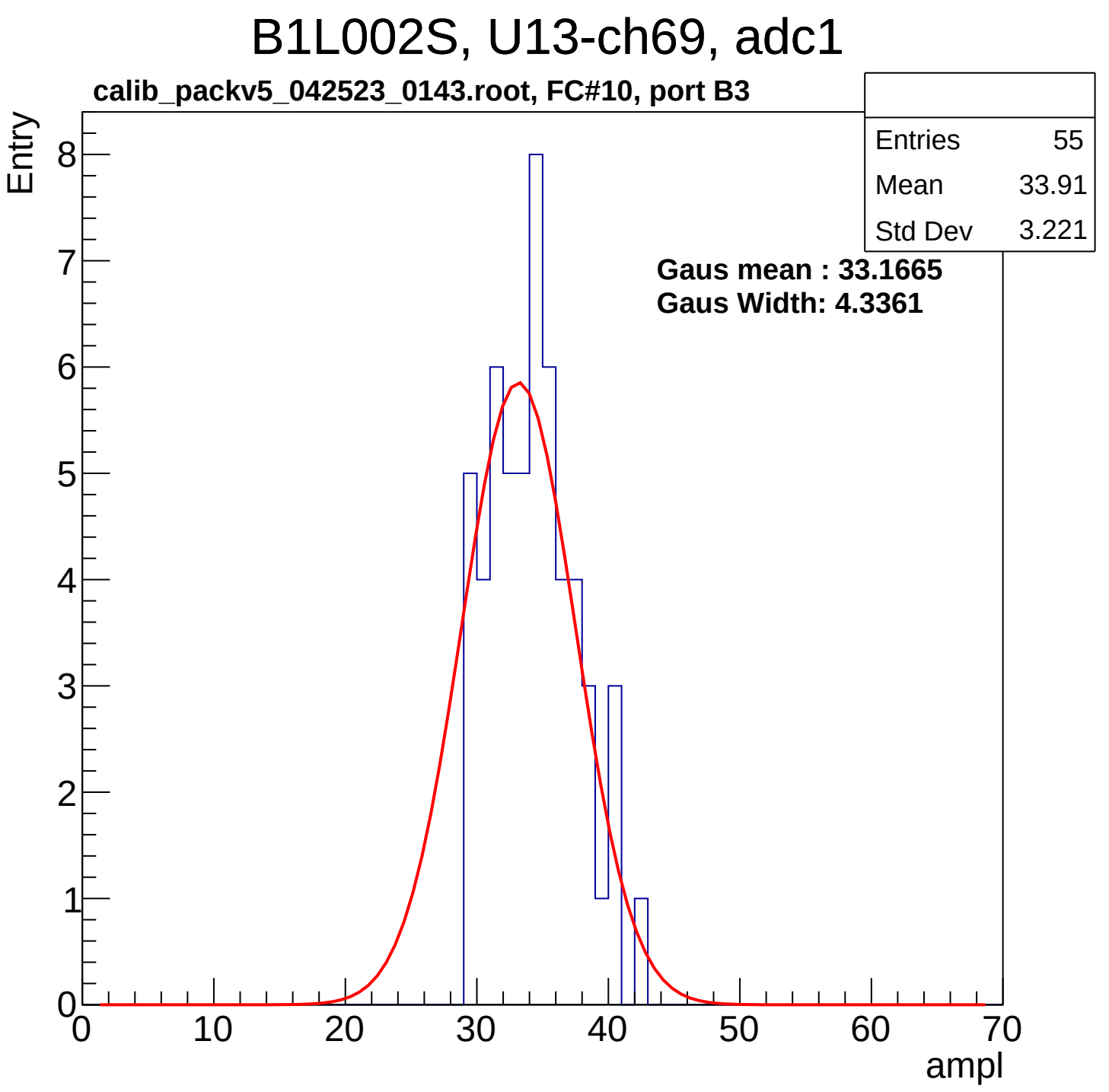
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 33.91
Std Dev 3.221
Gaus mean : 33.1665
Gaus Width: 4.3361
Entry
ampl
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2
3
4
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6
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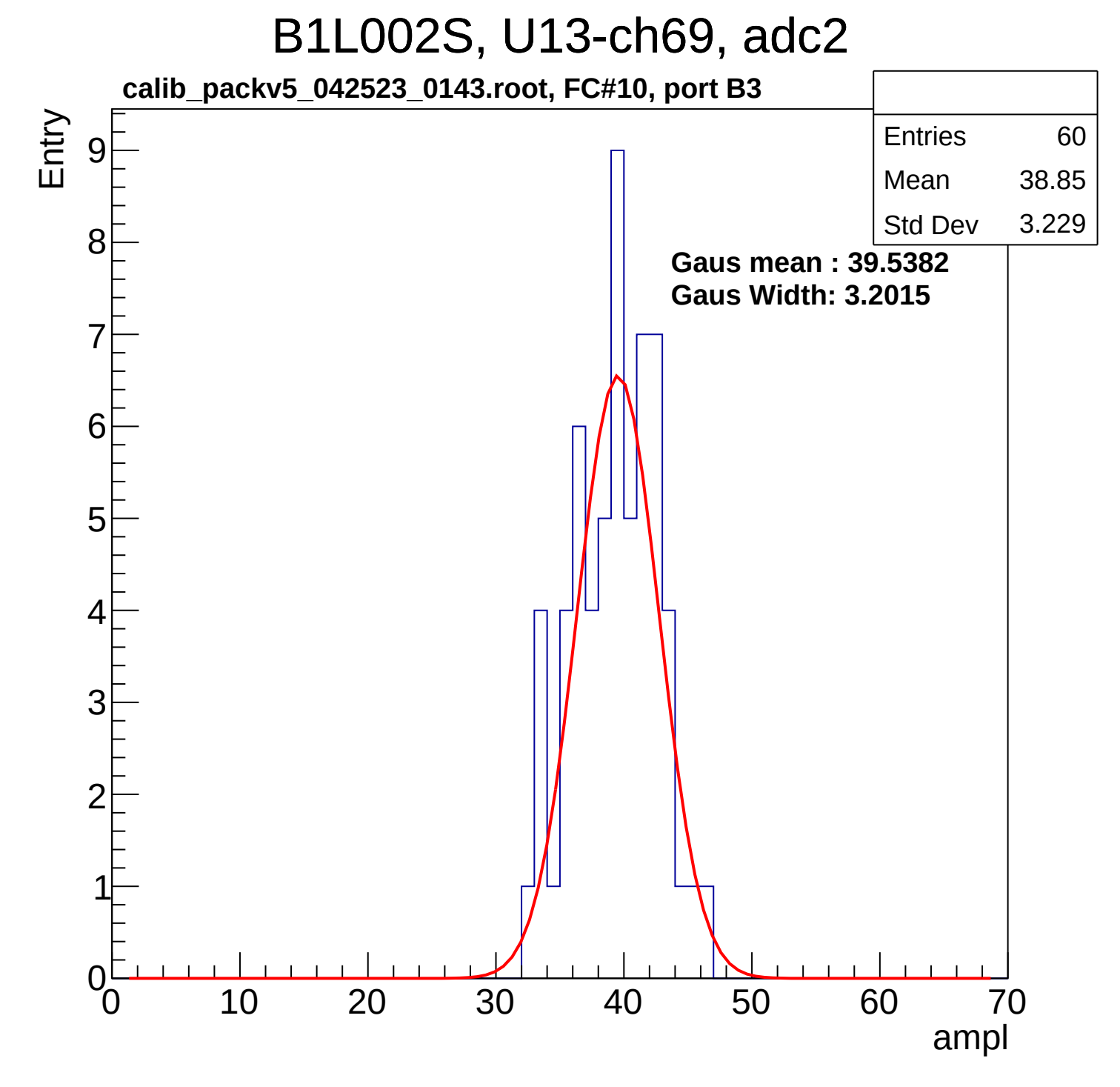
B1L002S, U13-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 38.85
Std Dev 3.229
Gaus mean : 39.5382
Gaus Width: 3.2015
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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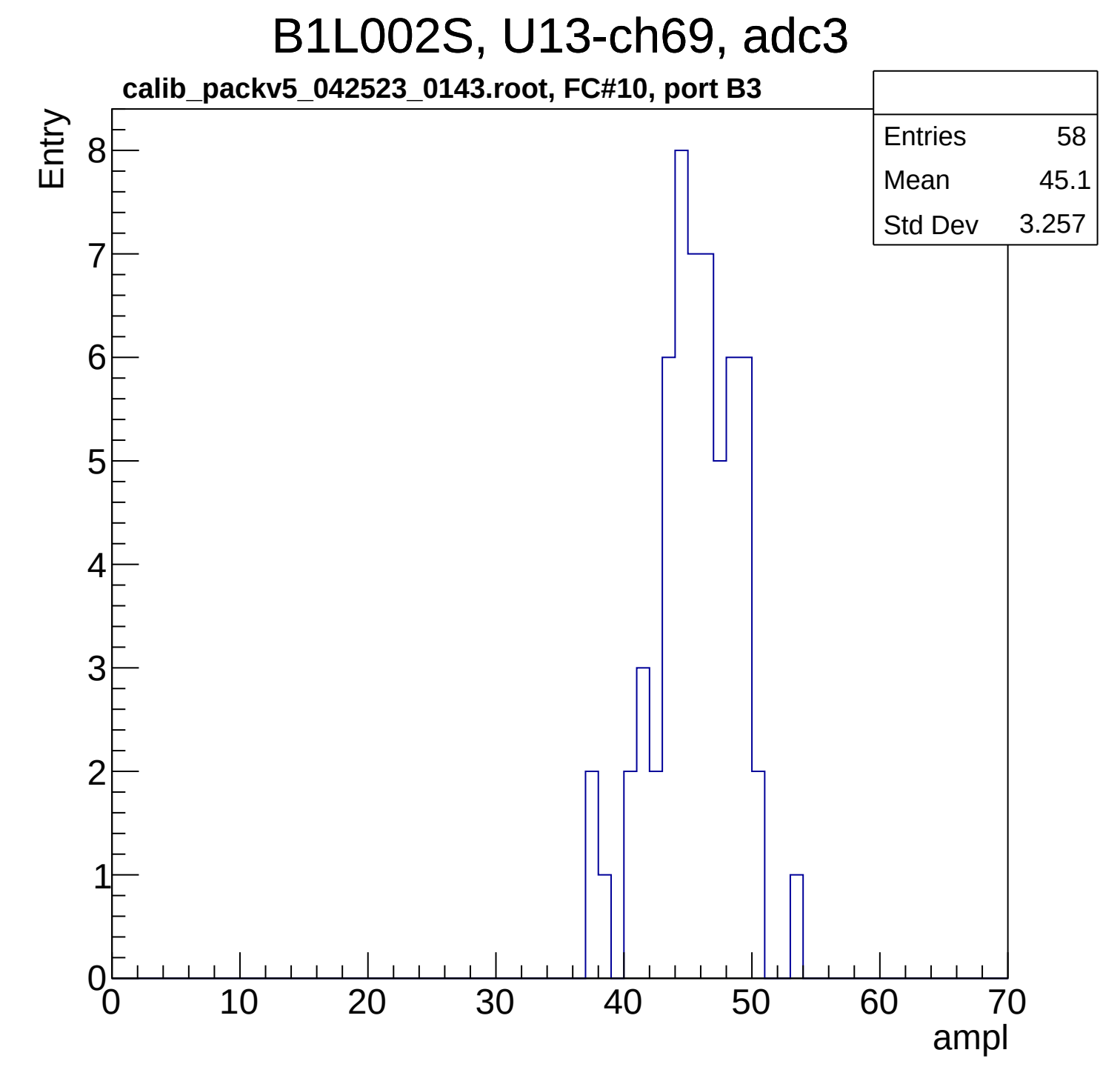
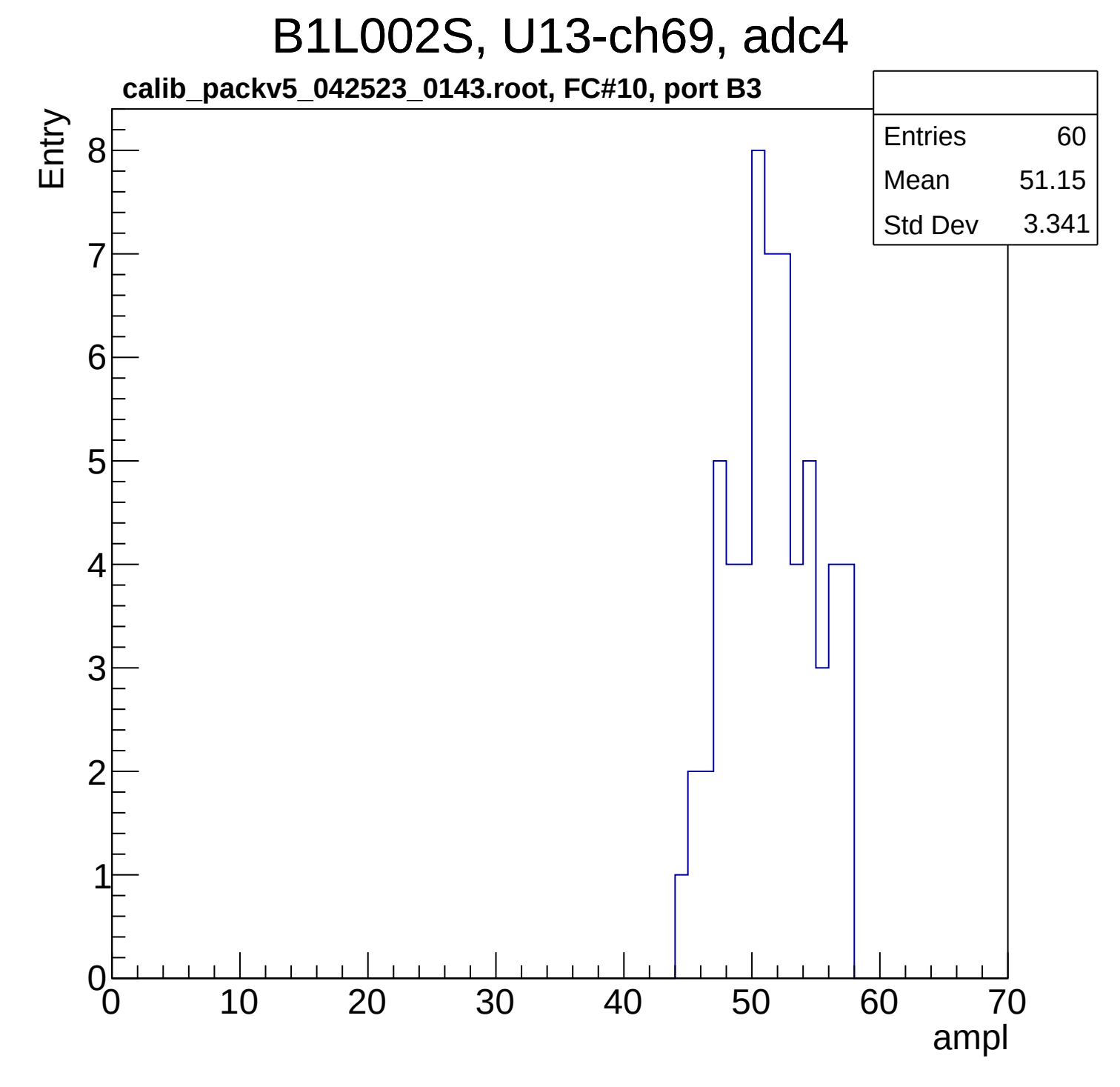

B1L002S, U13-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 45.1
Std Dev 3.257
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.15
Std Dev 3.341
Entry
ampl
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1
2
3
4
5
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7
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0
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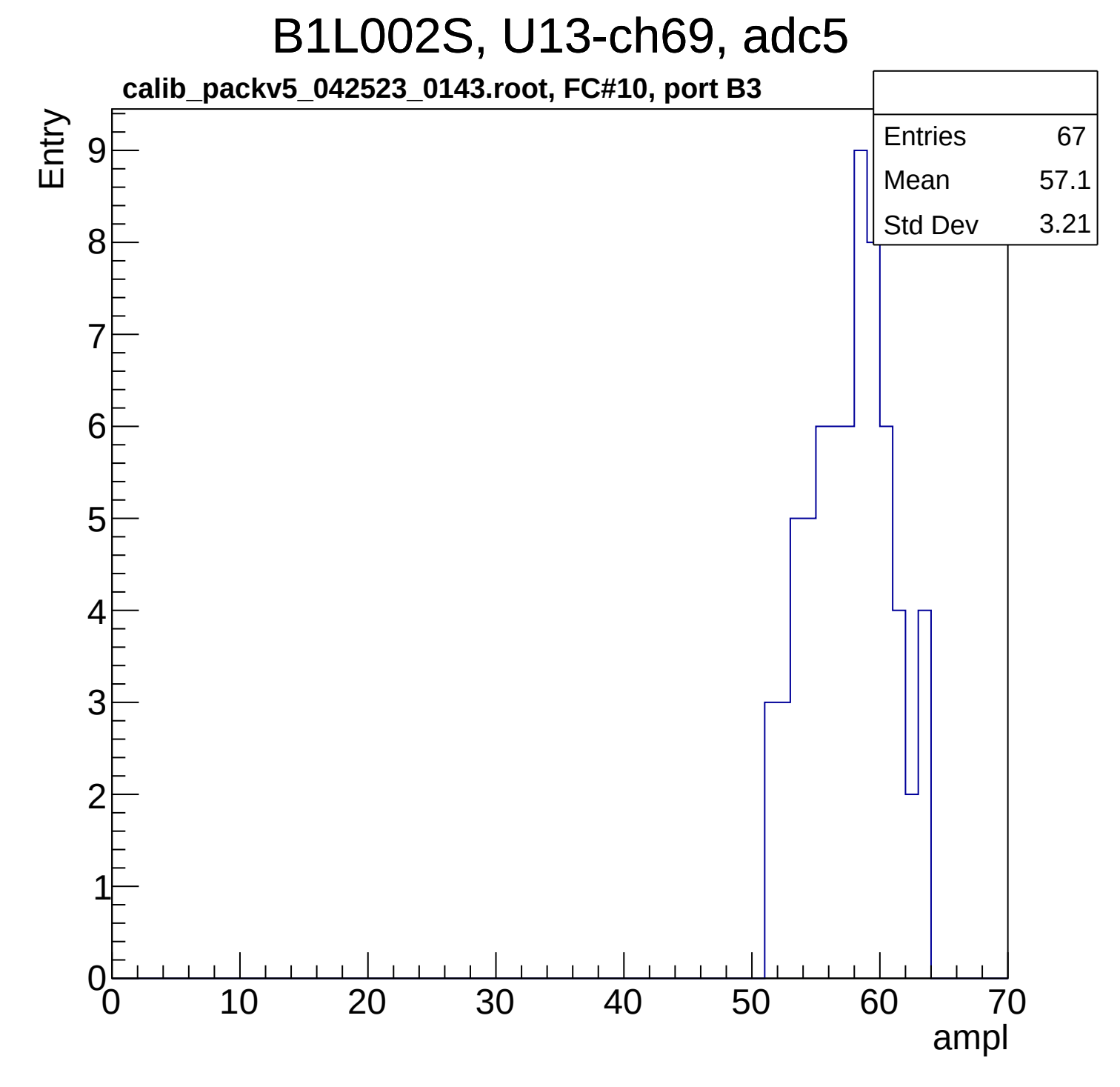
B1L002S, U13-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.1
Std Dev 3.21
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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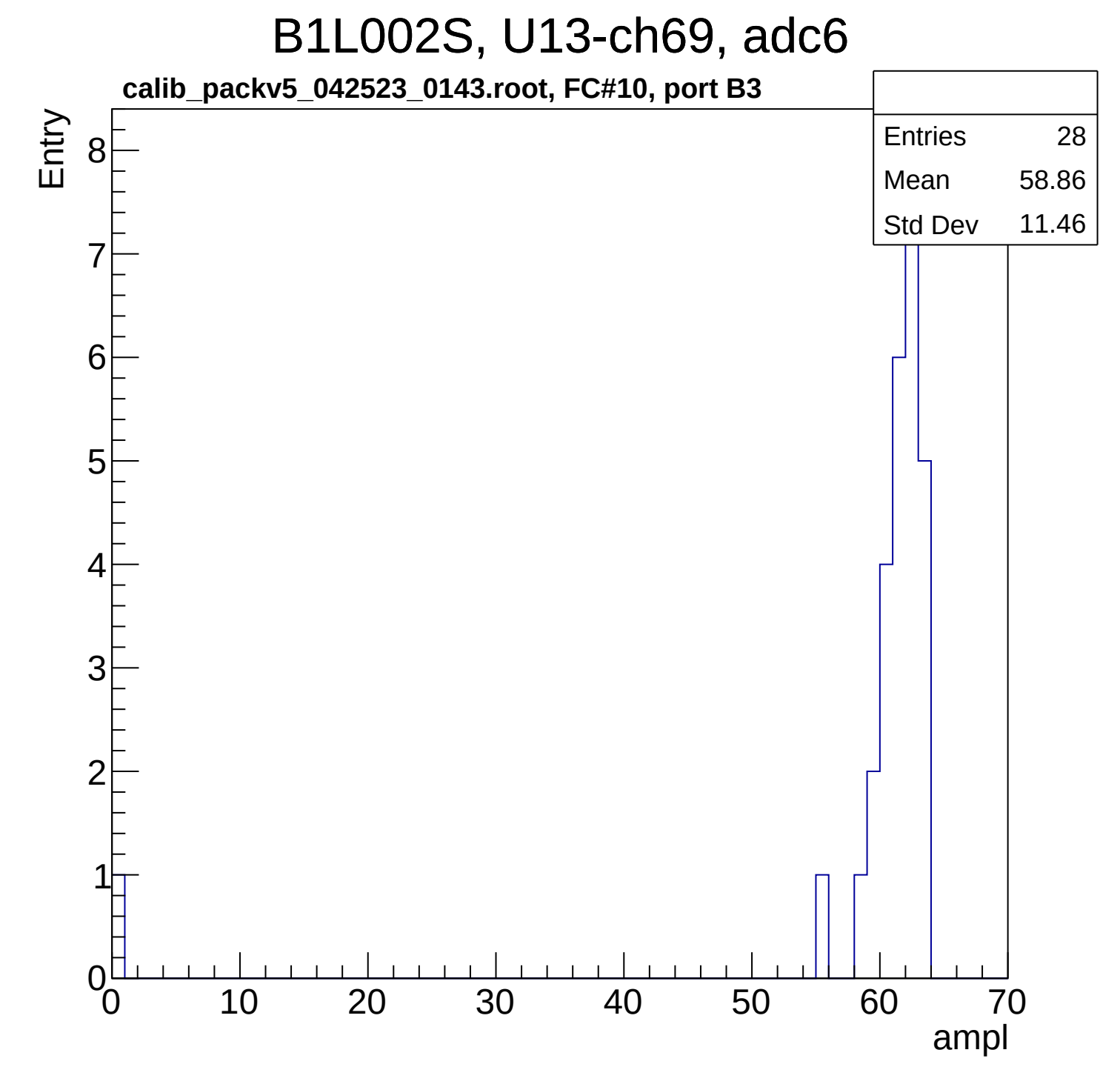
B1L002S, U13-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.86
Std Dev 11.46
Entry
ampl
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2
3
4
5
6
7
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0
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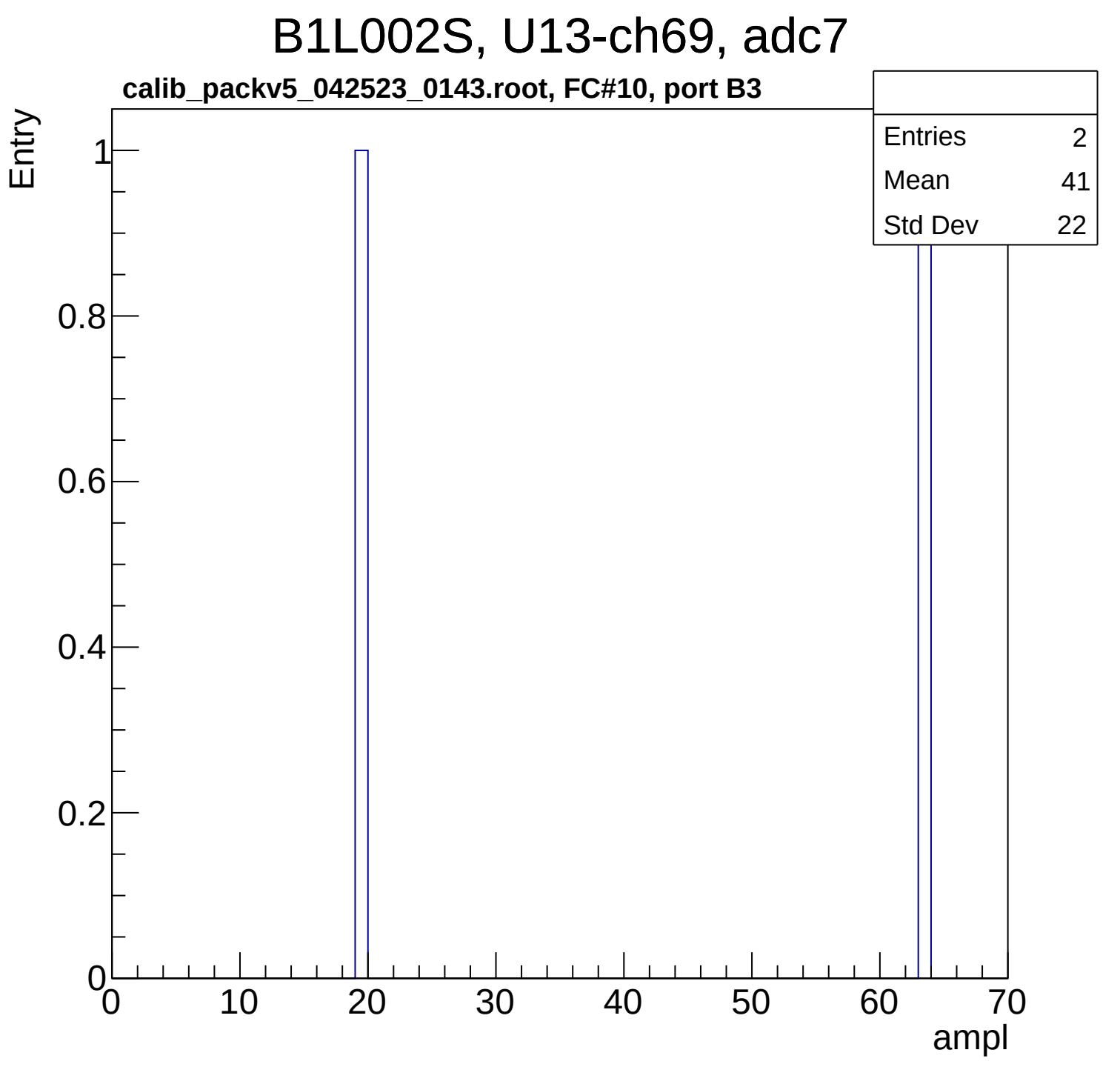
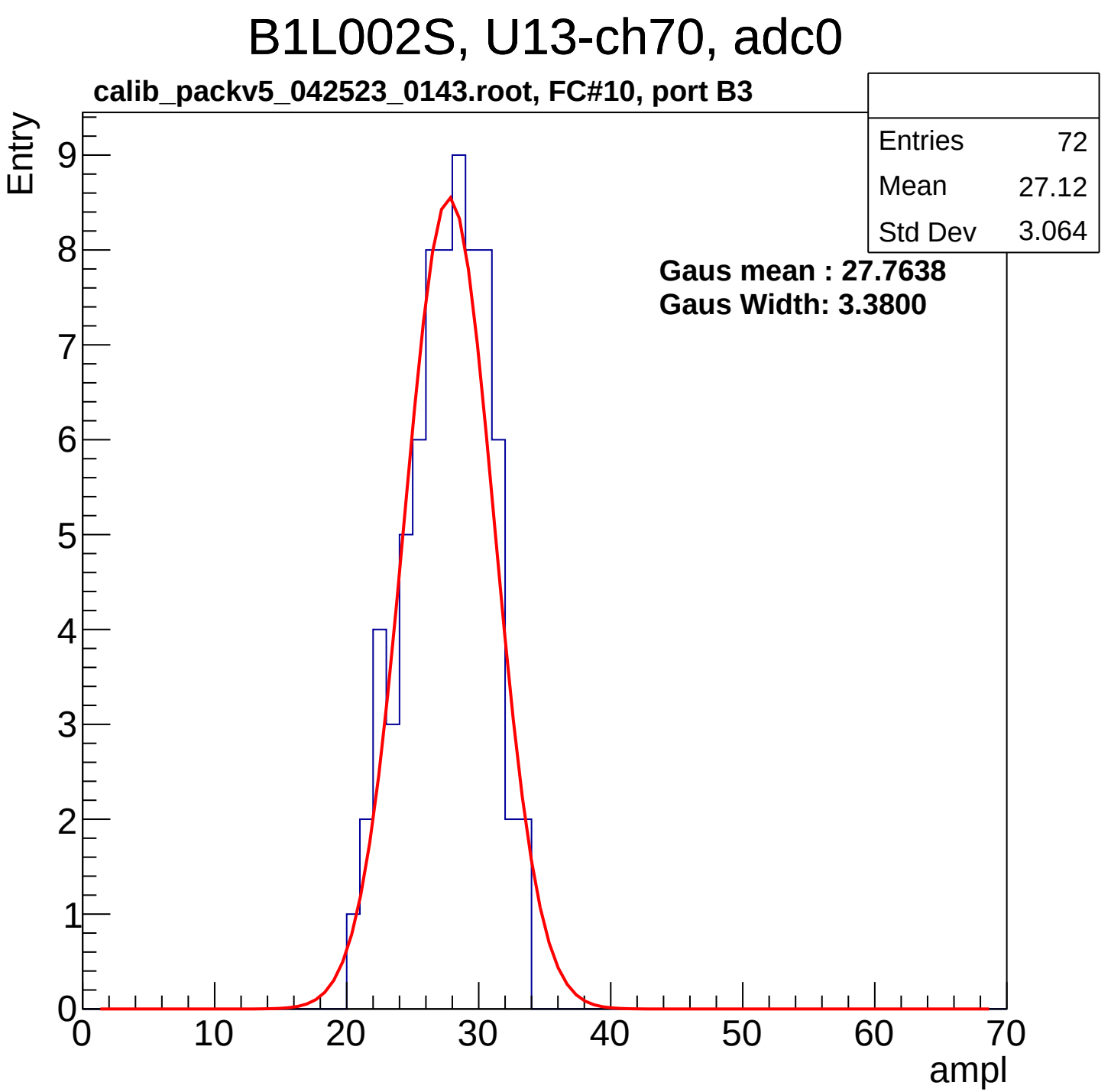

B1L002S, U13-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 22
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.12
Std Dev 3.064
Gaus mean : 27.7638
Gaus Width: 3.3800
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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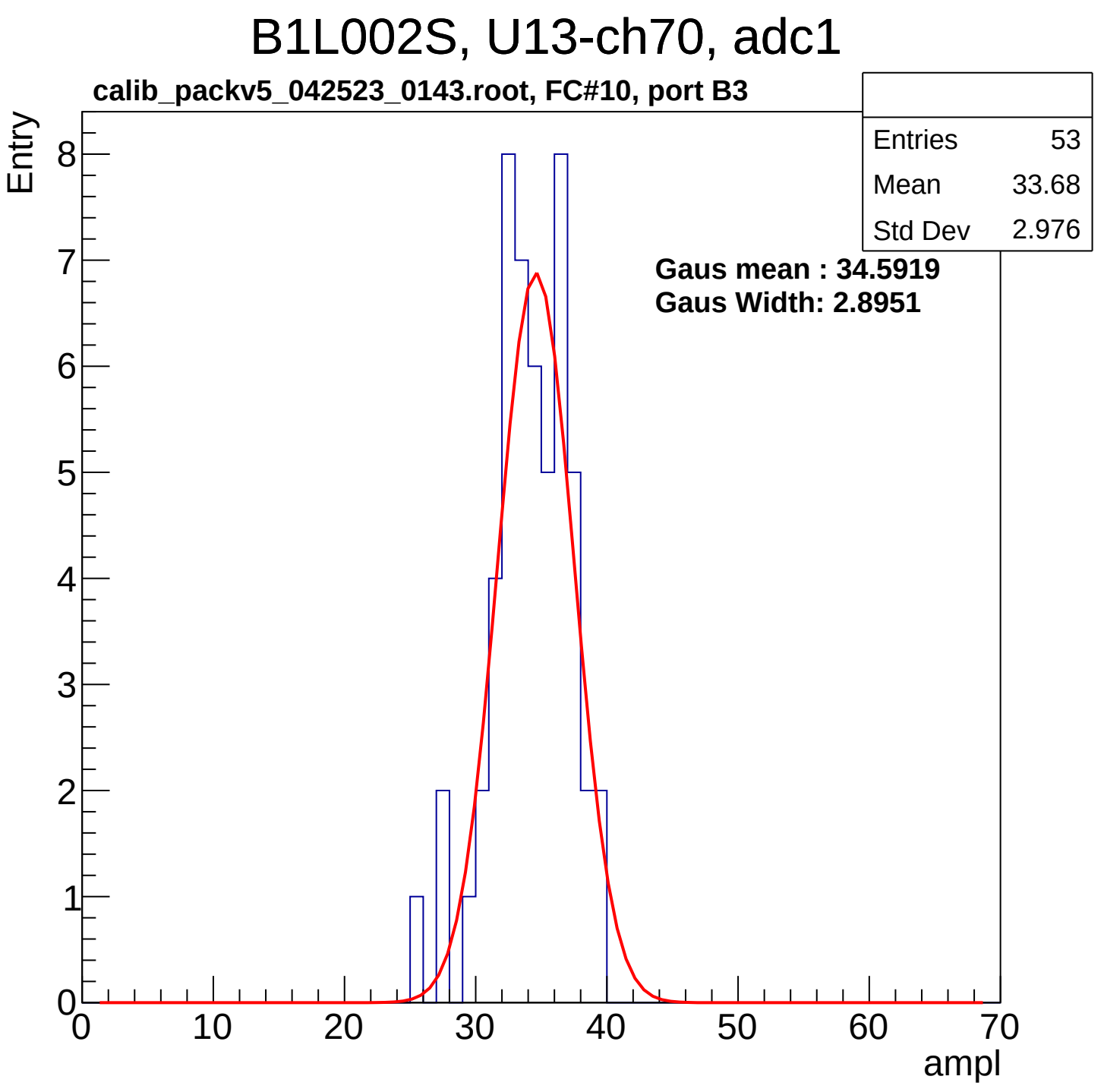
B1L002S, U13-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.68
Std Dev 2.976
Gaus mean : 34.5919
Gaus Width: 2.8951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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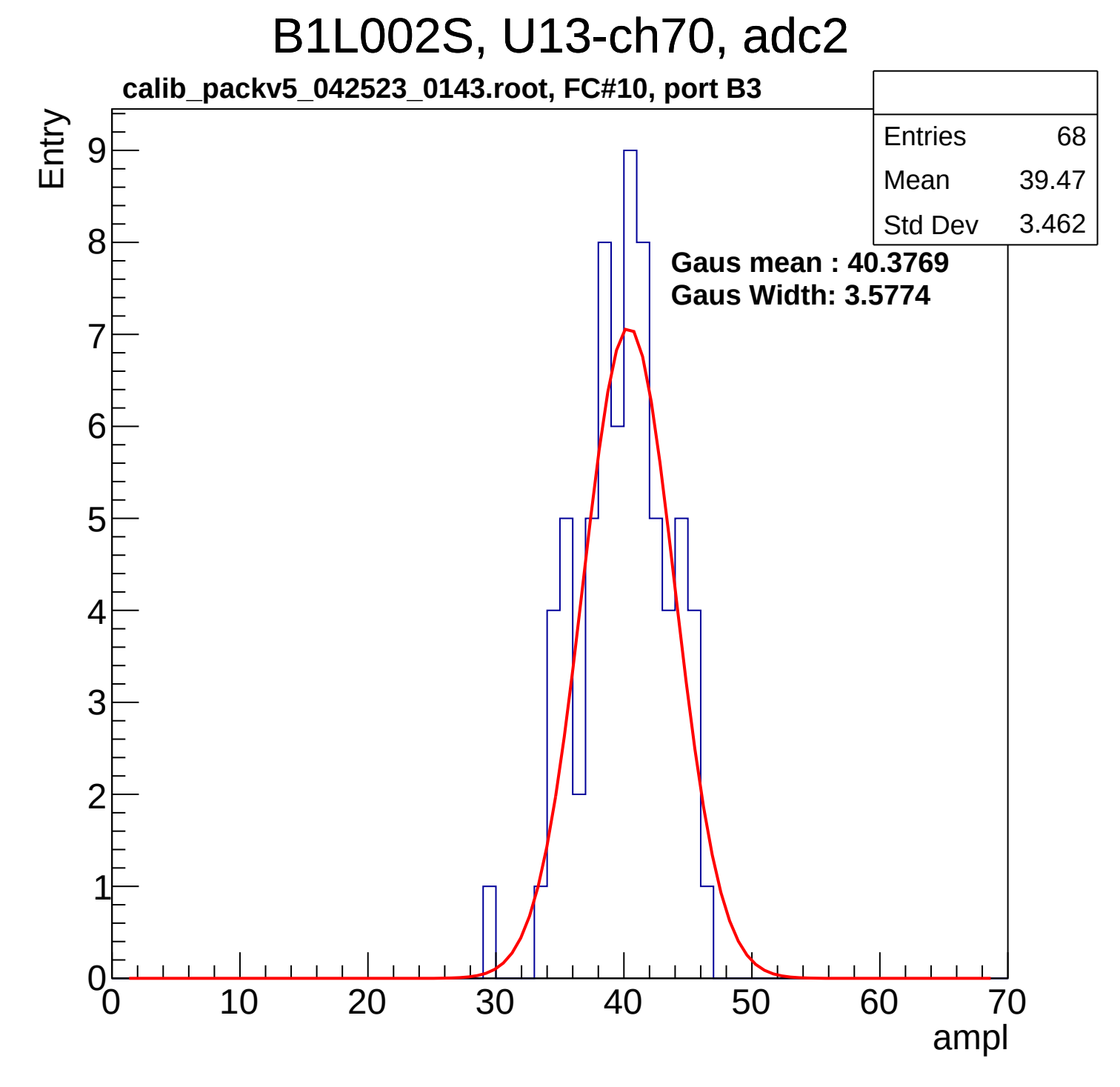
B1L002S, U13-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 39.47
Std Dev 3.462
Gaus mean : 40.3769
Gaus Width: 3.5774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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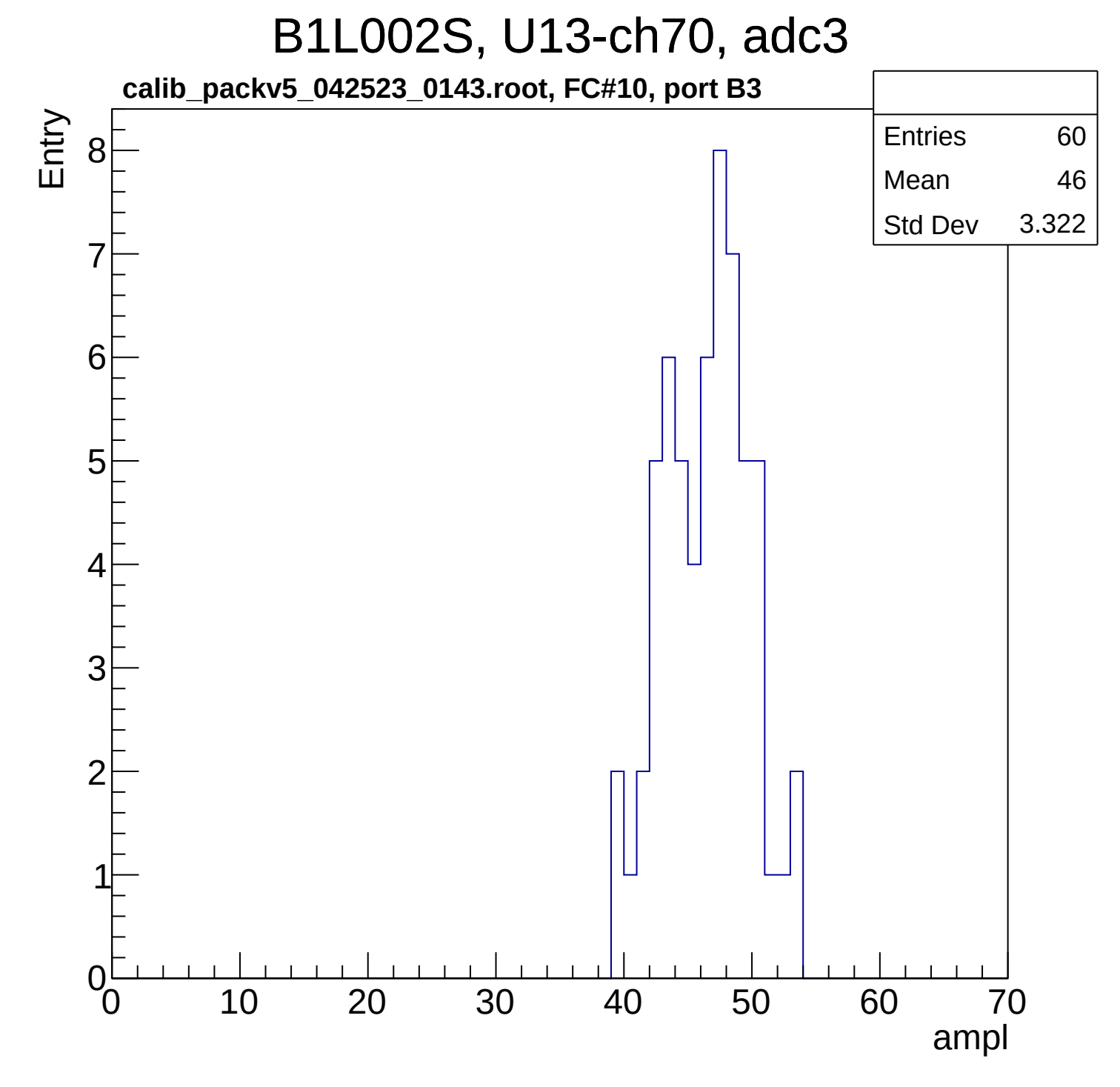
B1L002S, U13-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46
Std Dev 3.322
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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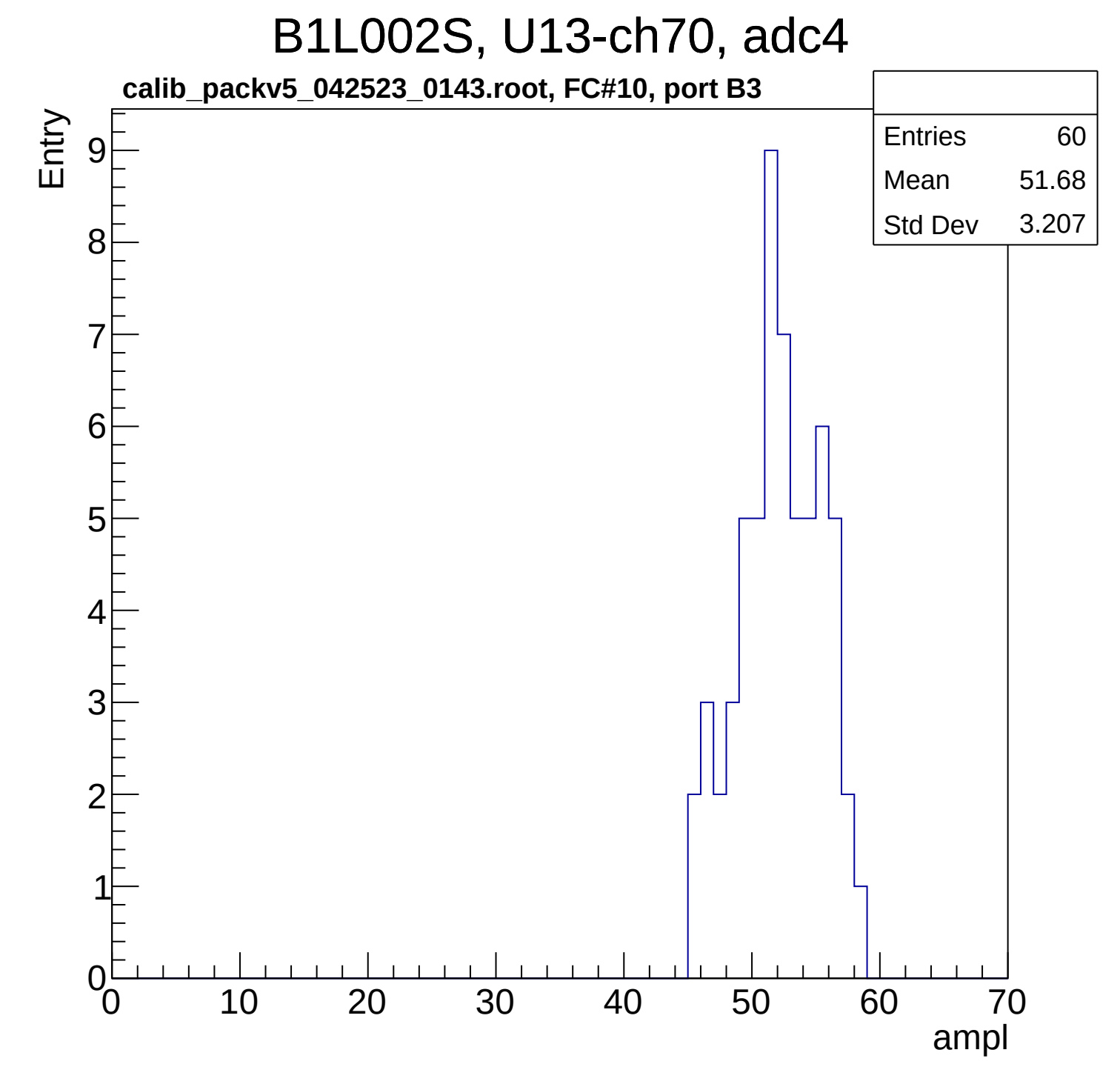
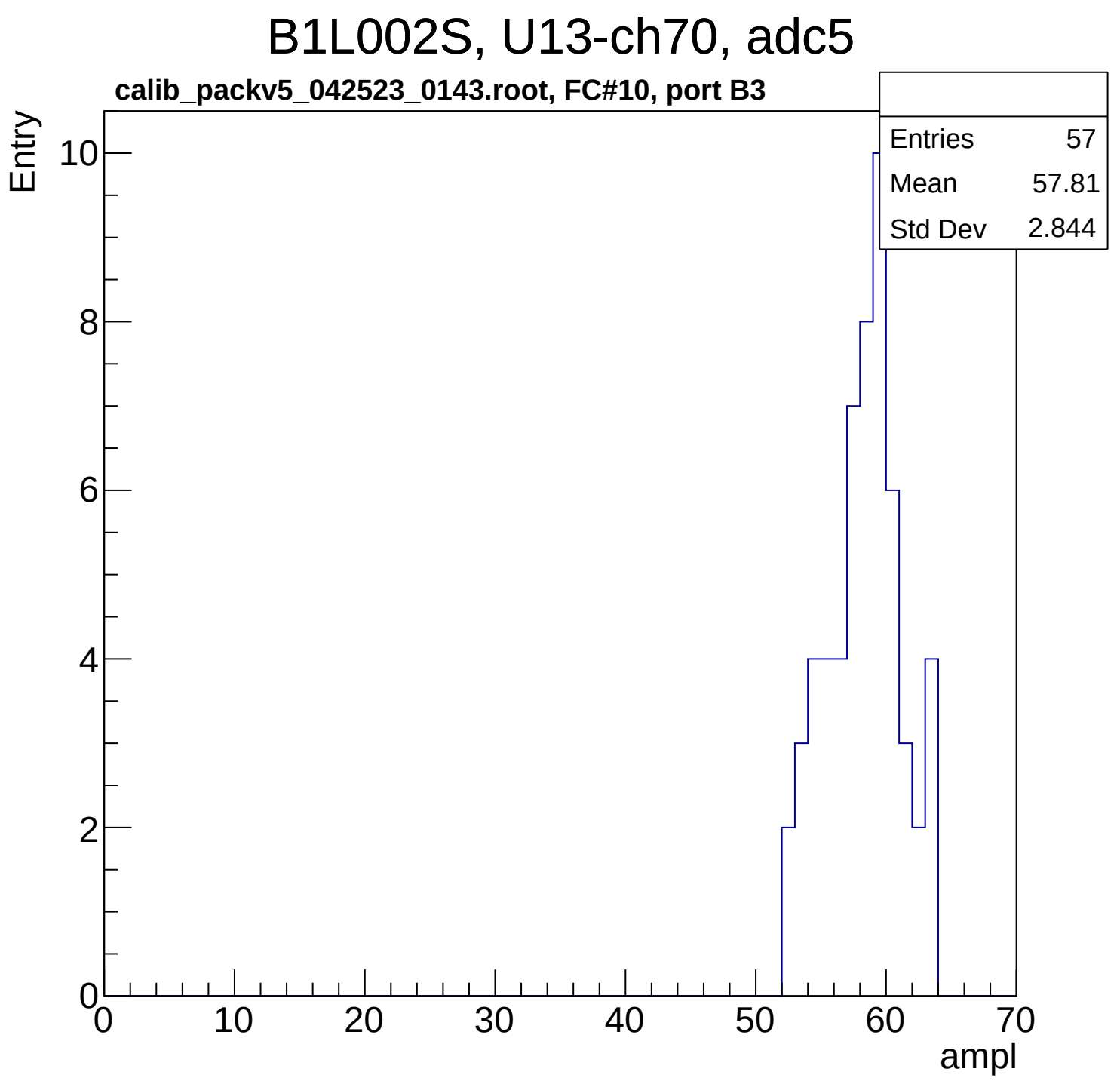

B1L002S, U13-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.68
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.81
Std Dev 2.844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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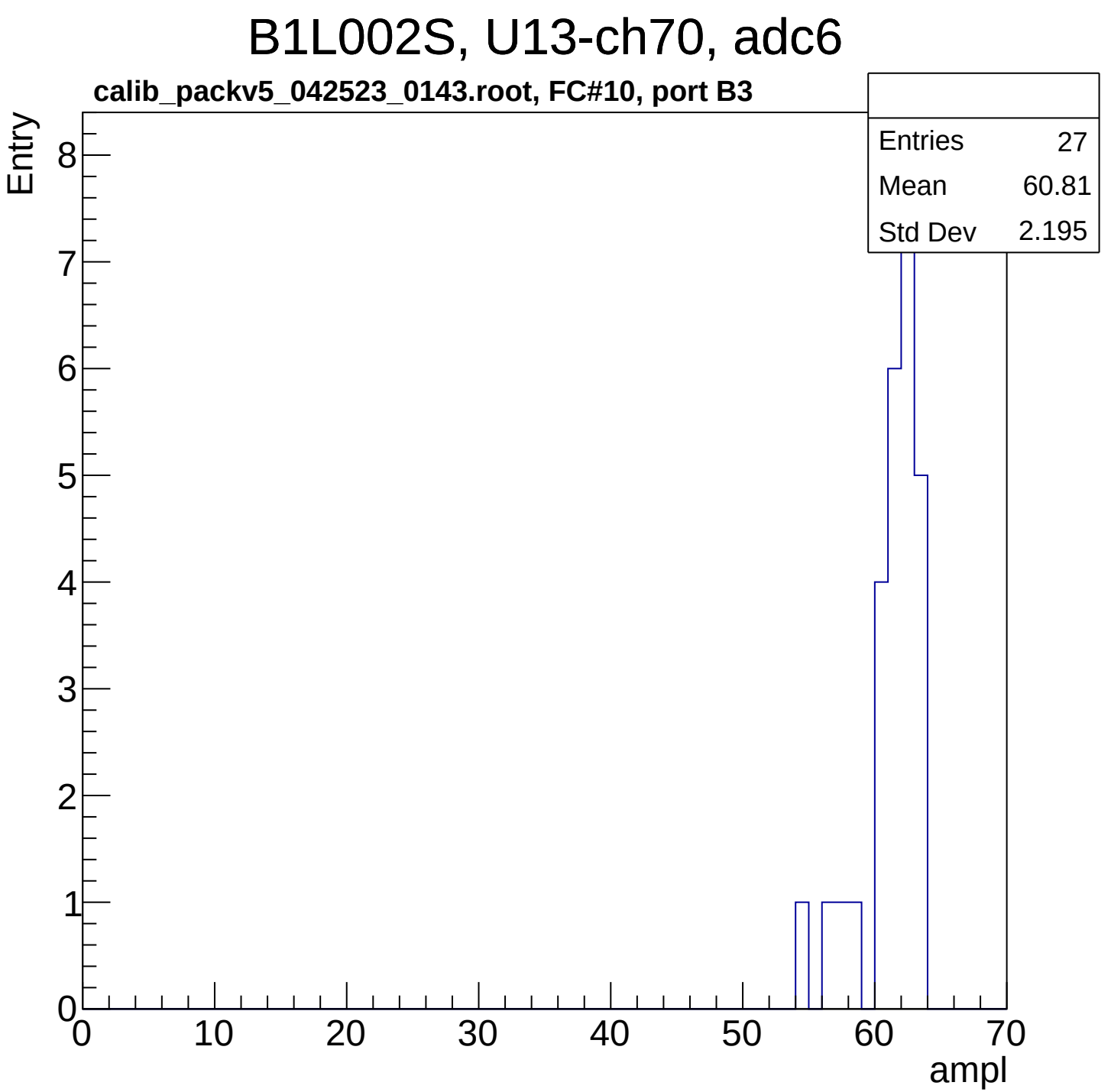
B1L002S, U13-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.81
Std Dev 2.195
Entry
ampl
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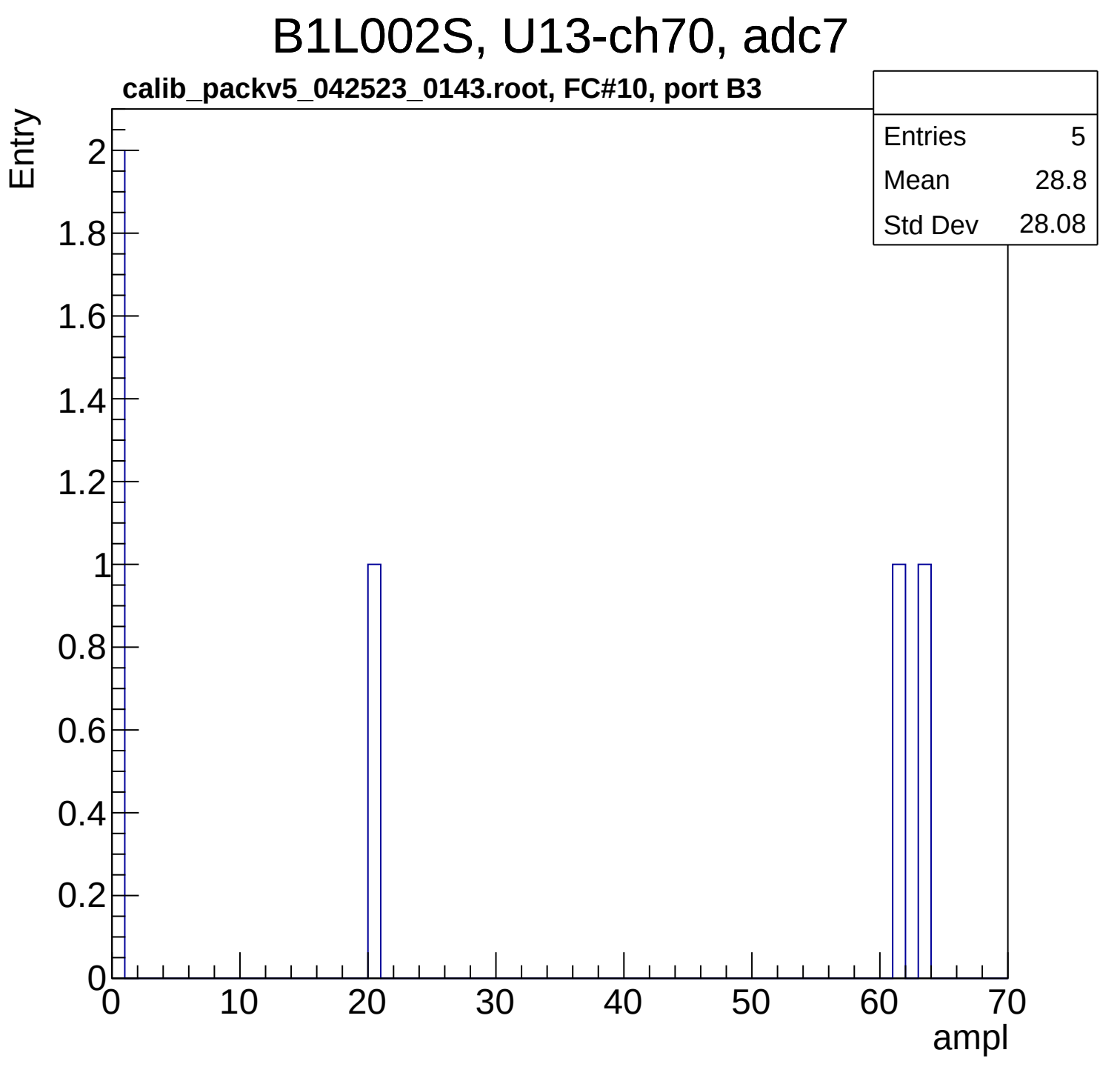
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 28.8
Std Dev 28.08
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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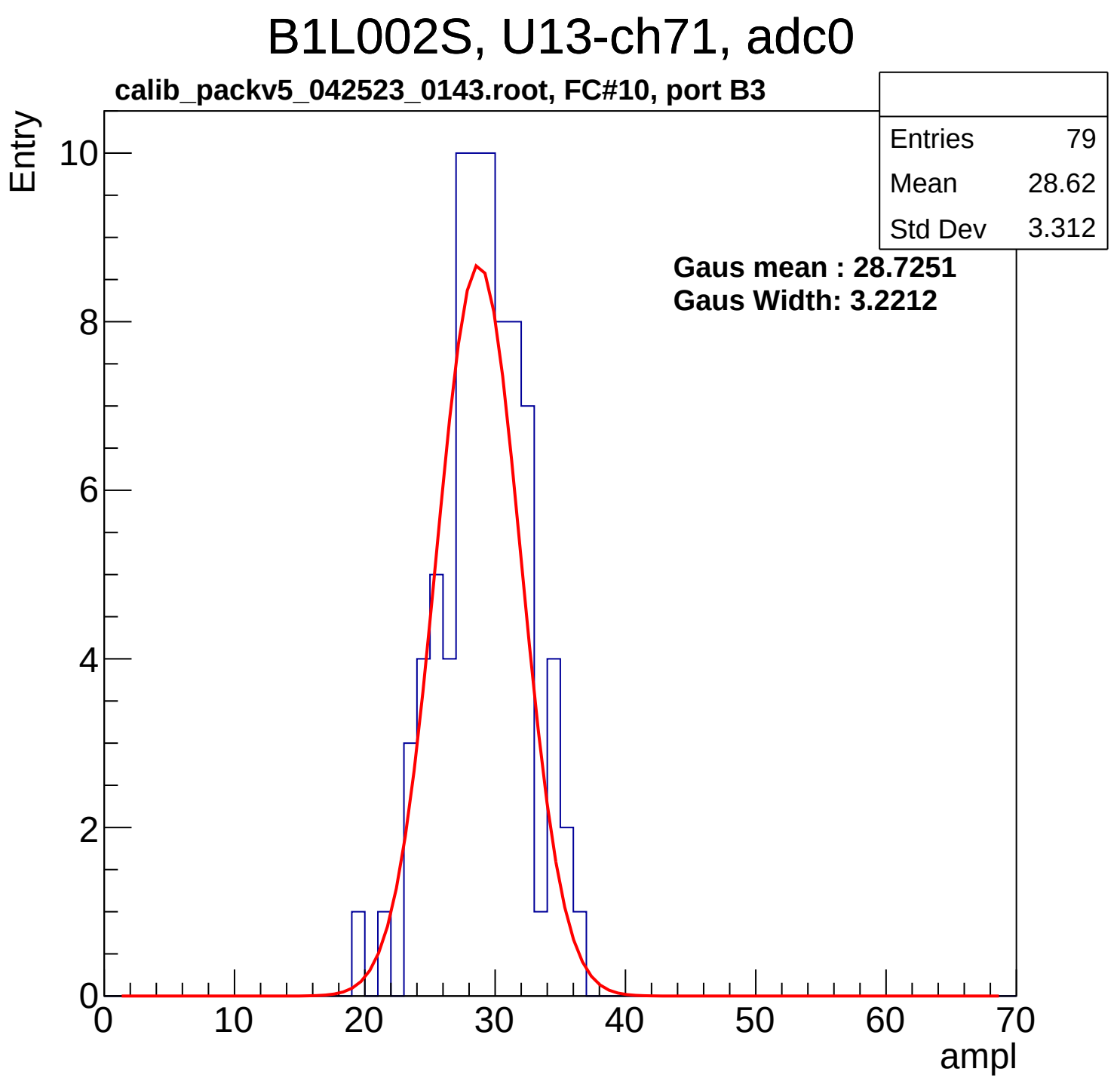
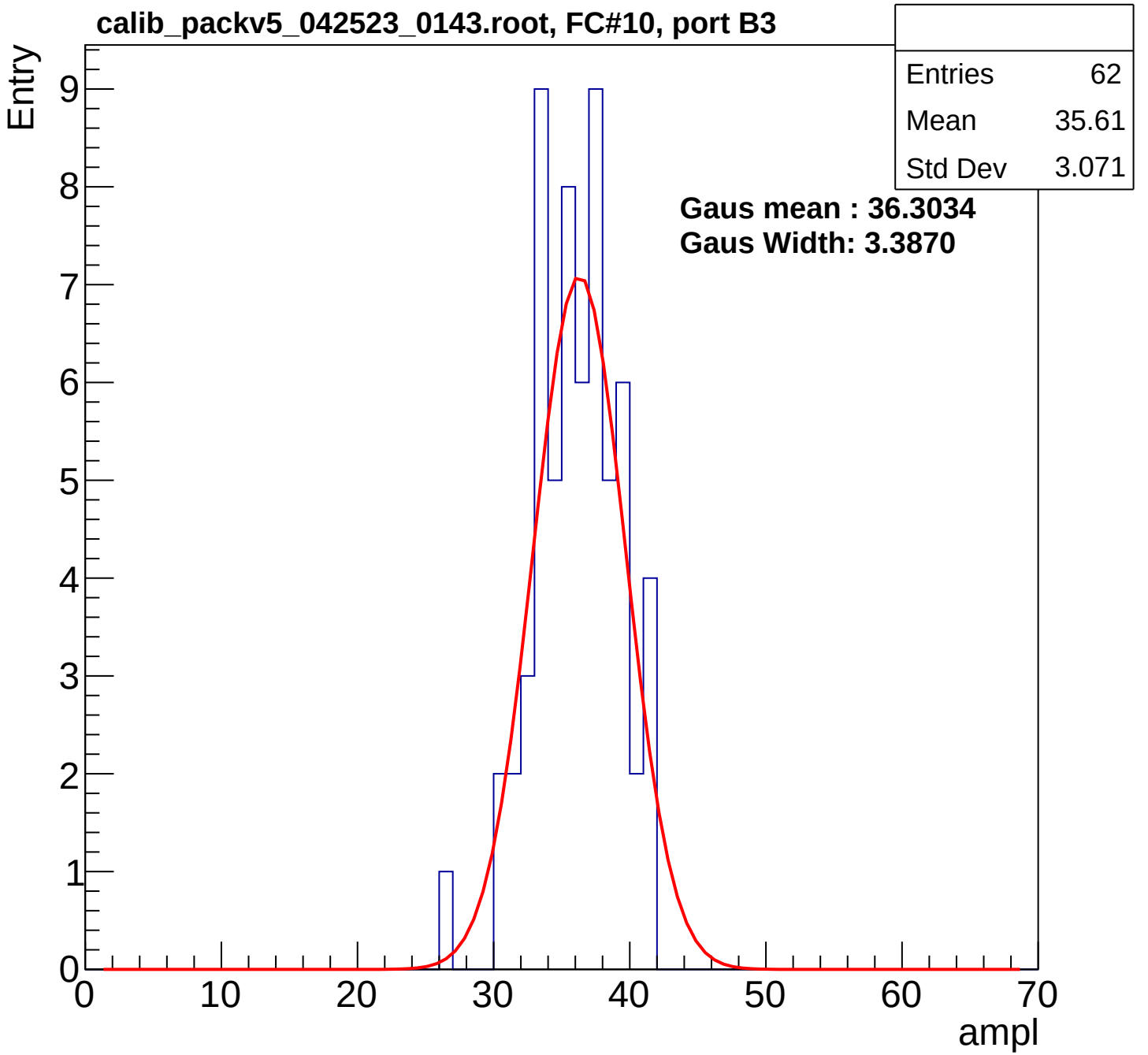
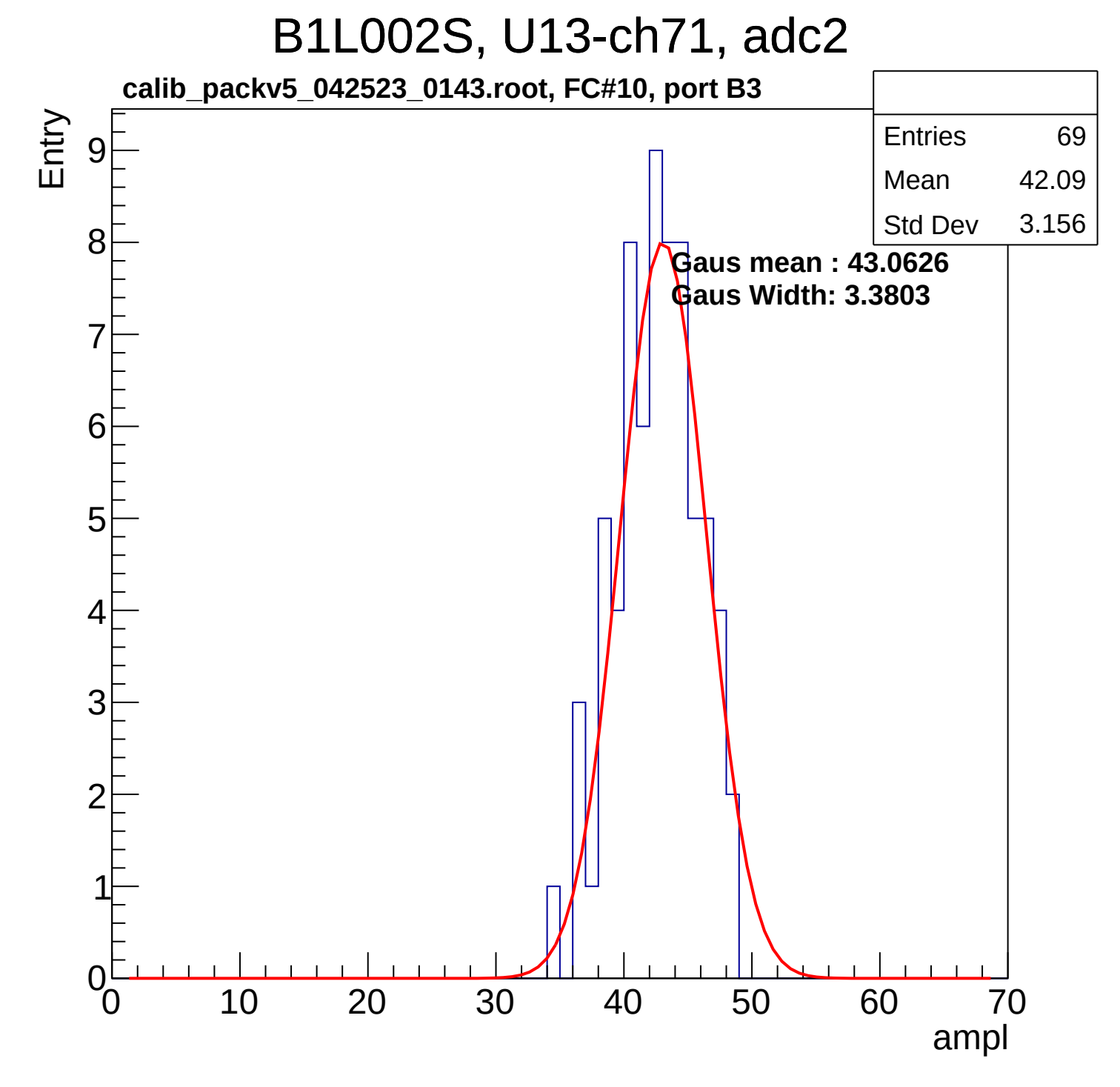
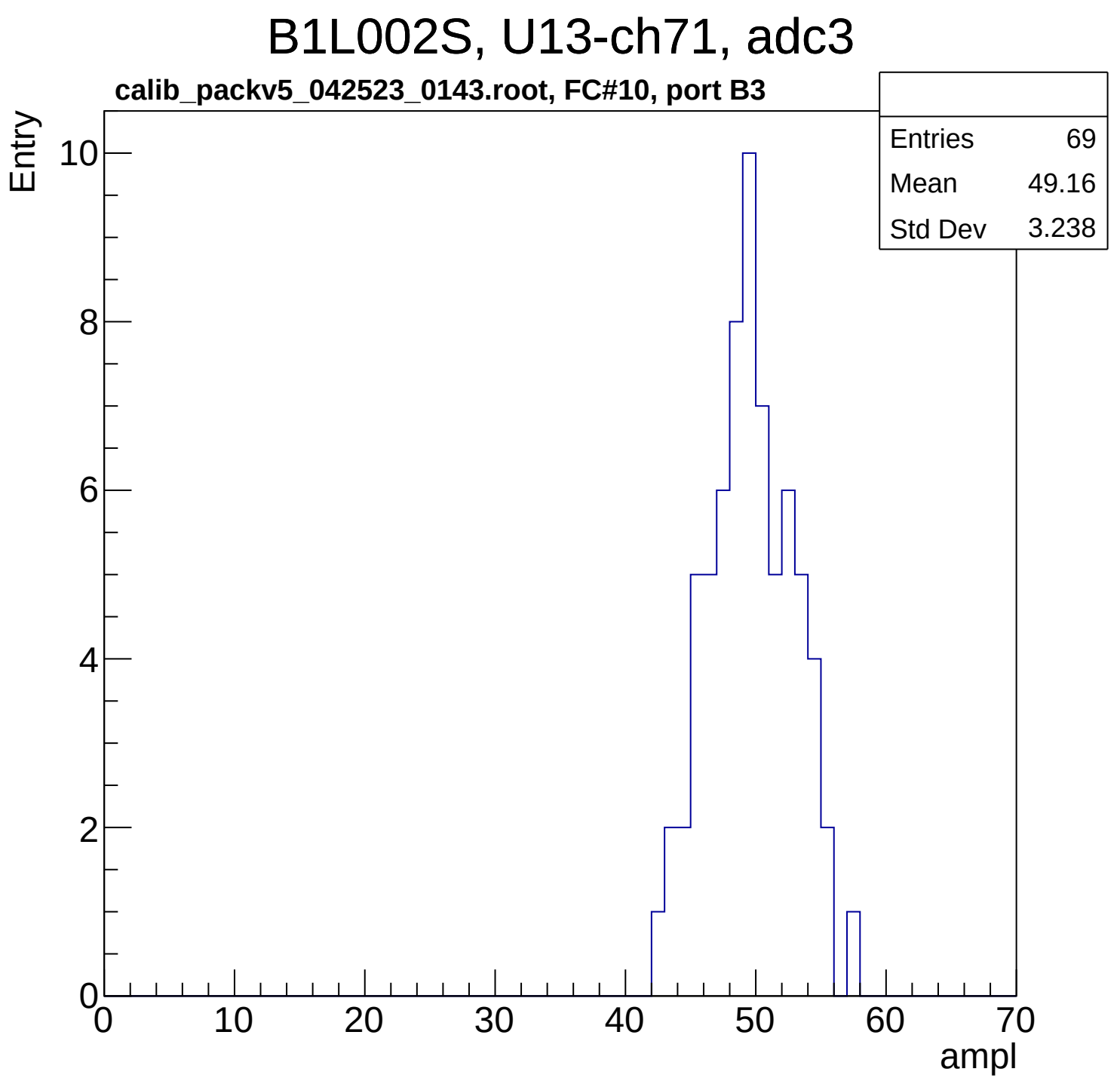
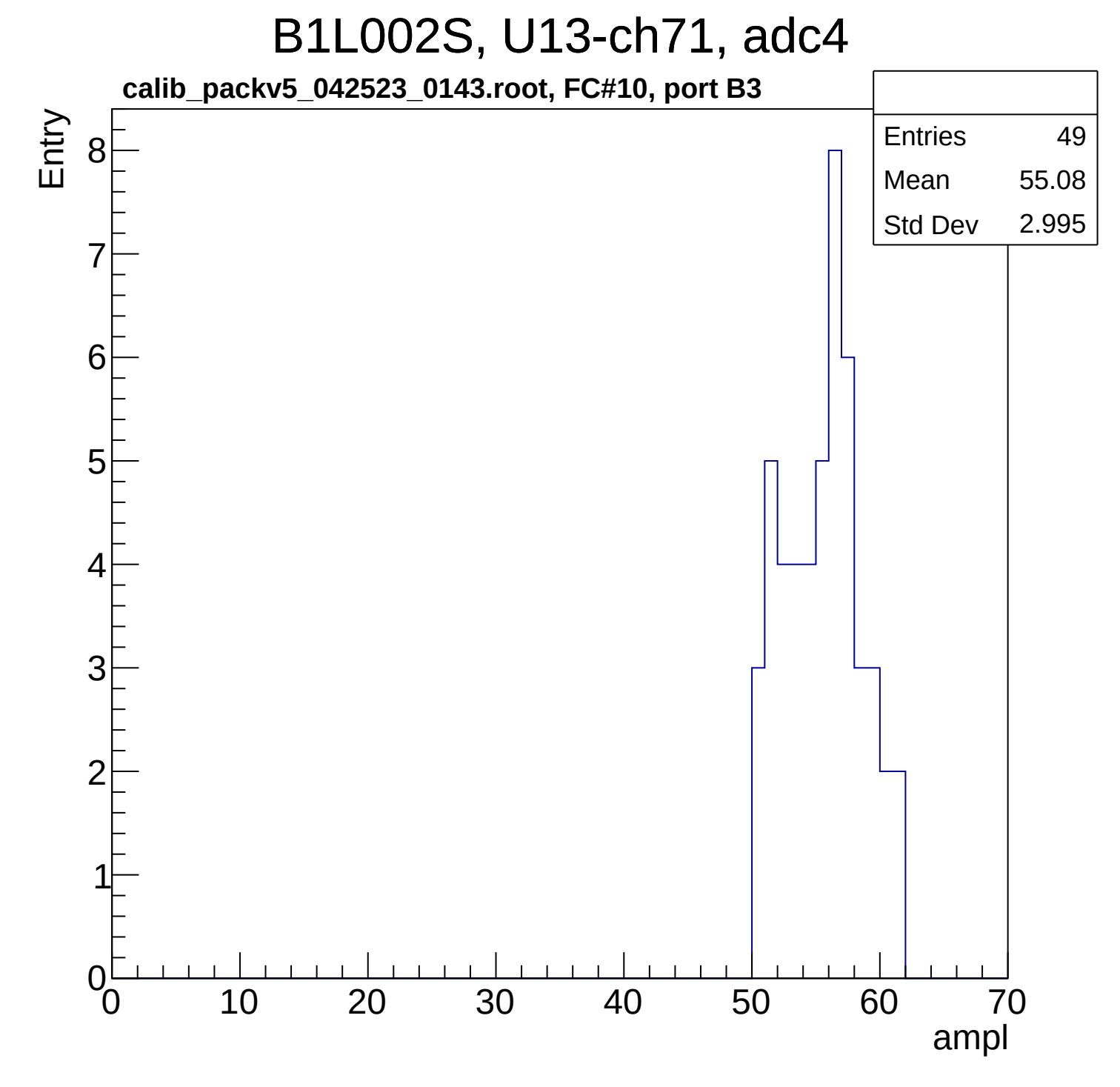

B1L002S, U13-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 28.62
Std Dev 3.312
Gaus mean : 28.7251
Gaus Width: 3.2212
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.61
Std Dev 3.071
Gaus mean : 36.3034
Gaus Width: 3.3870
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.09
Std Dev 3.156
Gaus mean : 43.0626
Gaus Width: 3.3803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 49.16
Std Dev 3.238
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 55.08
Std Dev 2.995
Entry
ampl
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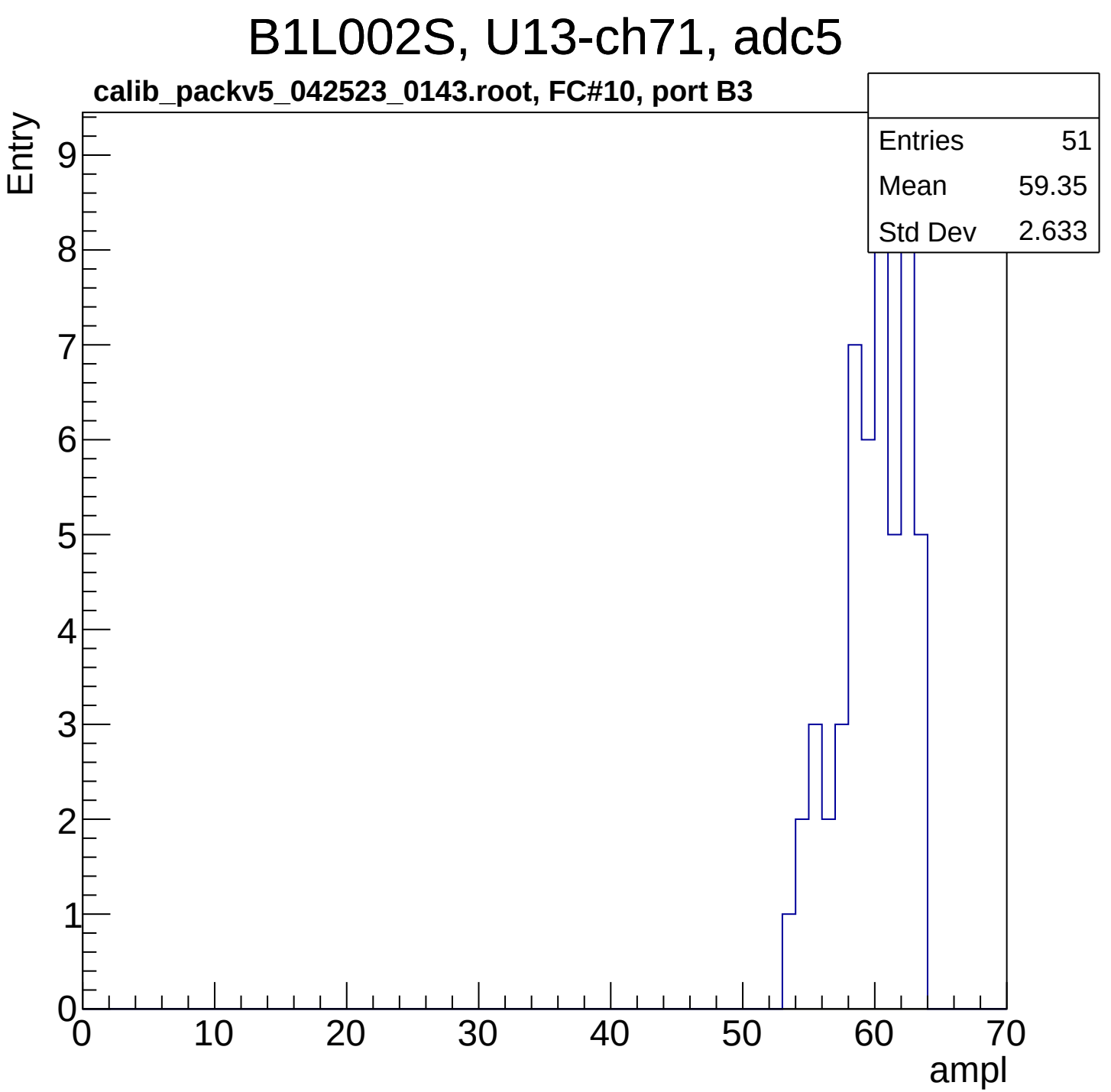
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.35
Std Dev 2.633
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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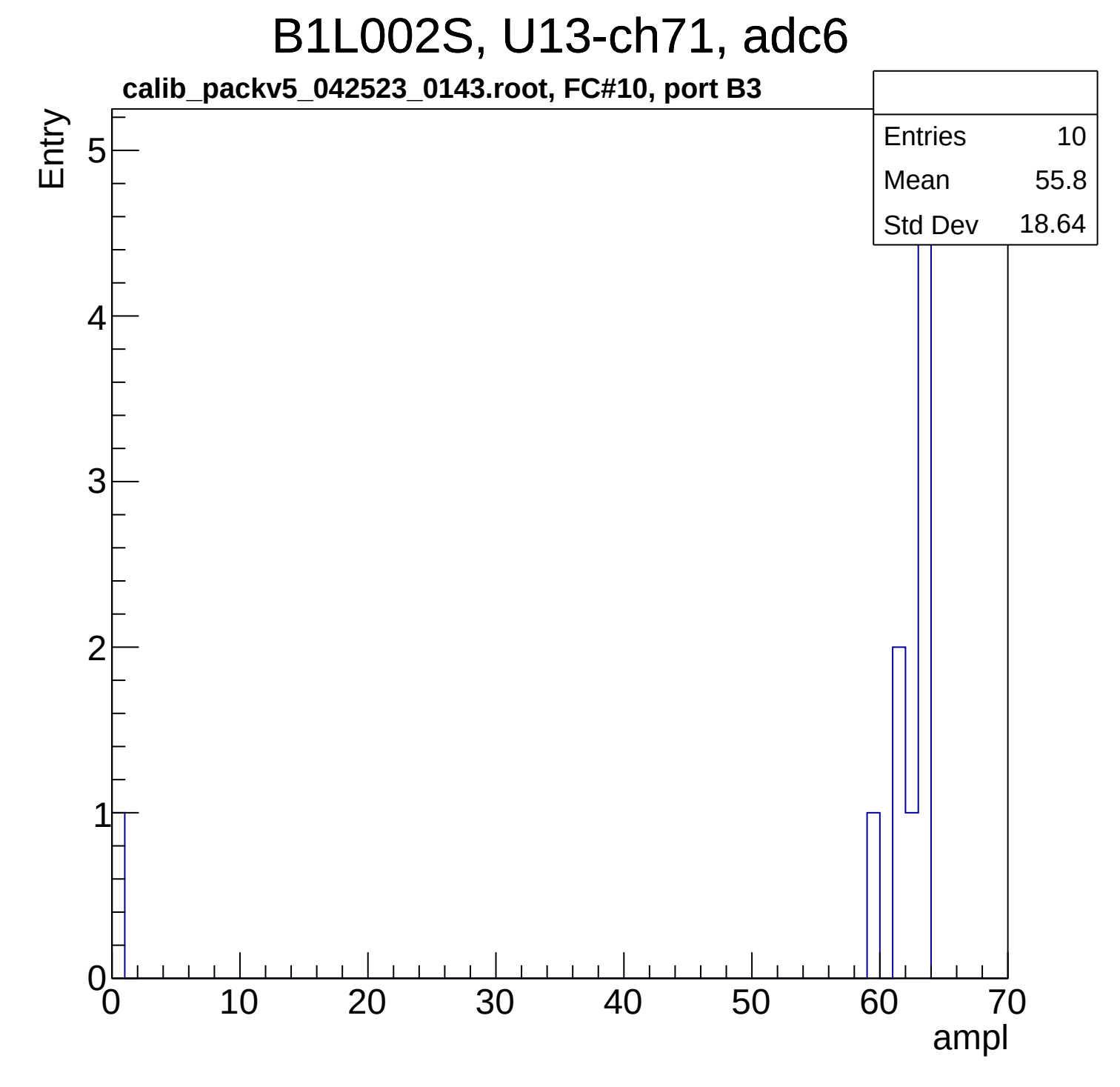
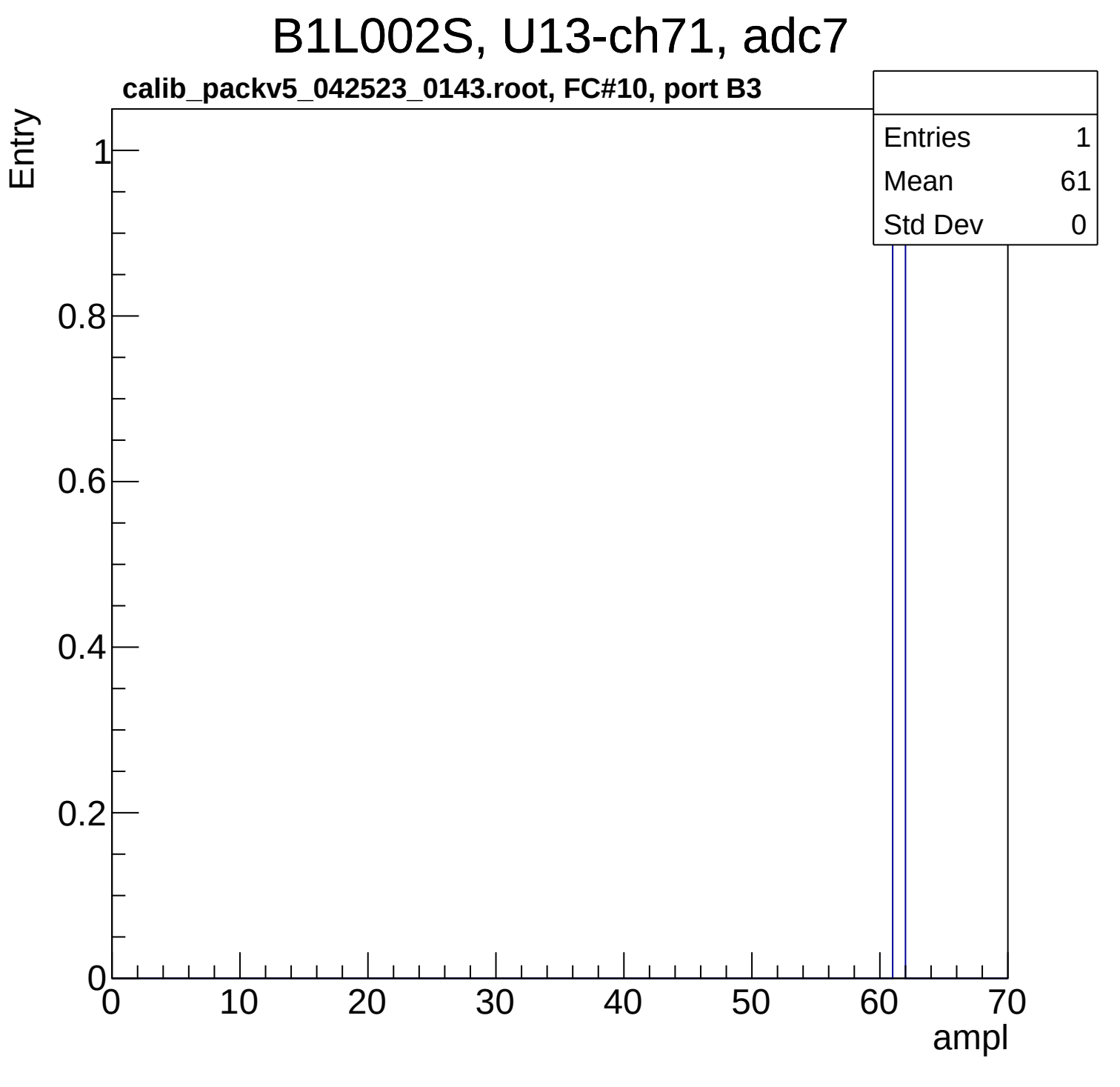

B1L002S, U13-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.64
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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0.6
0.4
0.2
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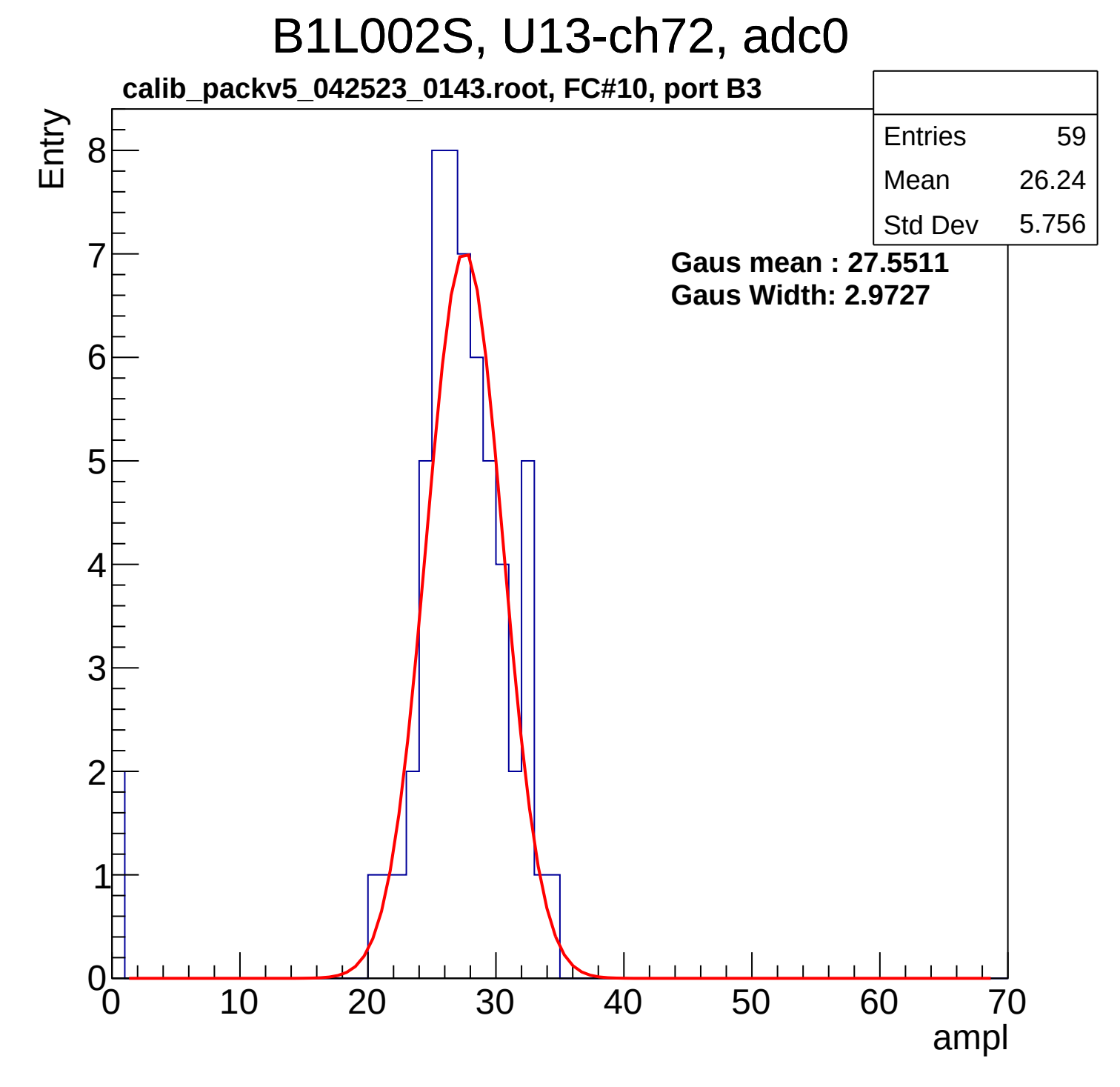
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.24
Std Dev 5.756
Gaus mean : 27.5511
Gaus Width: 2.9727
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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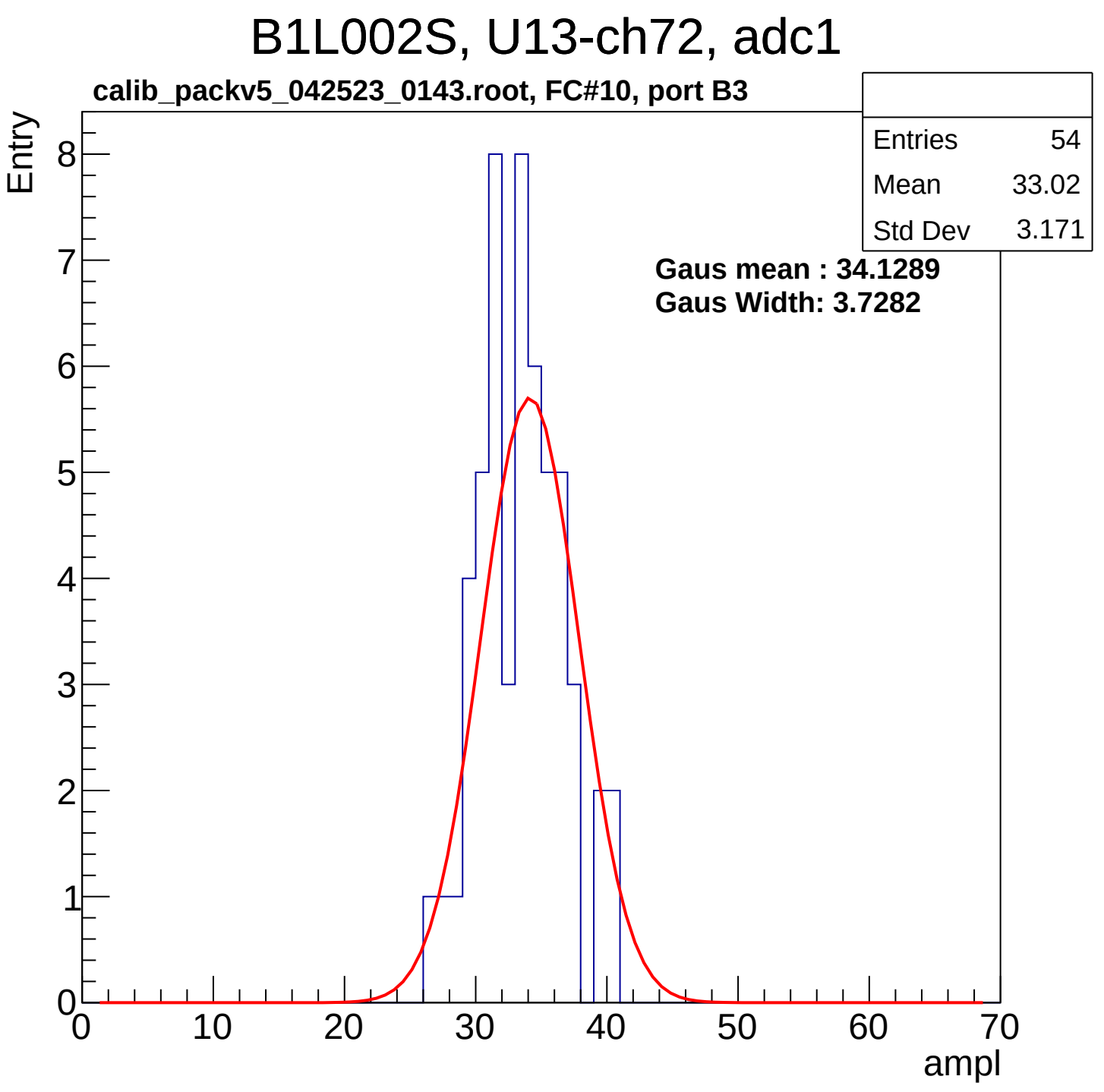
B1L002S, U13-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 33.02
Std Dev 3.171
Gaus mean : 34.1289
Gaus Width: 3.7282
Entry
ampl
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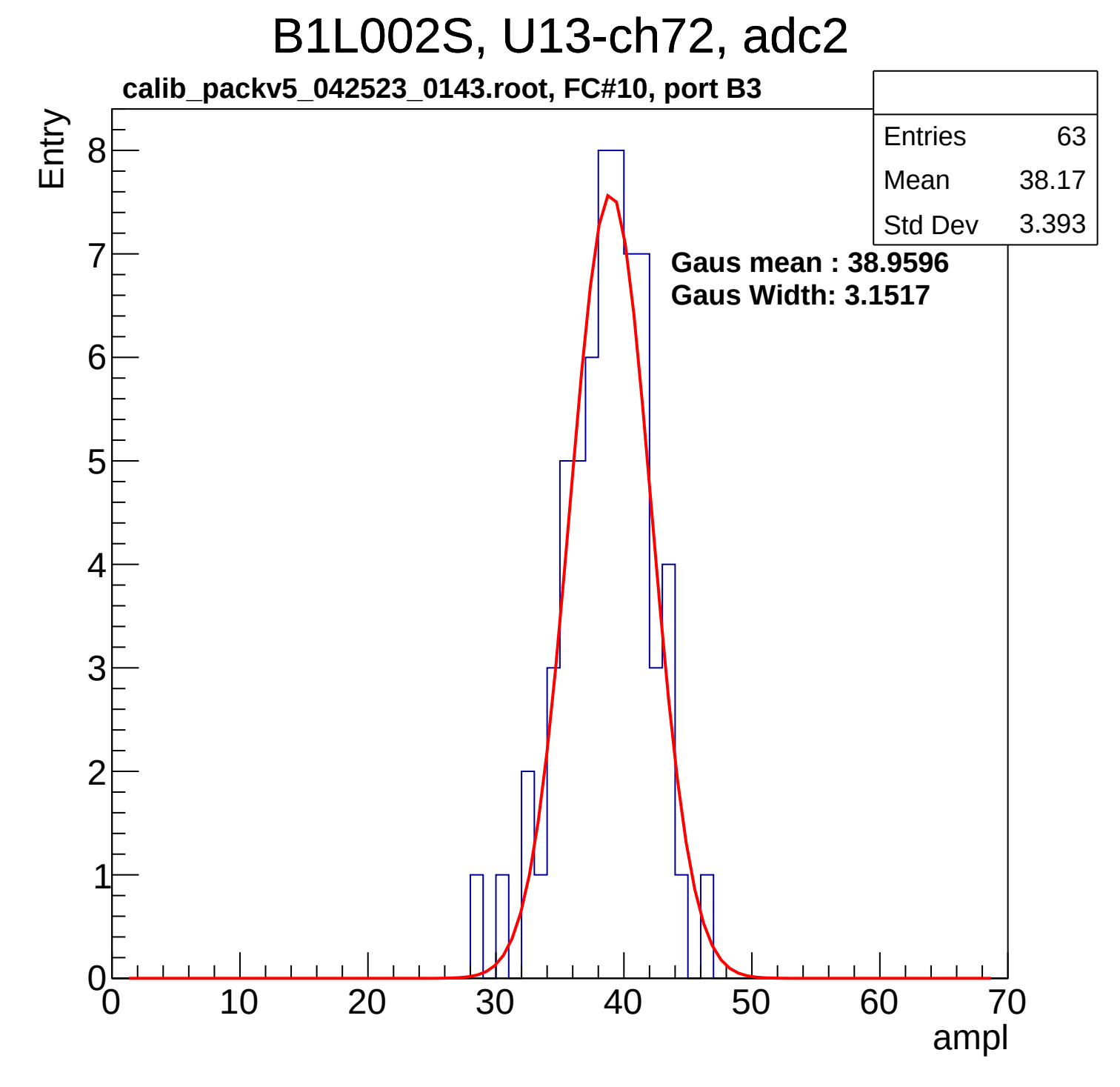
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 38.17
Std Dev 3.393
Gaus mean : 38.9596
Gaus Width: 3.1517
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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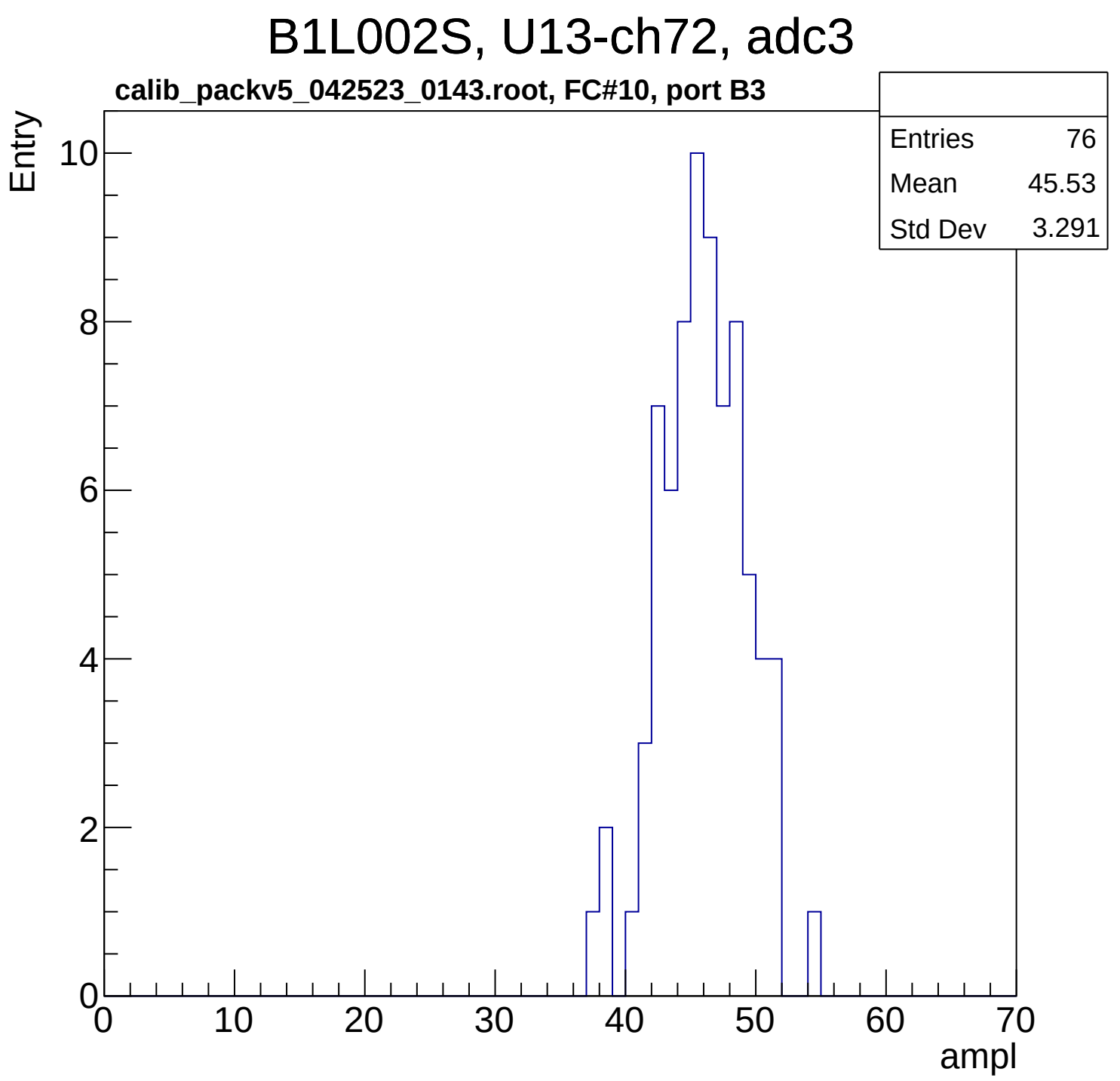
B1L002S, U13-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 45.53
Std Dev 3.291
Entry
ampl
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2
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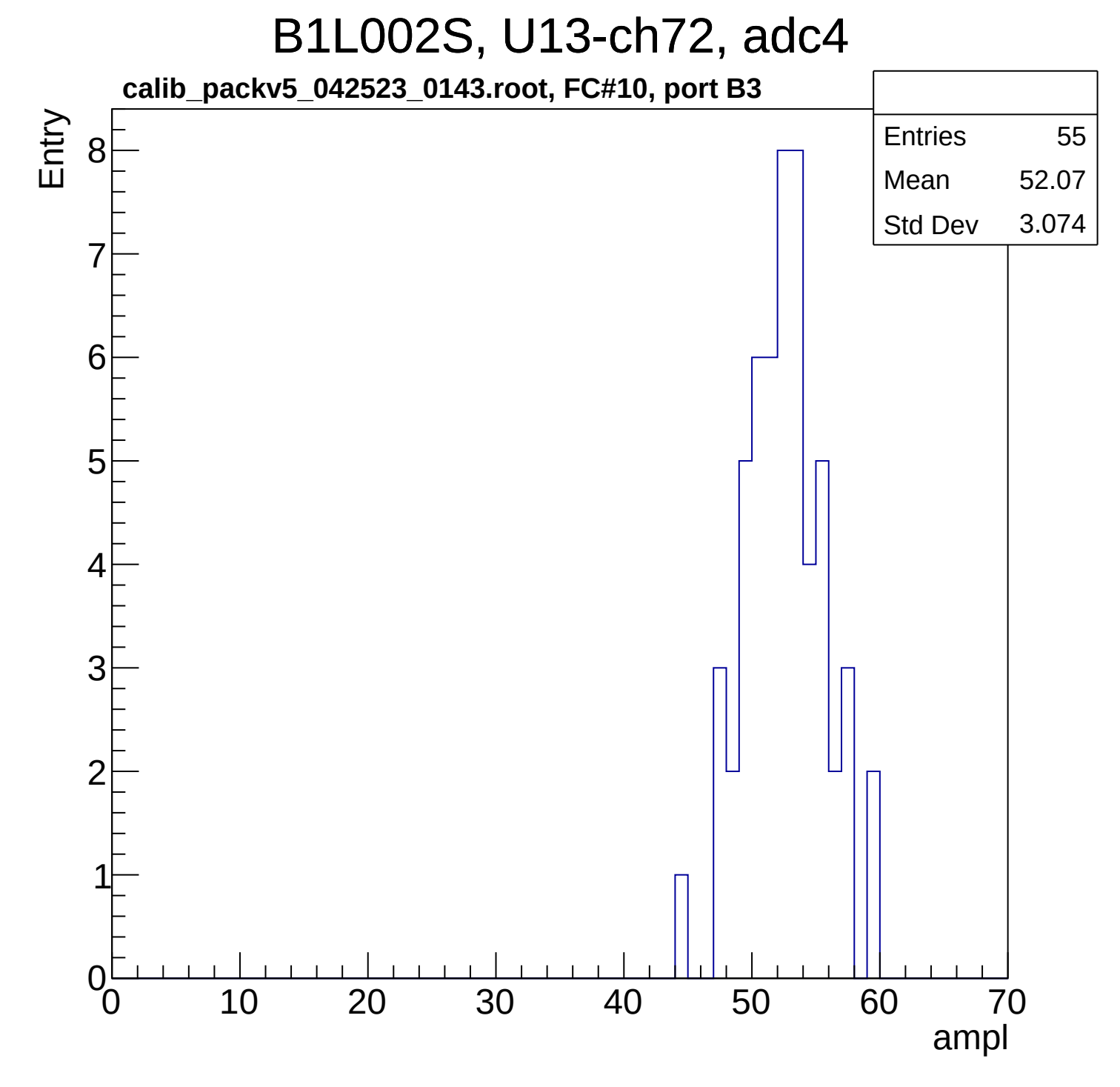
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.07
Std Dev 3.074
Entry
ampl
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2
3
4
5
6
7
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0
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60
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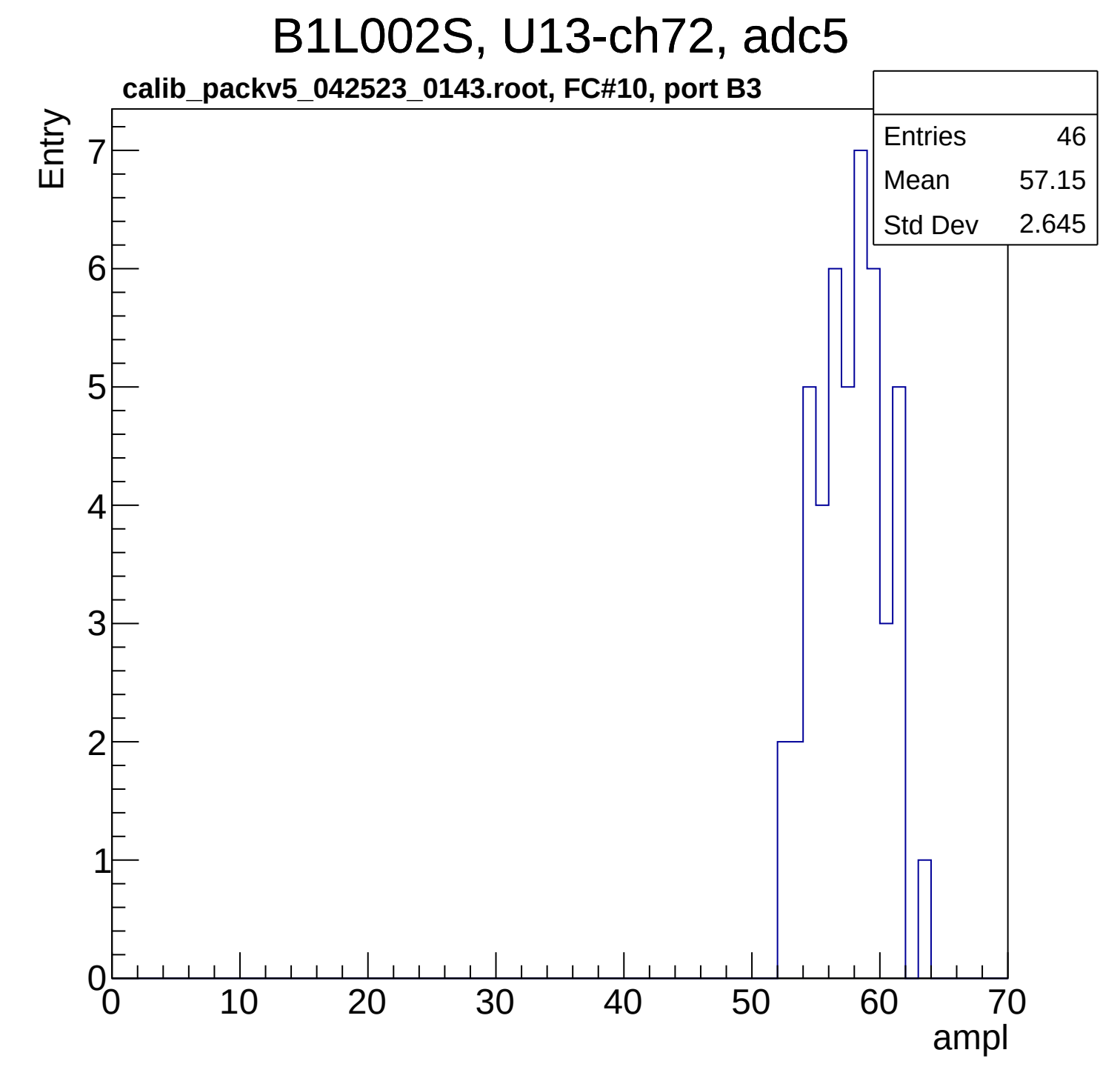
B1L002S, U13-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.15
Std Dev 2.645
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
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60
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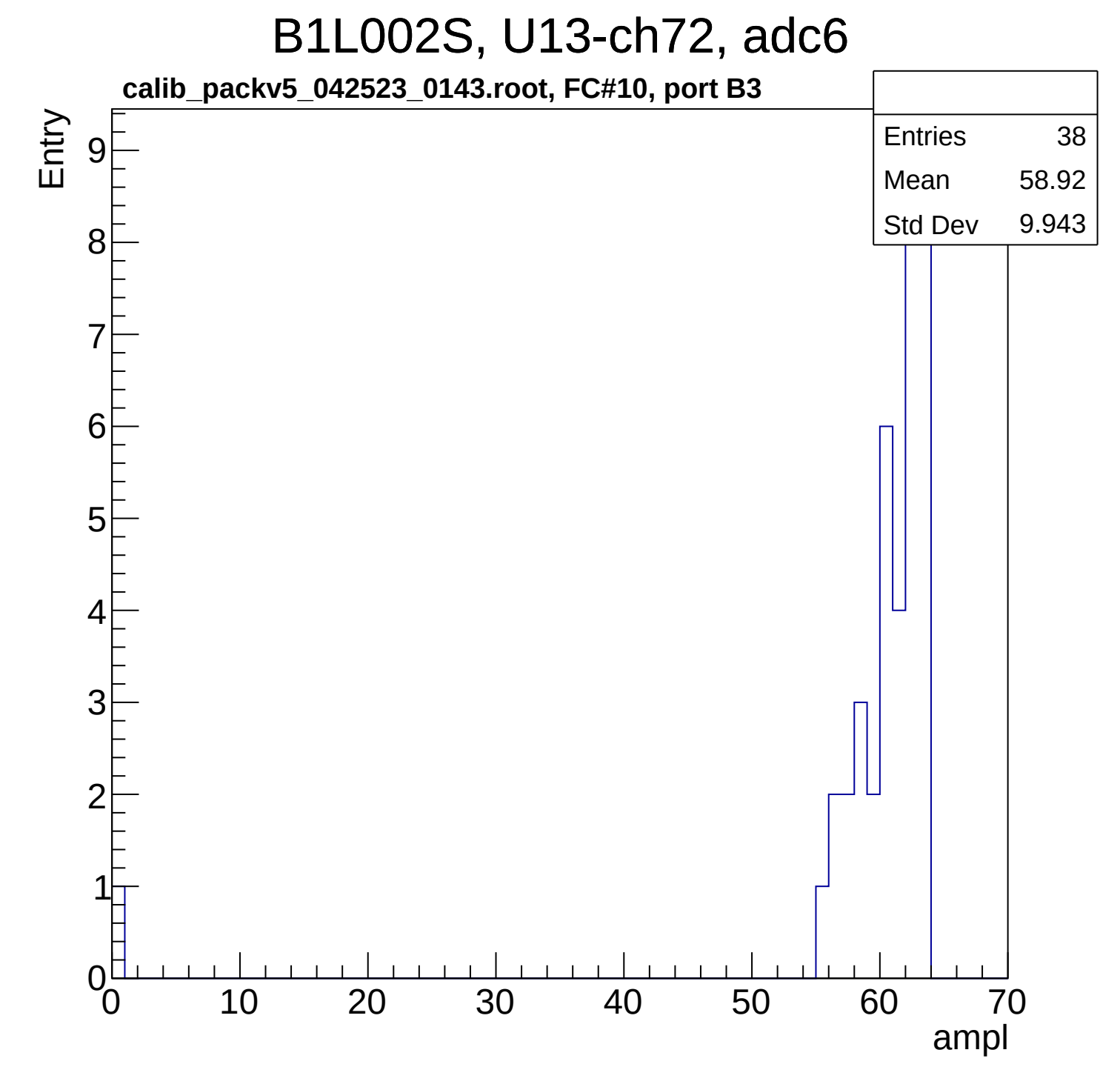
B1L002S, U13-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 58.92
Std Dev 9.943
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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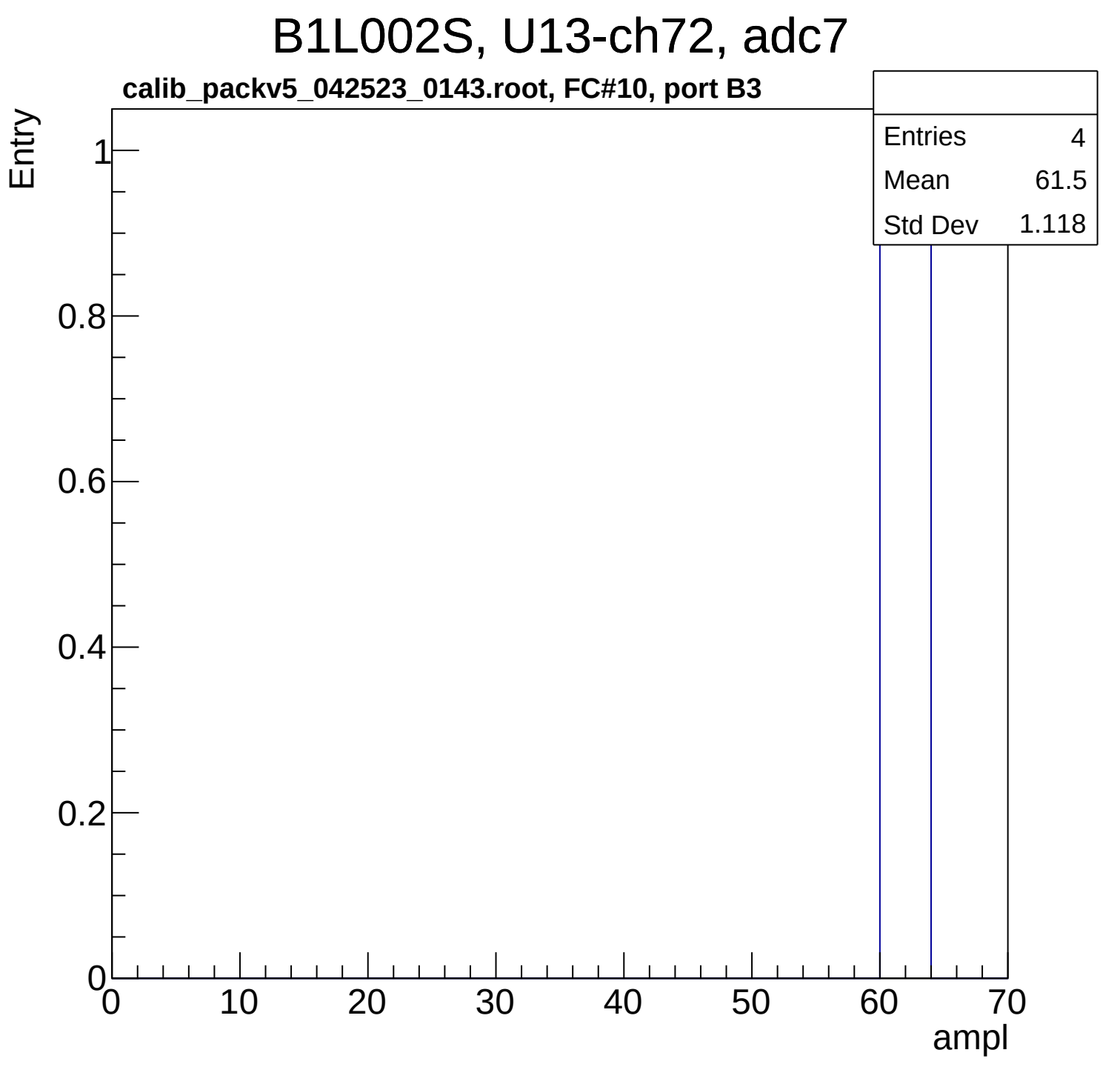
B1L002S, U13-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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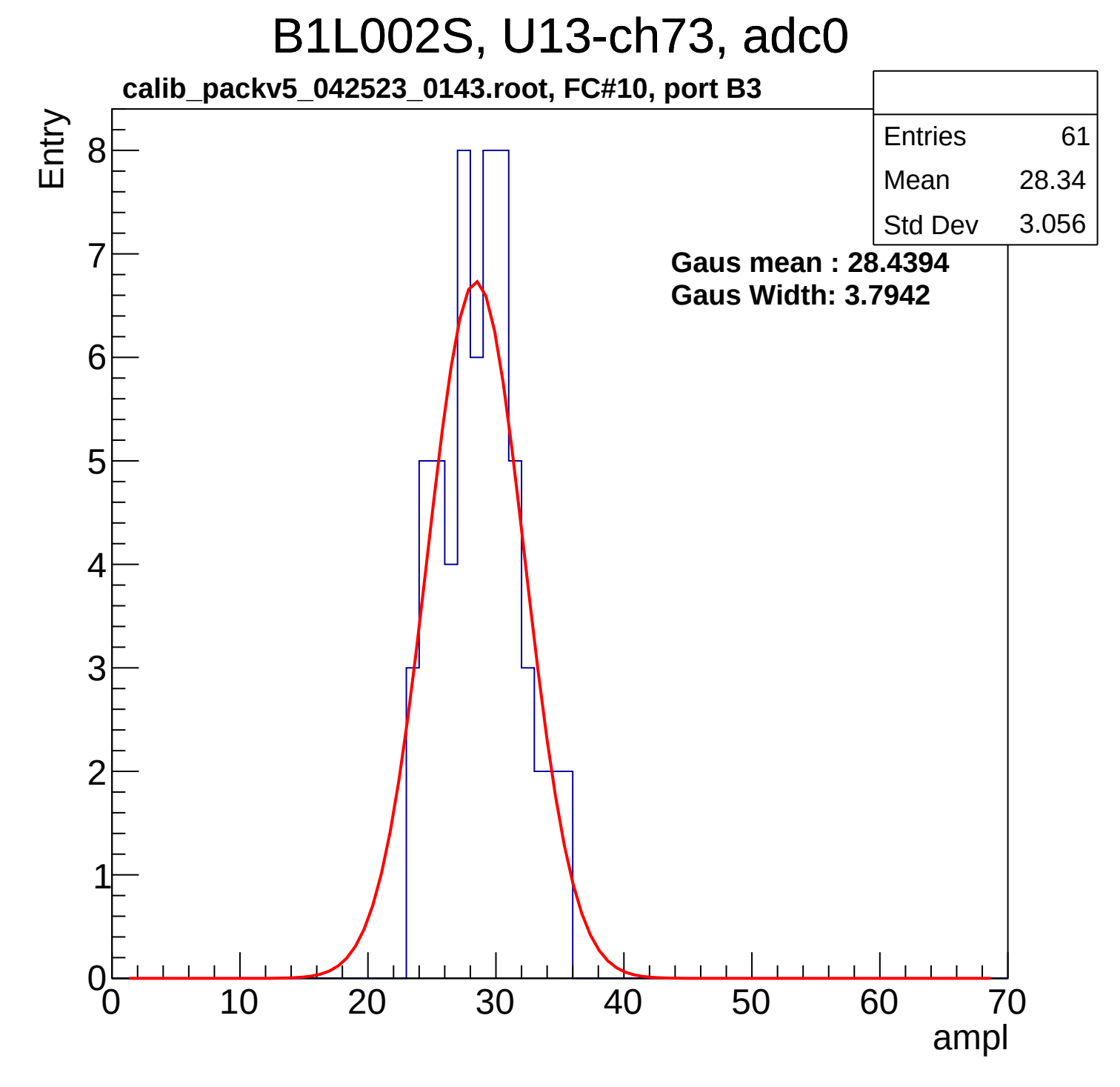
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.34
Std Dev 3.056
Gaus mean : 28.4394
Gaus Width: 3.7942
Entry
ampl
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1
2
3
4
5
6
7
8
0
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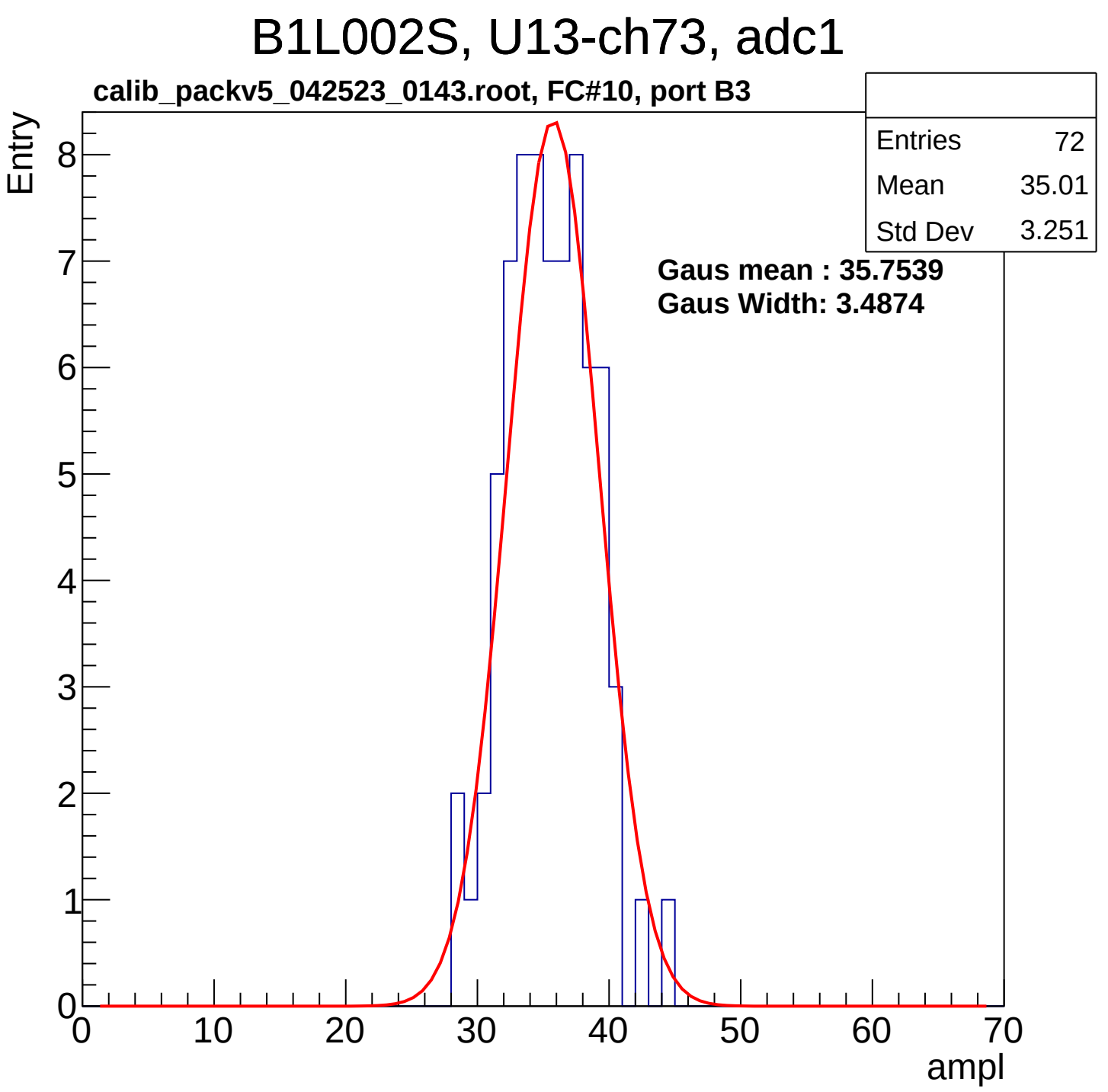
B1L002S, U13-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.01
Std Dev 3.251
Gaus mean : 35.7539
Gaus Width: 3.4874
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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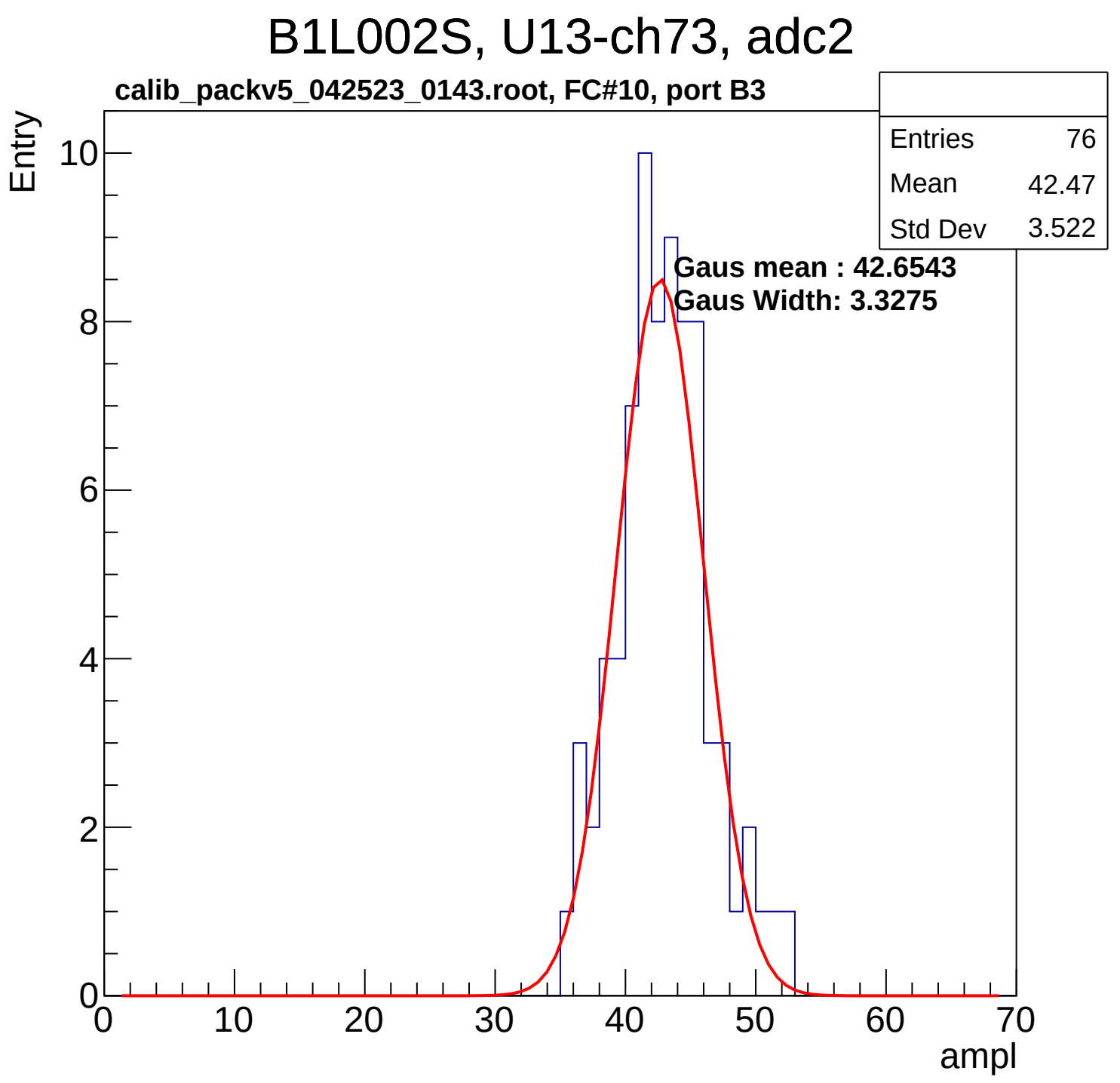
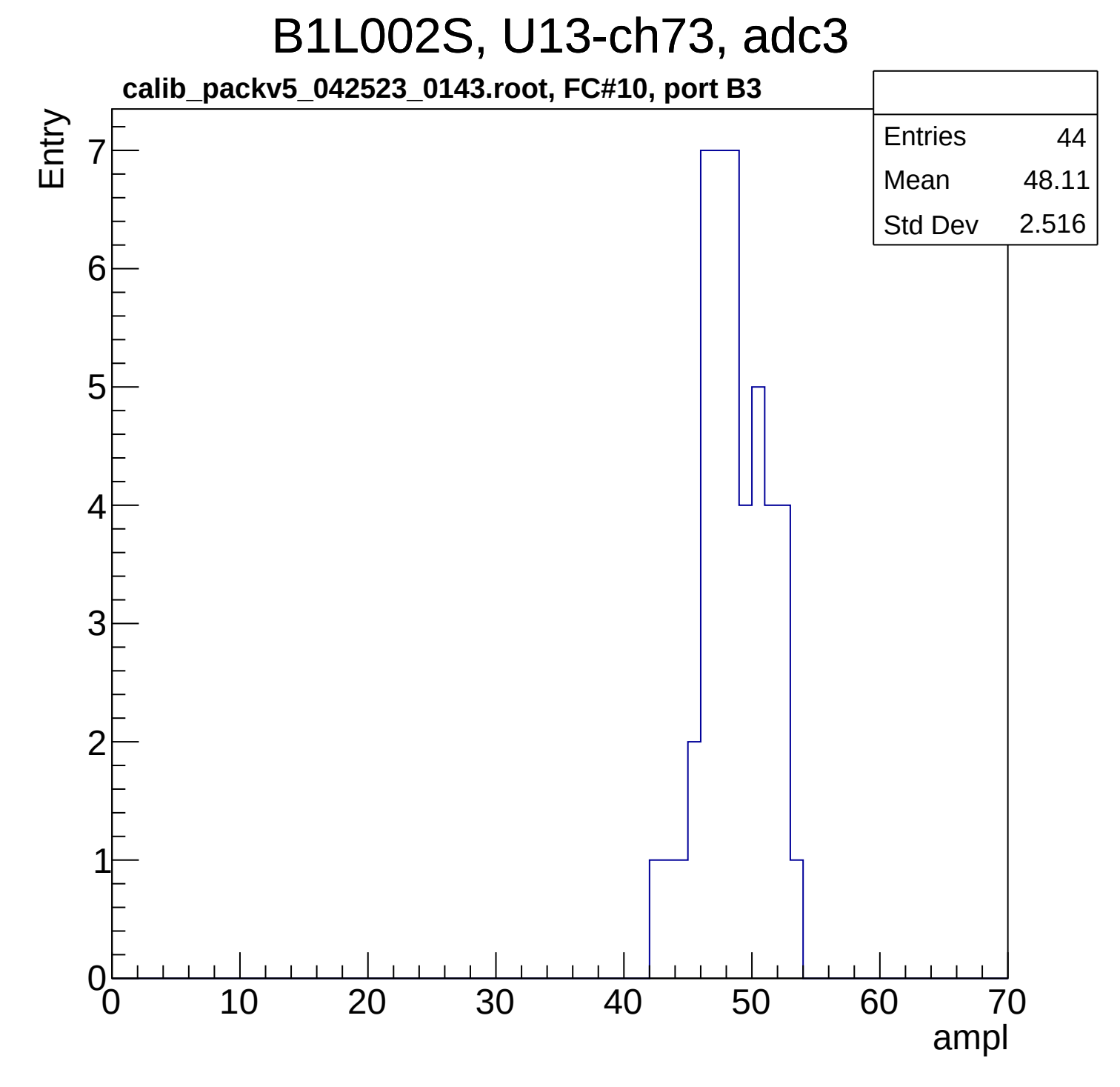

B1L002S, U13-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 42.47
Std Dev 3.522
Gaus mean : 42.6543
Gaus Width: 3.3275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 48.11
Std Dev 2.516
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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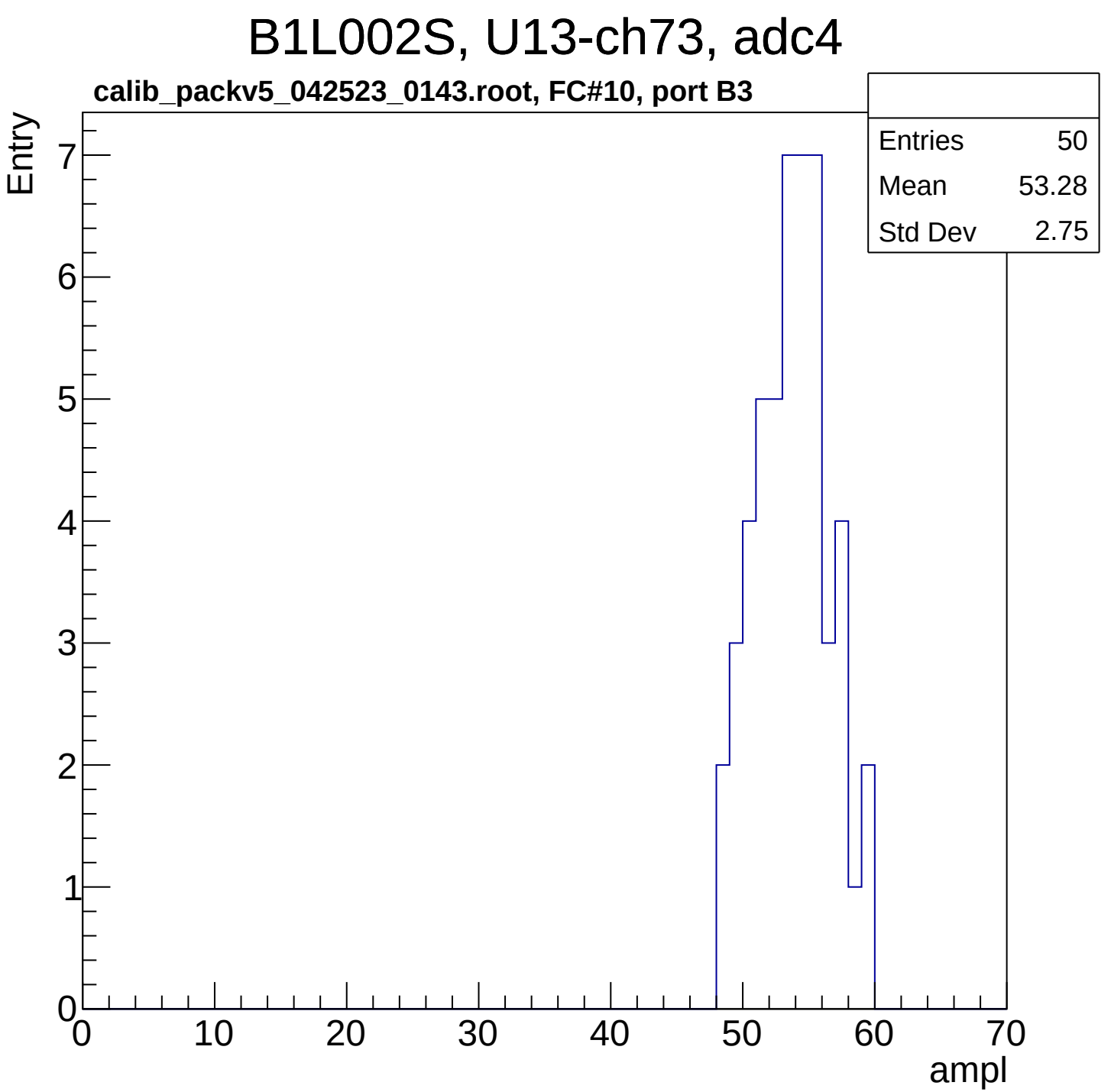
B1L002S, U13-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.28
Std Dev 2.75
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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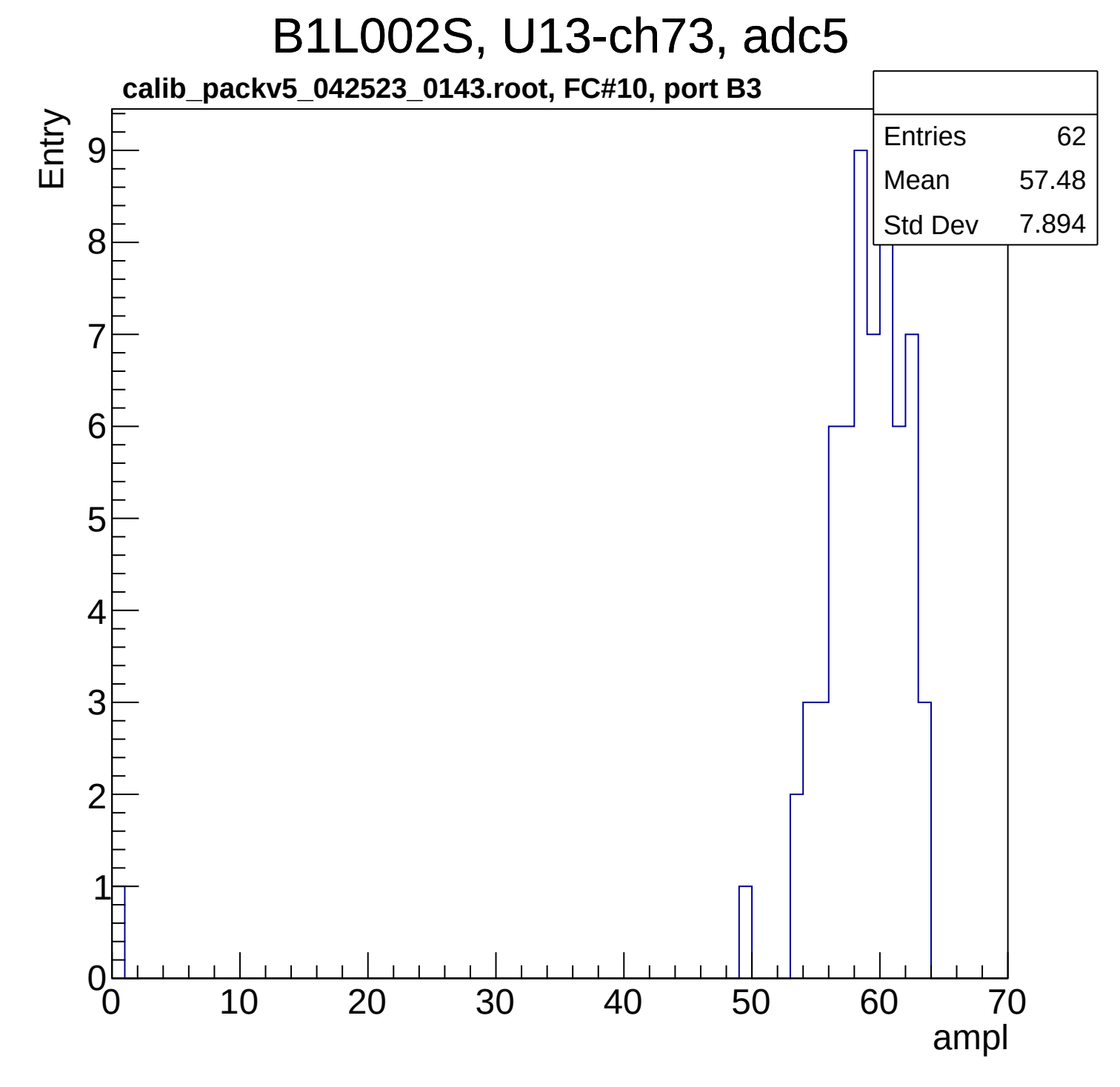
B1L002S, U13-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.48
Std Dev 7.894
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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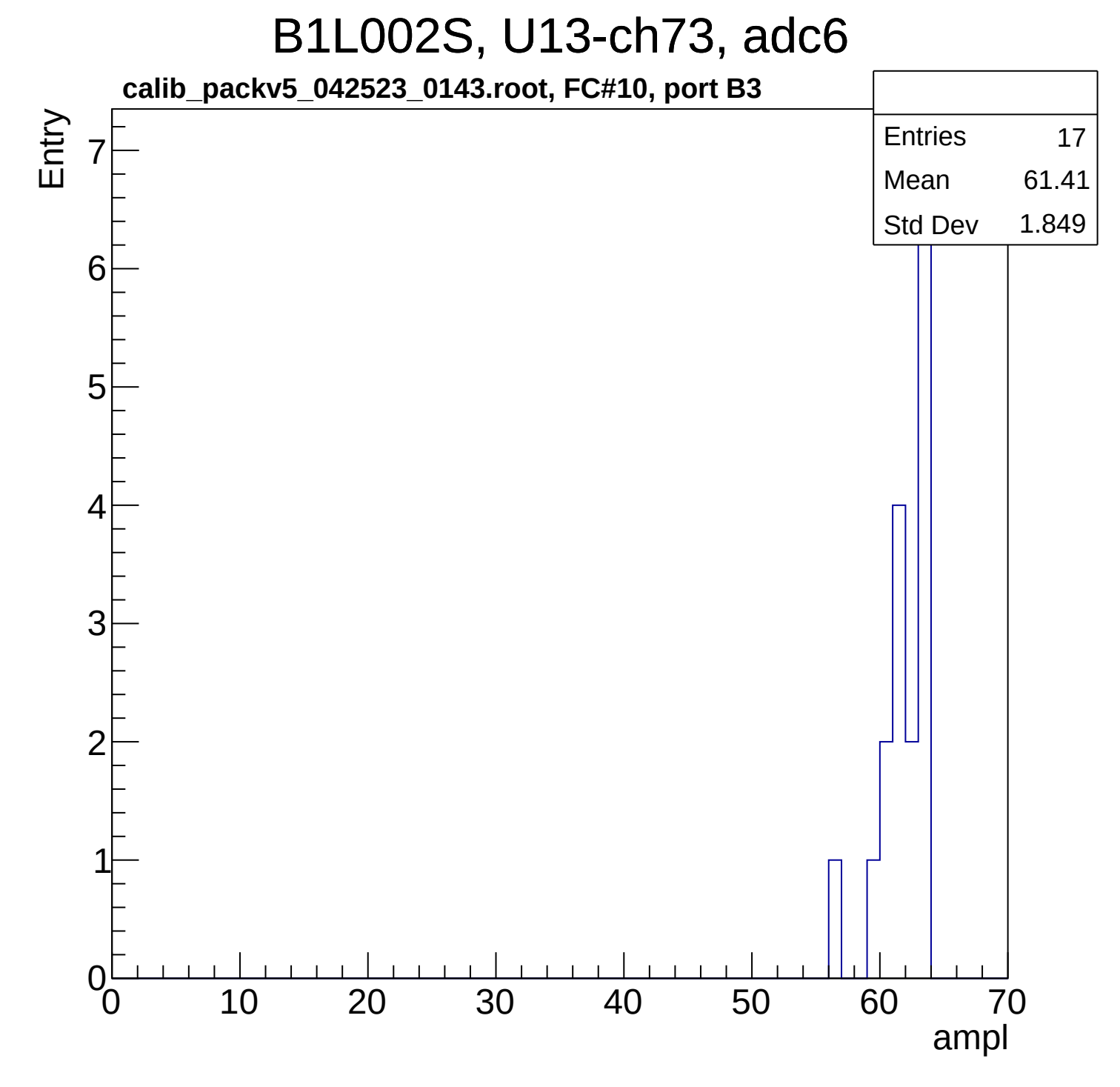
B1L002S, U13-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.41
Std Dev 1.849
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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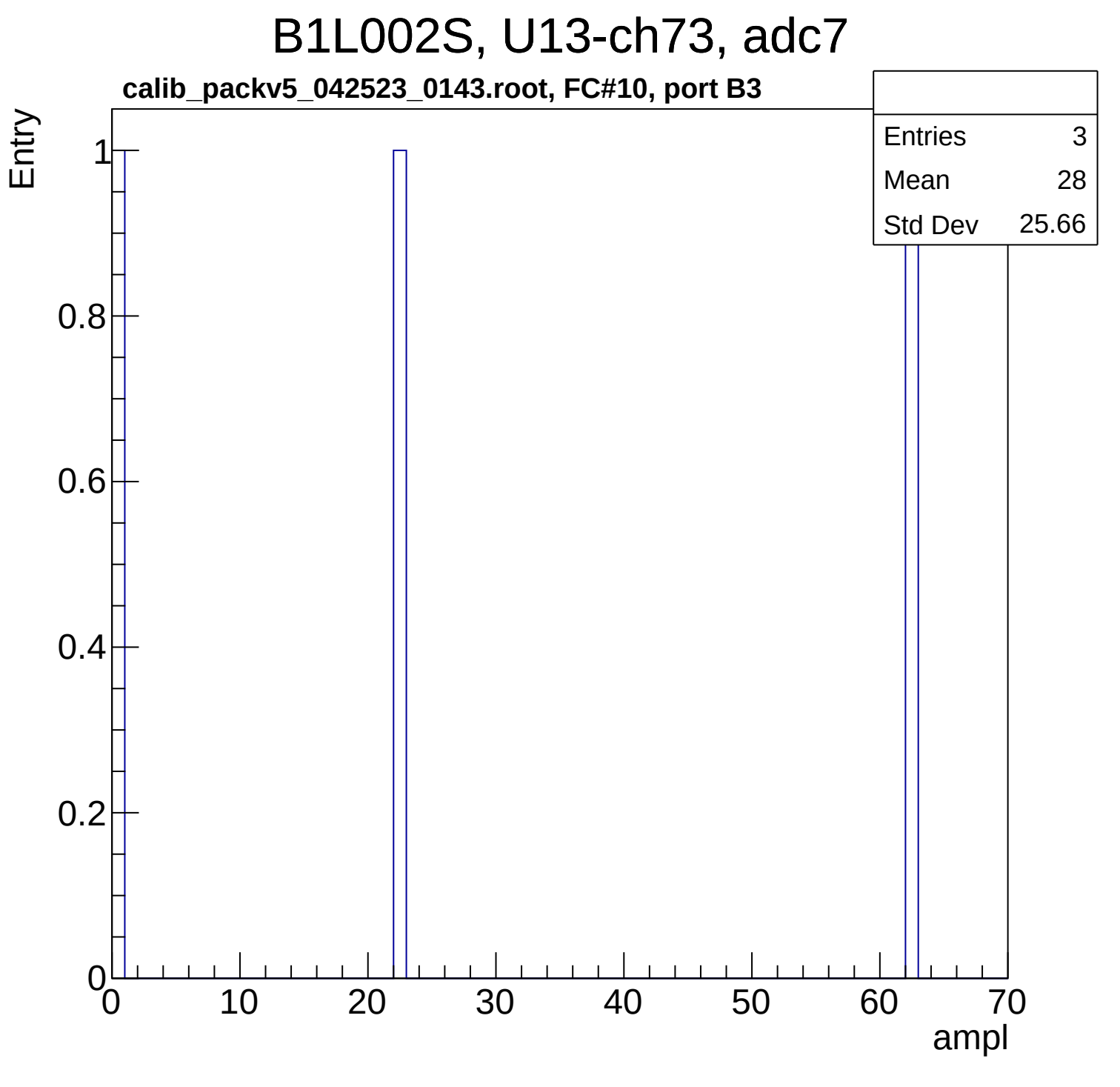
B1L002S, U13-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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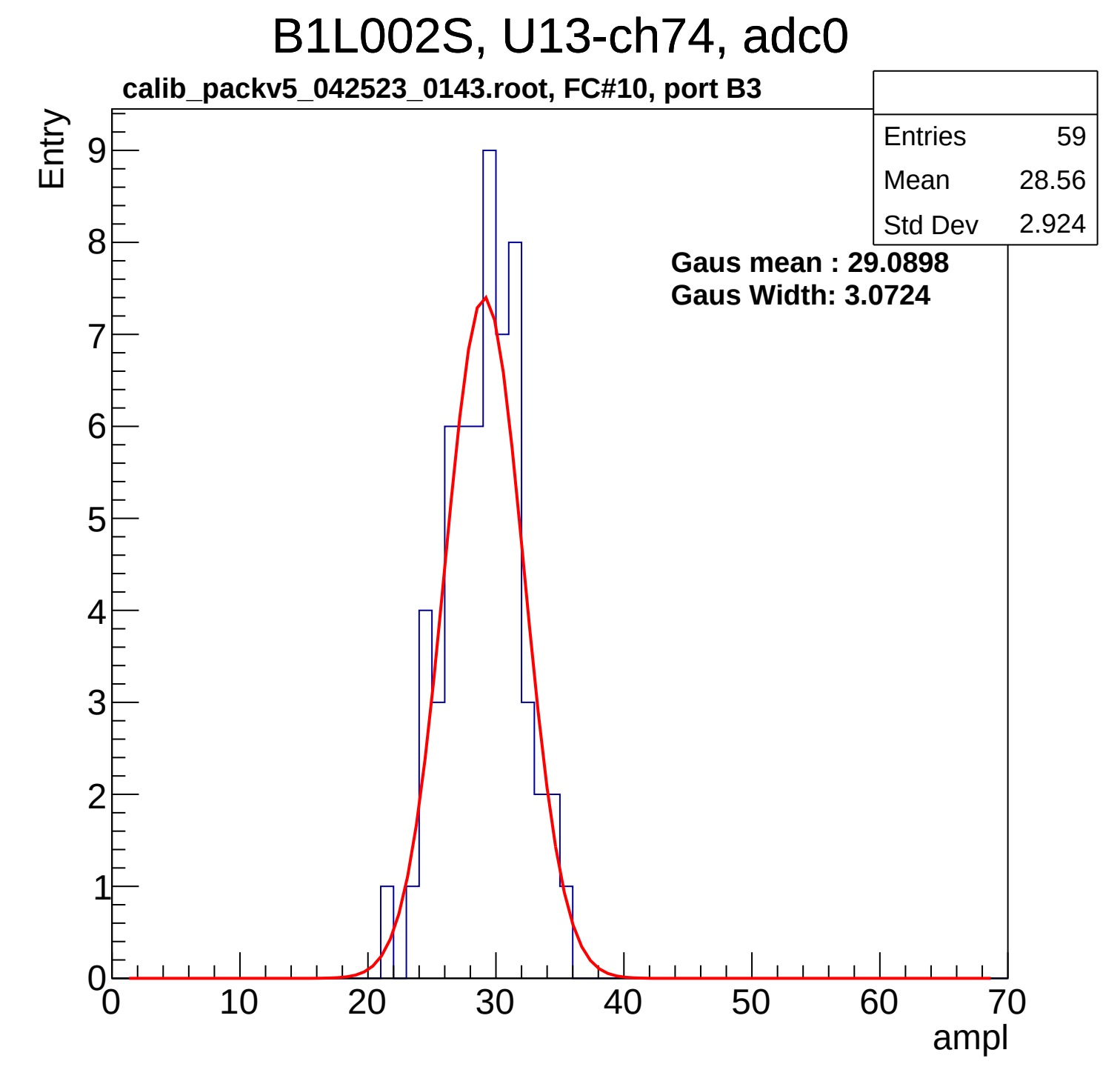
B1L002S, U13-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.56
Std Dev 2.924
Gaus mean : 29.0898
Gaus Width: 3.0724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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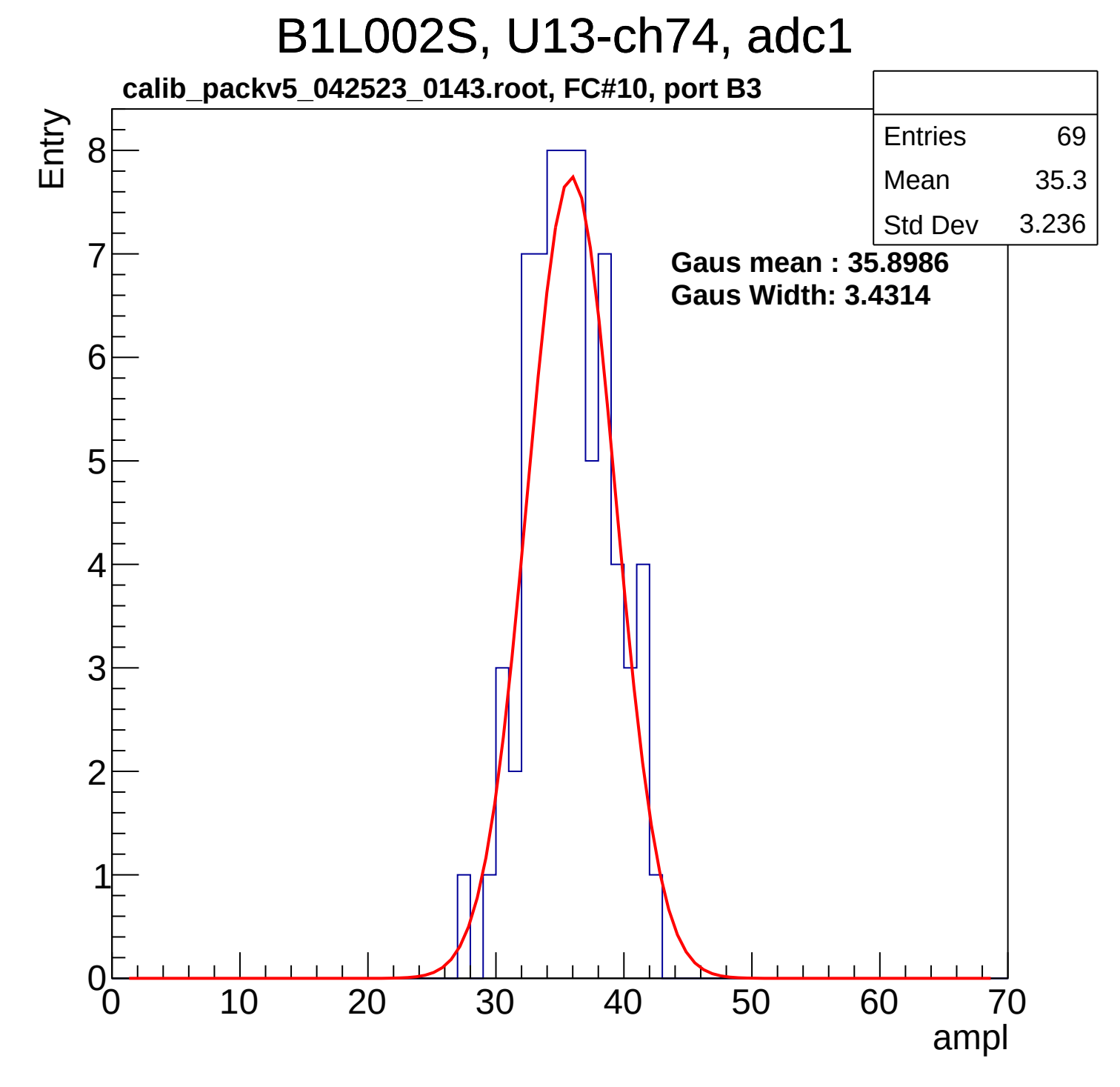
B1L002S, U13-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.3
Std Dev 3.236
Gaus mean : 35.8986
Gaus Width: 3.4314
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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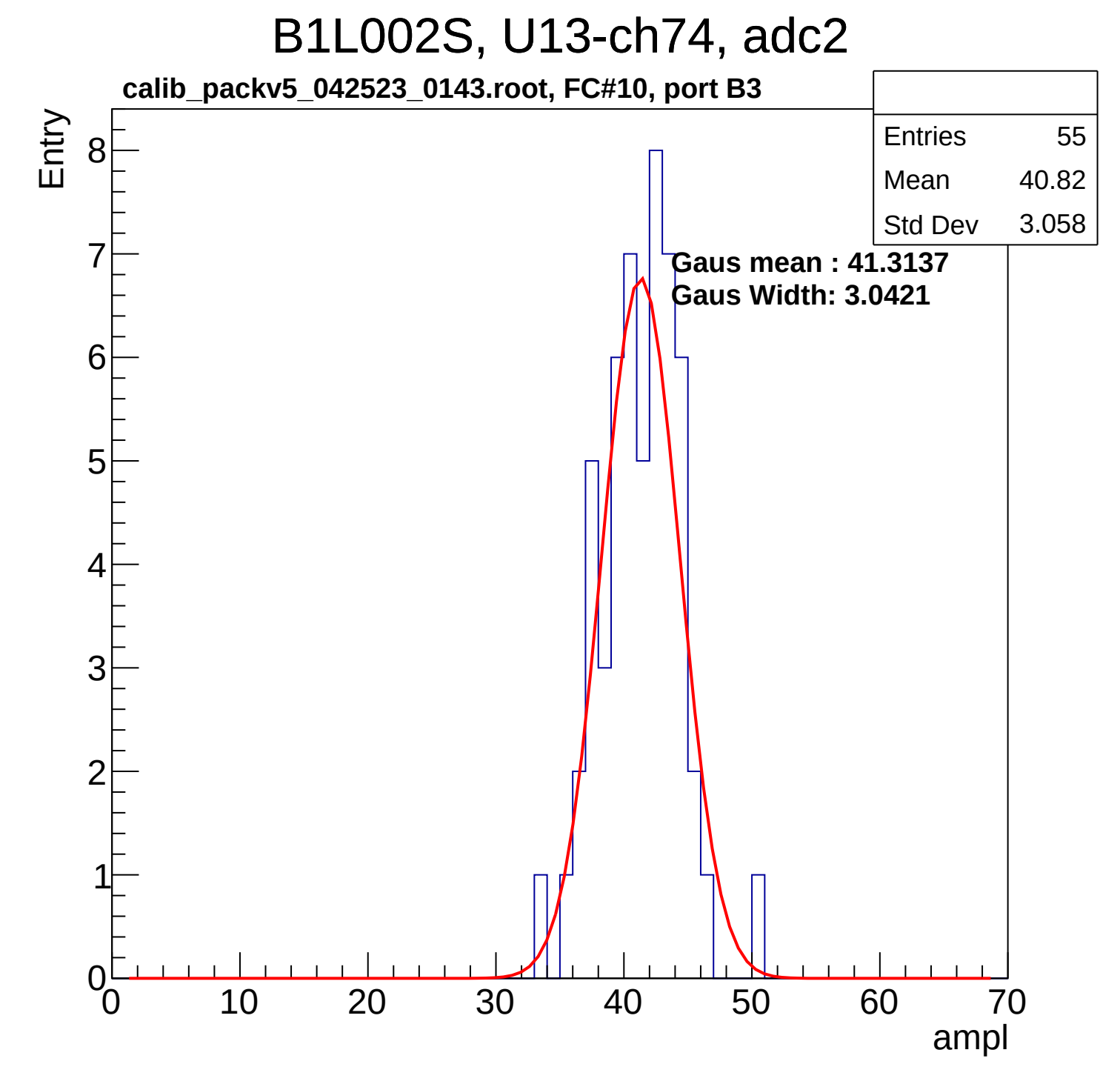
B1L002S, U13-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.82
Std Dev 3.058
Gaus mean : 41.3137
Gaus Width: 3.0421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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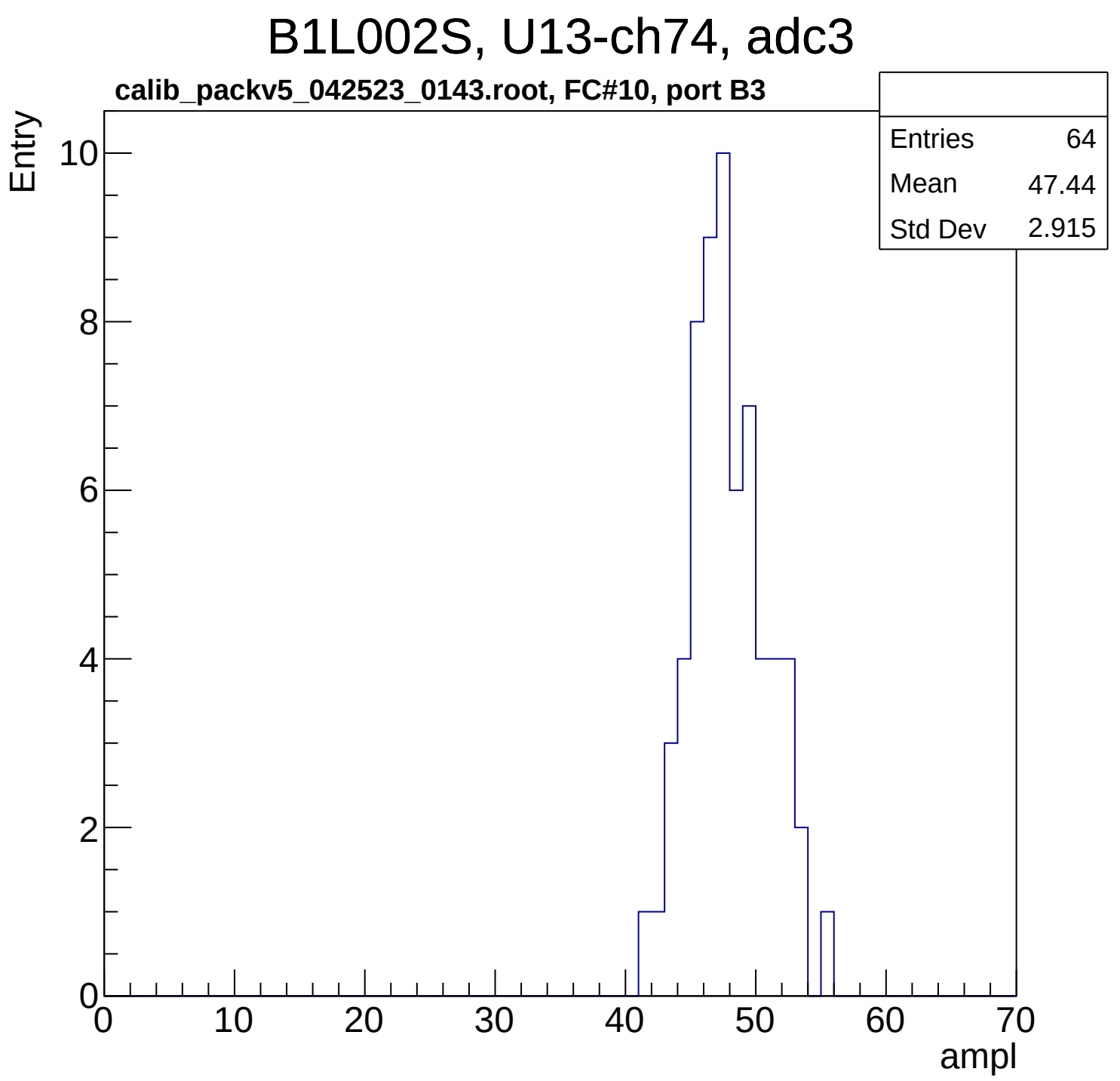
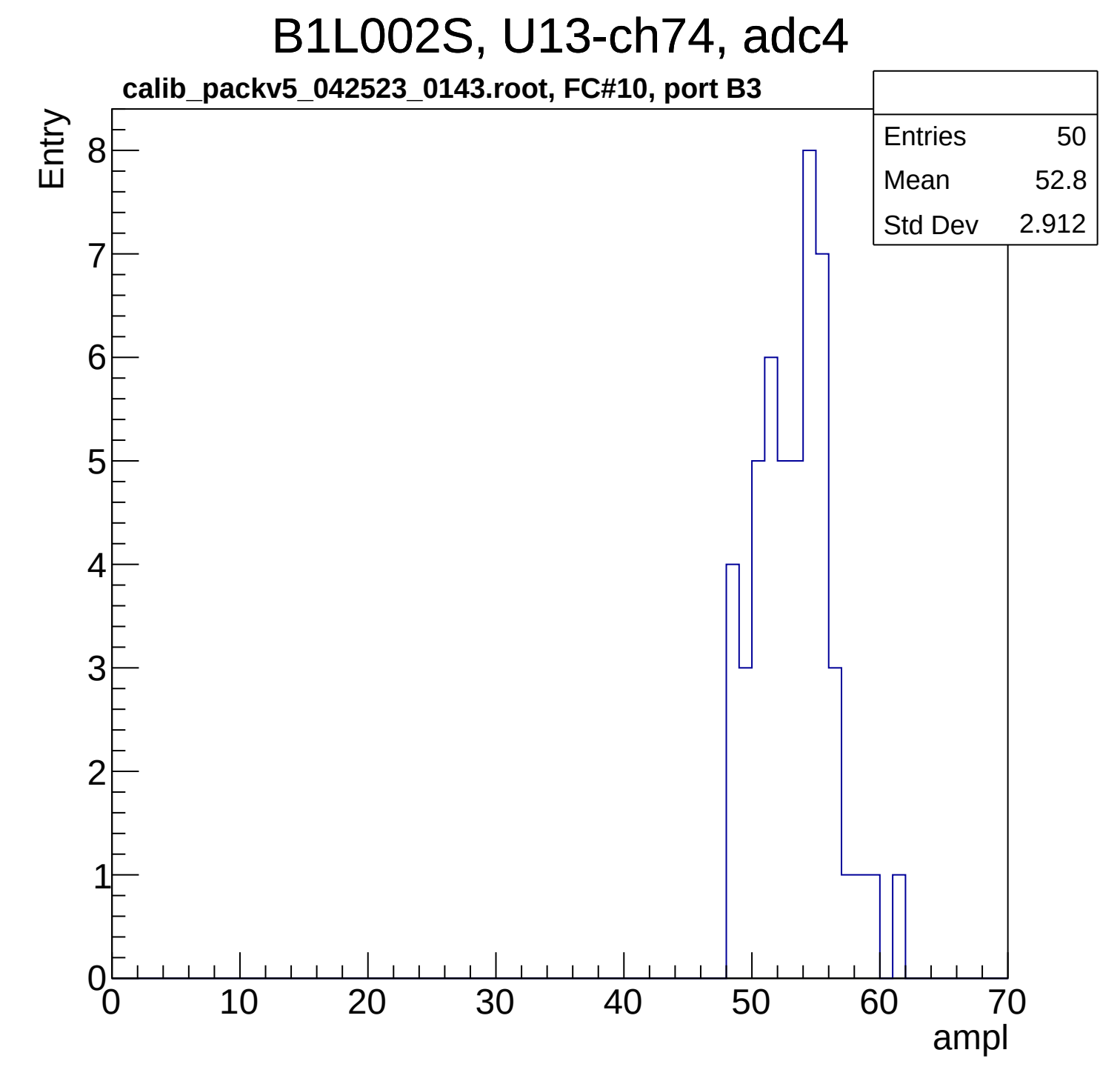
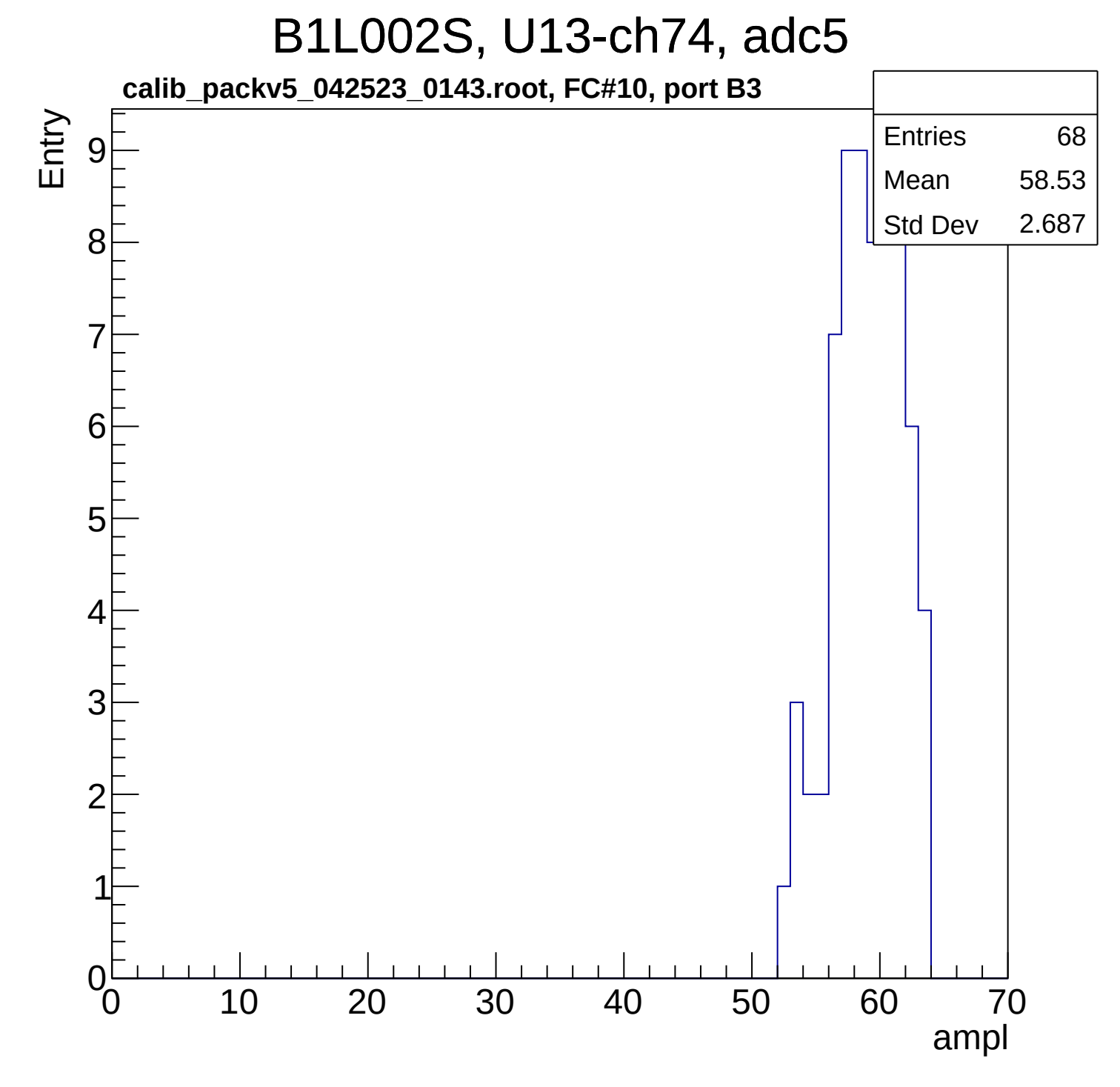
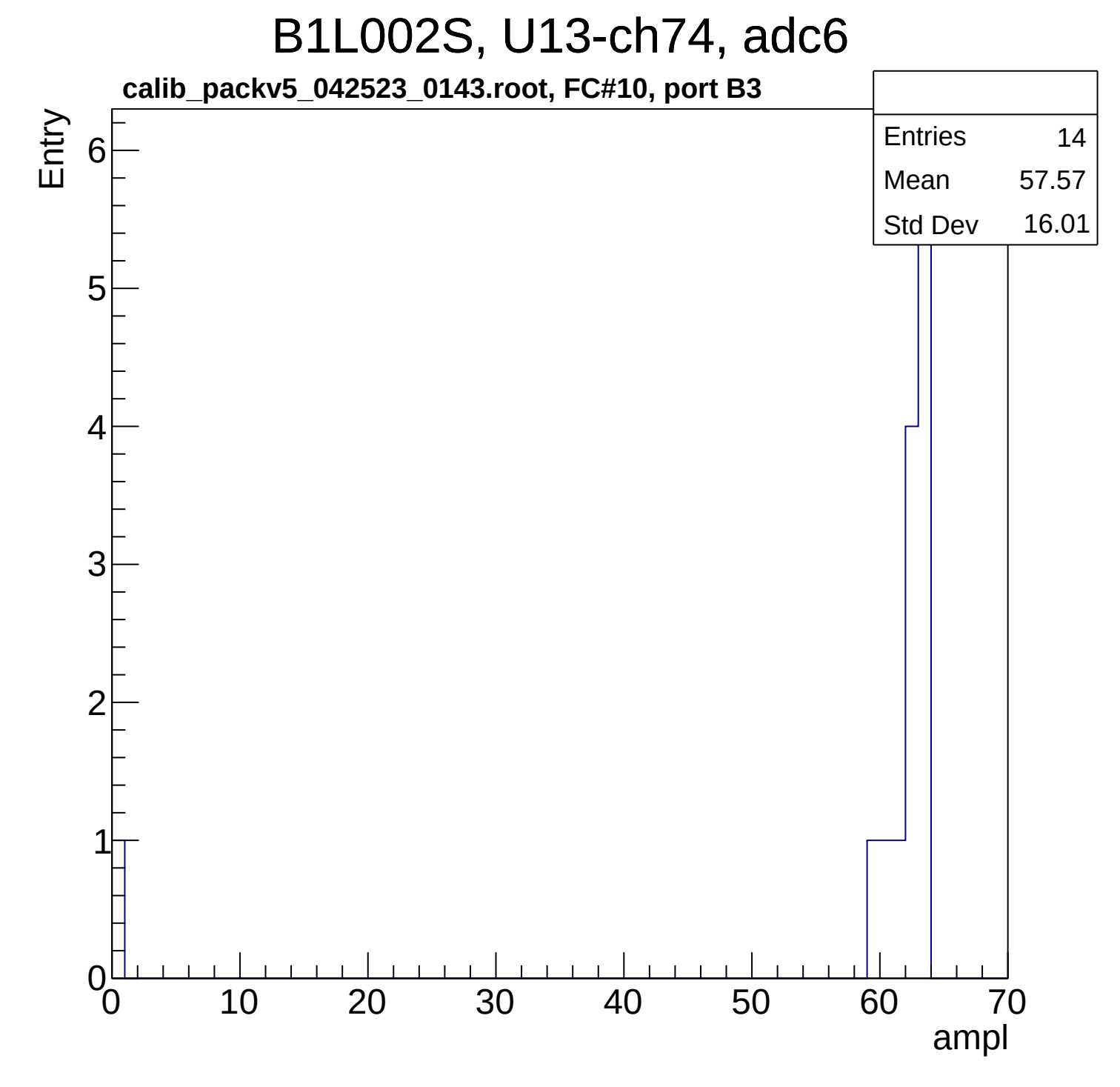
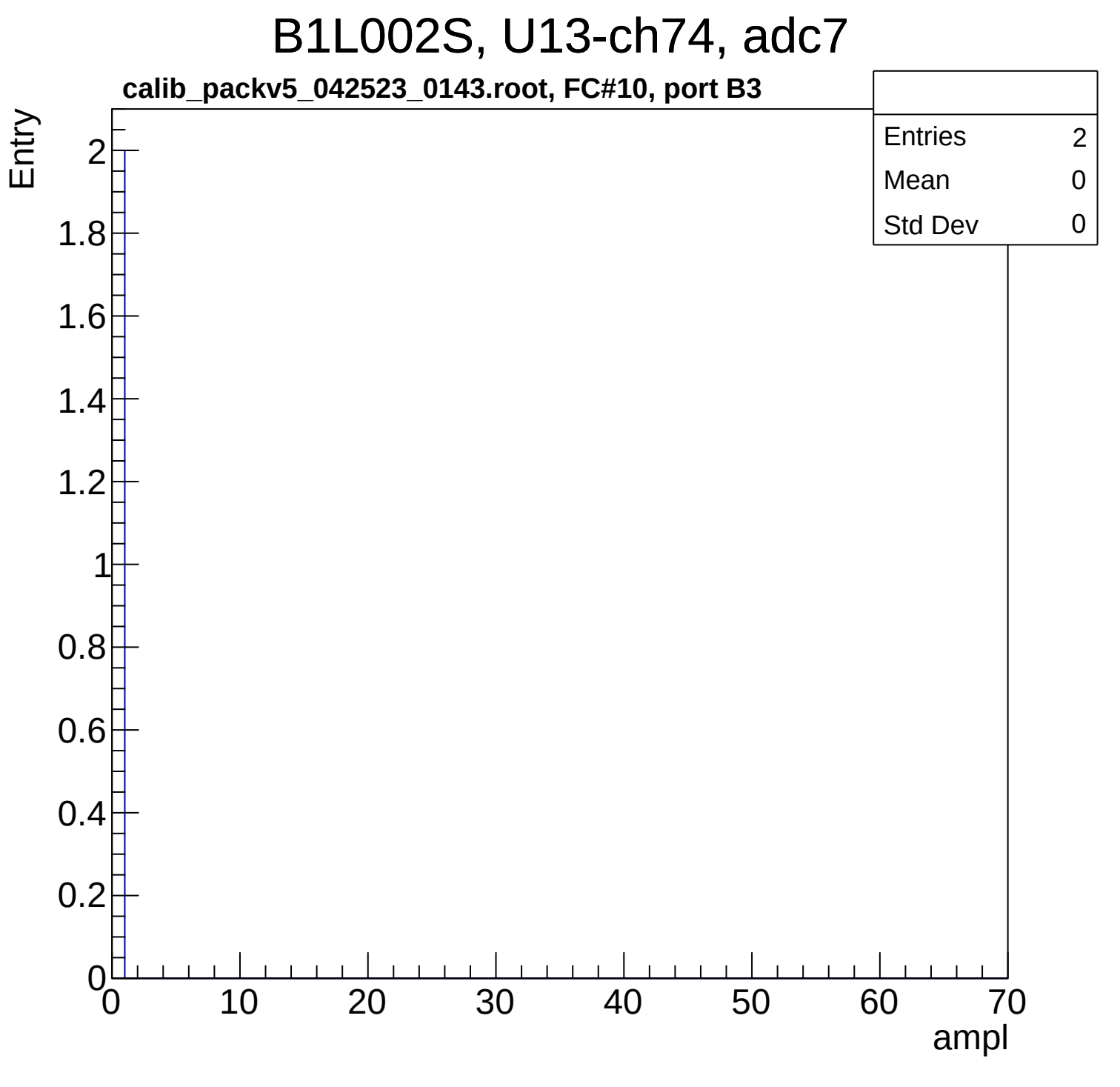
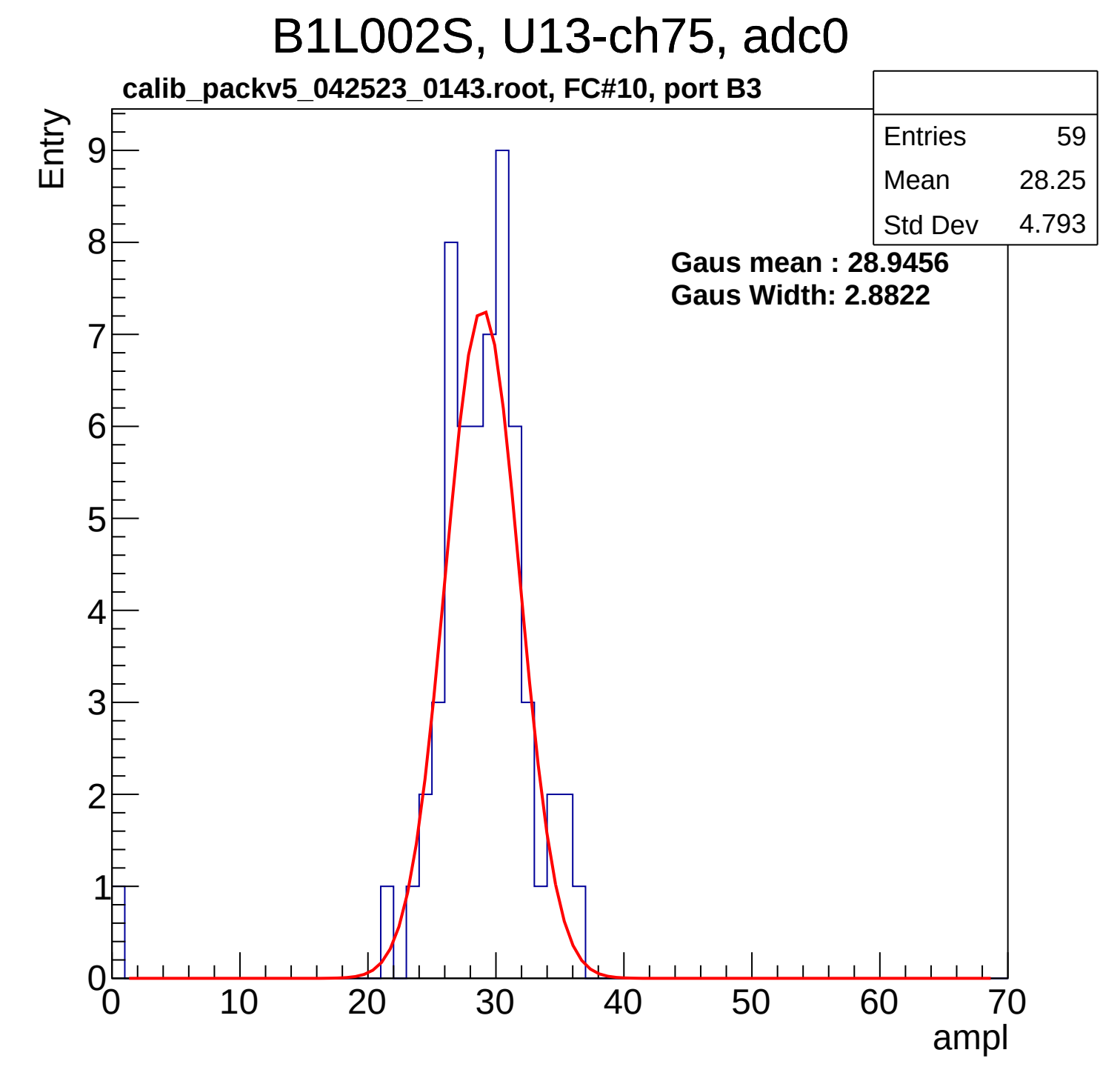

B1L002S, U13-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.44
Std Dev 2.915
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

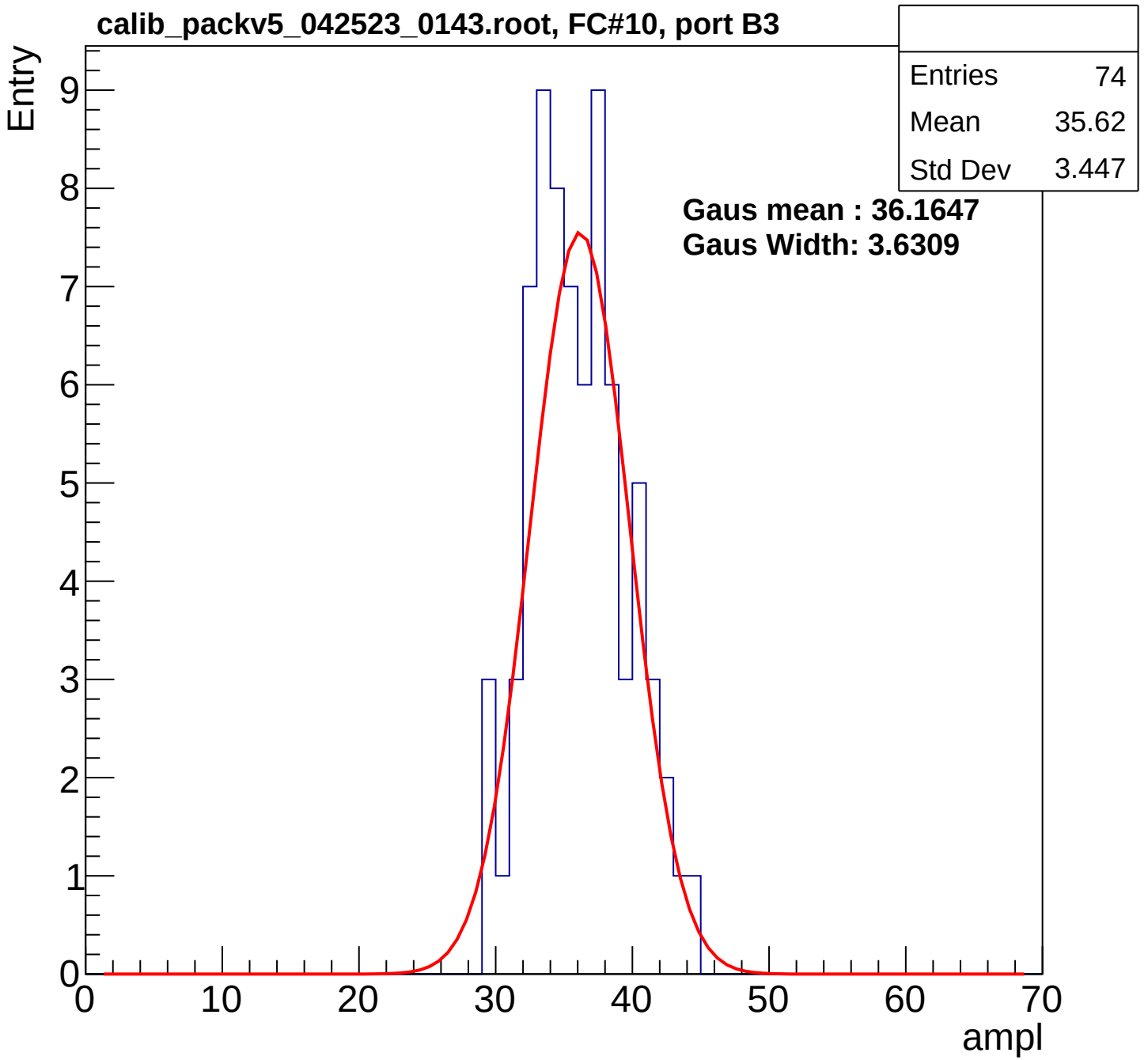

B1L002S, U13-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.8
Std Dev 2.912
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

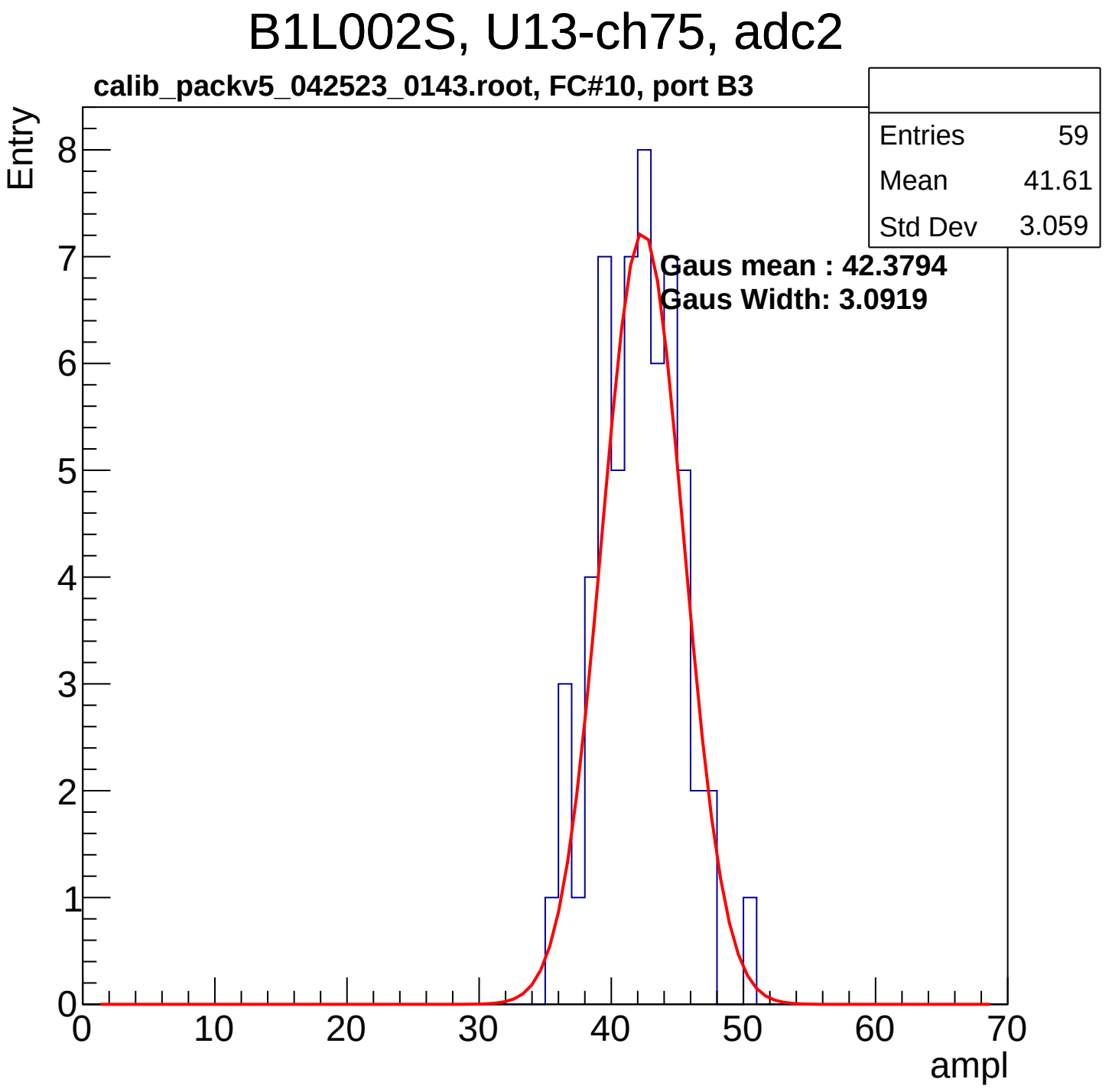

B1L002S, U13-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 58.53
Std Dev 2.687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.57
Std Dev 16.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.25
Std Dev 4.793
Gaus mean : 28.9456
Gaus Width: 2.8822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.62
Std Dev 3.447
Gaus mean : 36.1647
Gaus Width: 3.6309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.61
Std Dev 3.059
Gaus mean : 42.3794
Gaus Width: 3.0919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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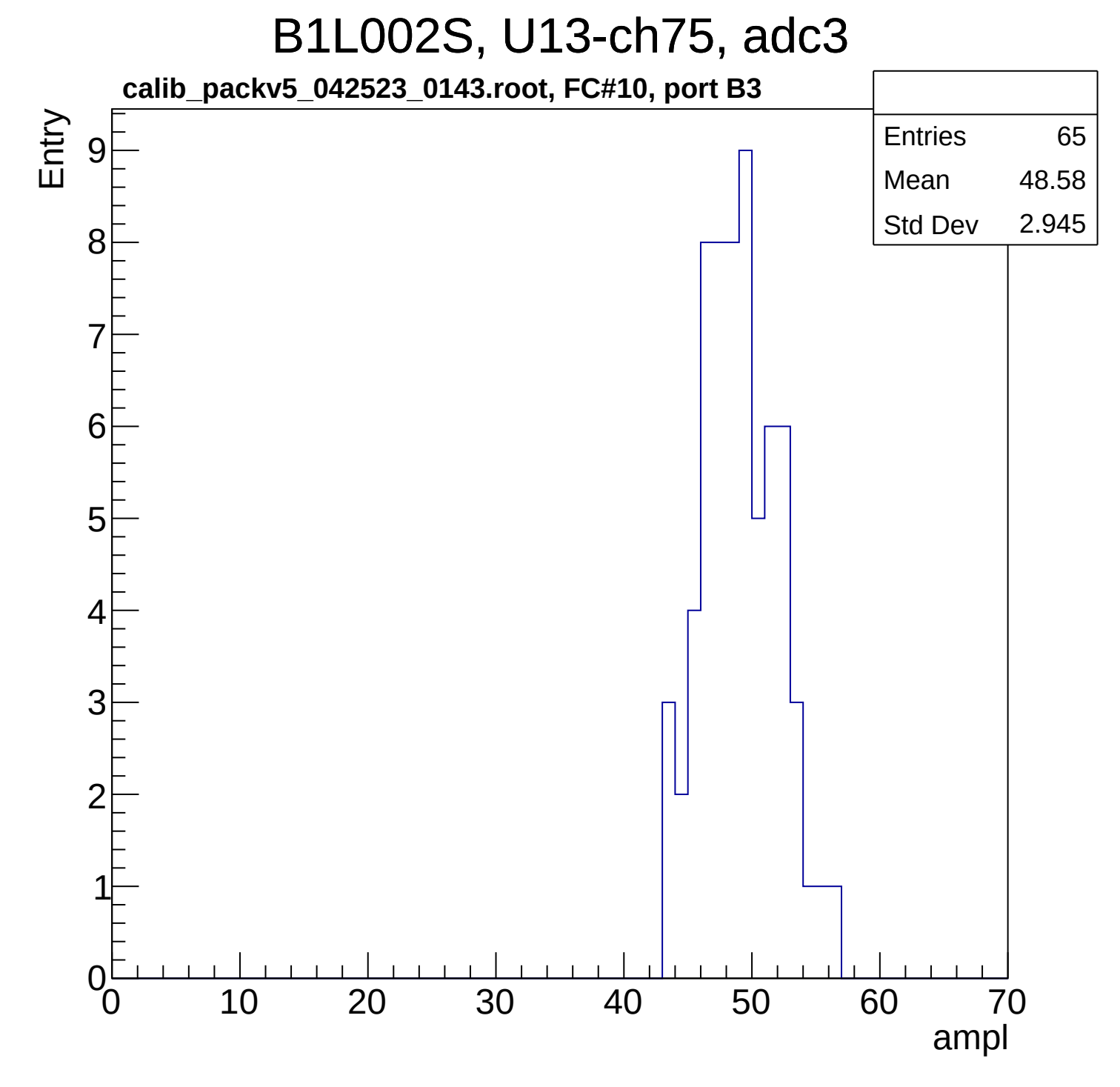
B1L002S, U13-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.58
Std Dev 2.945
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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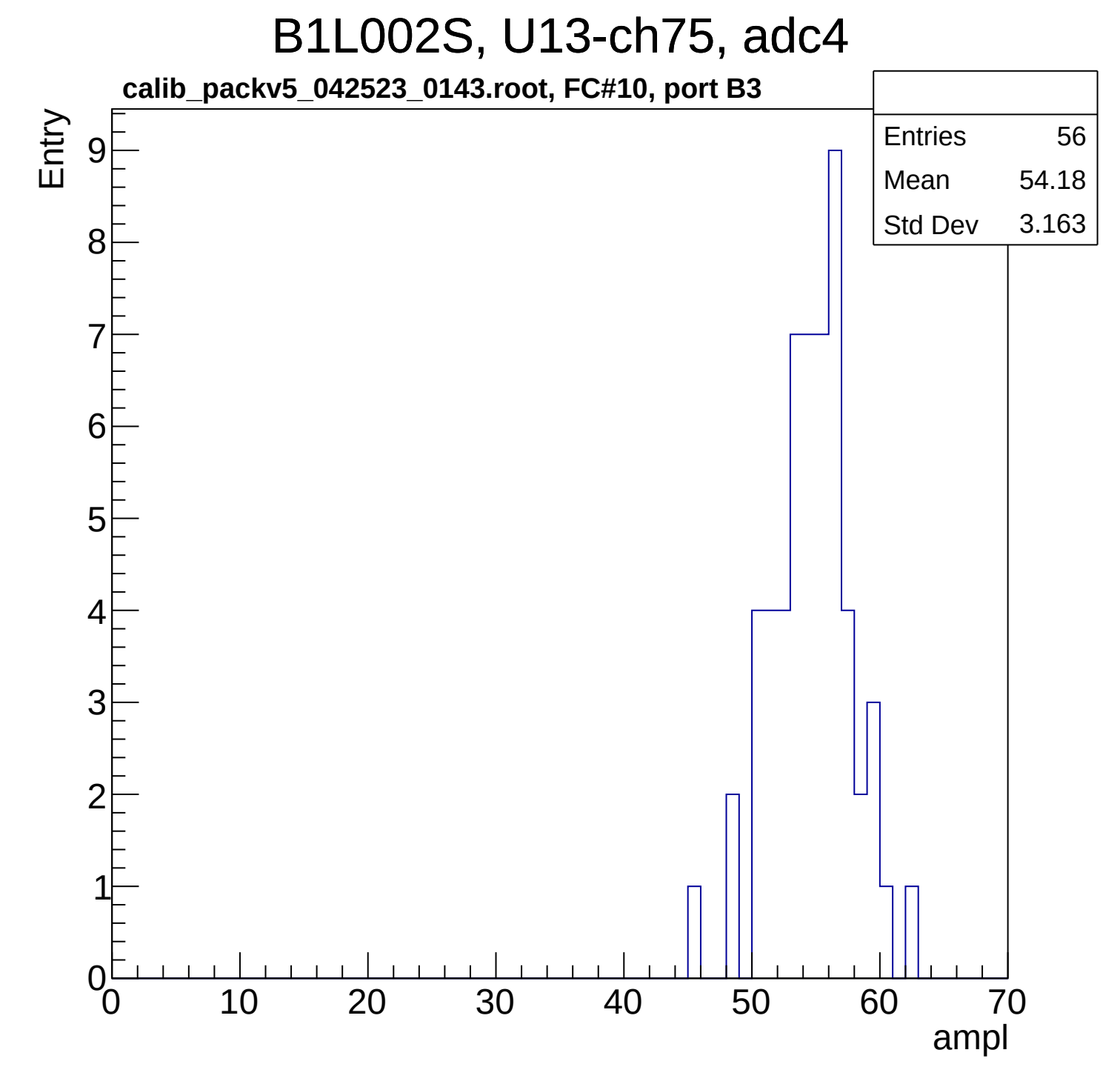
60
70


B1L002S, U13-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.18
Std Dev 3.163
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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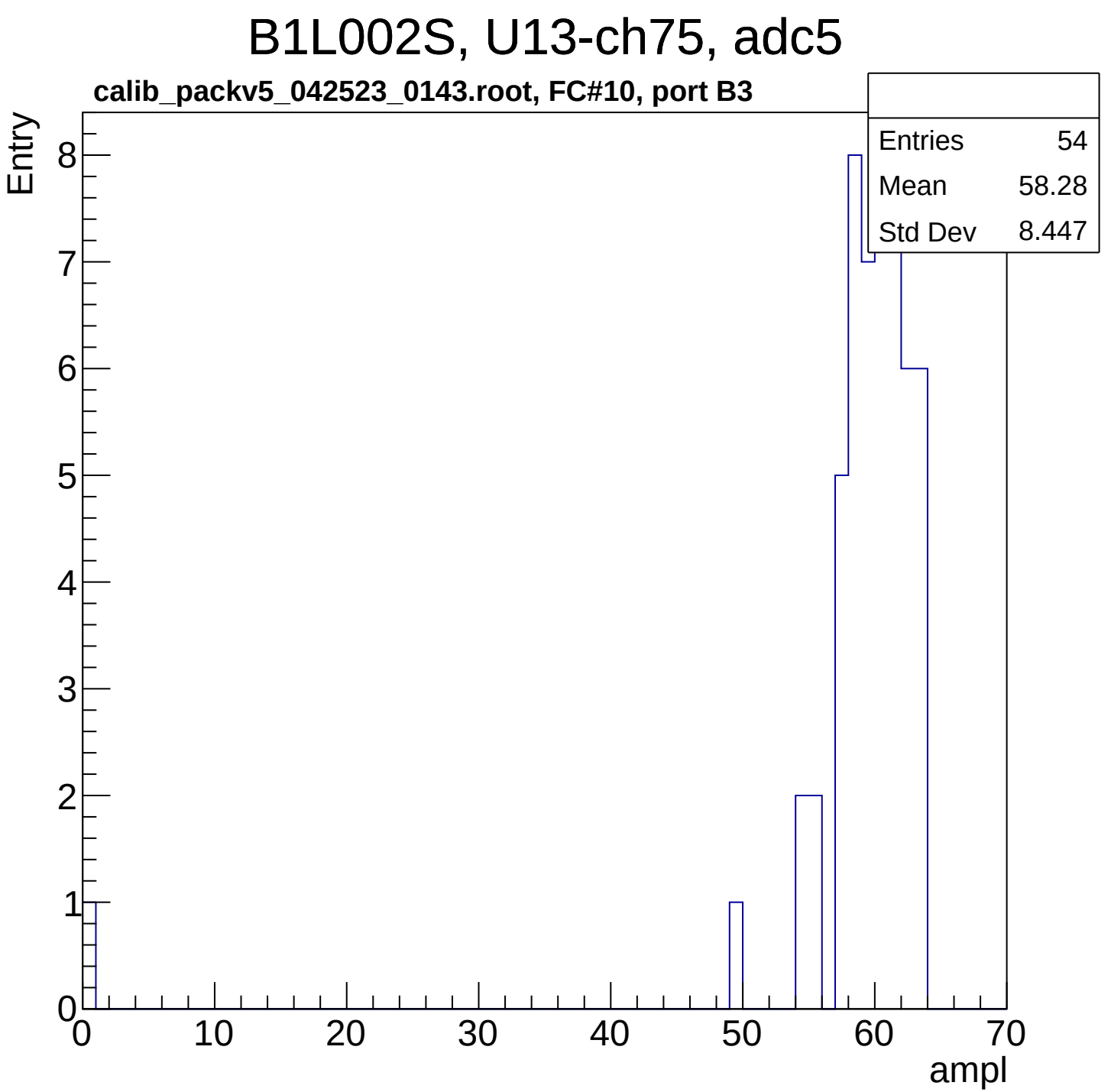
B1L002S, U13-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.28
Std Dev 8.447
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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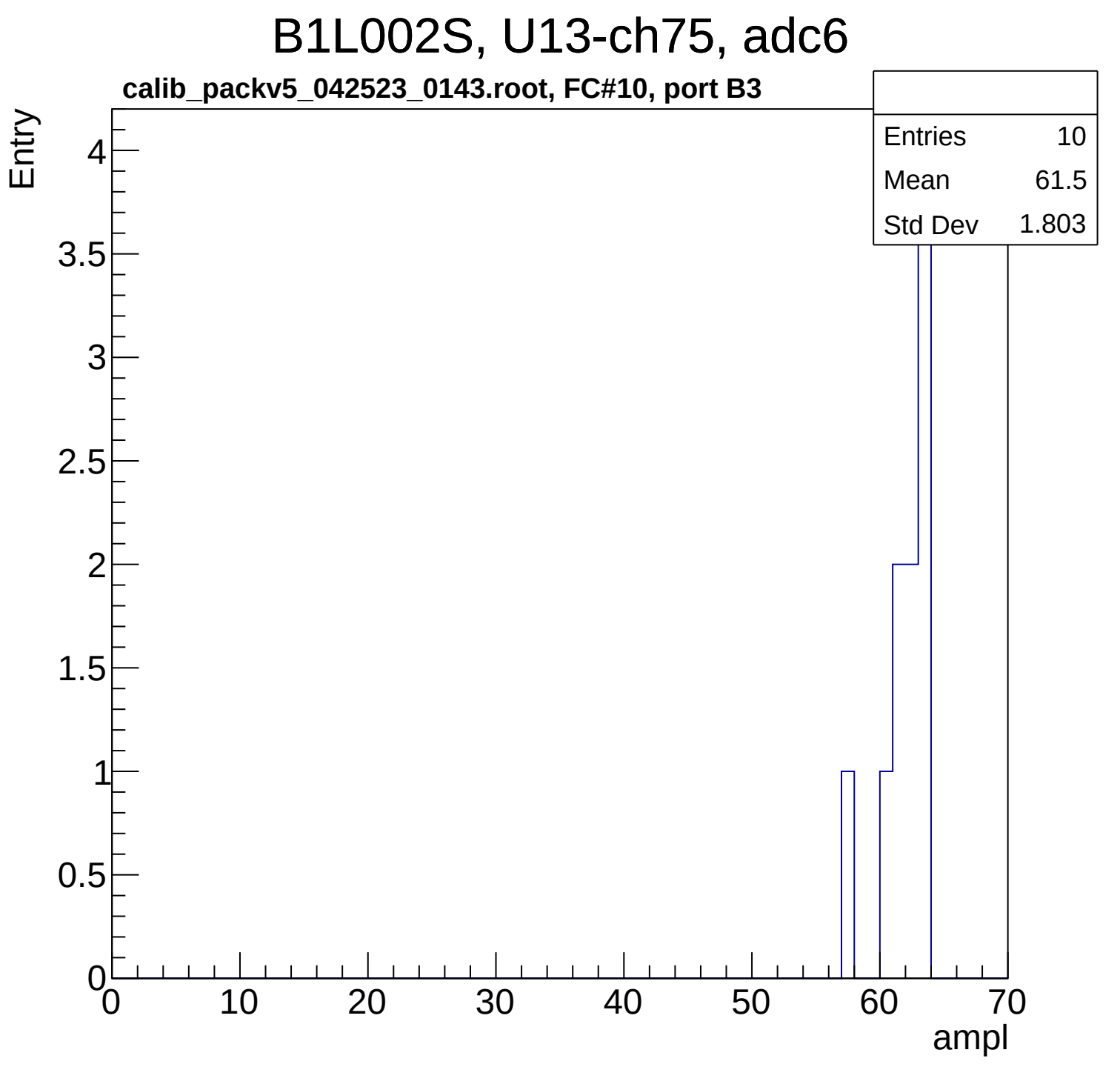
B1L002S, U13-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.5
Std Dev 1.803
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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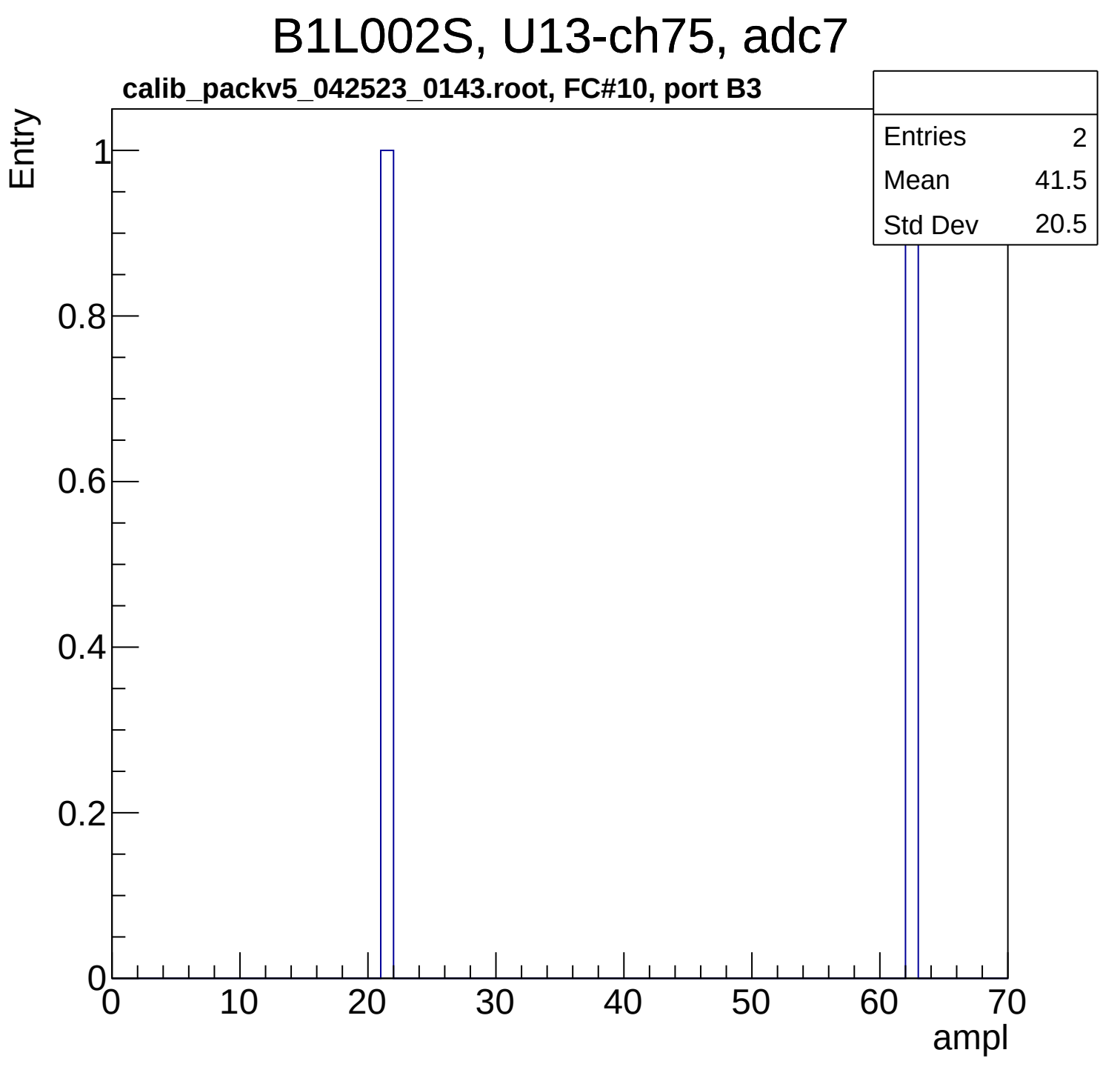
B1L002S, U13-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41.5
Std Dev 20.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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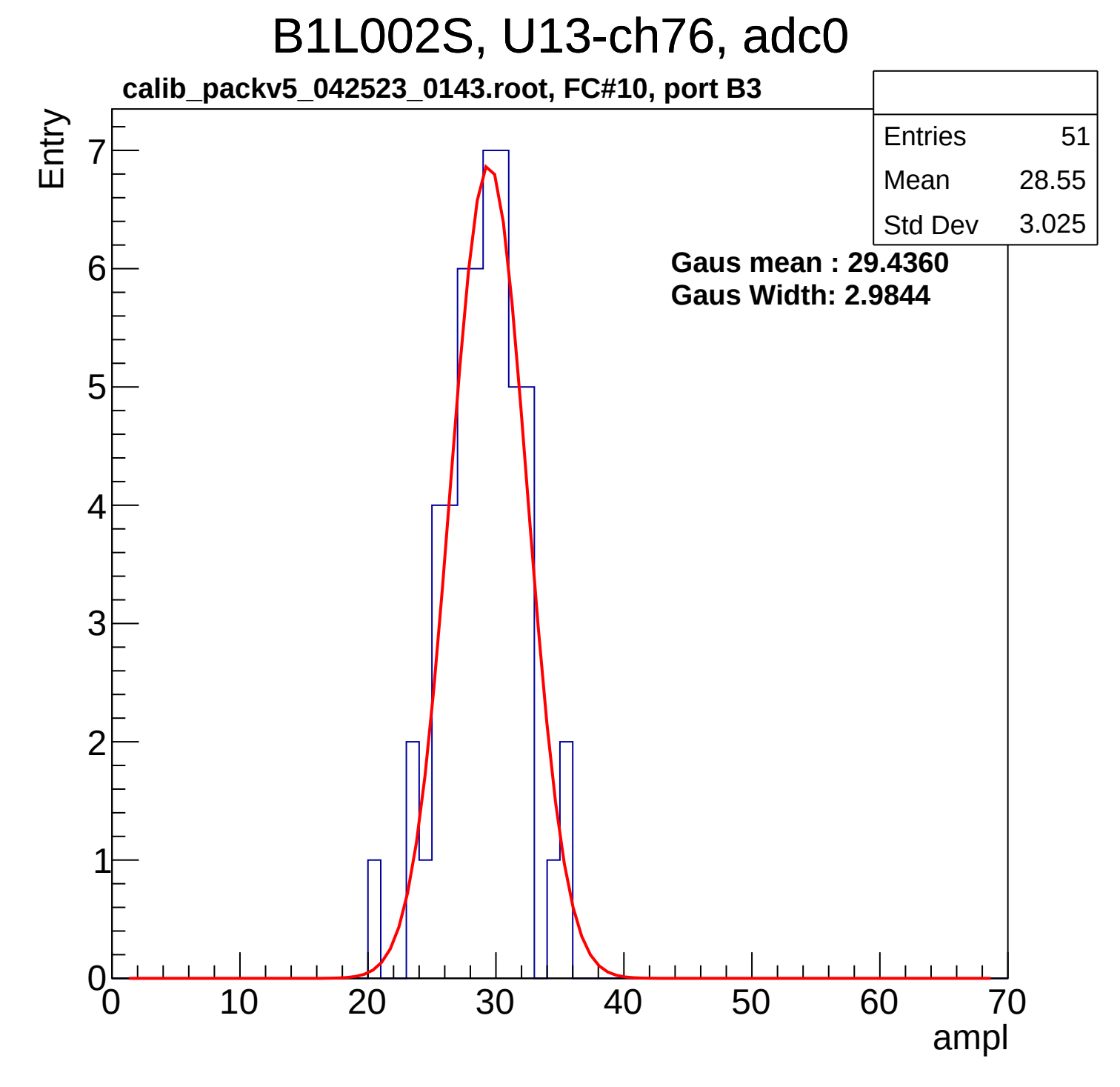
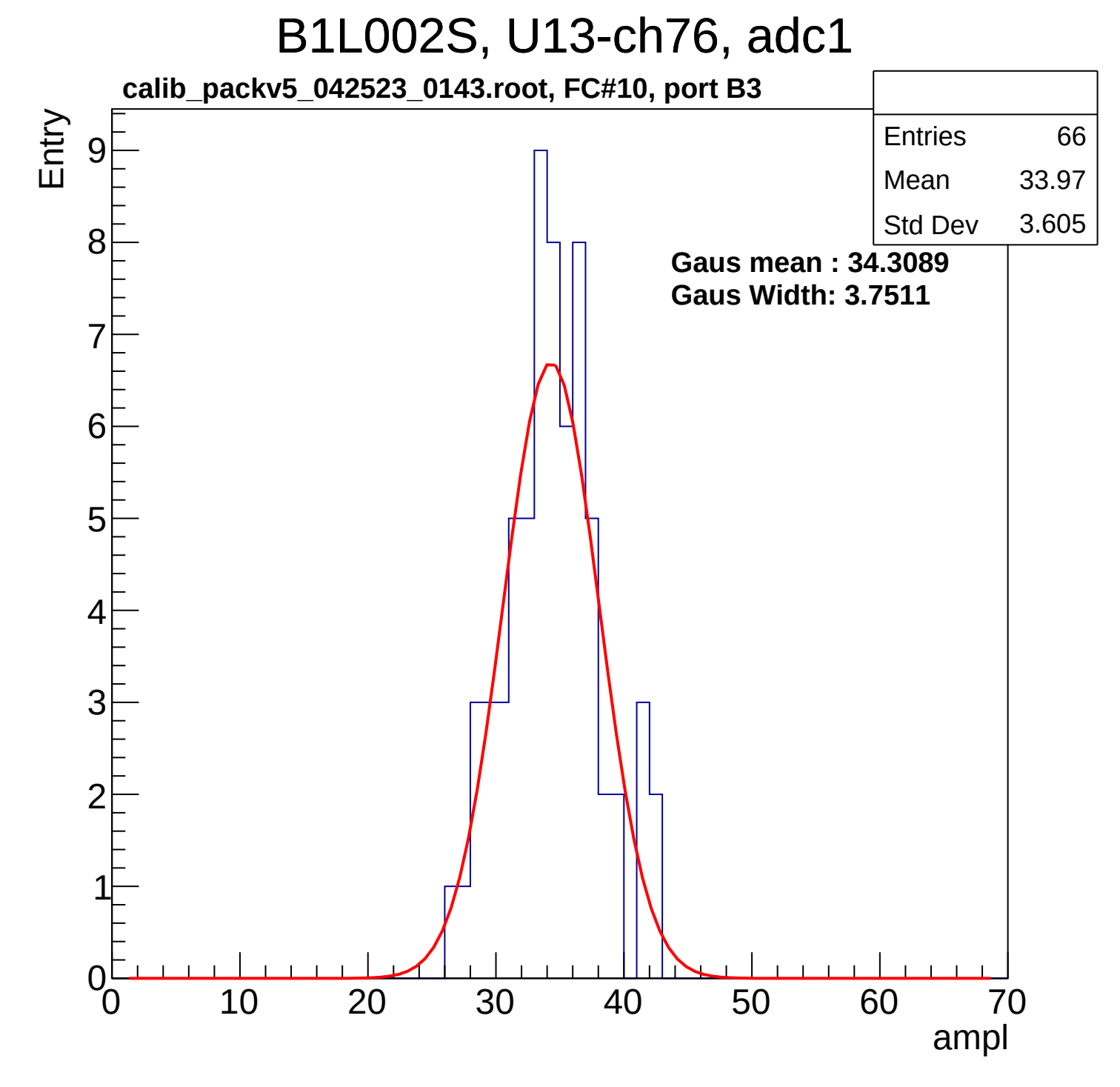
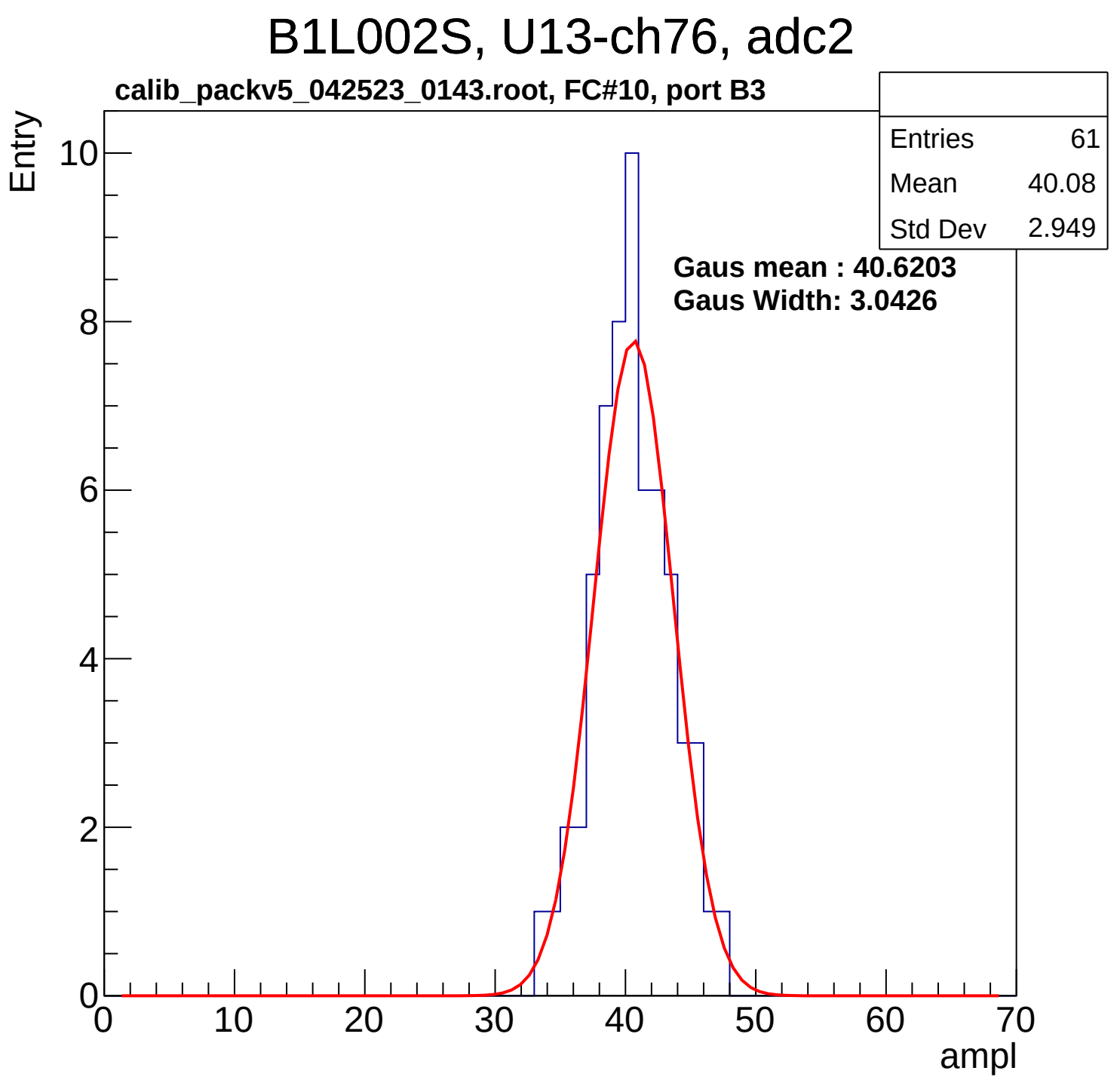
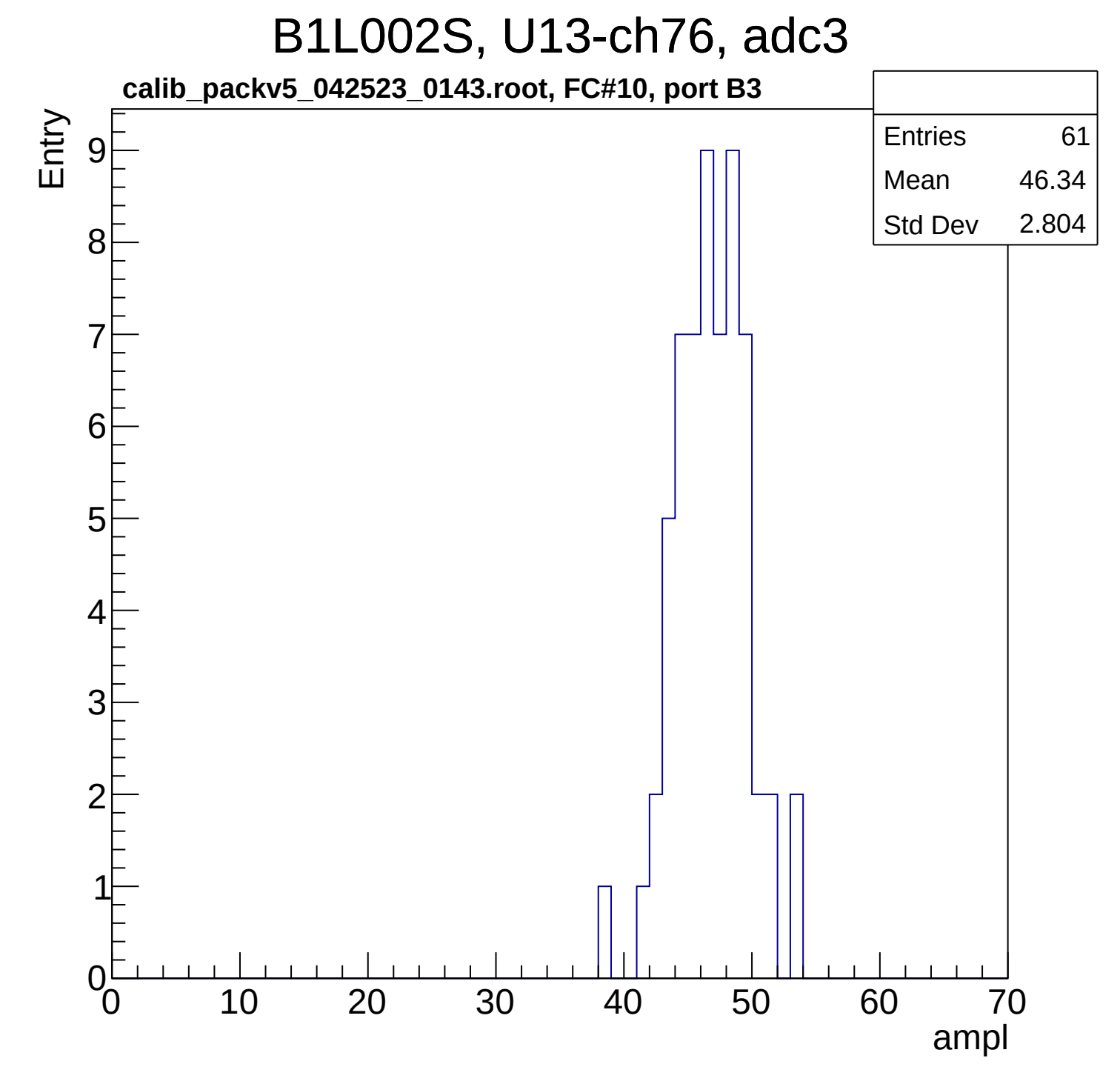

B1L002S, U13-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 28.55
Std Dev 3.025
Gaus mean : 29.4360
Gaus Width: 2.9844
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.97
Std Dev 3.605
Gaus mean : 34.3089
Gaus Width: 3.7511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.08
Std Dev 2.949
Gaus mean : 40.6203
Gaus Width: 3.0426
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.34
Std Dev 2.804
Entry
ampl
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3
4
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6
7
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0
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40
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60
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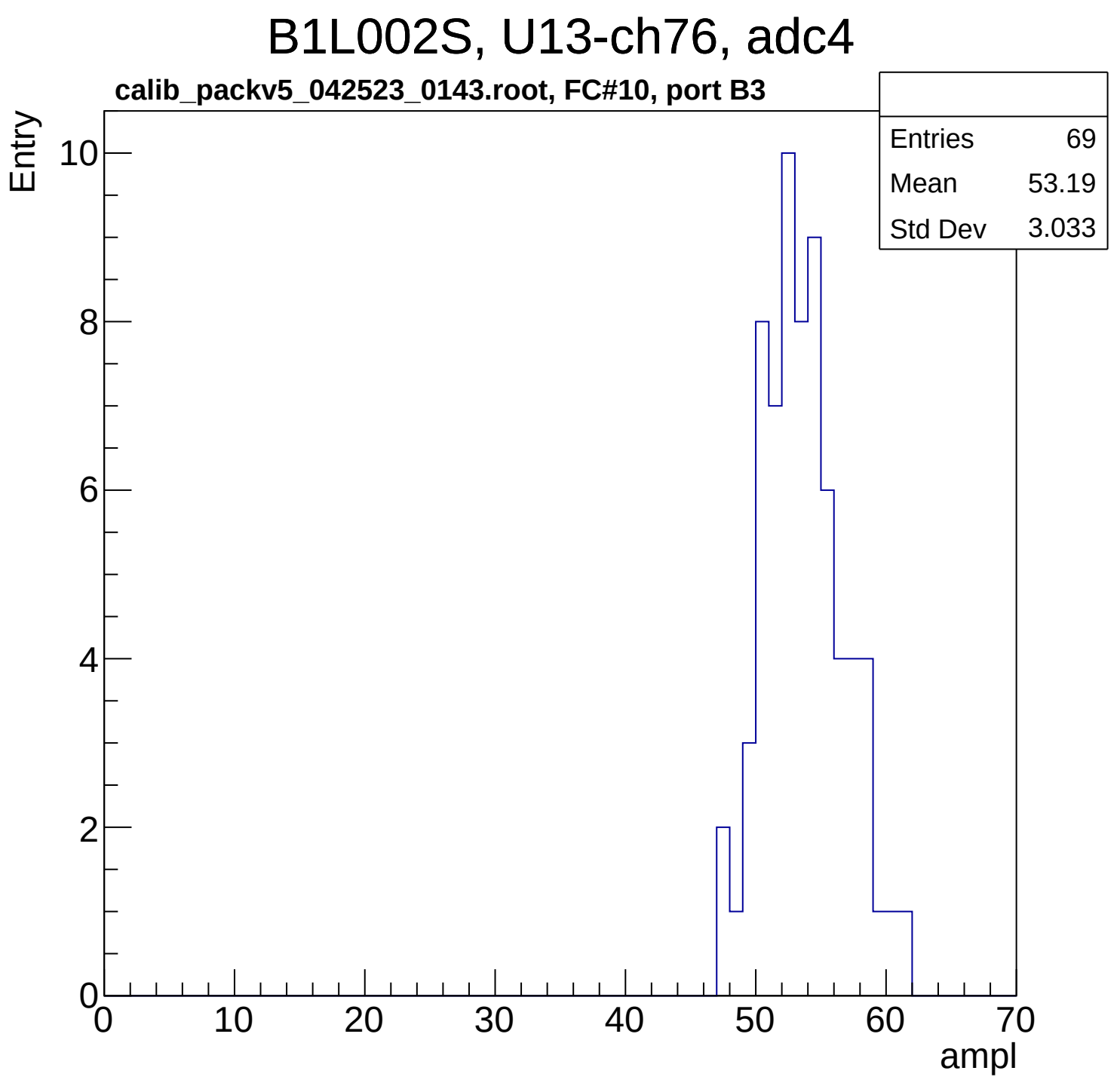
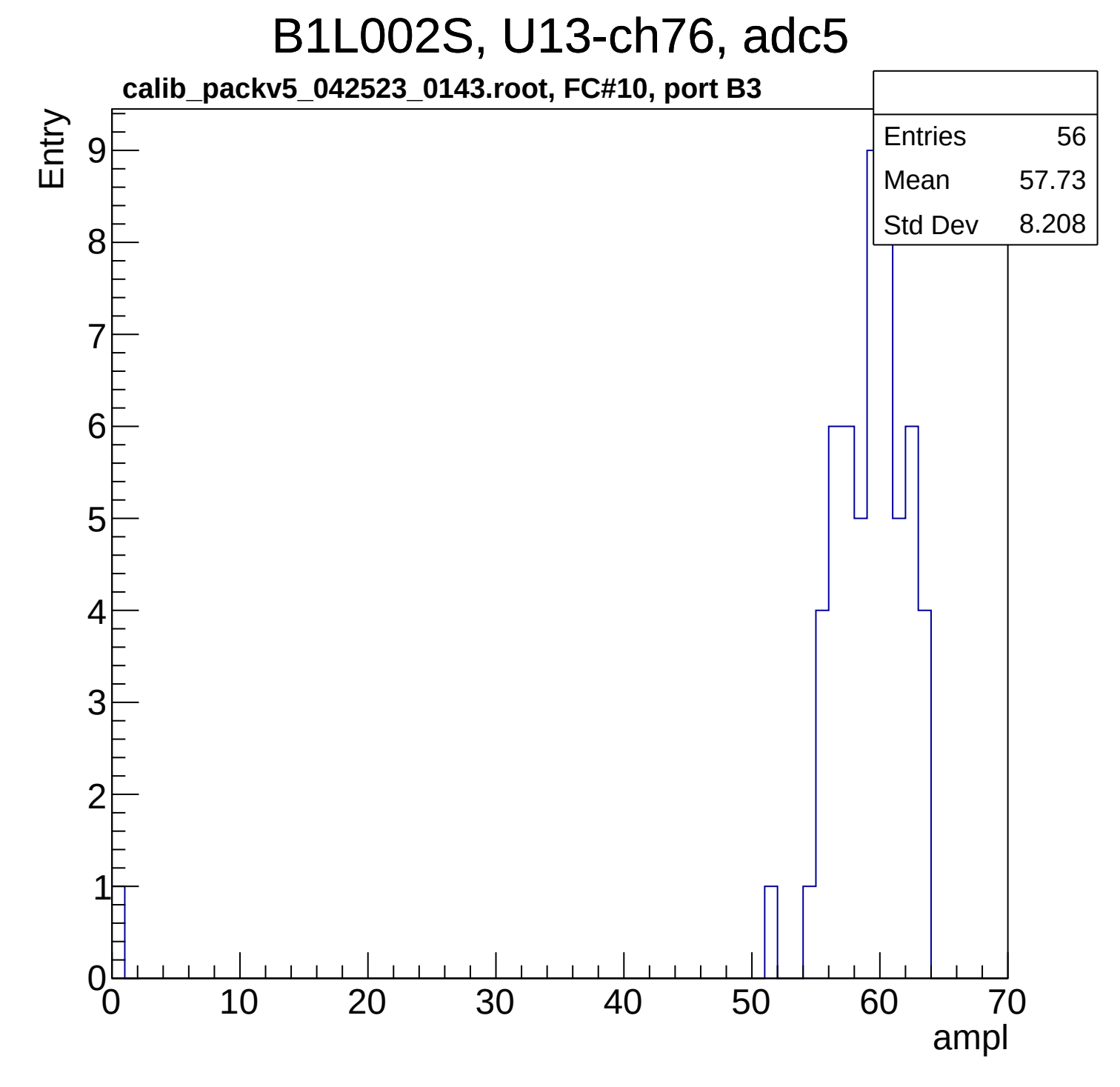

B1L002S, U13-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.19
Std Dev 3.033
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.73
Std Dev 8.208
Entry
ampl
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2
3
4
5
6
7
8
9
0
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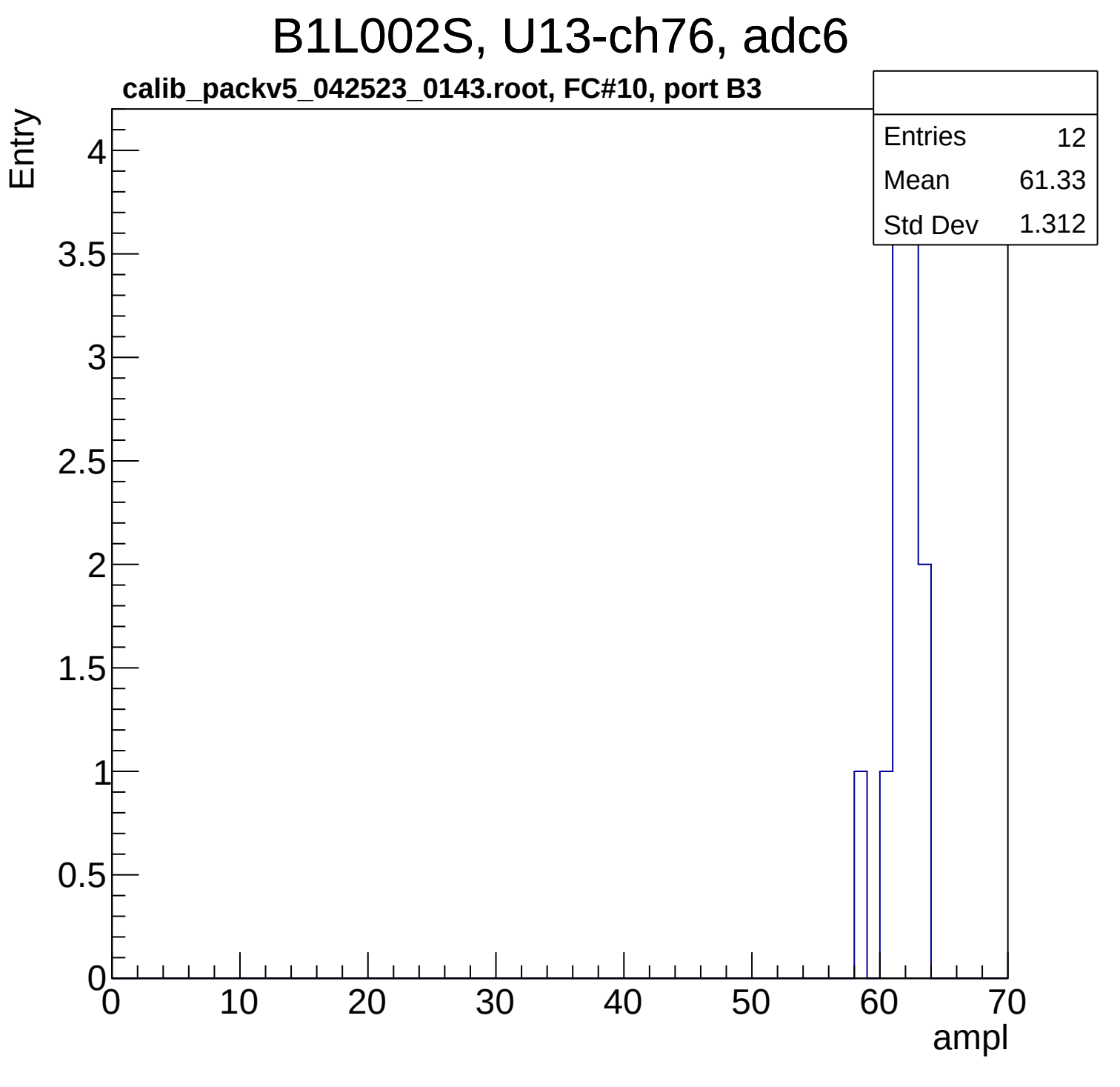
B1L002S, U13-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.33
Std Dev 1.312
Entry
ampl
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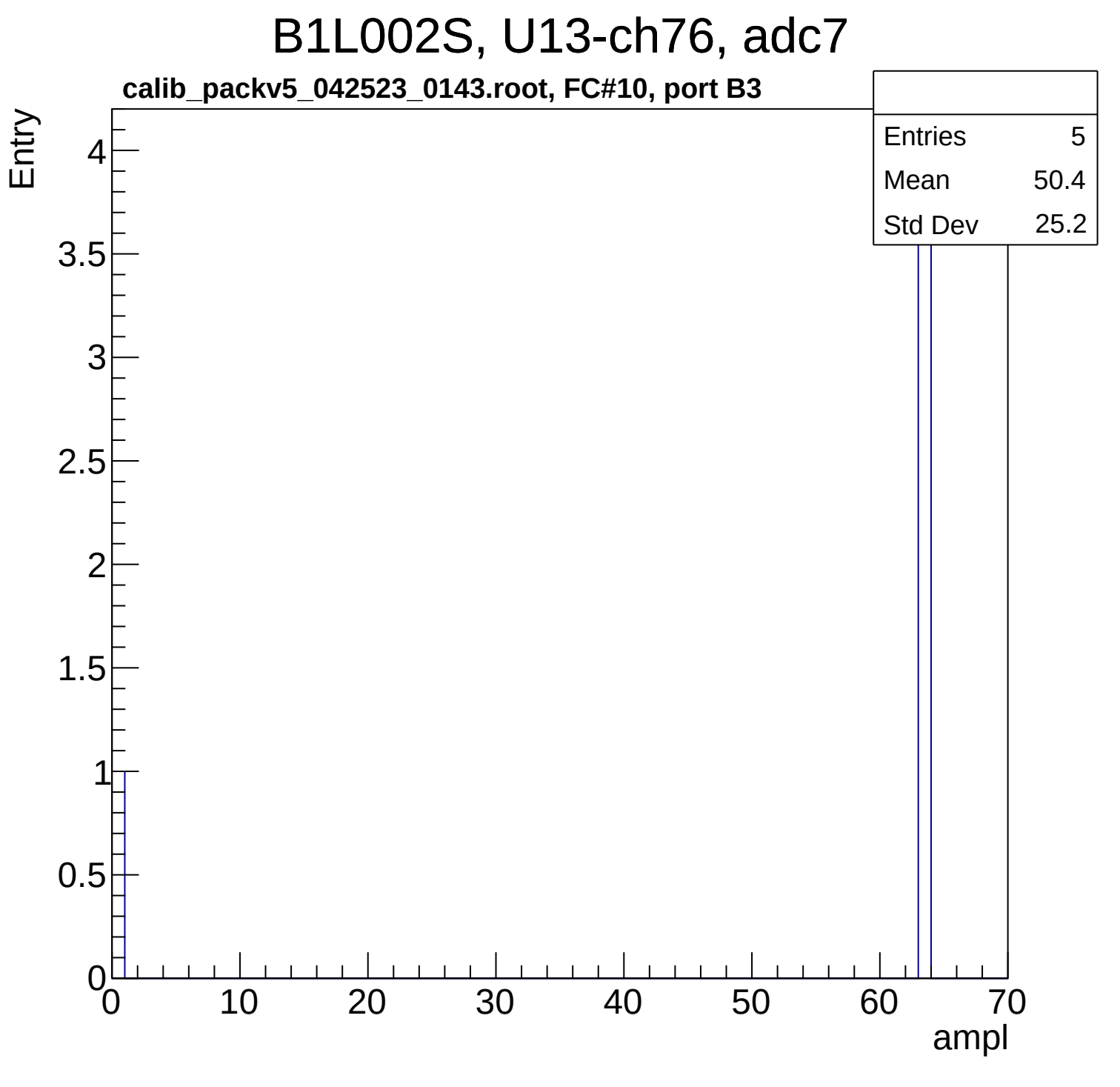
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50.4
Std Dev 25.2
Entry
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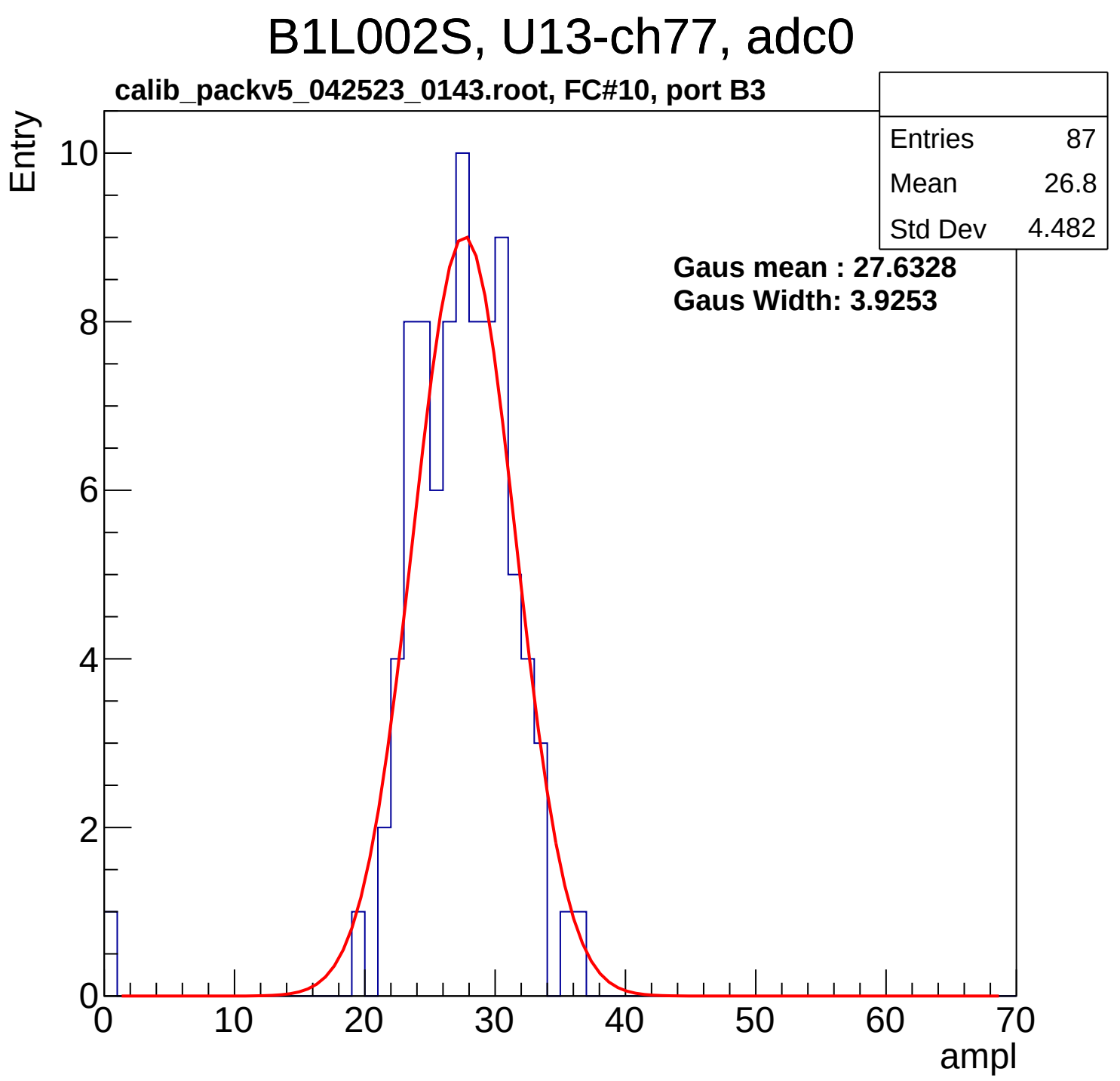
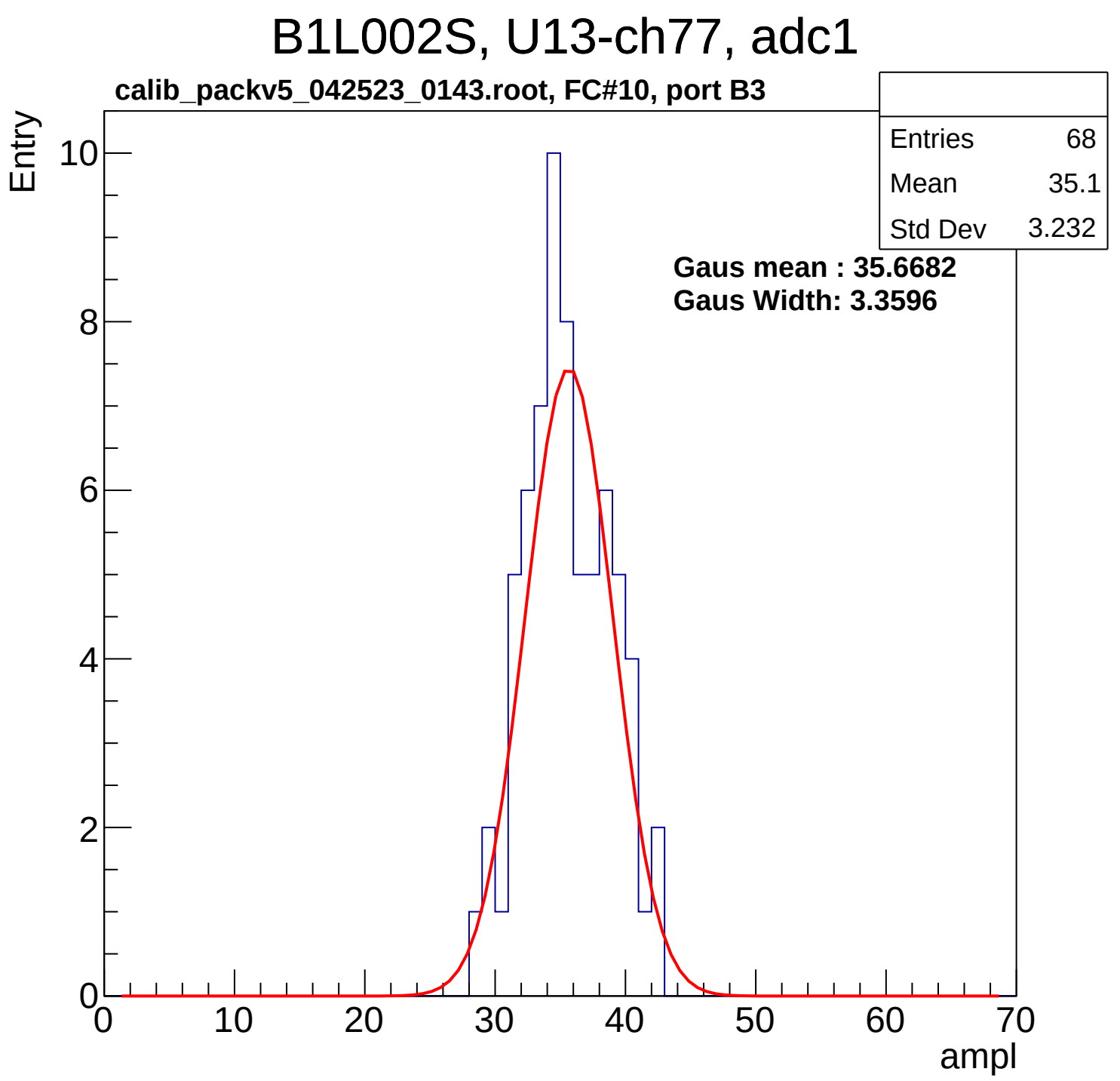
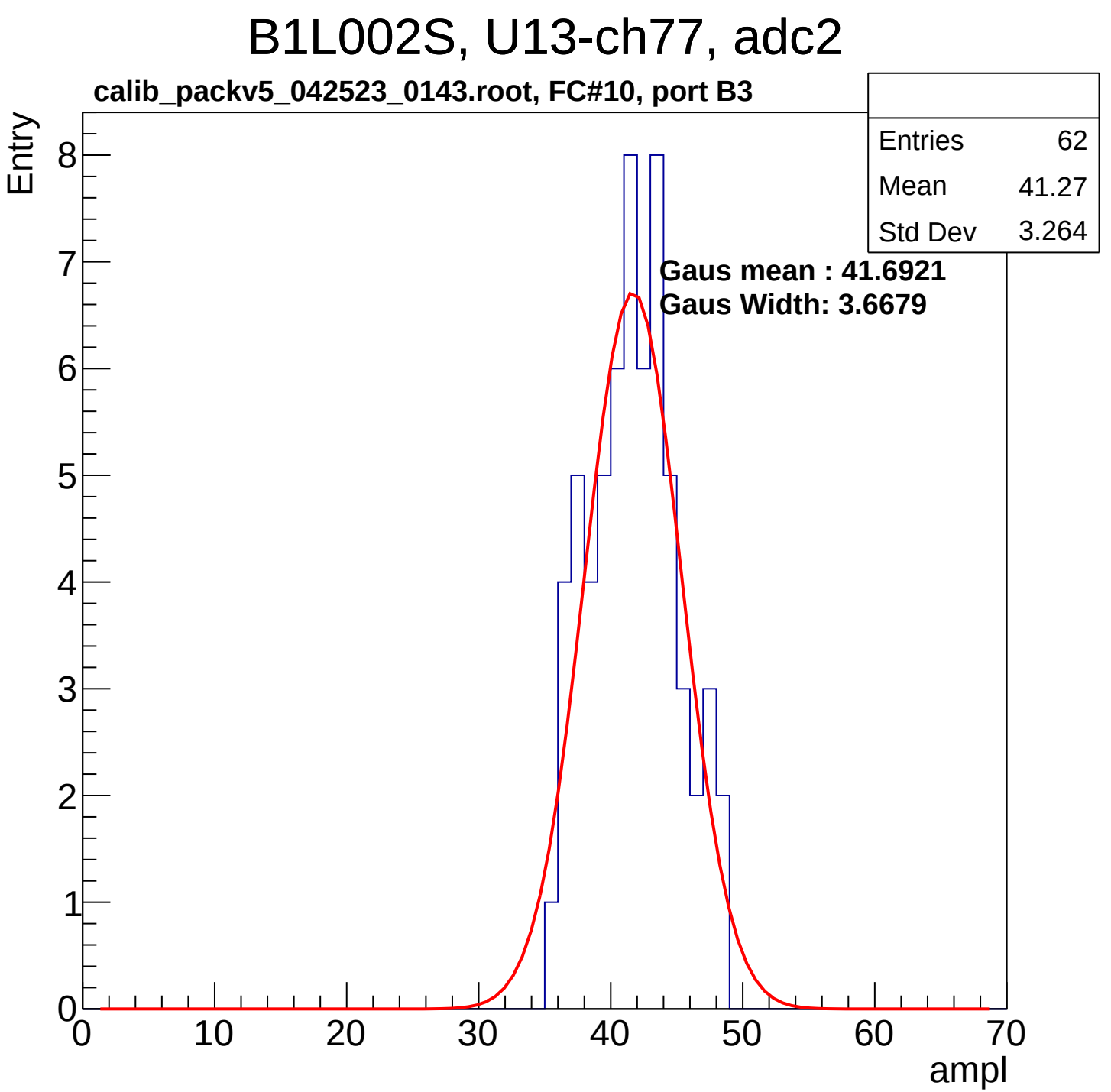
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 26.8
Std Dev 4.482
Gaus mean : 27.6328
Gaus Width: 3.9253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.1
Std Dev 3.232
Gaus mean : 35.6682
Gaus Width: 3.3596
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.27
Std Dev 3.264
Gaus mean : 41.6921
Gaus Width: 3.6679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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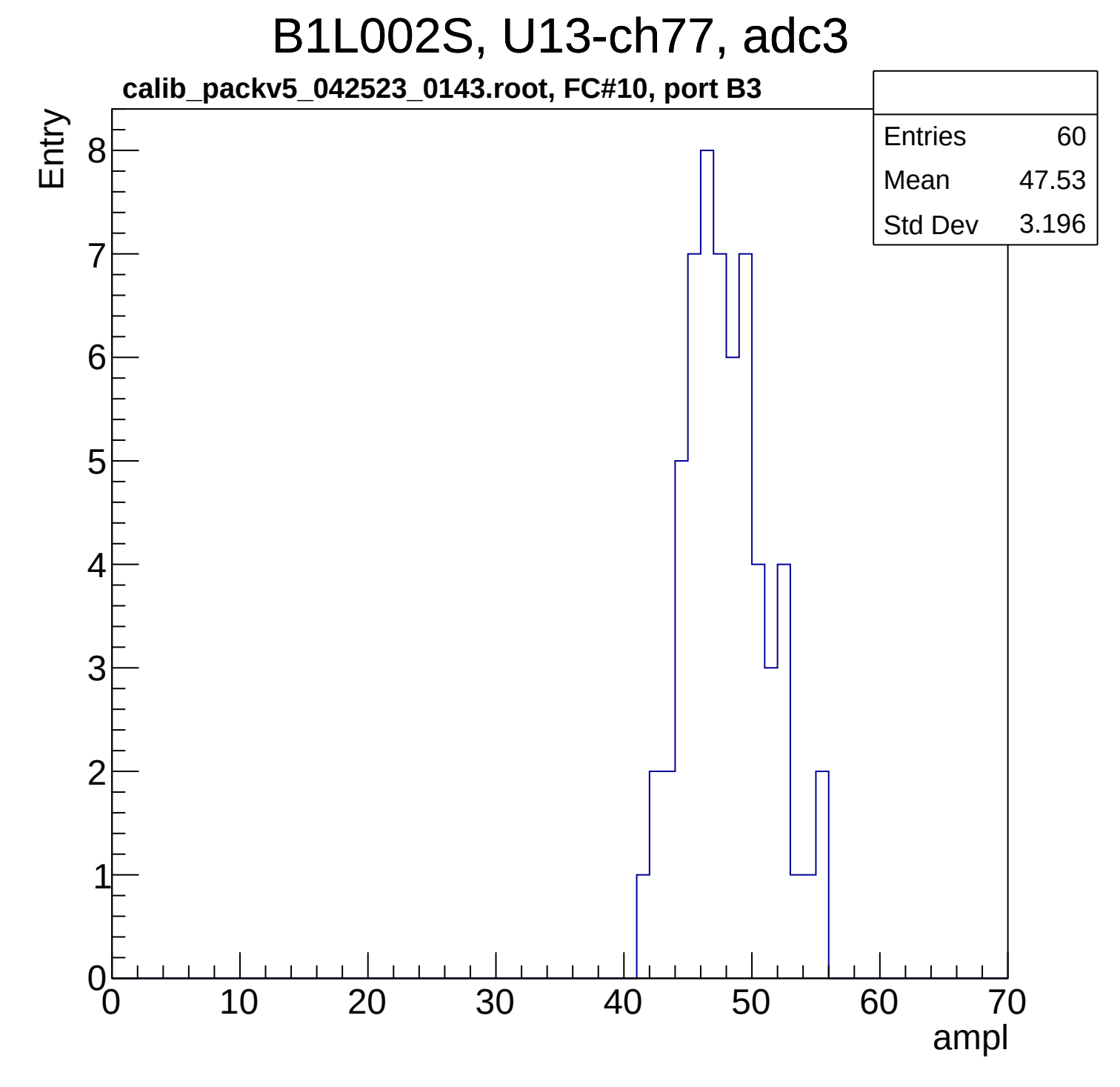
B1L002S, U13-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.53
Std Dev 3.196
Entry
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2
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6
7
8
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10
20
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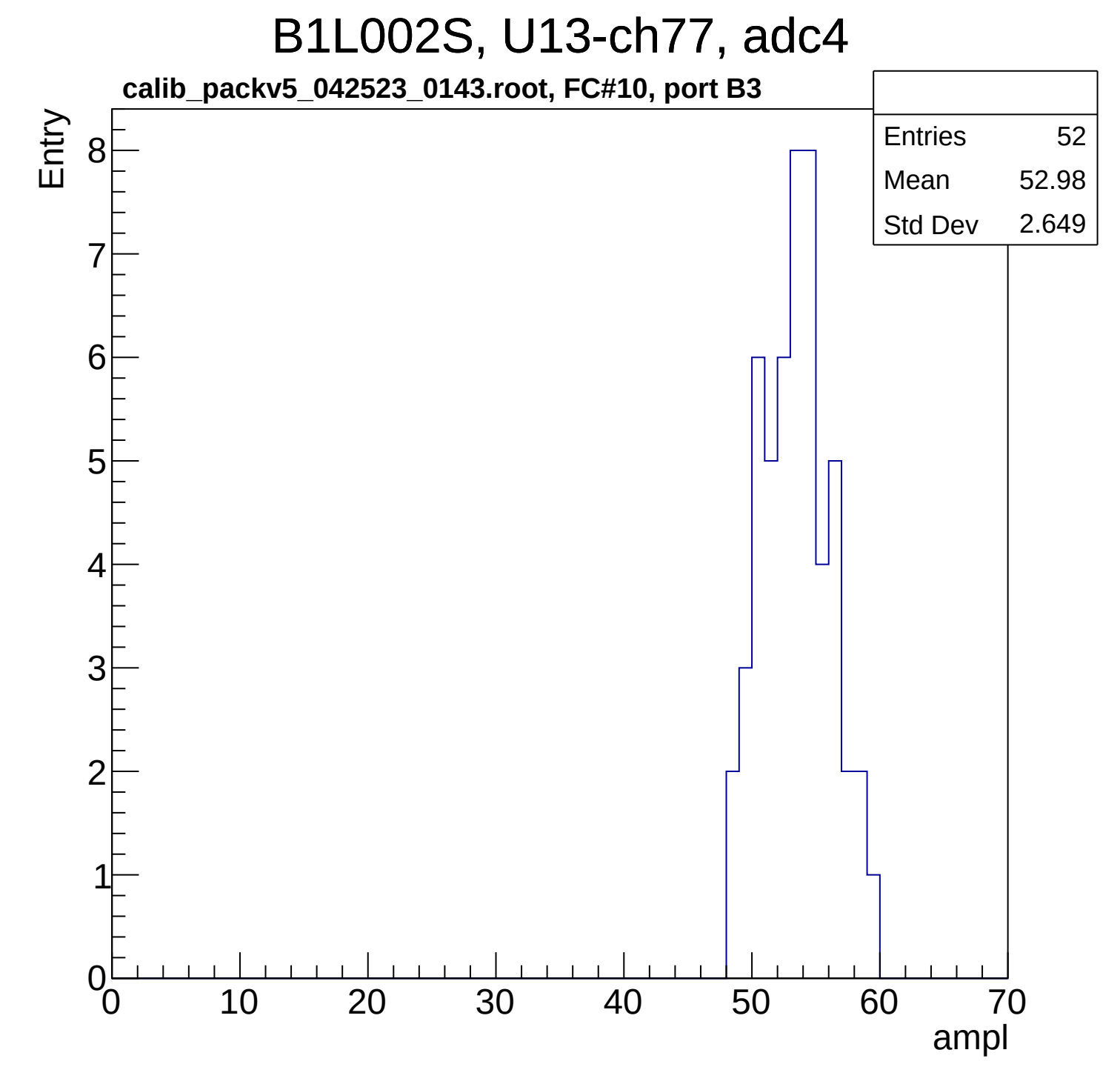
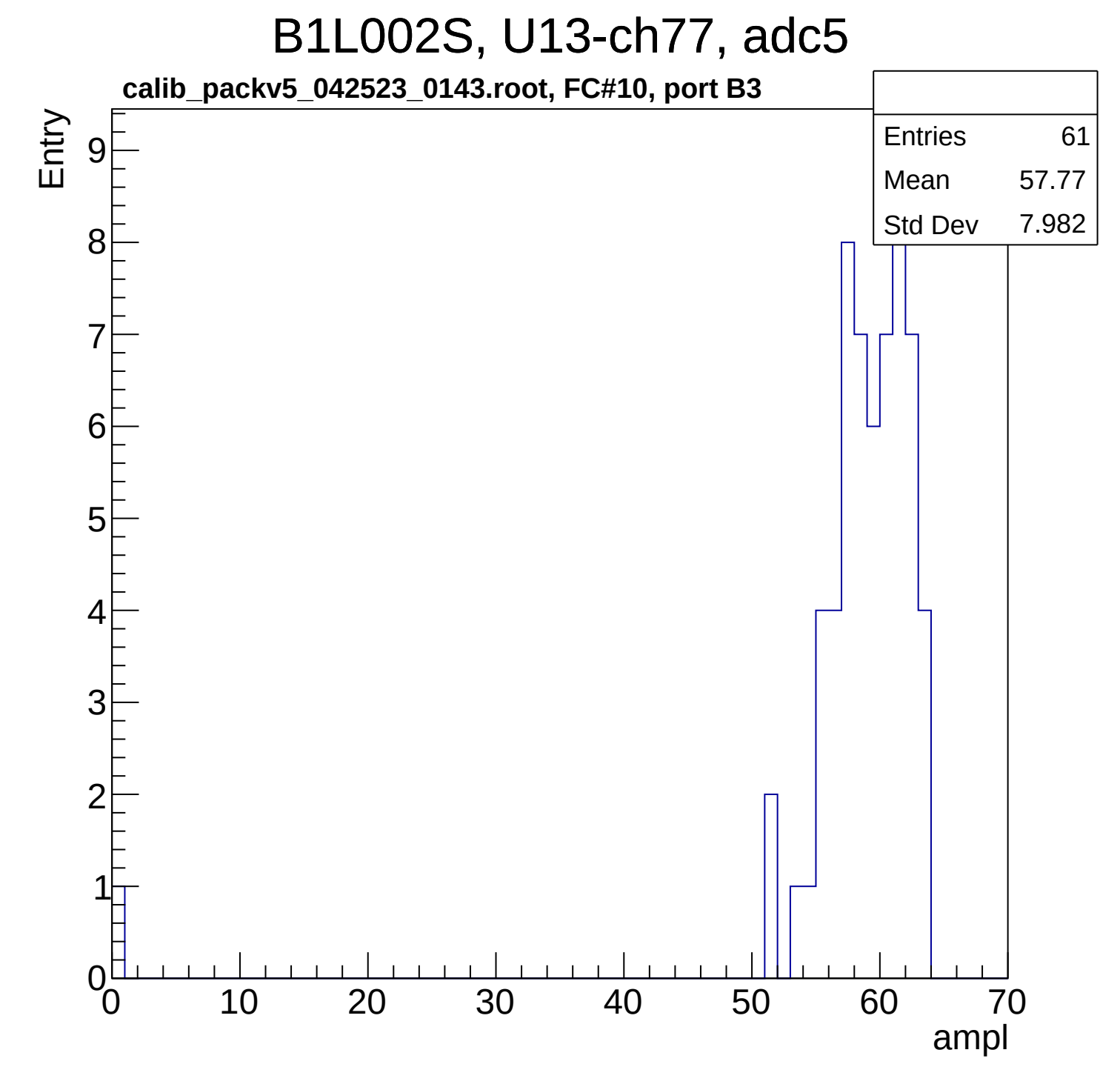
40
50
60
70
ampl


B1L002S, U13-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.98
Std Dev 2.649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.77
Std Dev 7.982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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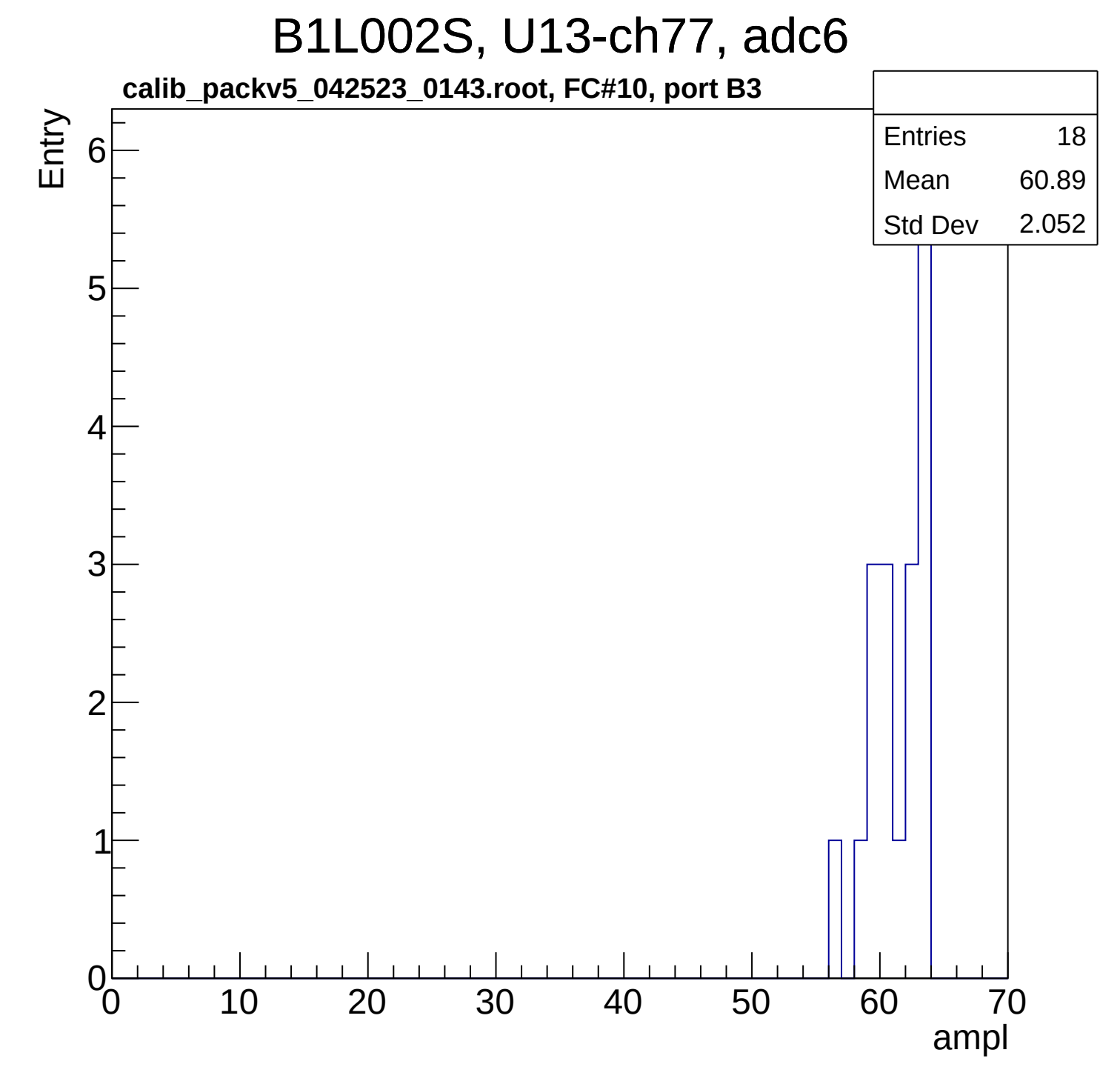
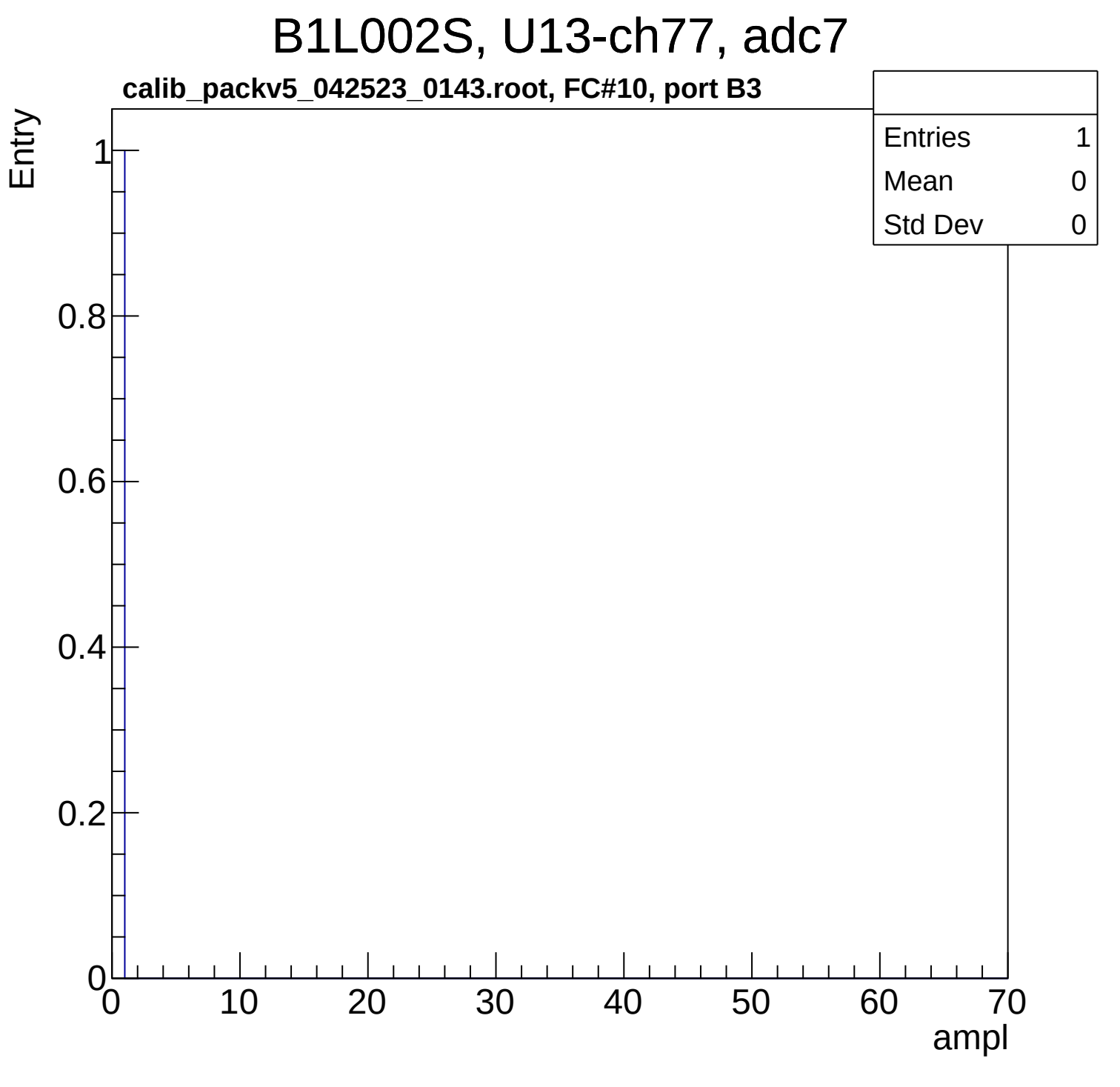
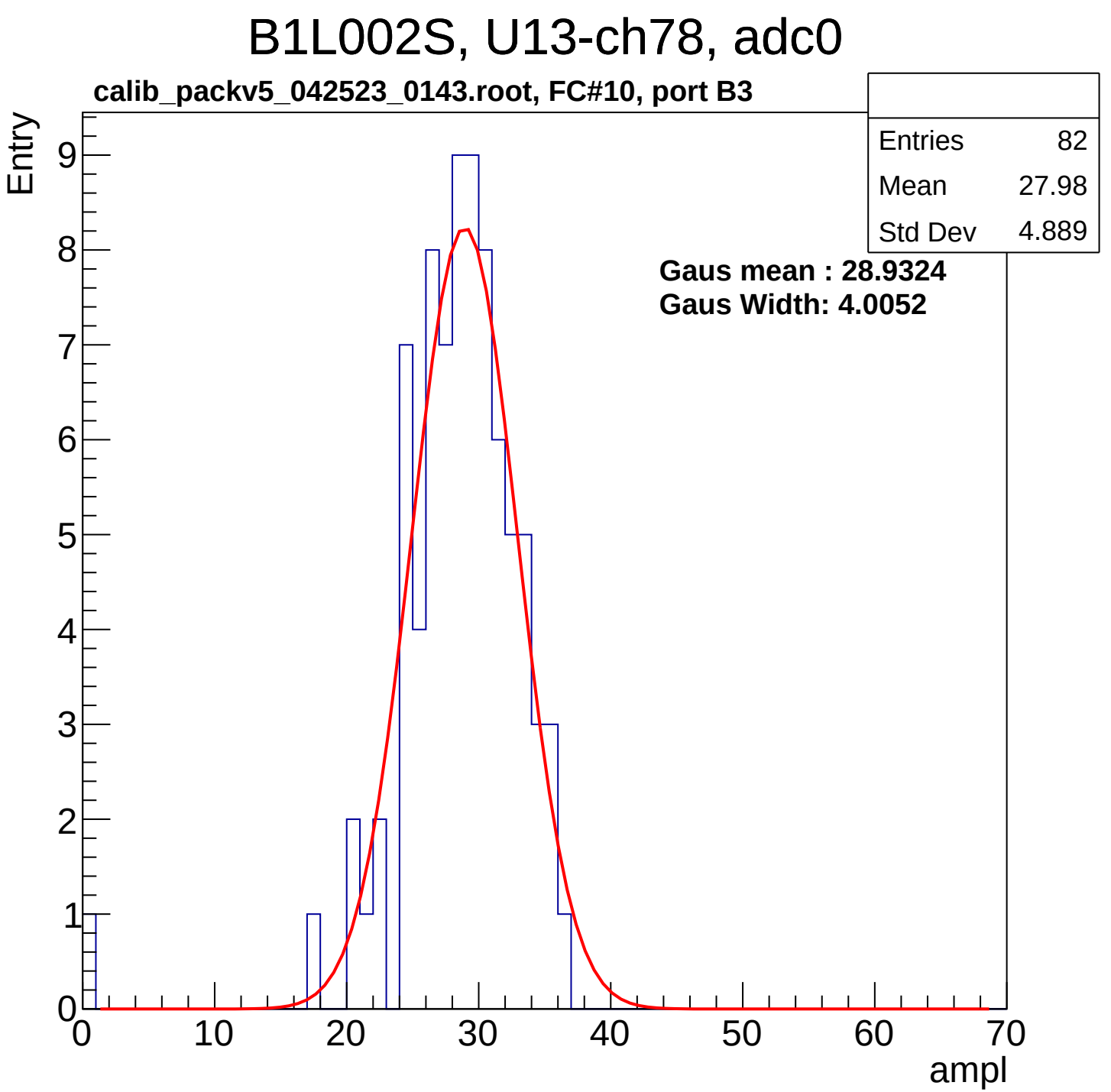

B1L002S, U13-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 60.89
Std Dev 2.052
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 27.98
Std Dev 4.889
Gaus mean : 28.9324
Gaus Width: 4.0052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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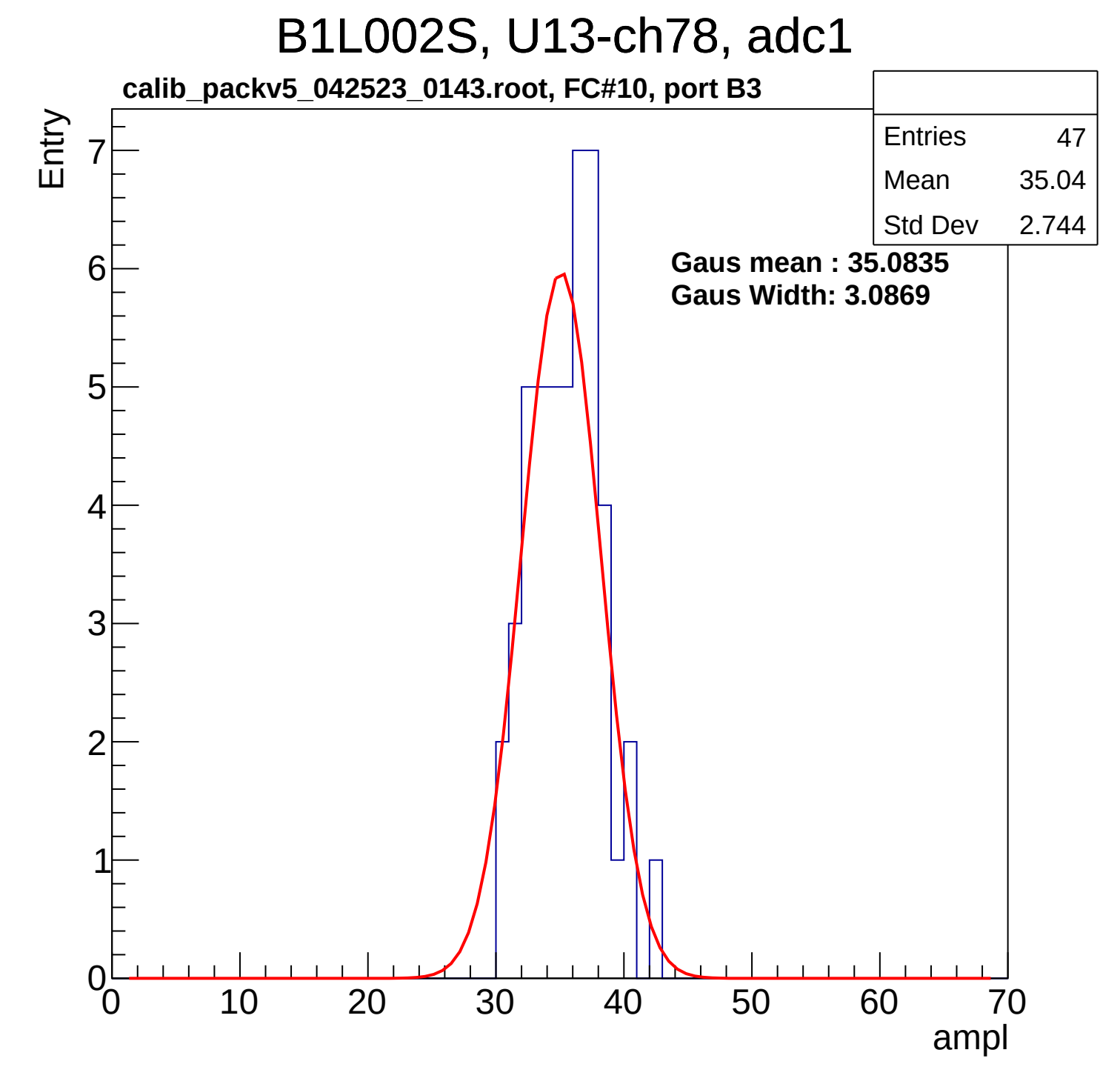
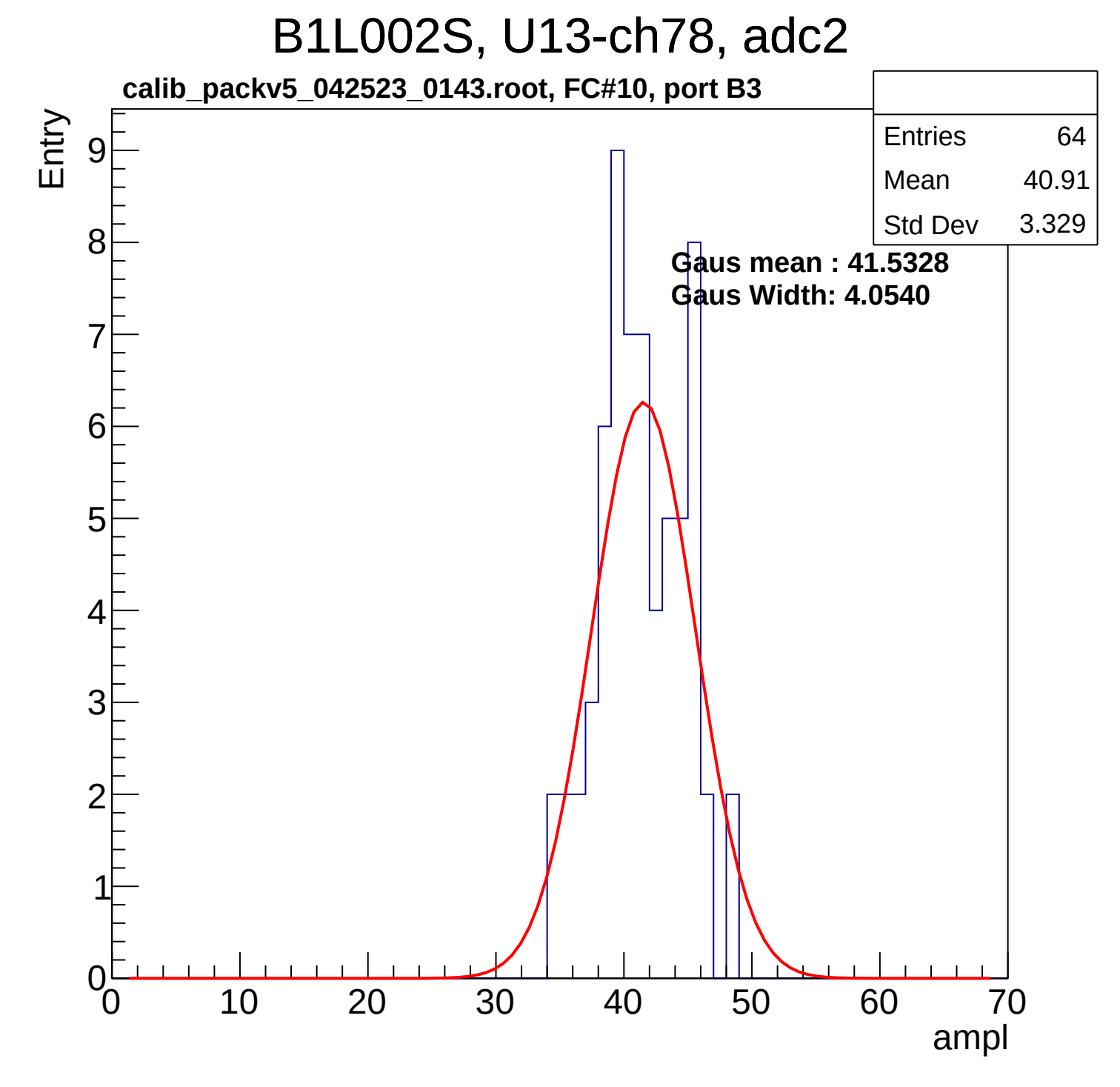
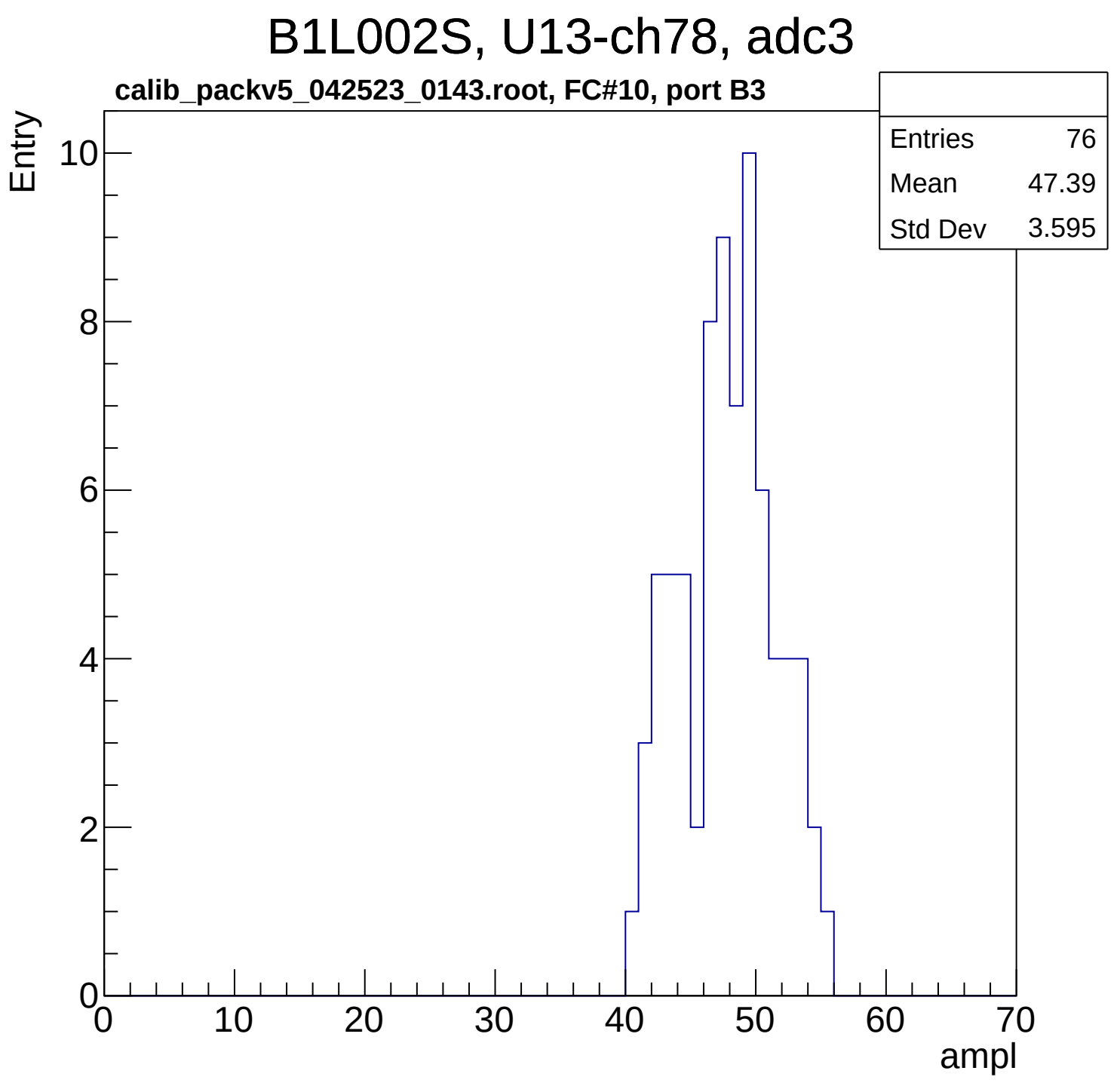
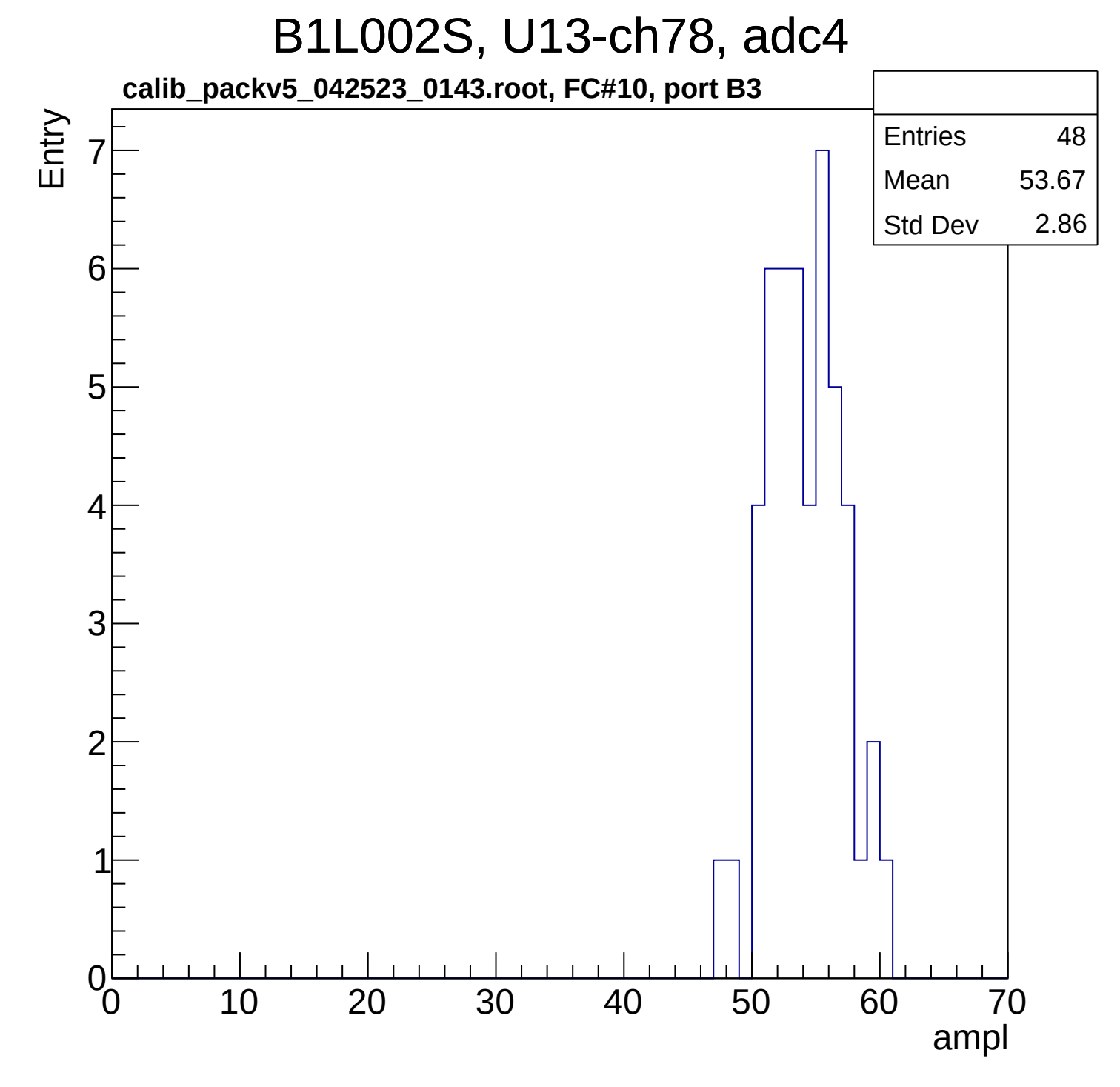
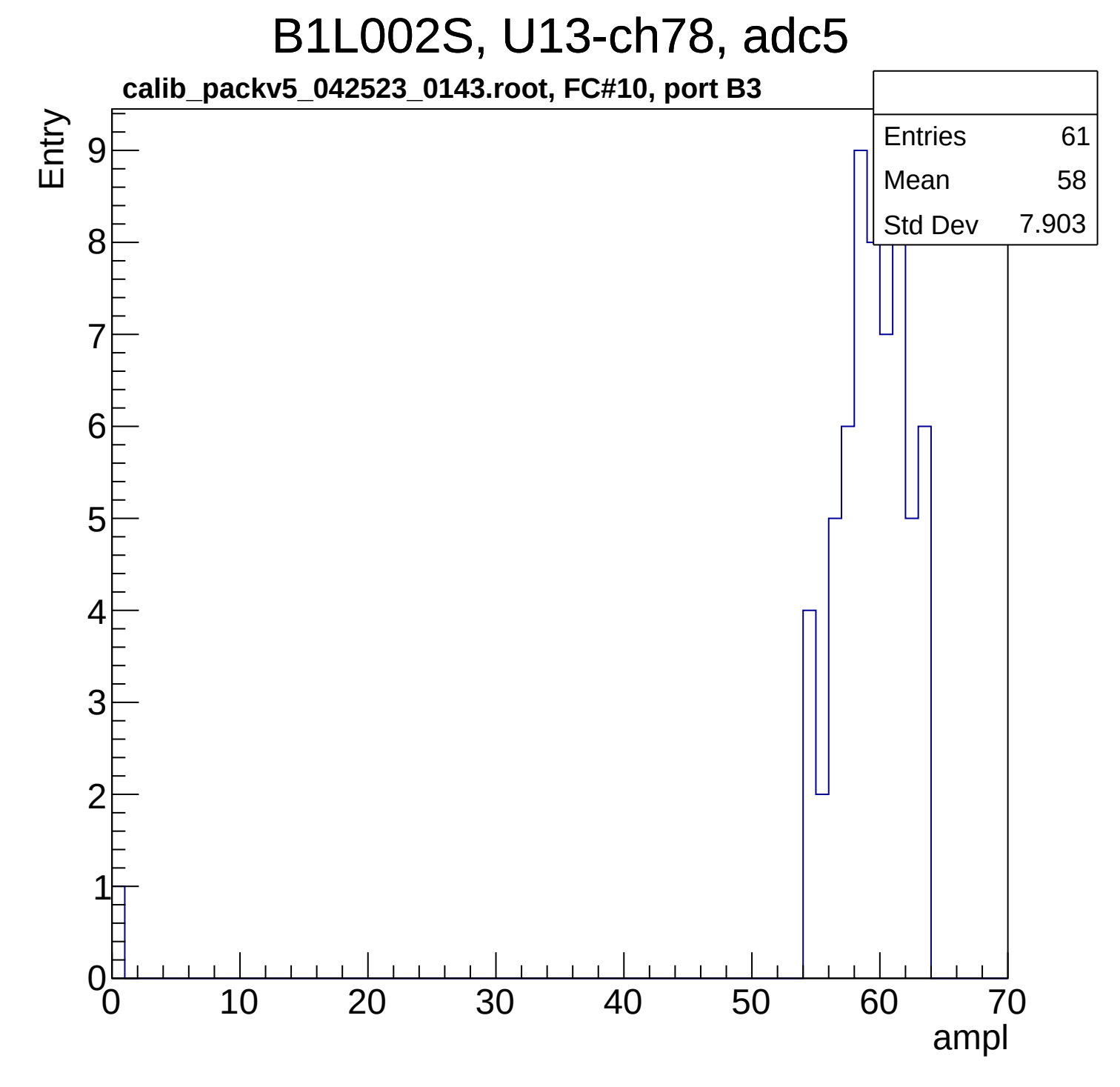
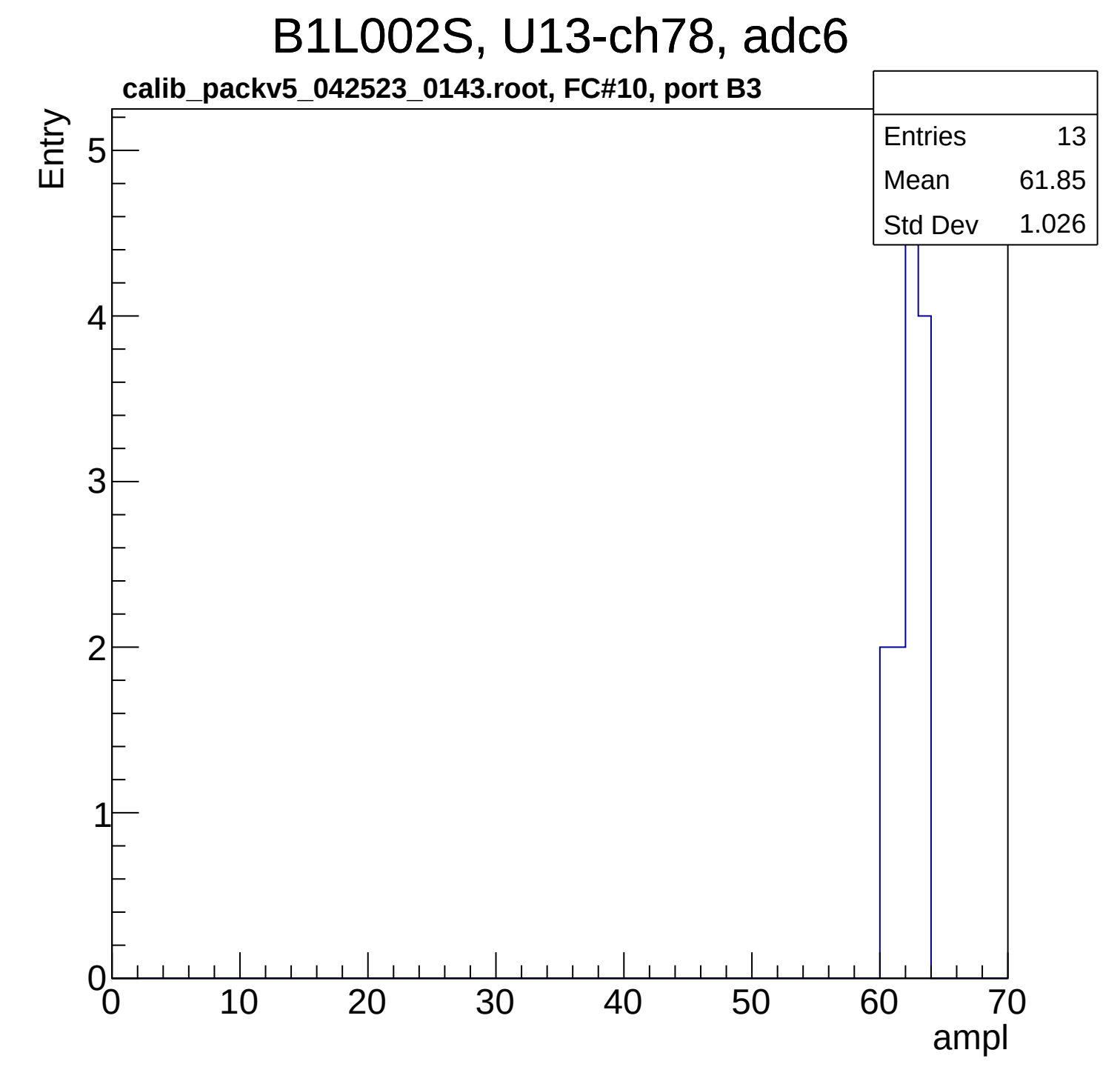

B1L002S, U13-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 35.04
Std Dev 2.744
Gaus mean : 35.0835
Gaus Width: 3.0869
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

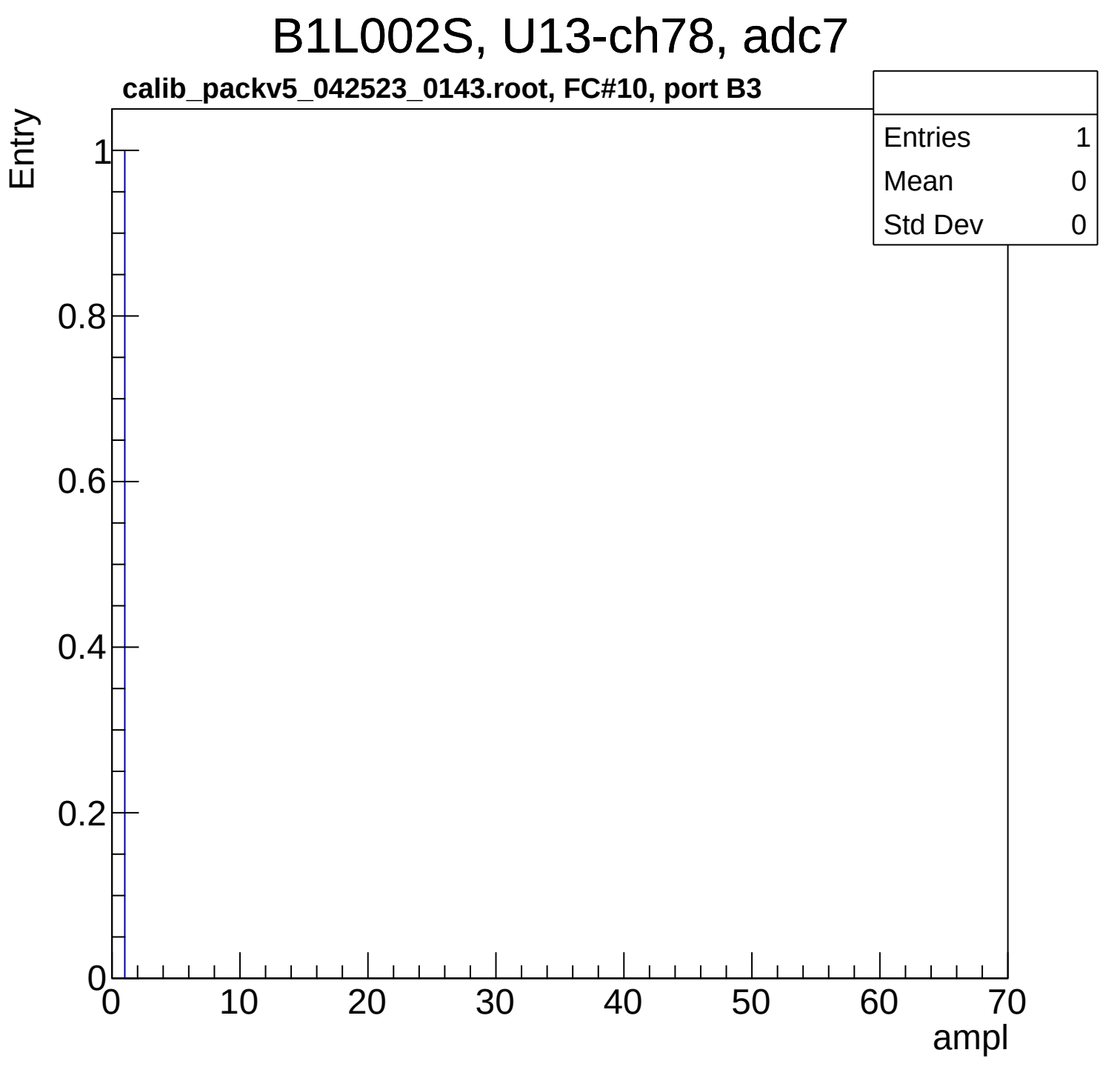

B1L002S, U13-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.91
Std Dev 3.329
Gaus mean : 41.5328
Gaus Width: 4.0540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

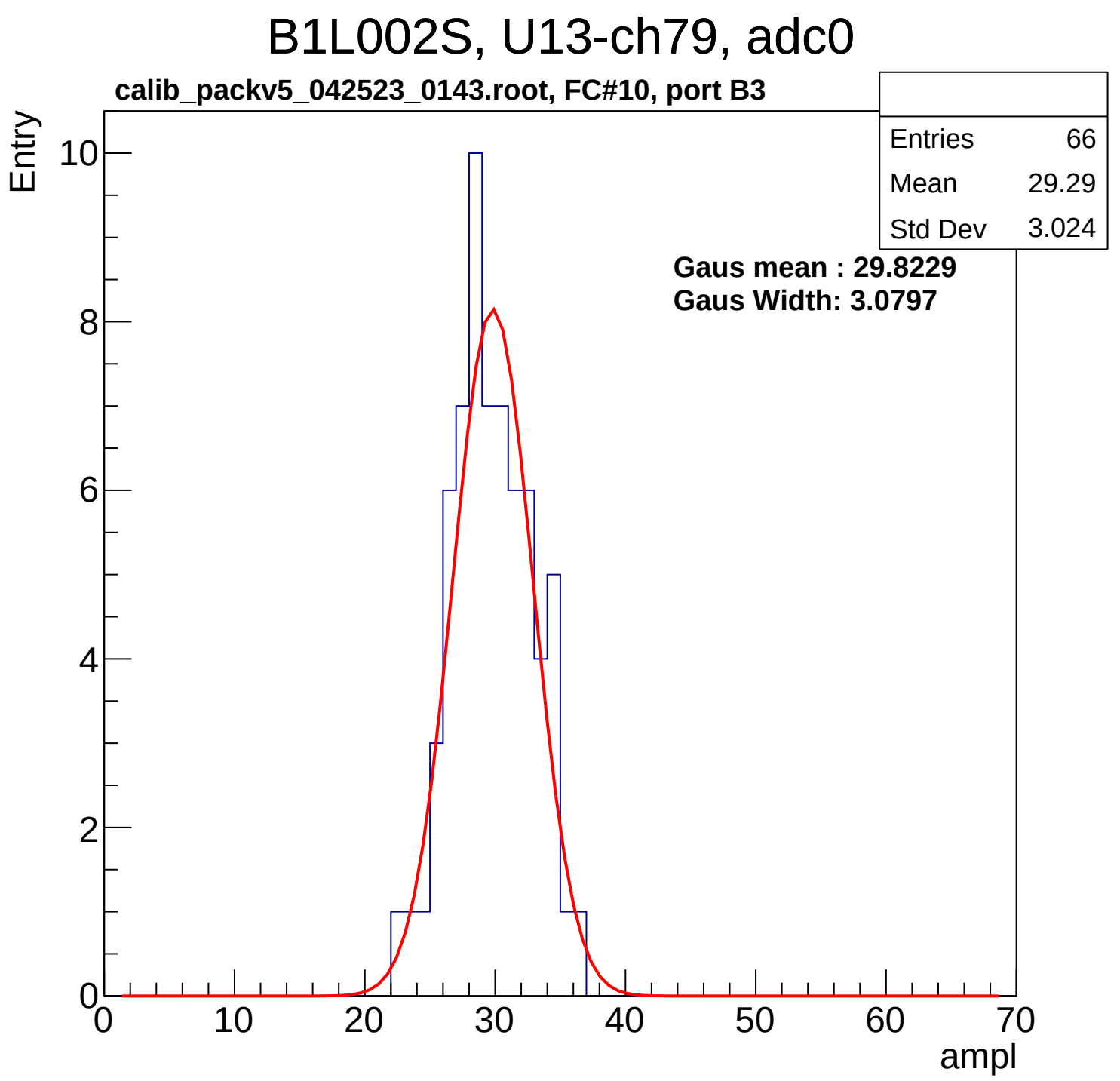

B1L002S, U13-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.39
Std Dev 3.595
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

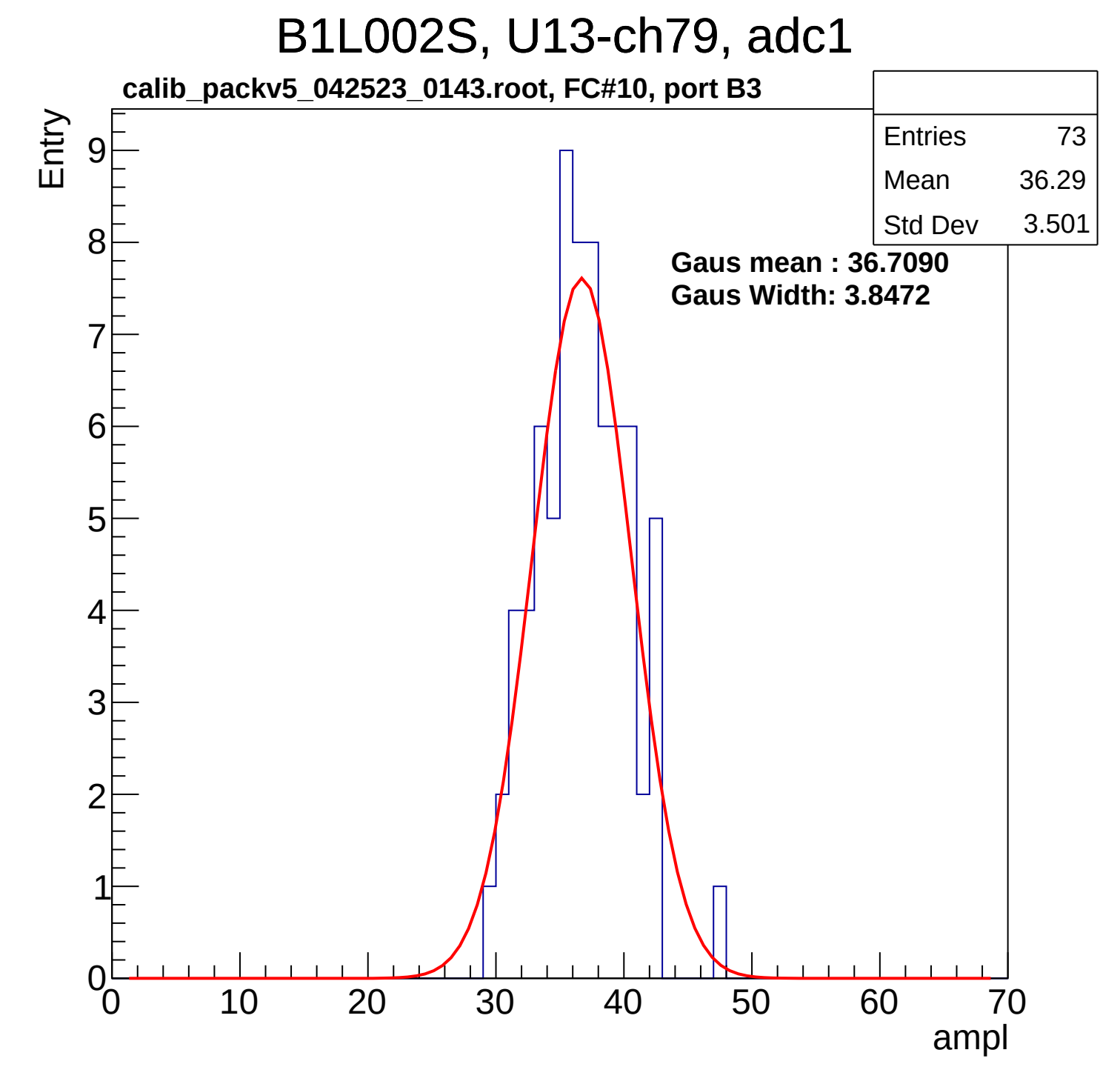

B1L002S, U13-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.67
Std Dev 2.86
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

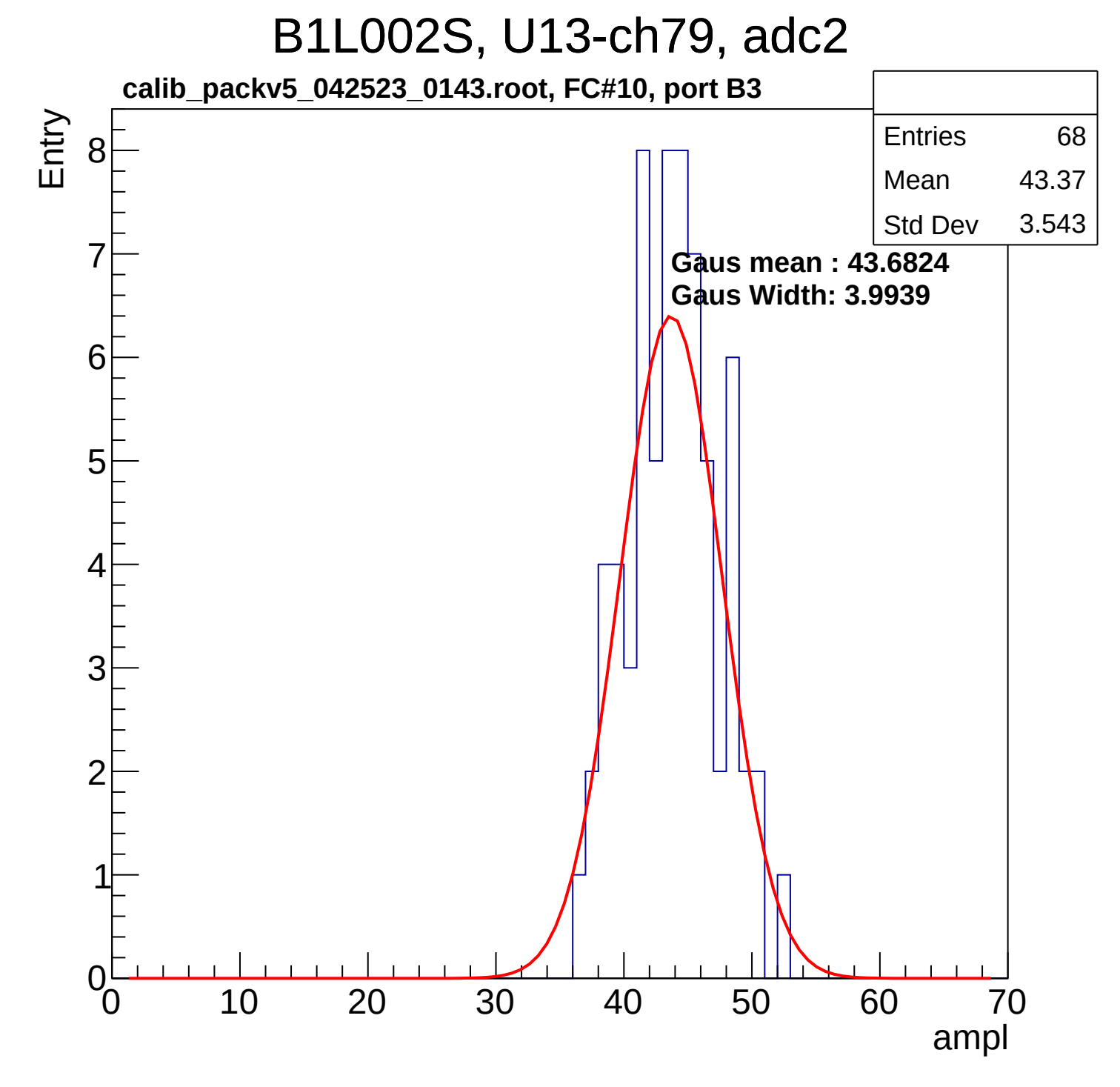

B1L002S, U13-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58
Std Dev 7.903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

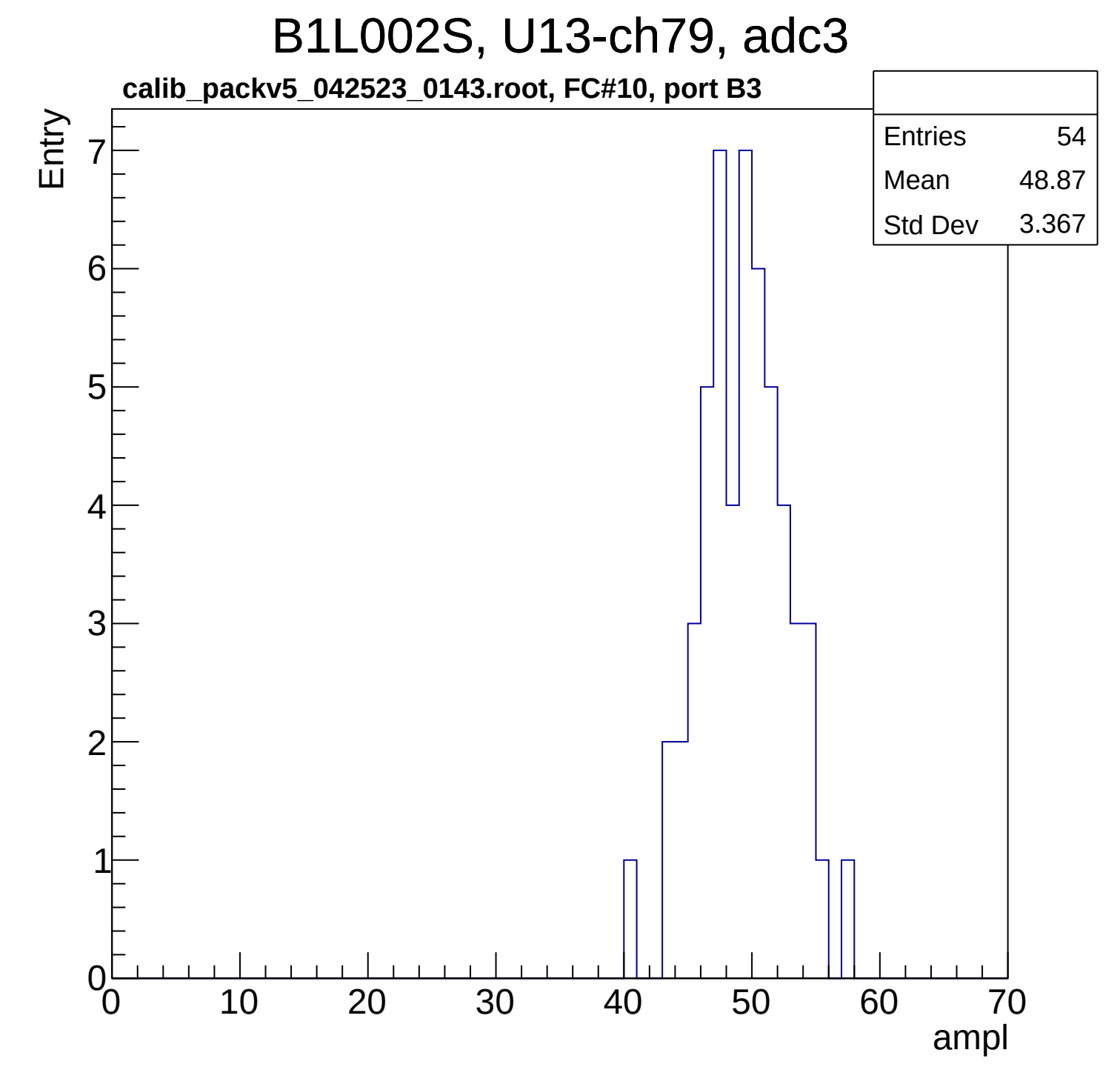

B1L002S, U13-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.85
Std Dev 1.026
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

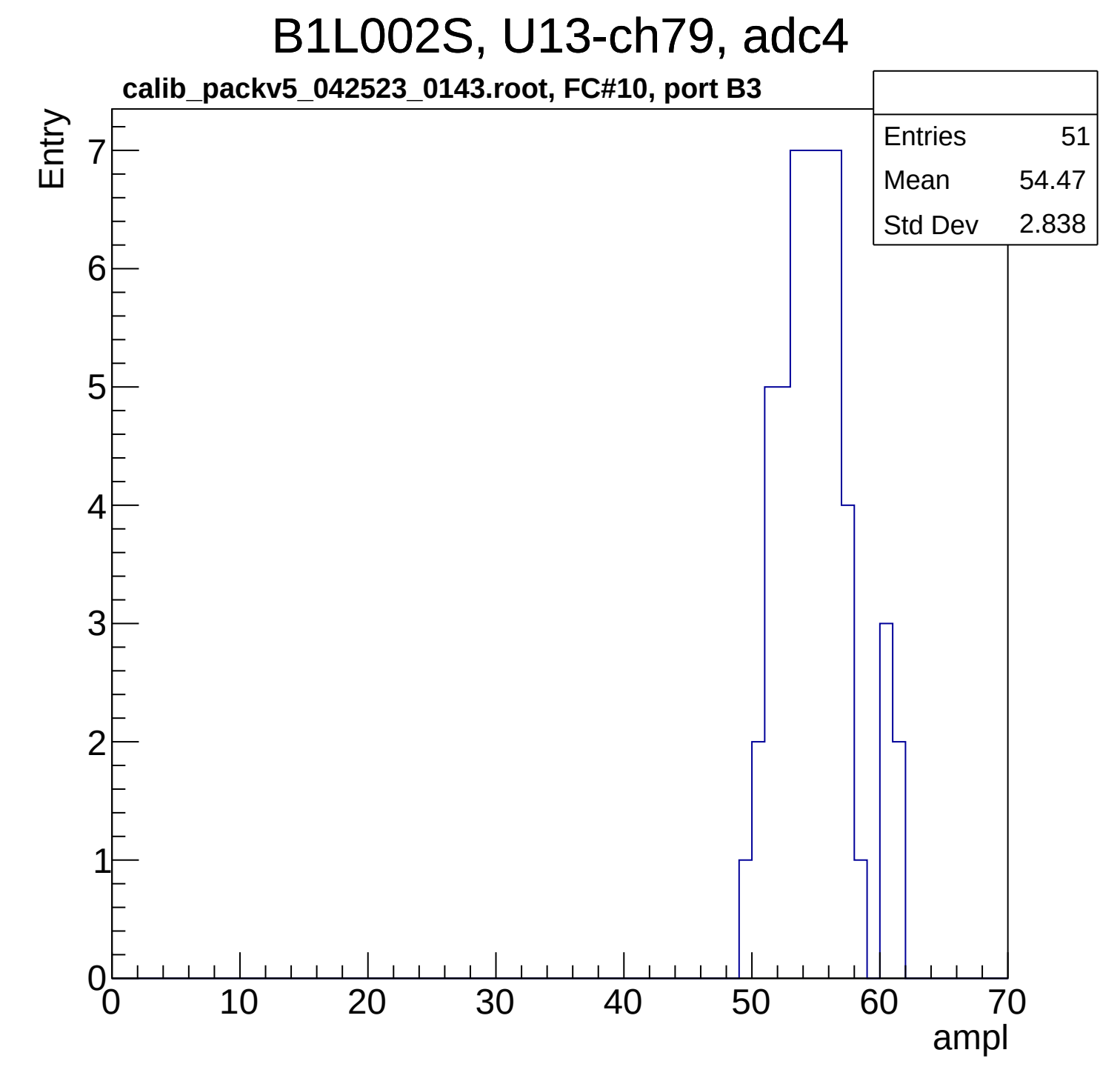

B1L002S, U13-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

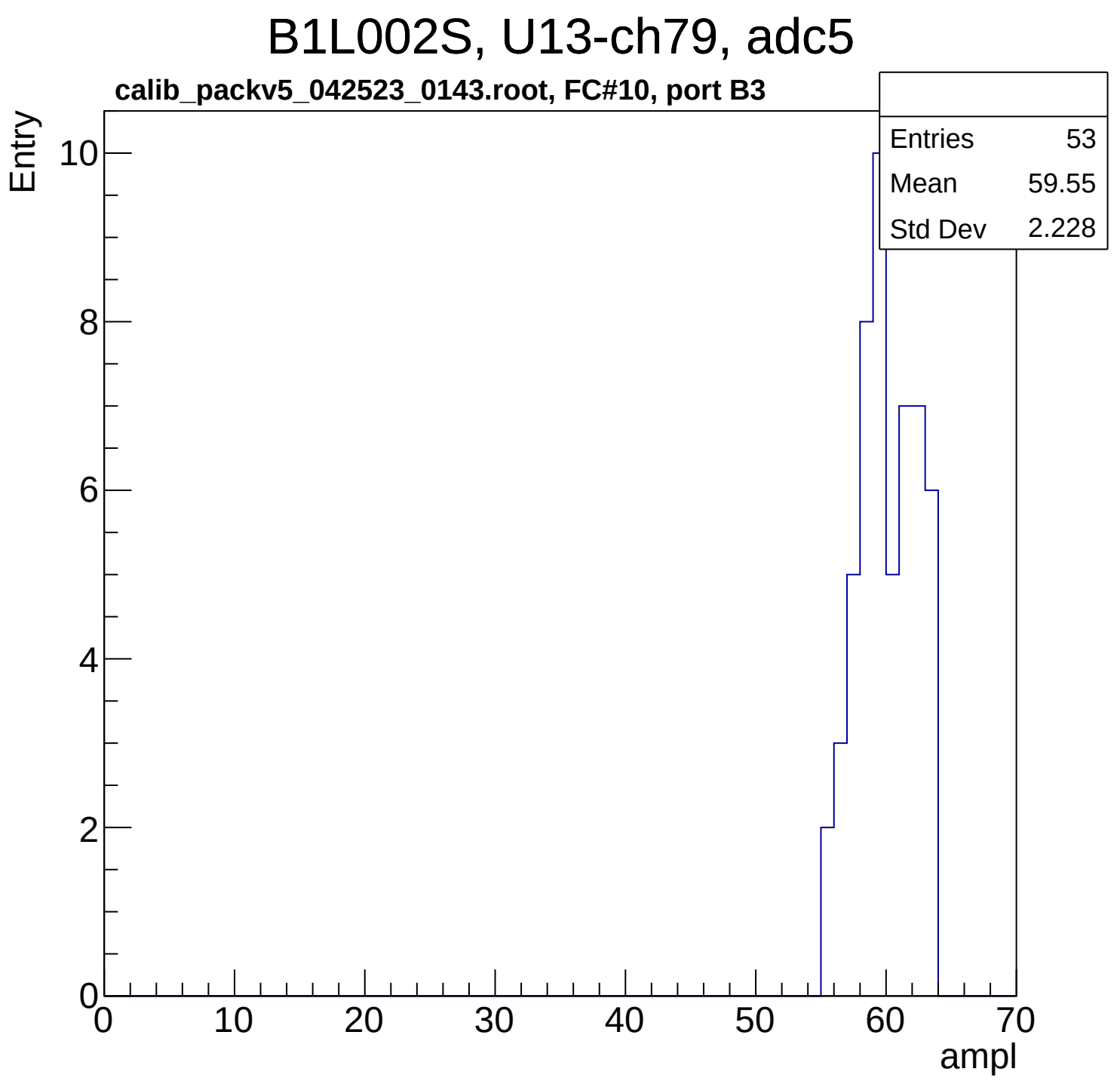

B1L002S, U13-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.29
Std Dev 3.024
Gaus mean : 29.8229
Gaus Width: 3.0797
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.29
Std Dev 3.501
Gaus mean : 36.7090
Gaus Width: 3.8472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 43.37
Std Dev 3.543
Gaus mean : 43.6824
Gaus Width: 3.9939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.87
Std Dev 3.367
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.47
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.55
Std Dev 2.228
Entry
ampl
0
2
4
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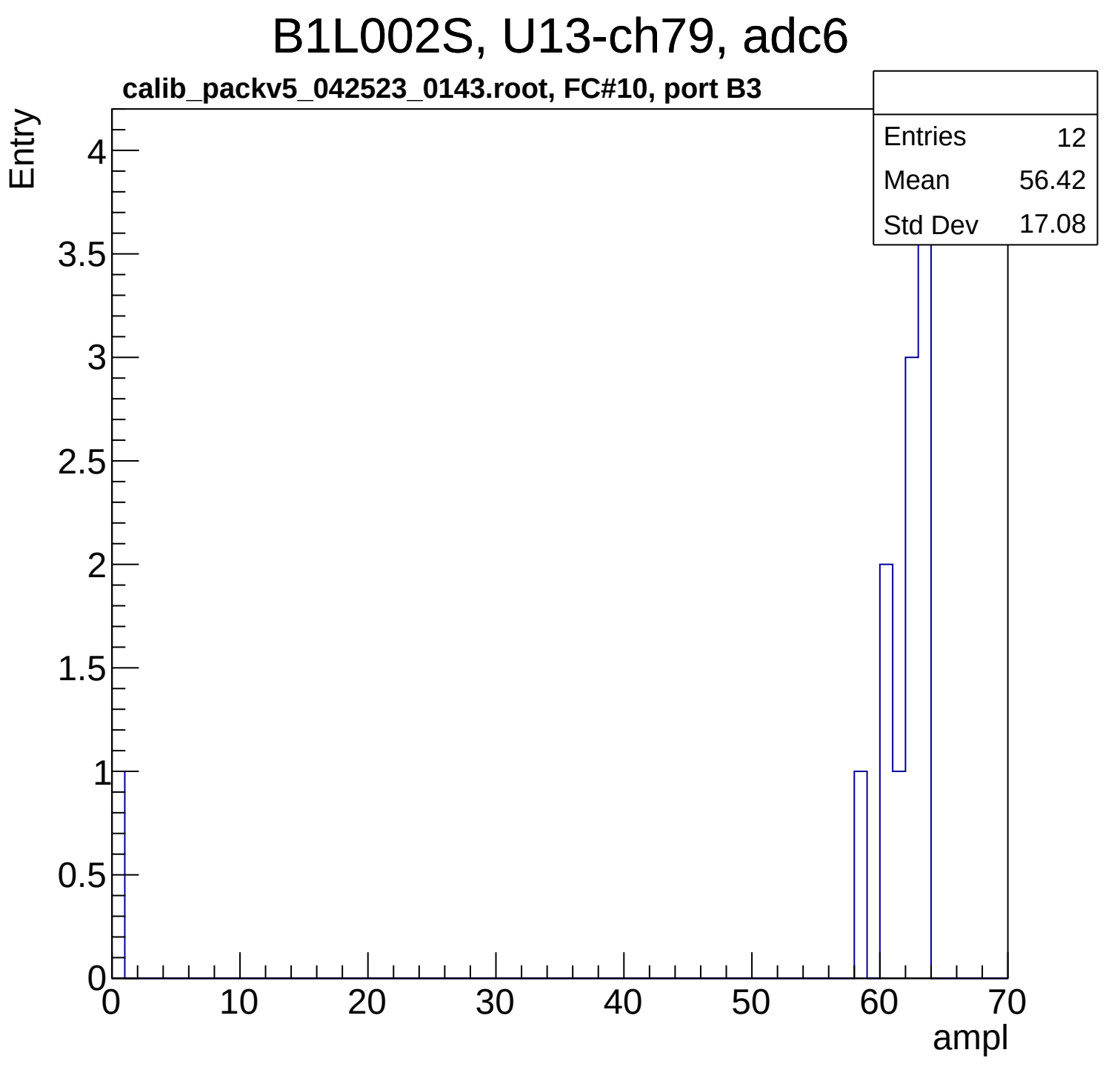
0
10
20
30
40
50
60
70


B1L002S, U13-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.42
Std Dev 17.08
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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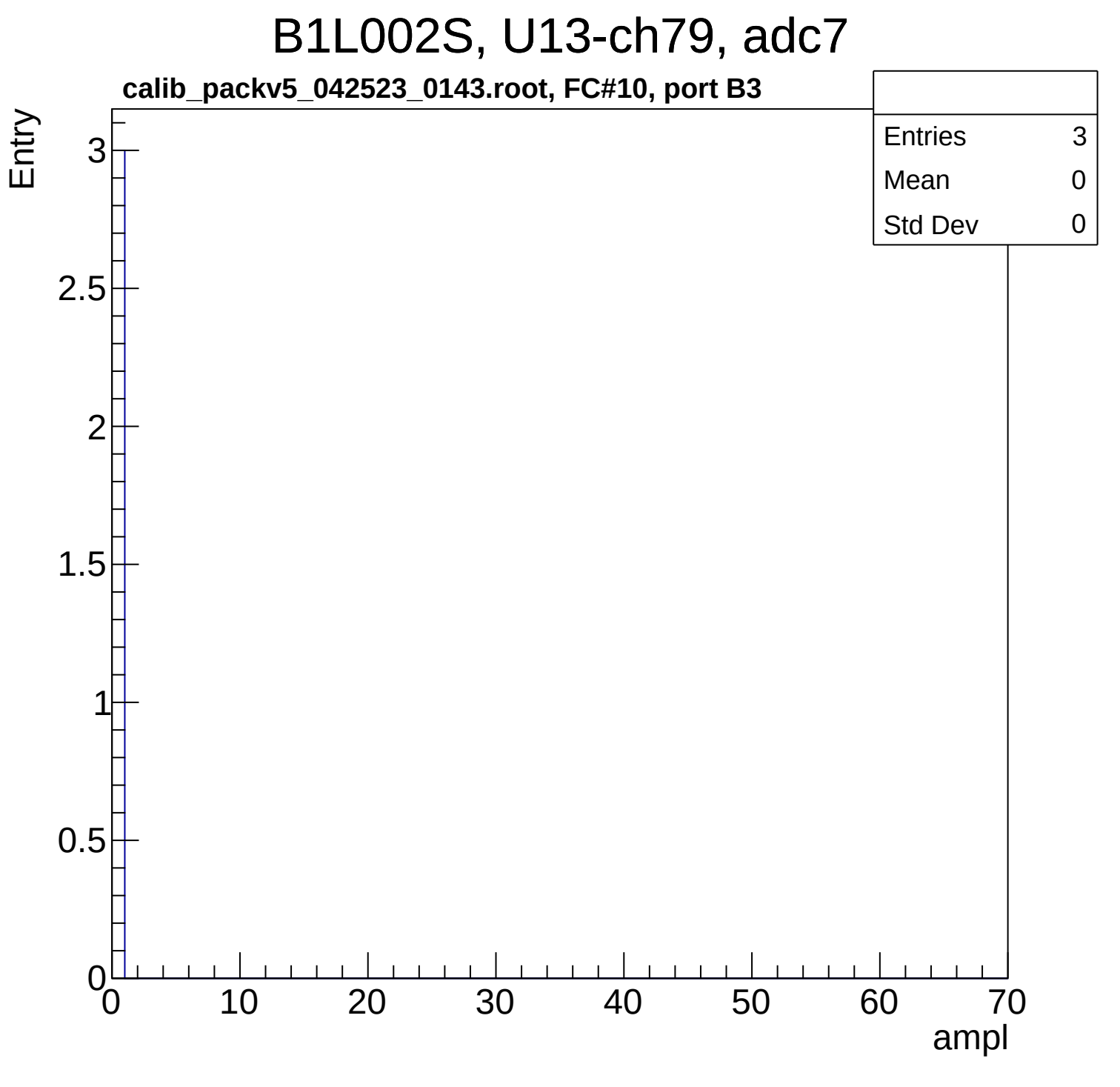
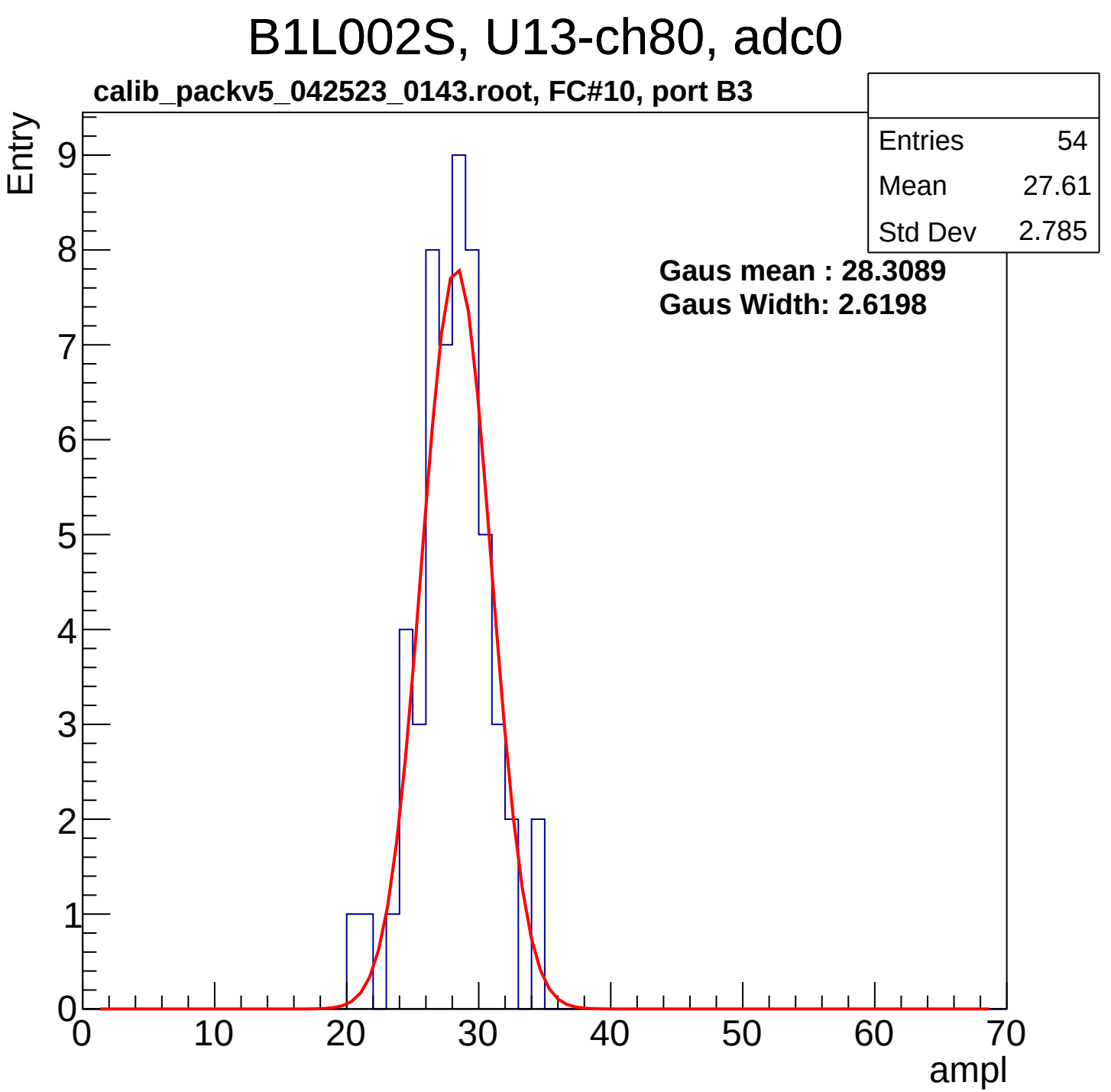
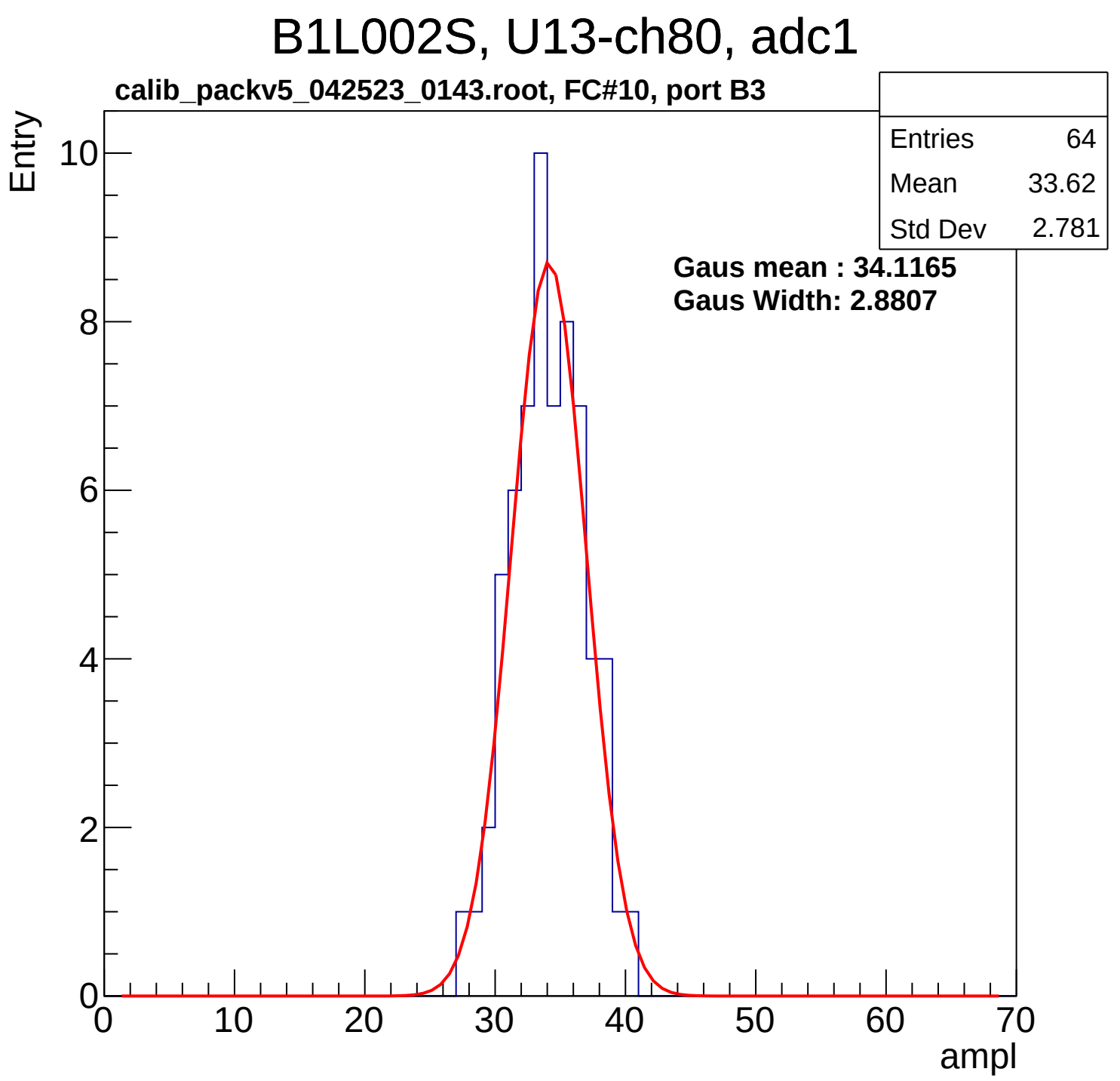
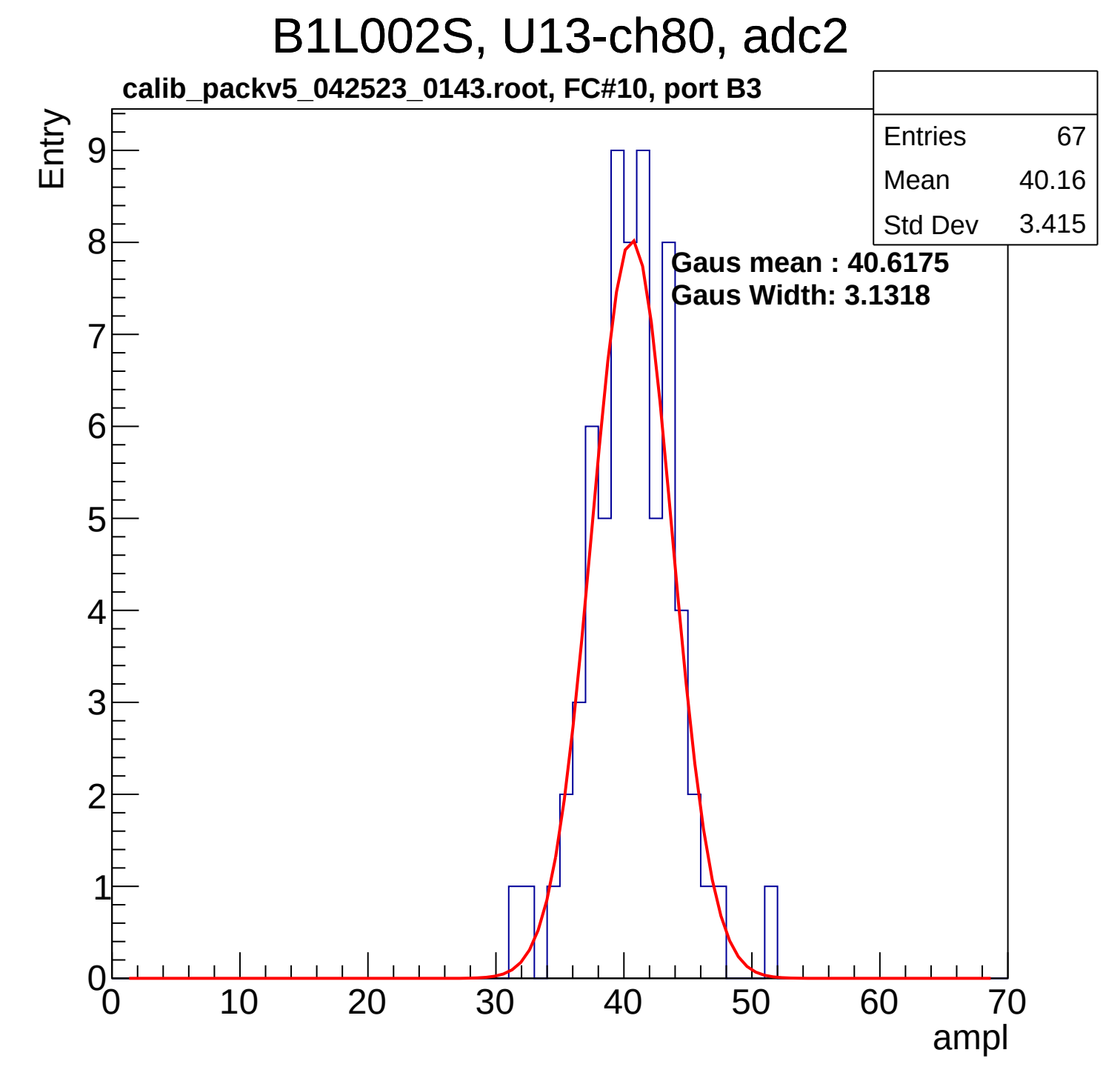

B1L002S, U13-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.61
Std Dev 2.785
Gaus mean : 28.3089
Gaus Width: 2.6198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 33.62
Std Dev 2.781
Gaus mean : 34.1165
Gaus Width: 2.8807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.16
Std Dev 3.415
Gaus mean : 40.6175
Gaus Width: 3.1318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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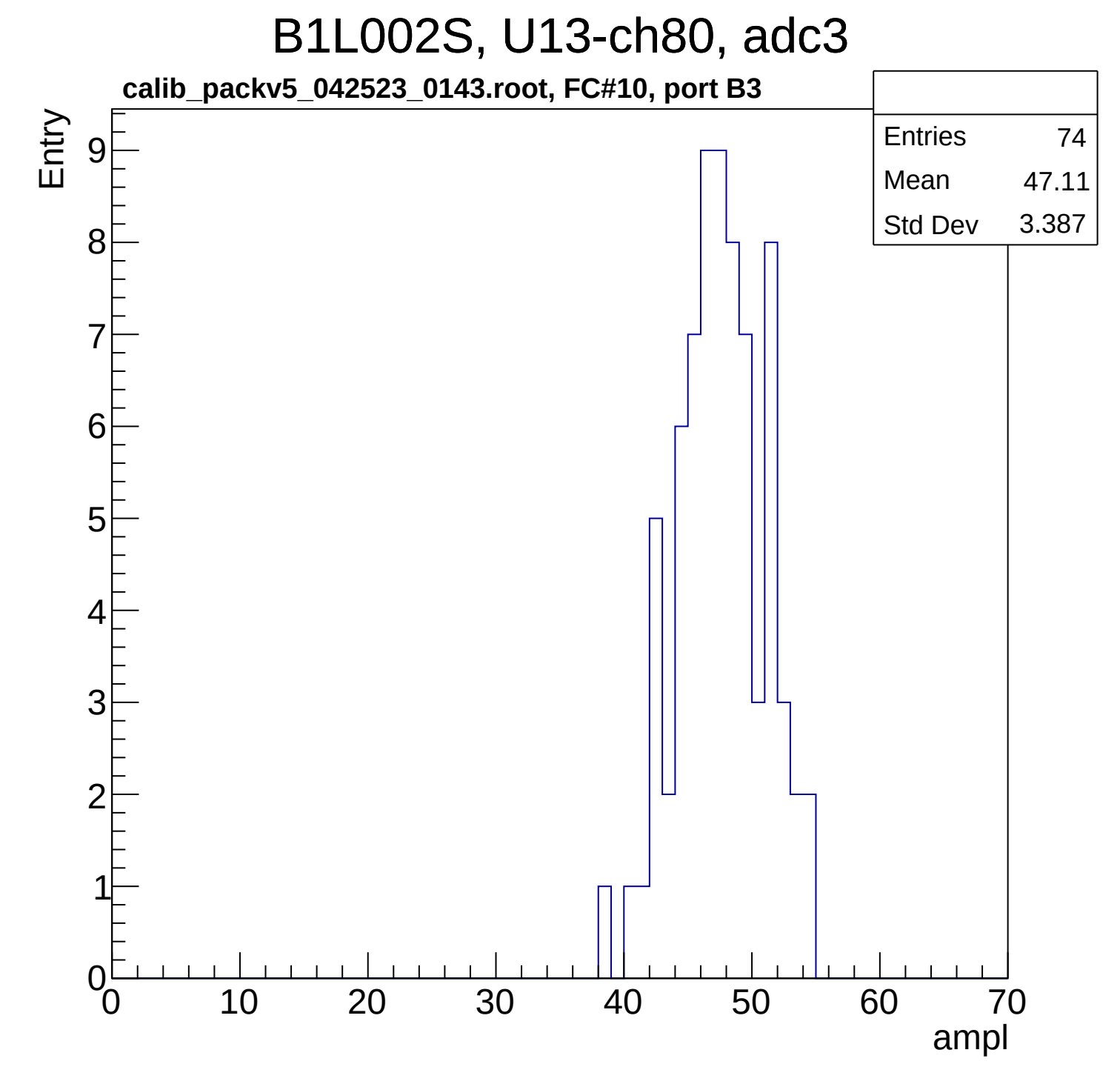
B1L002S, U13-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.11
Std Dev 3.387
Entry
ampl
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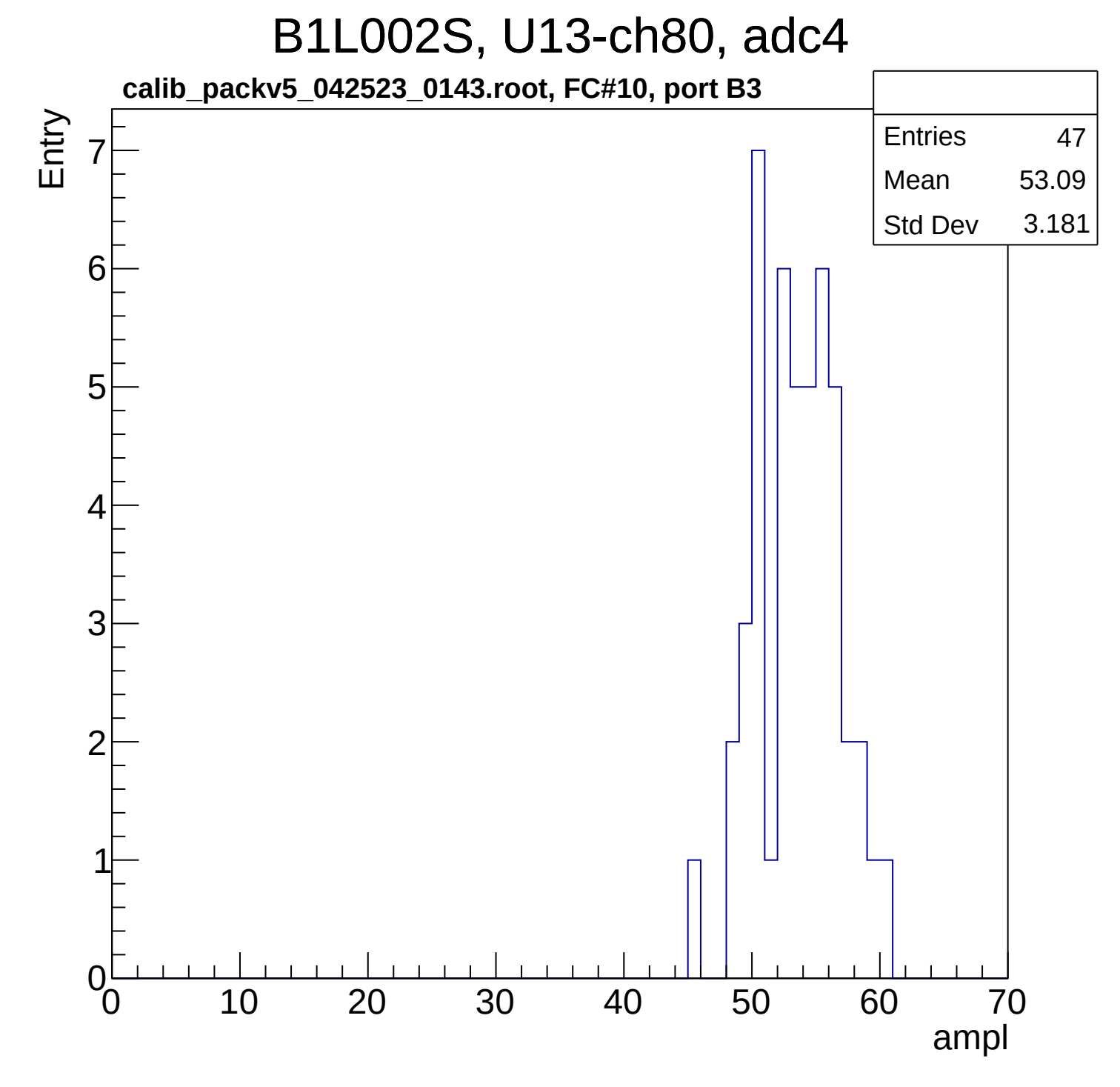
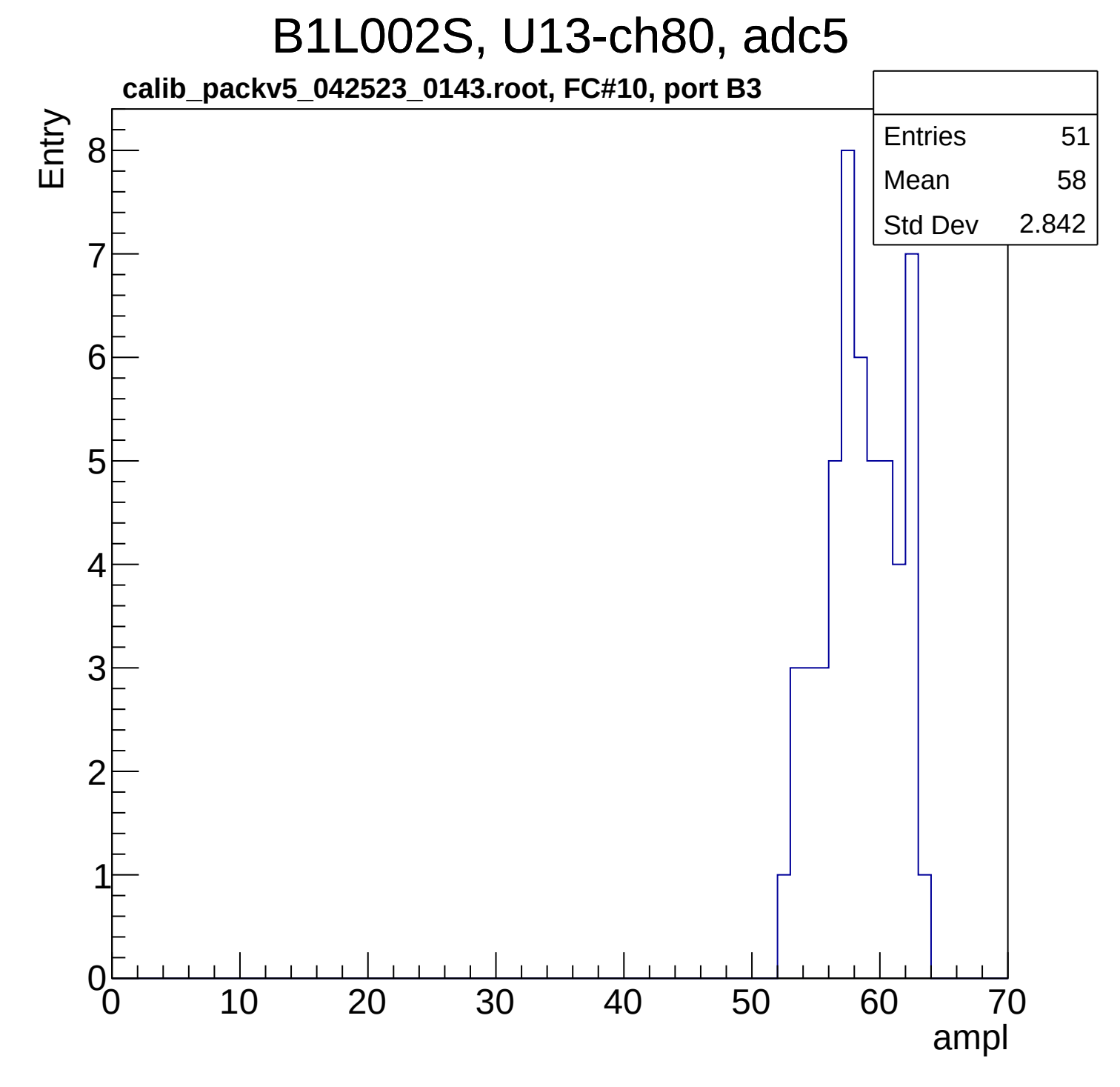
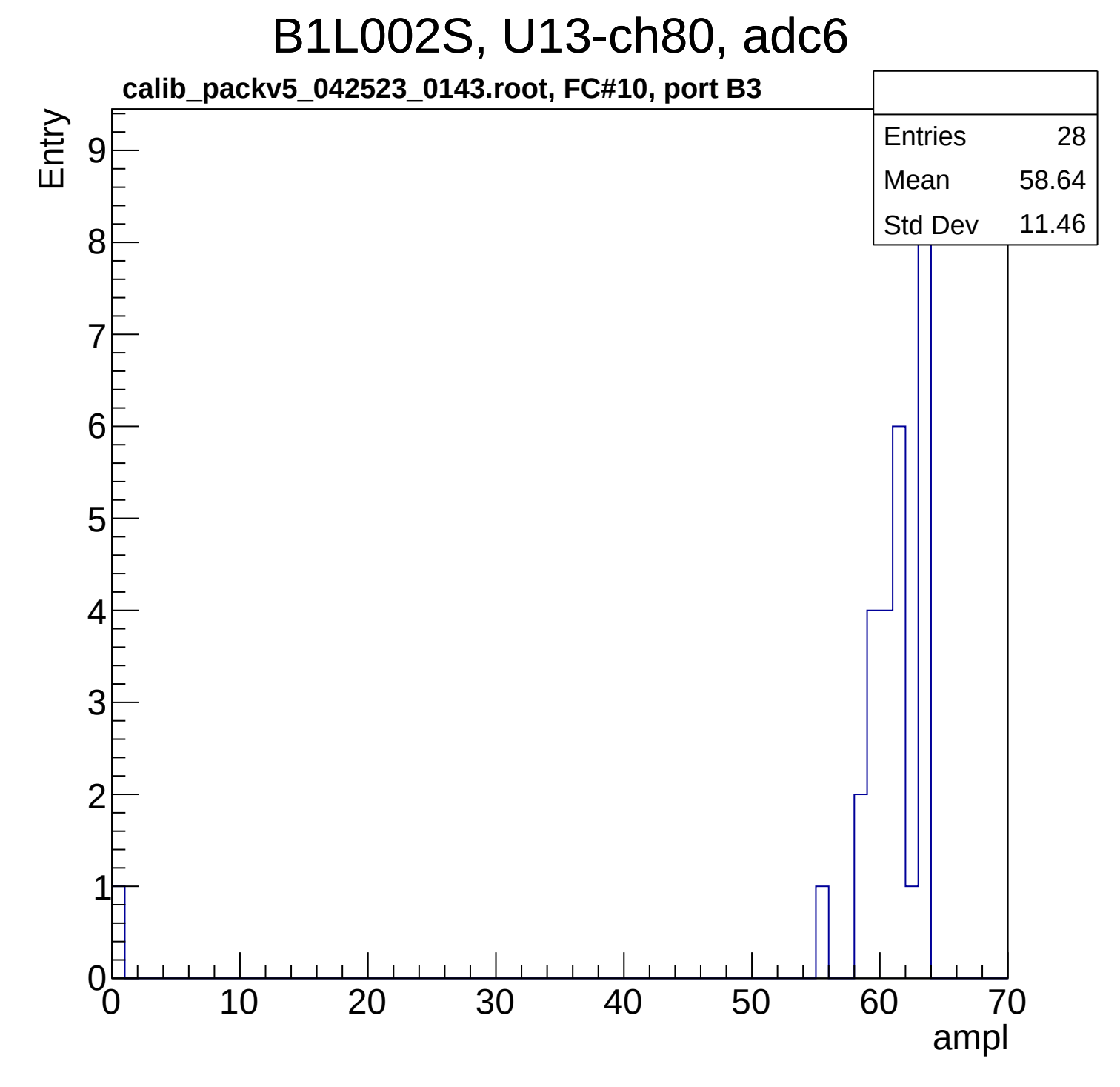
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.09
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58
Std Dev 2.842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.64
Std Dev 11.46
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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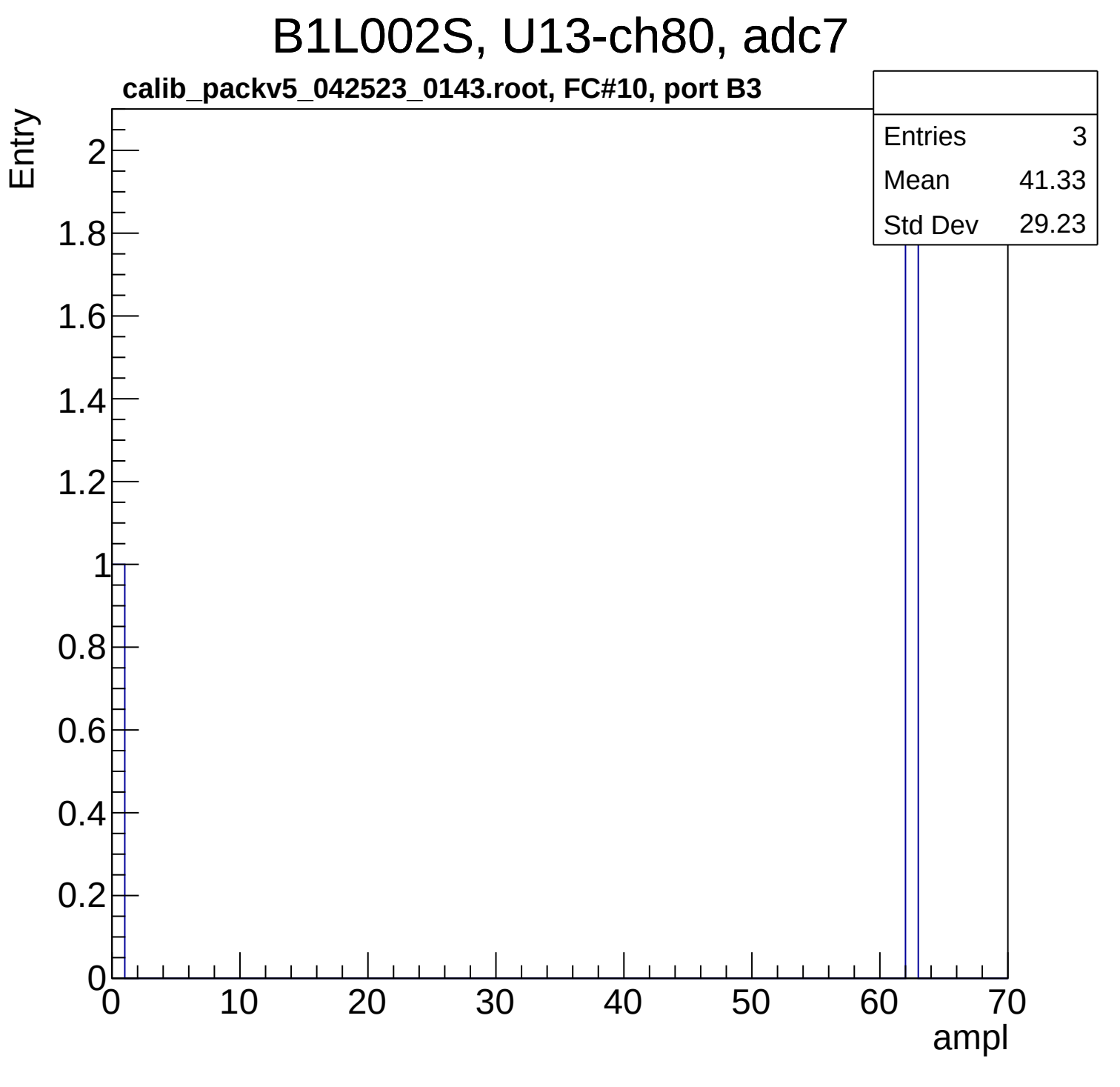
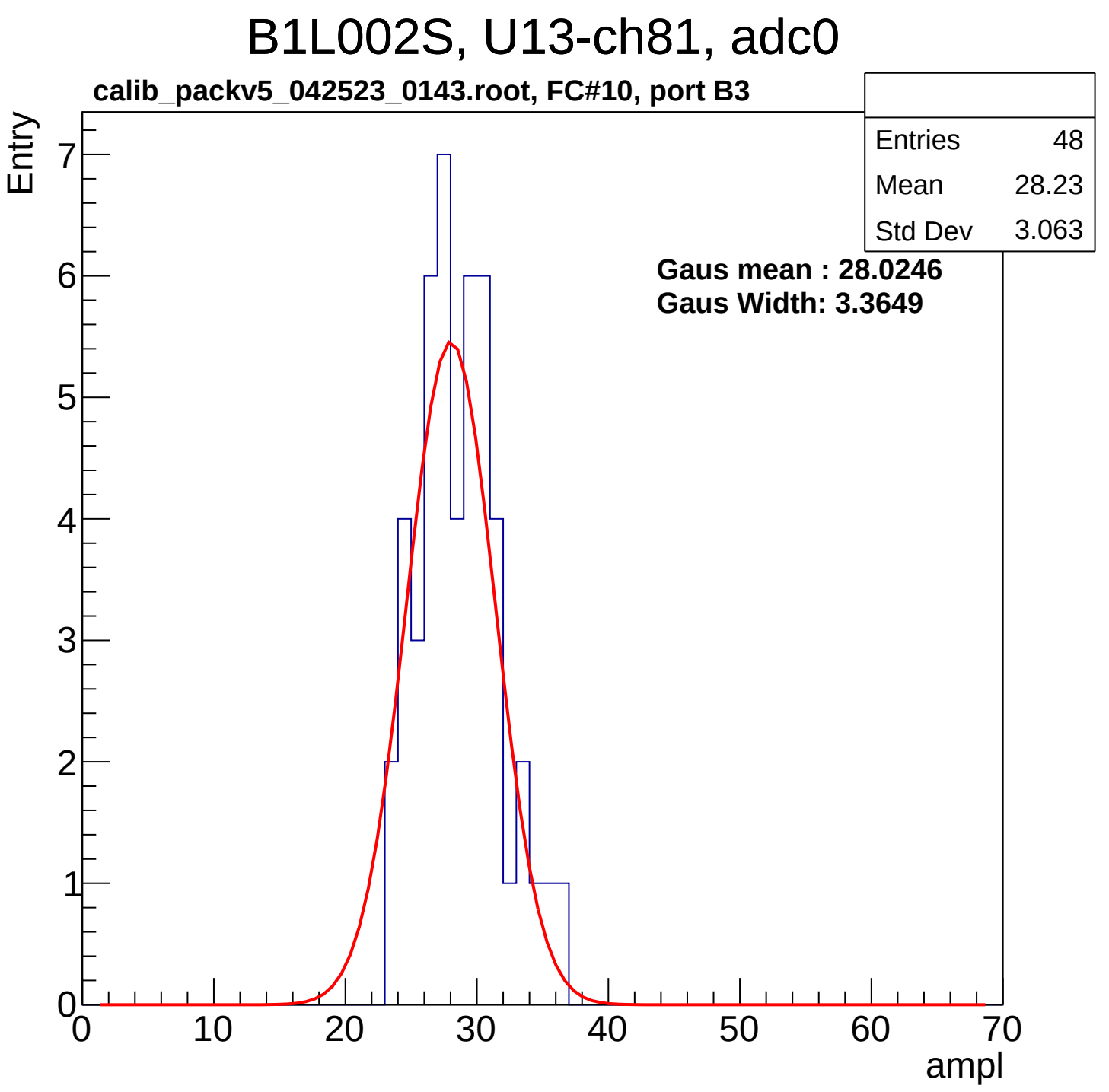
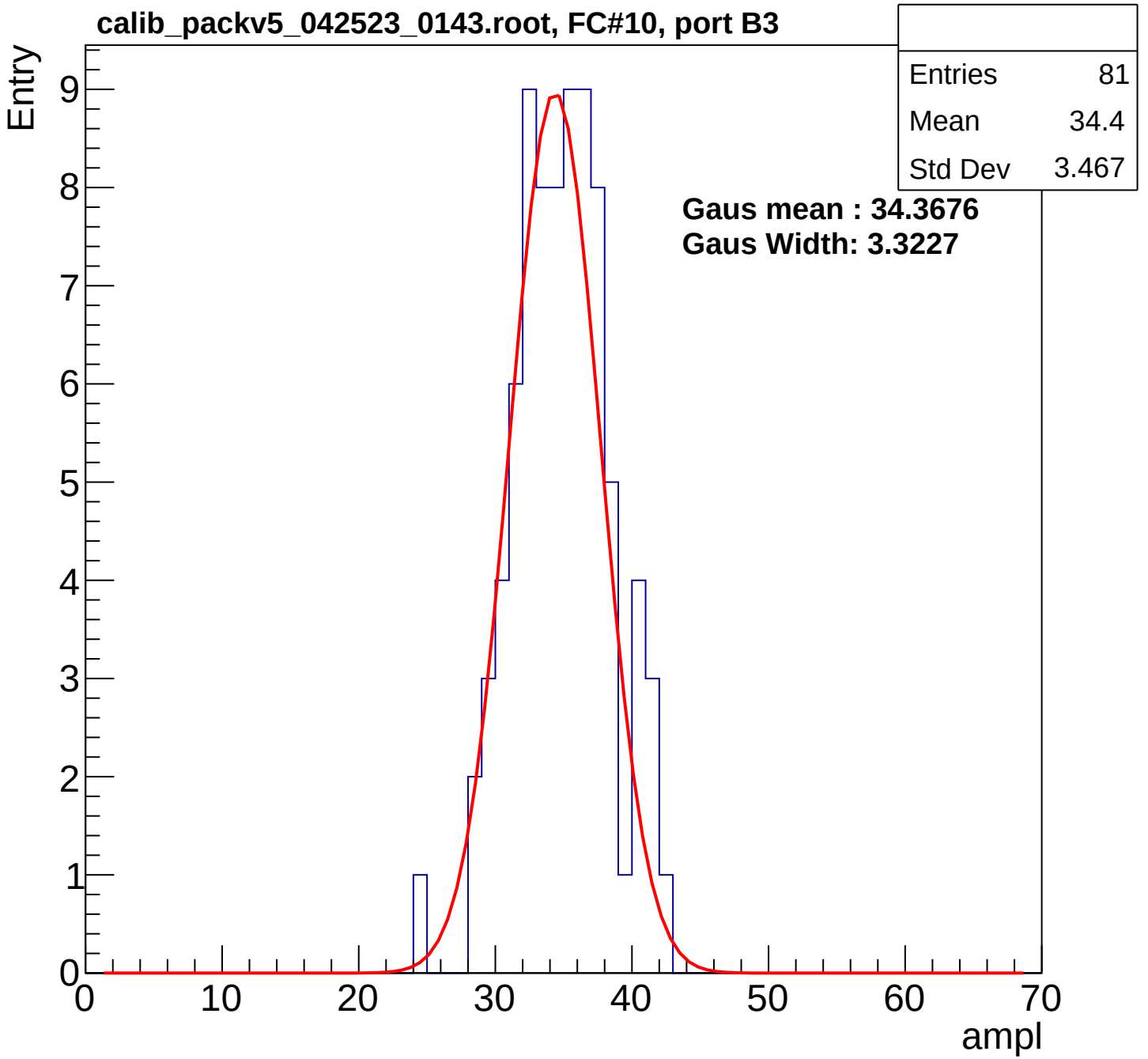

B1L002S, U13-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.23
Std Dev 3.063
Gaus mean : 28.0246
Gaus Width: 3.3649
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.4
Std Dev 3.467
Gaus mean : 34.3676
Gaus Width: 3.3227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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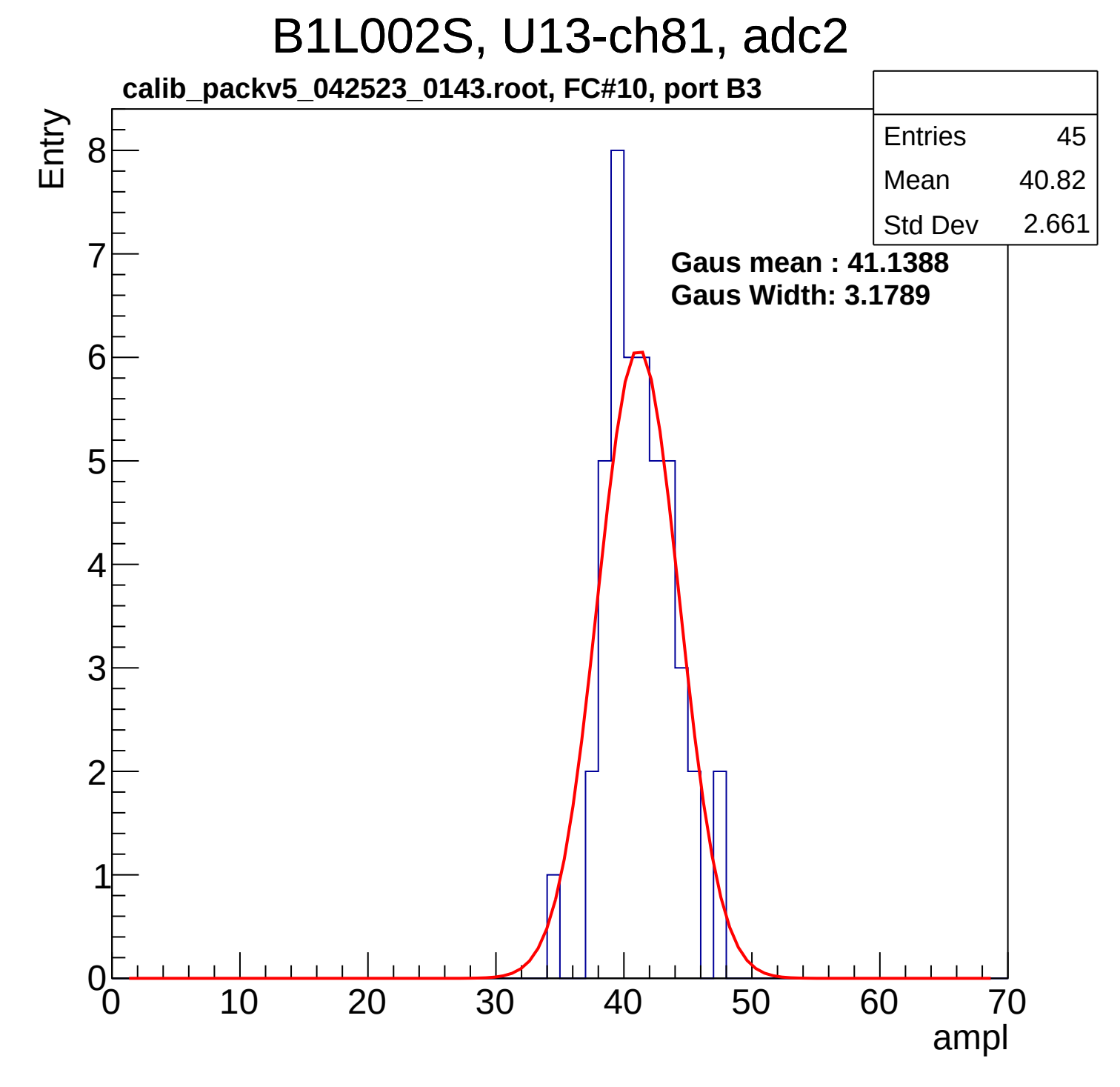
B1L002S, U13-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 40.82
Std Dev 2.661
Gaus mean : 41.1388
Gaus Width: 3.1789
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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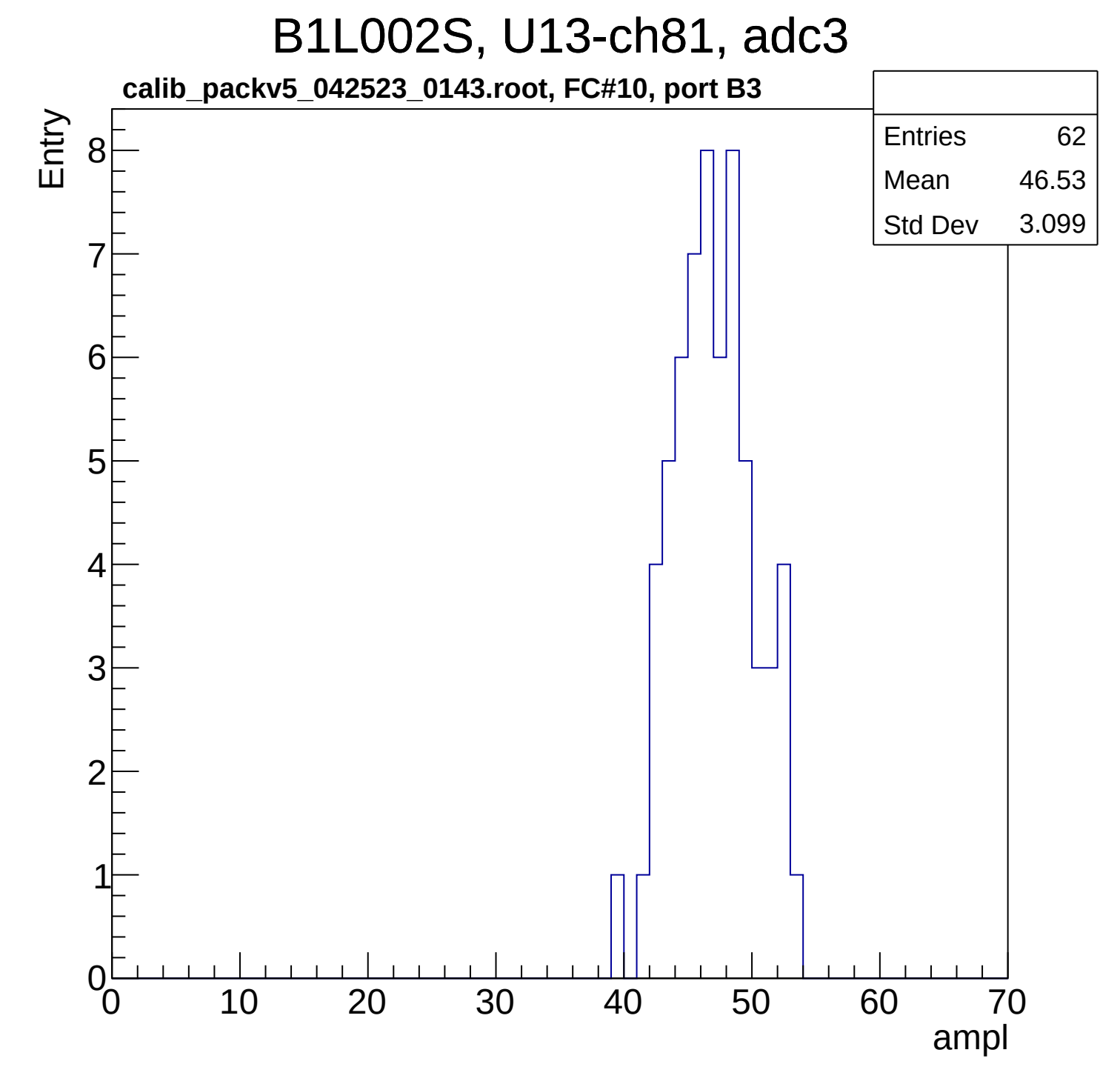
B1L002S, U13-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.53
Std Dev 3.099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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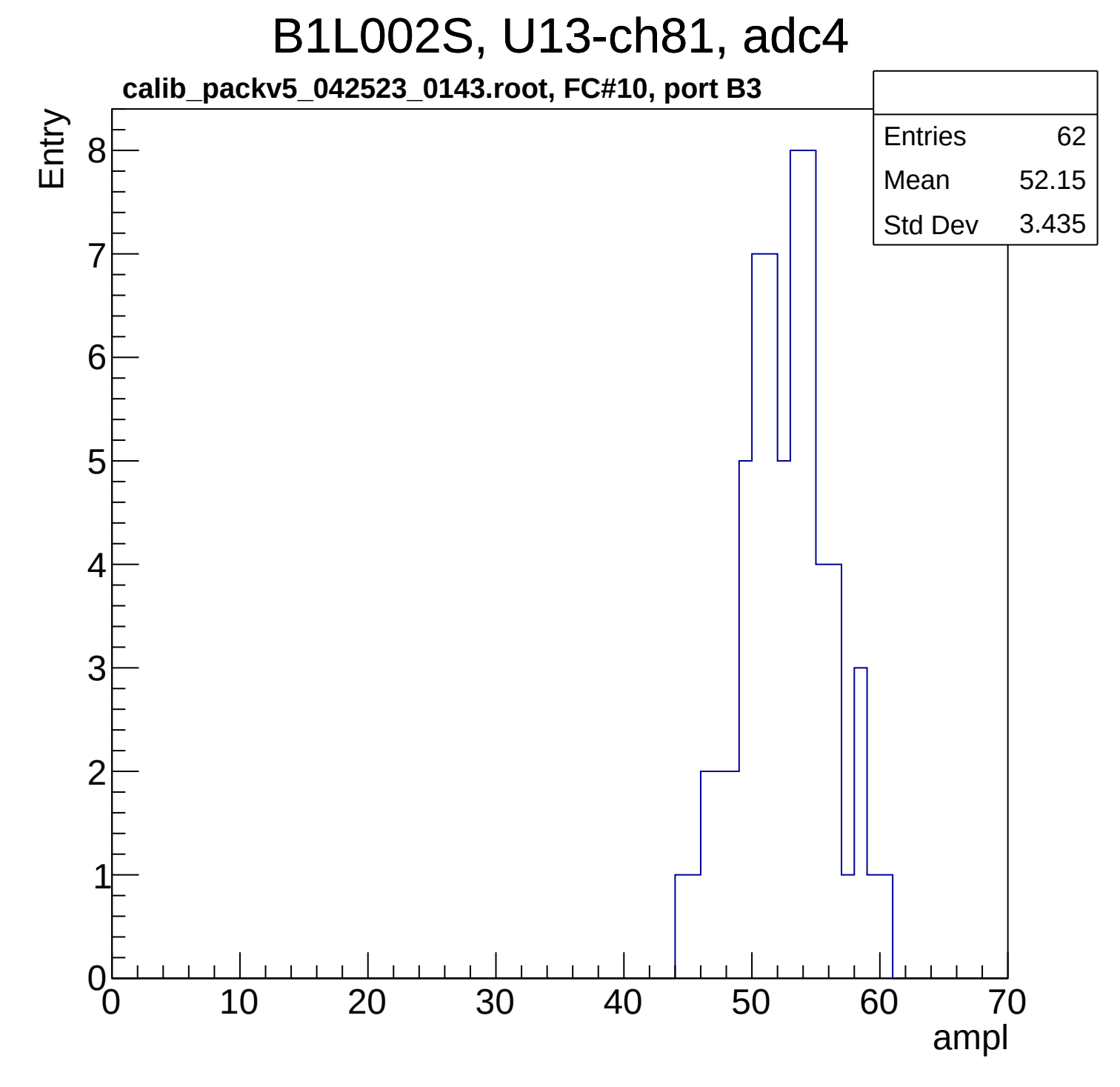
B1L002S, U13-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.15
Std Dev 3.435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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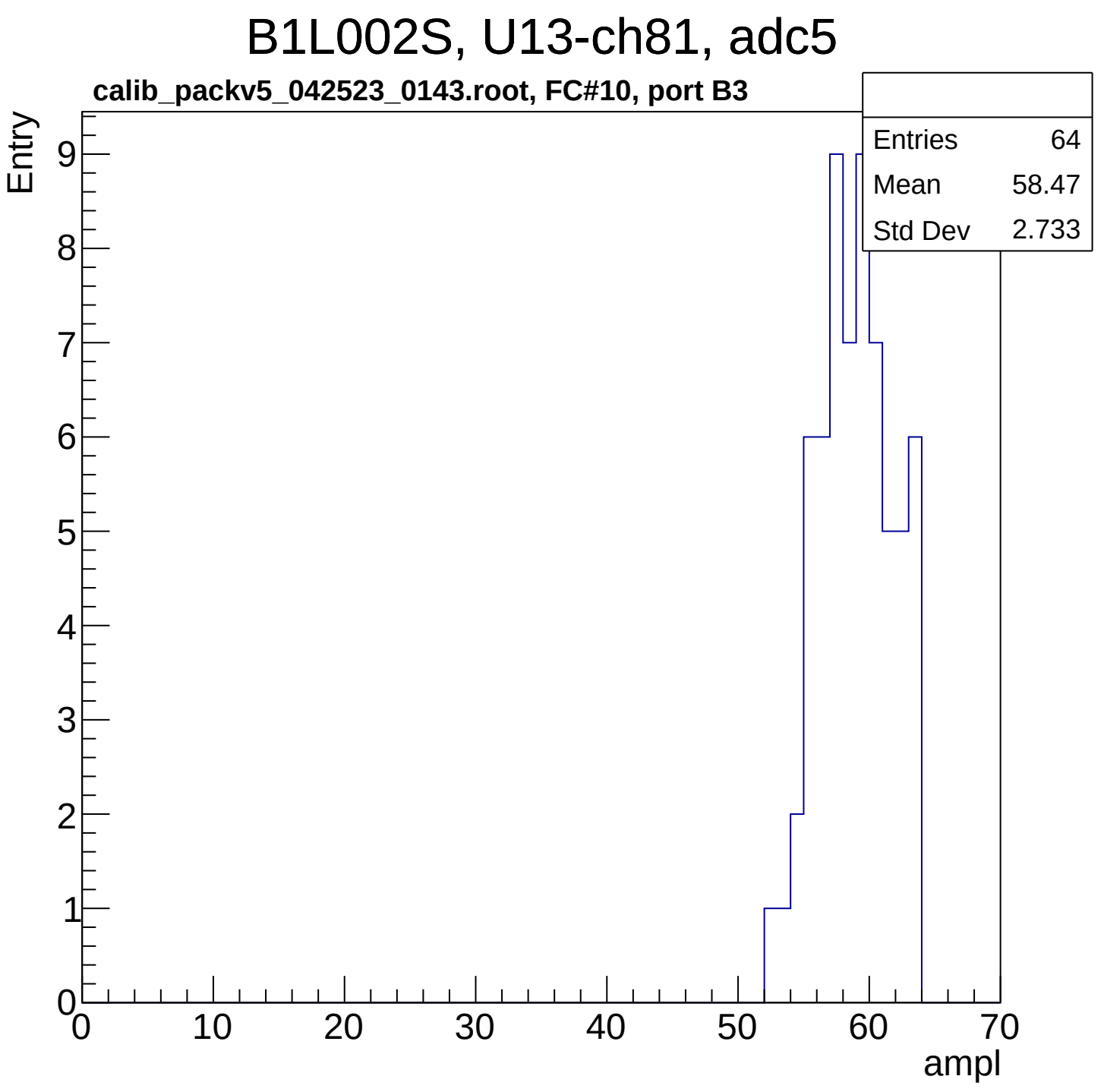
B1L002S, U13-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.47
Std Dev 2.733
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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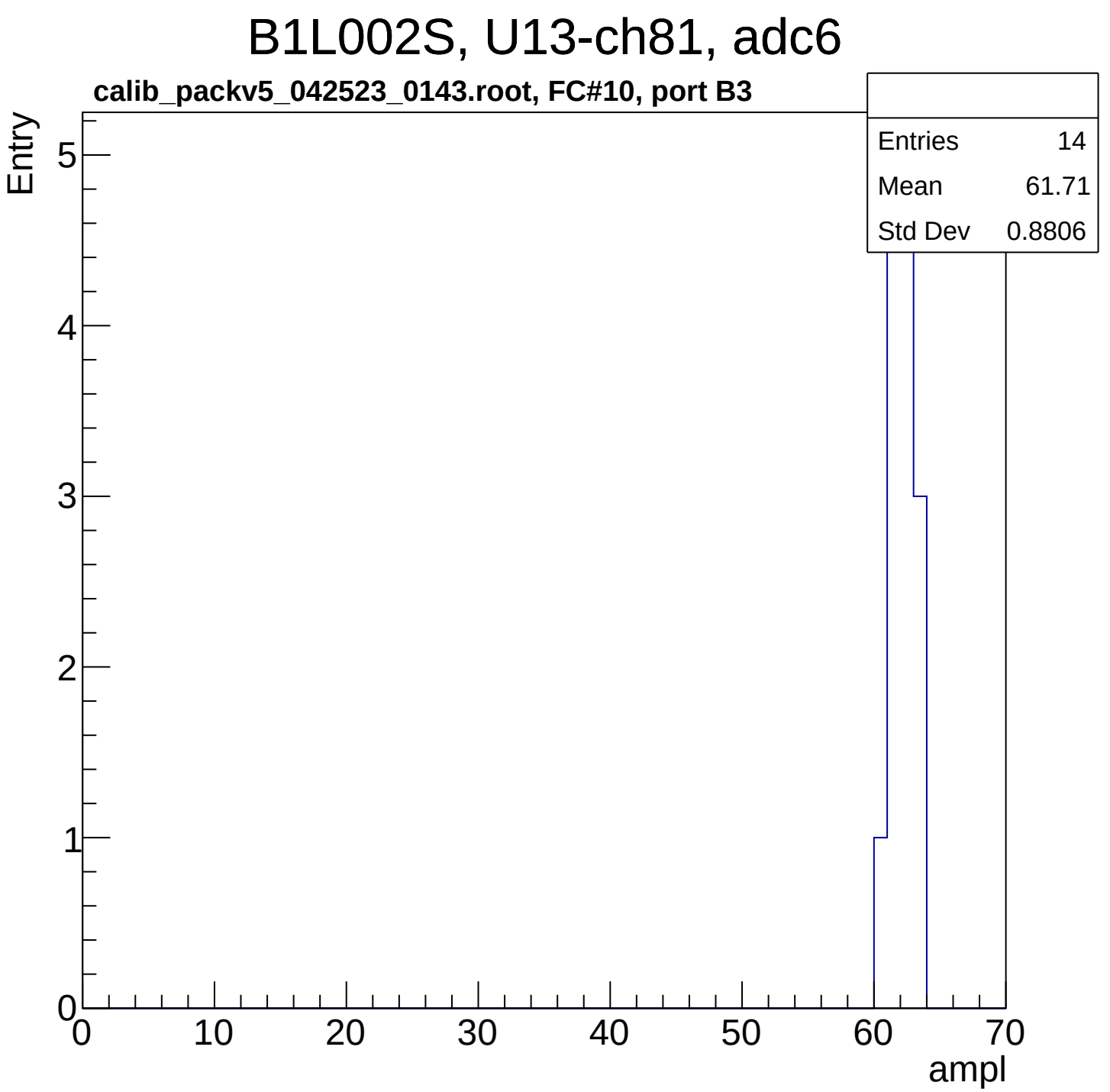
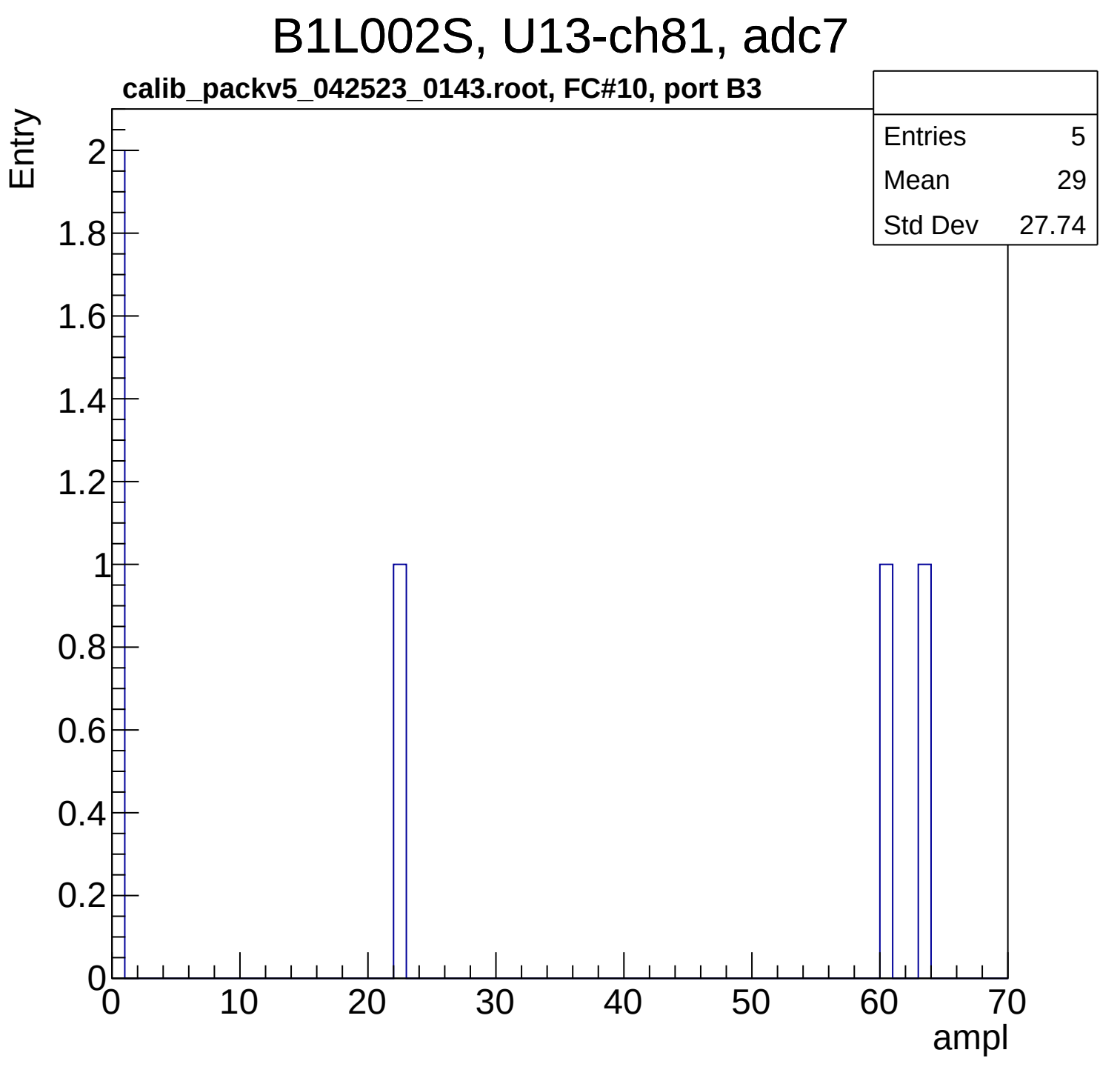

B1L002S, U13-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.71
Std Dev 0.8806
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 29
Std Dev 27.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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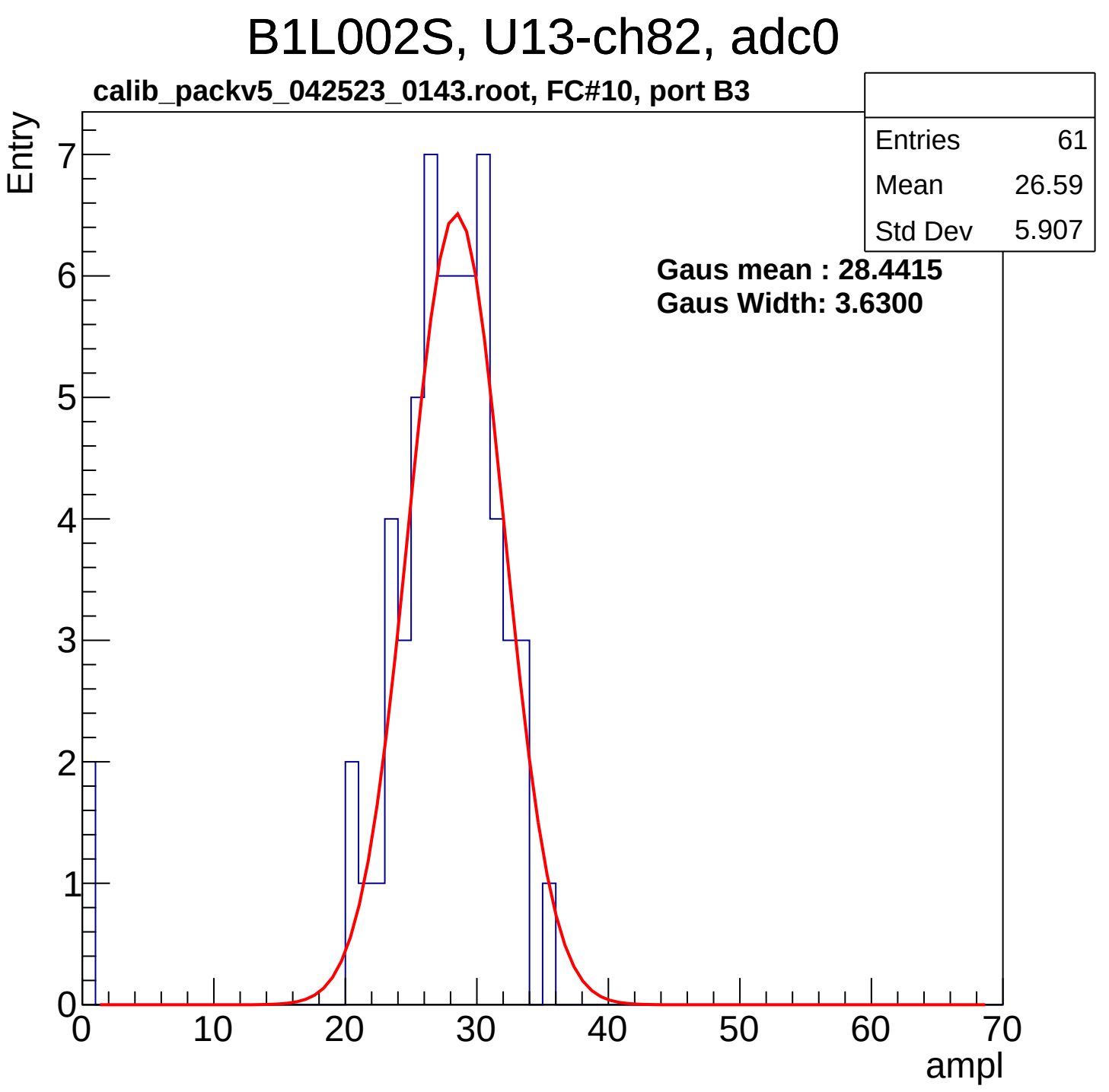
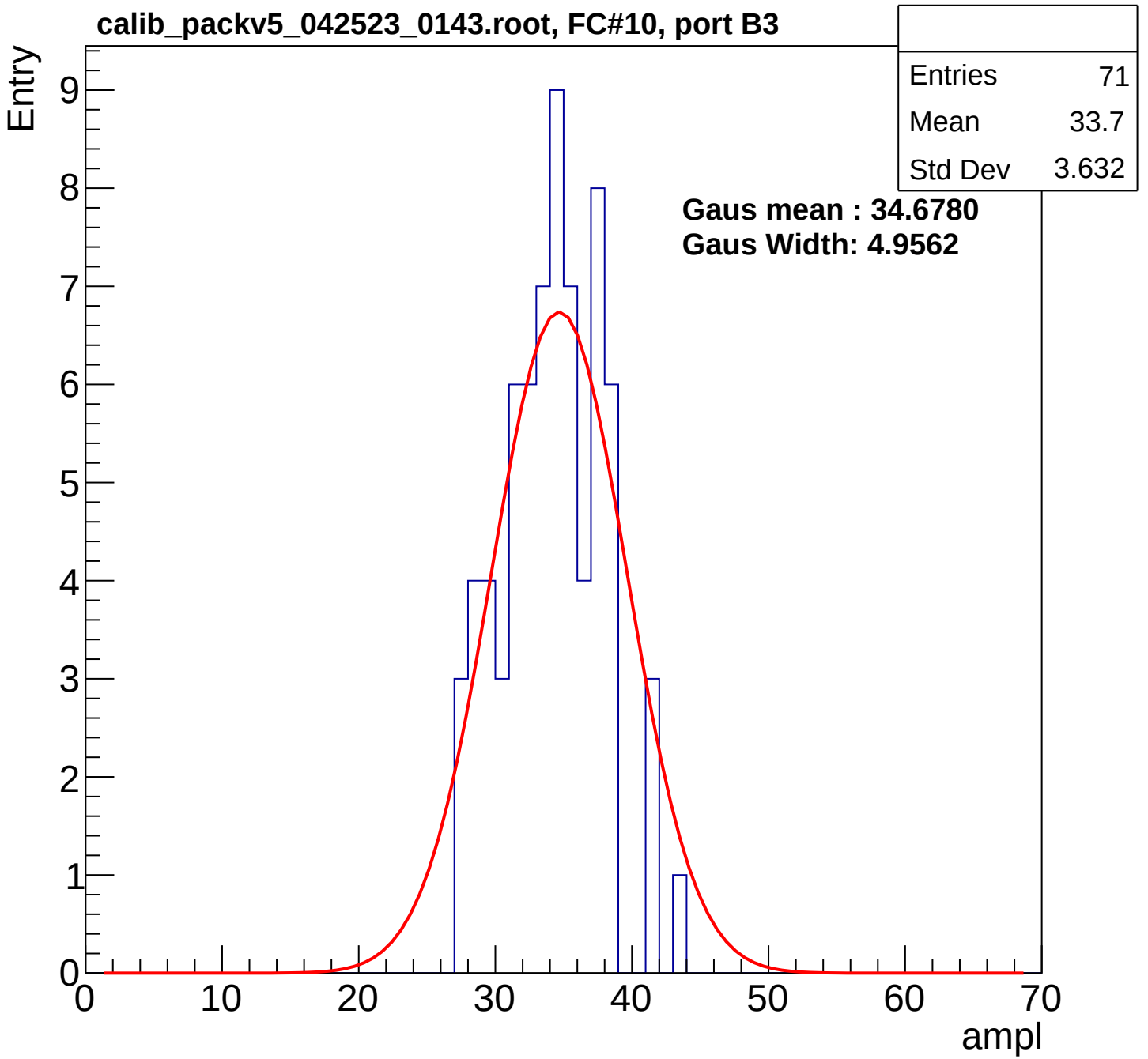
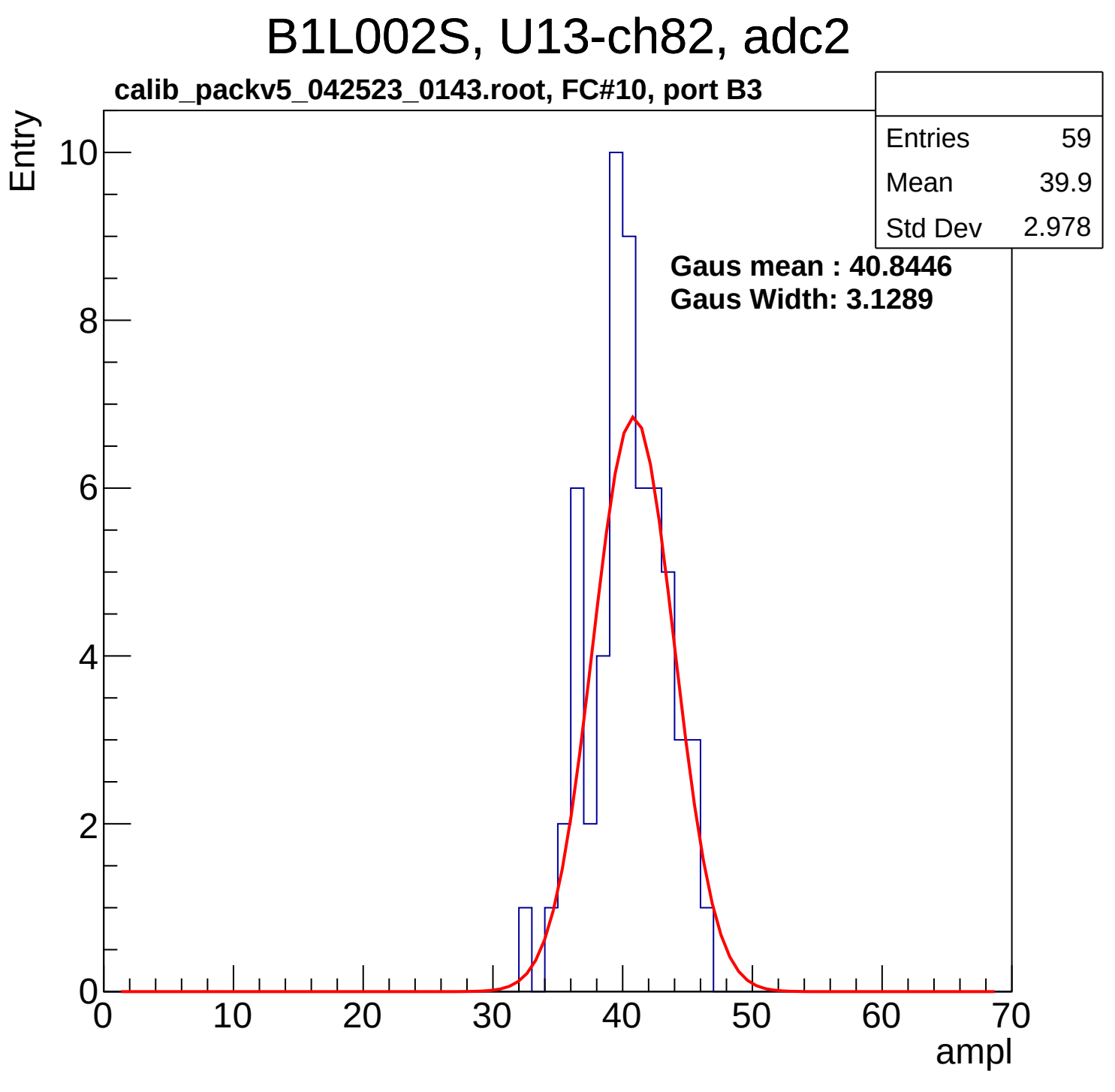
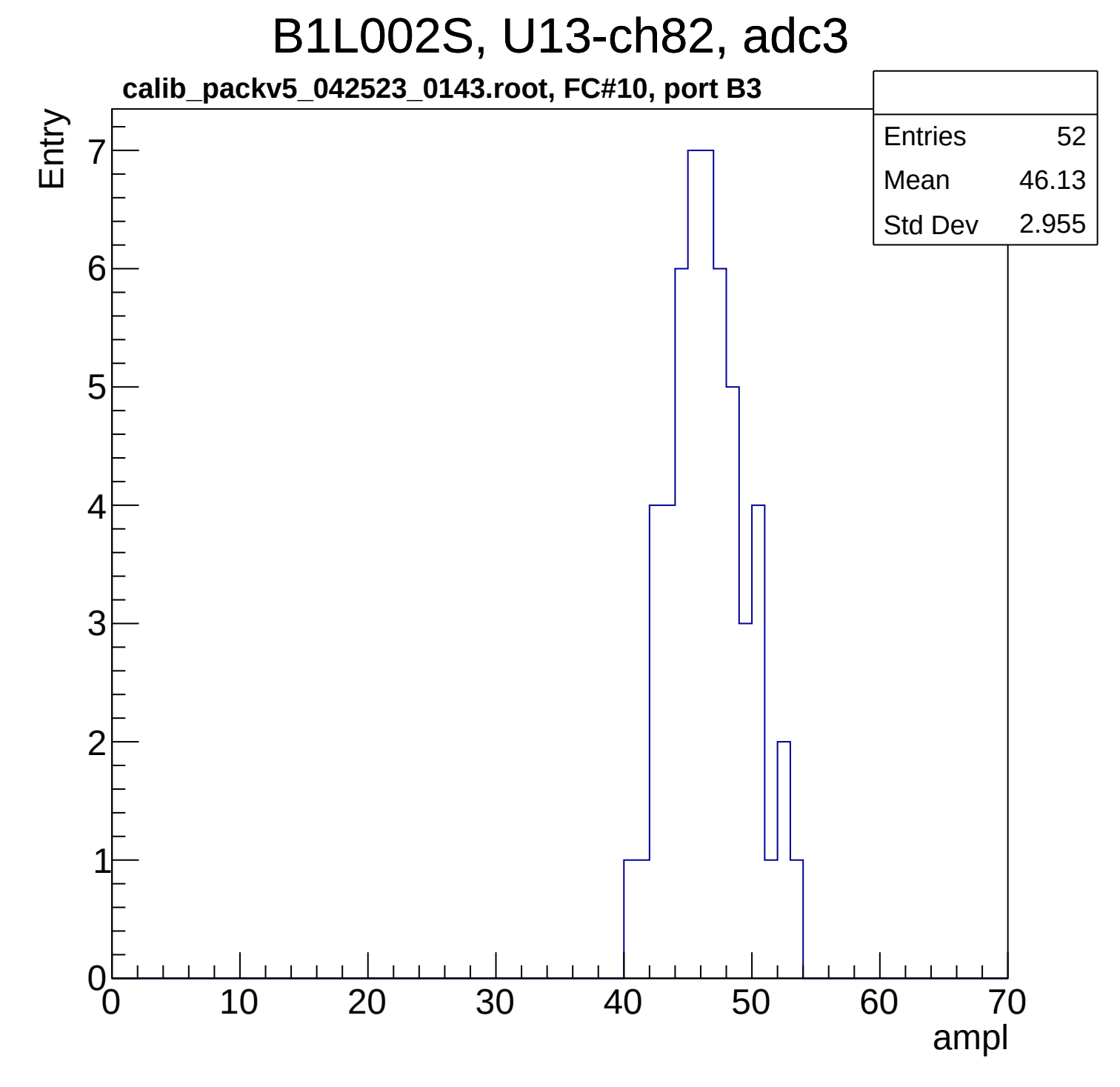
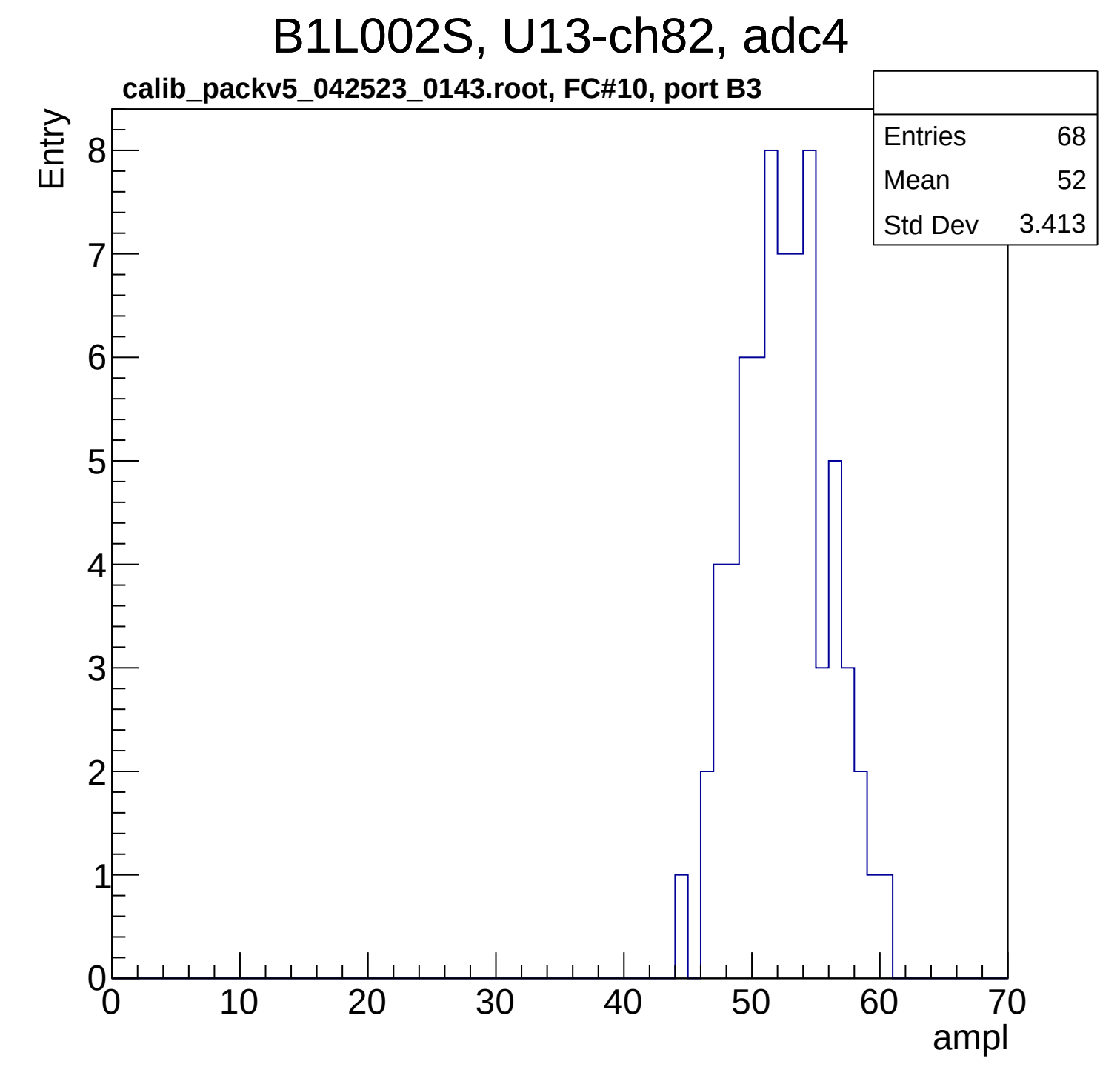

B1L002S, U13-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.59
Std Dev 5.907
Gaus mean : 28.4415
Gaus Width: 3.6300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.7
Std Dev 3.632
Gaus mean : 34.6780
Gaus Width: 4.9562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.9
Std Dev 2.978
Gaus mean : 40.8446
Gaus Width: 3.1289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 46.13
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52
Std Dev 3.413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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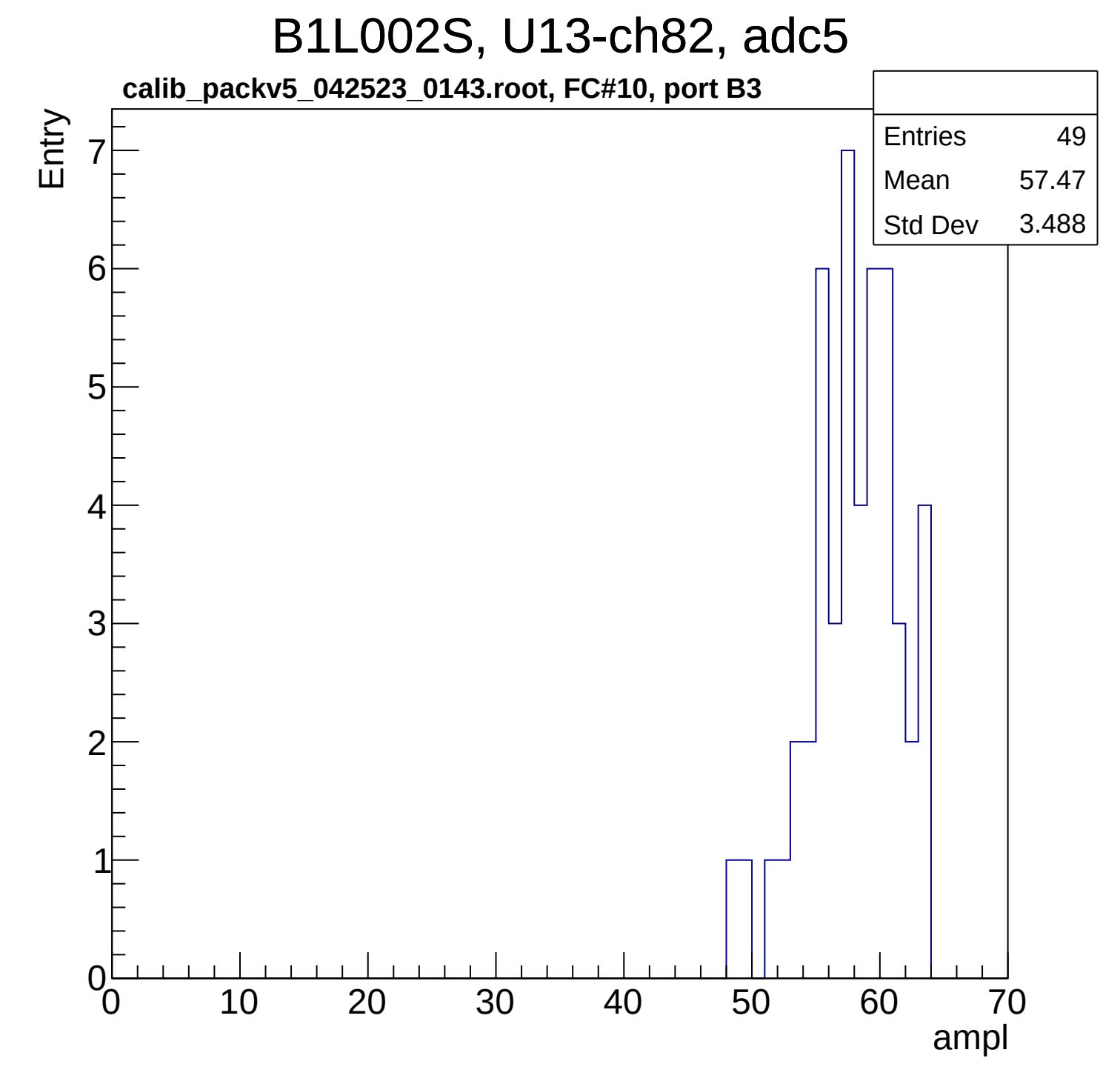
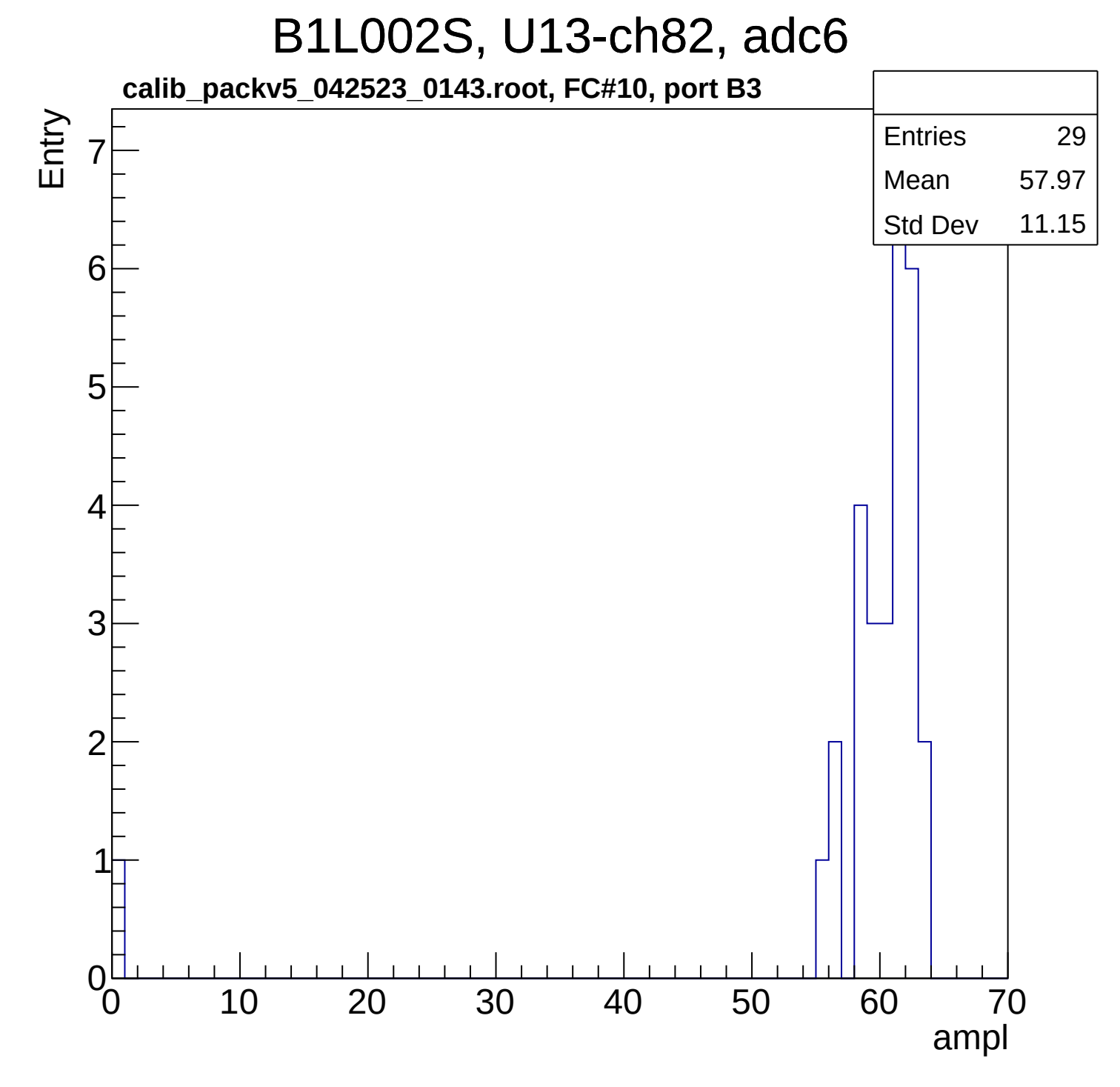
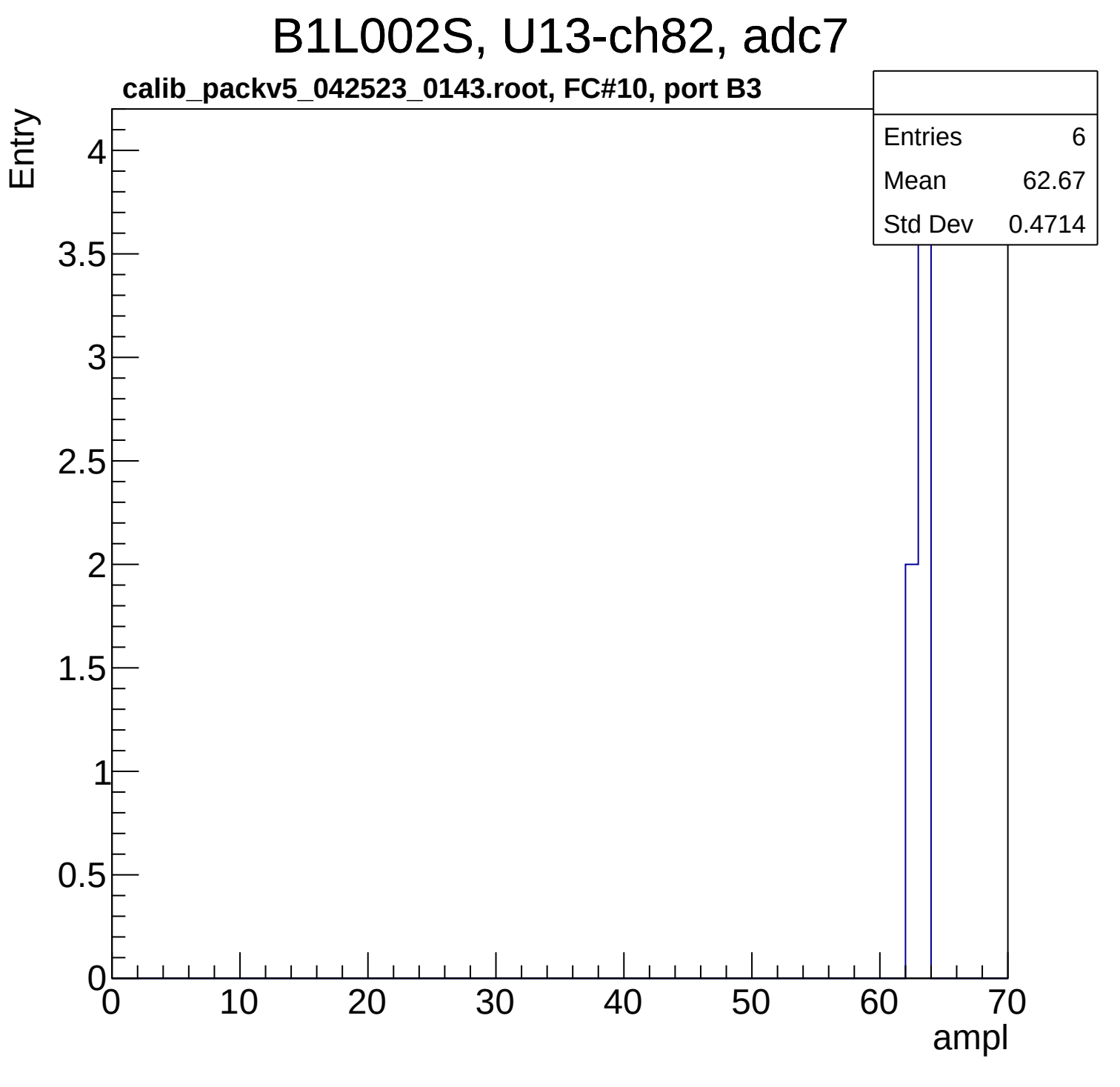
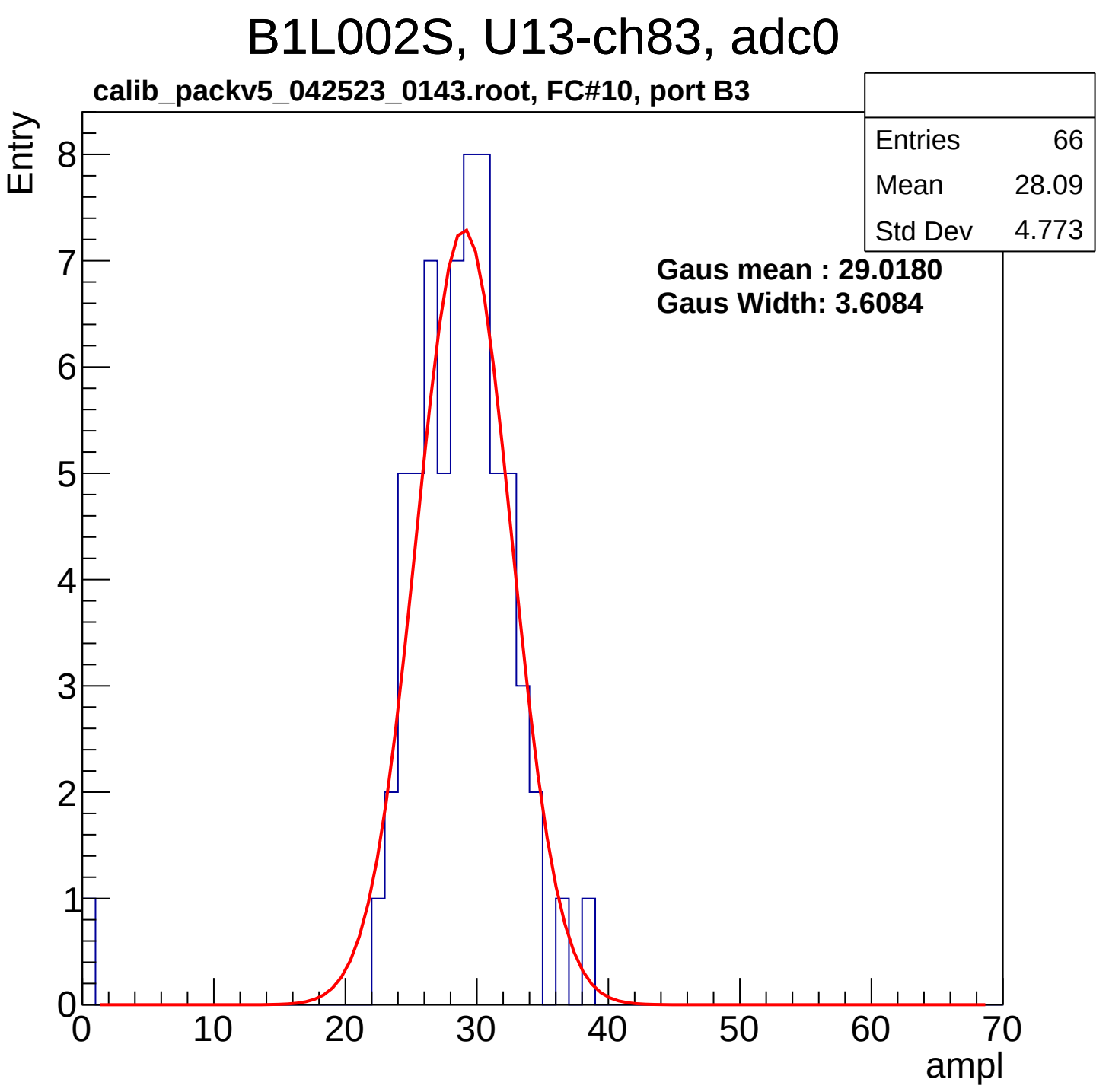

B1L002S, U13-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.47
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 57.97
Std Dev 11.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.09
Std Dev 4.773
Gaus mean : 29.0180
Gaus Width: 3.6084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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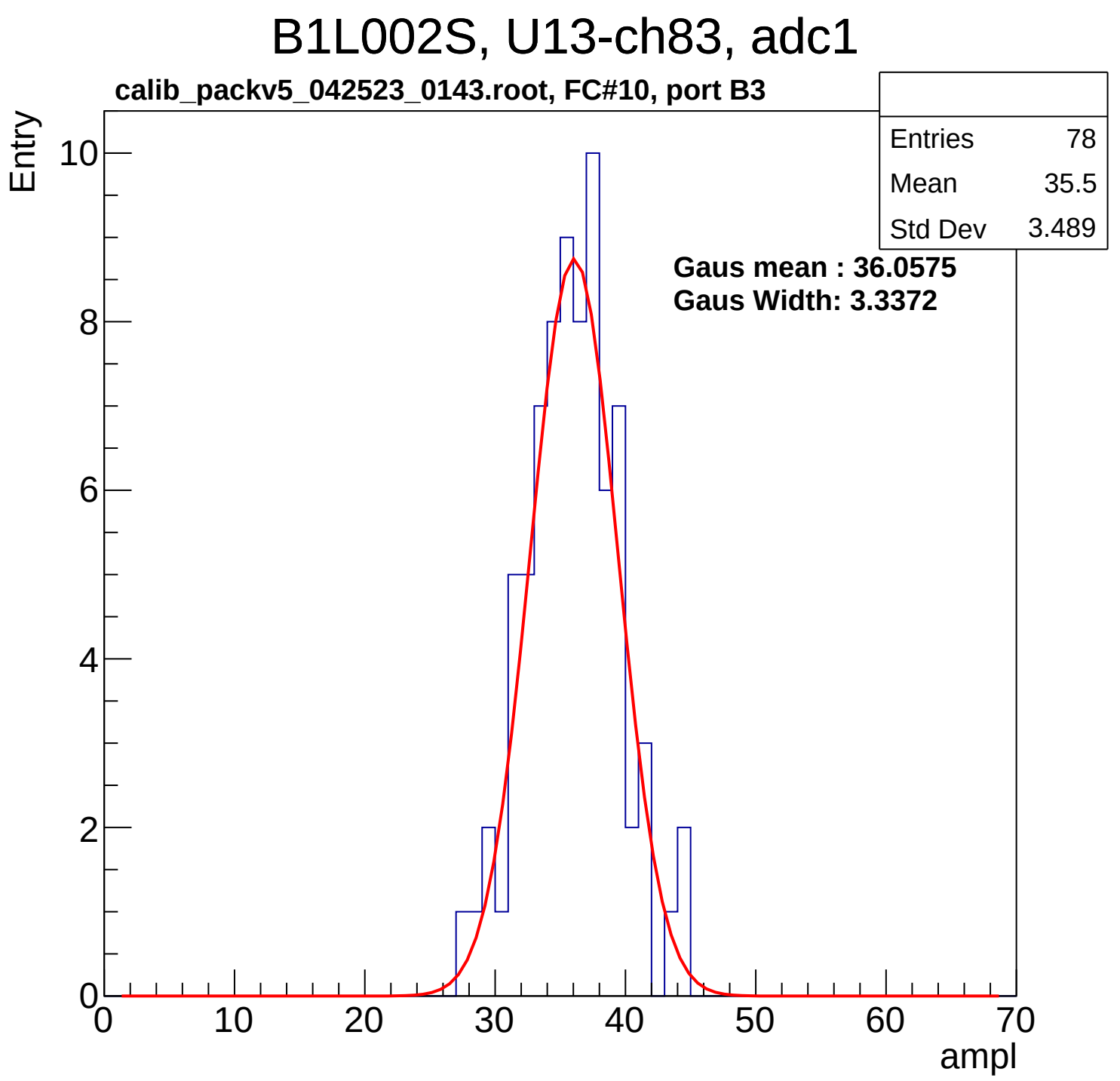
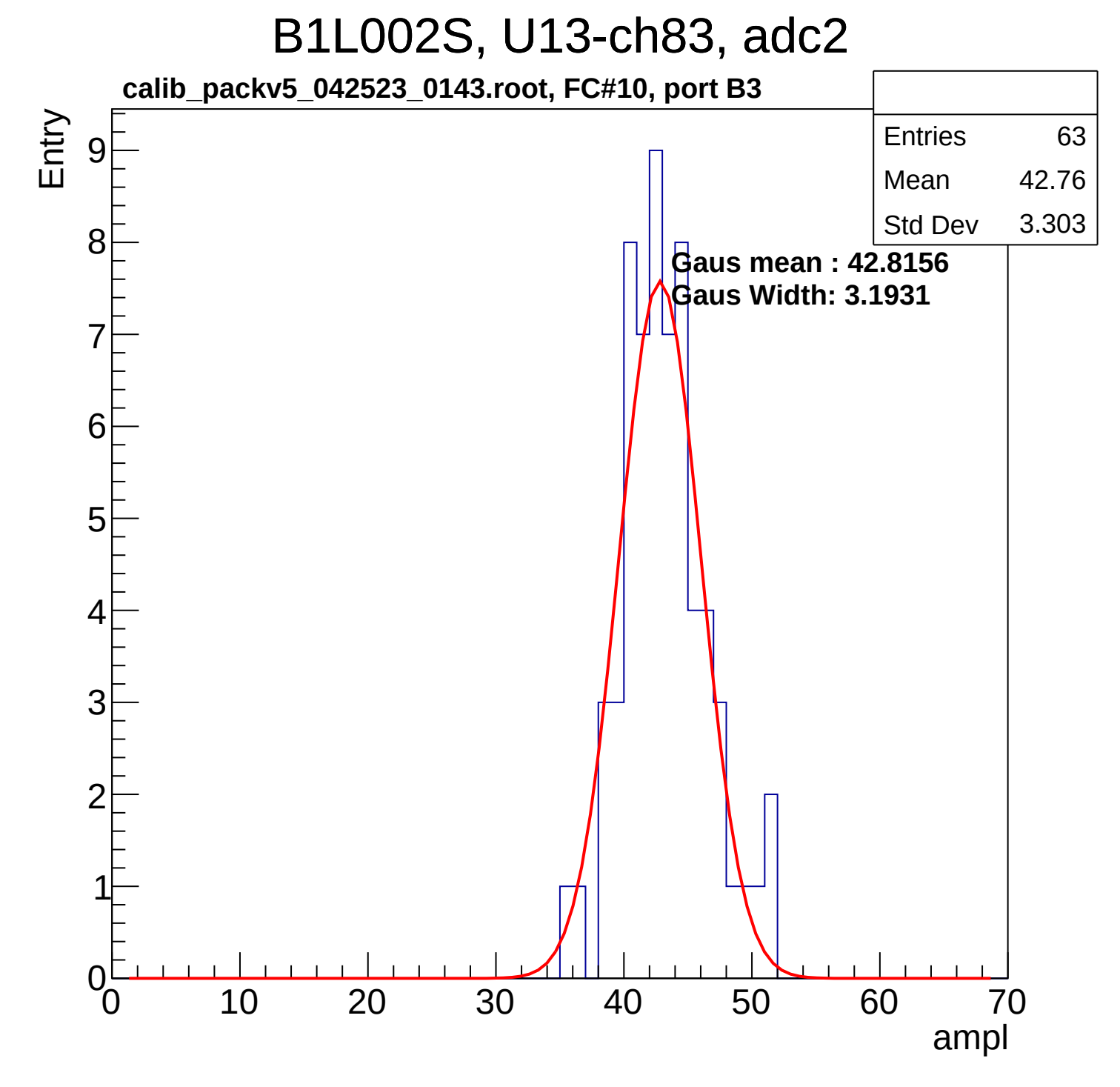
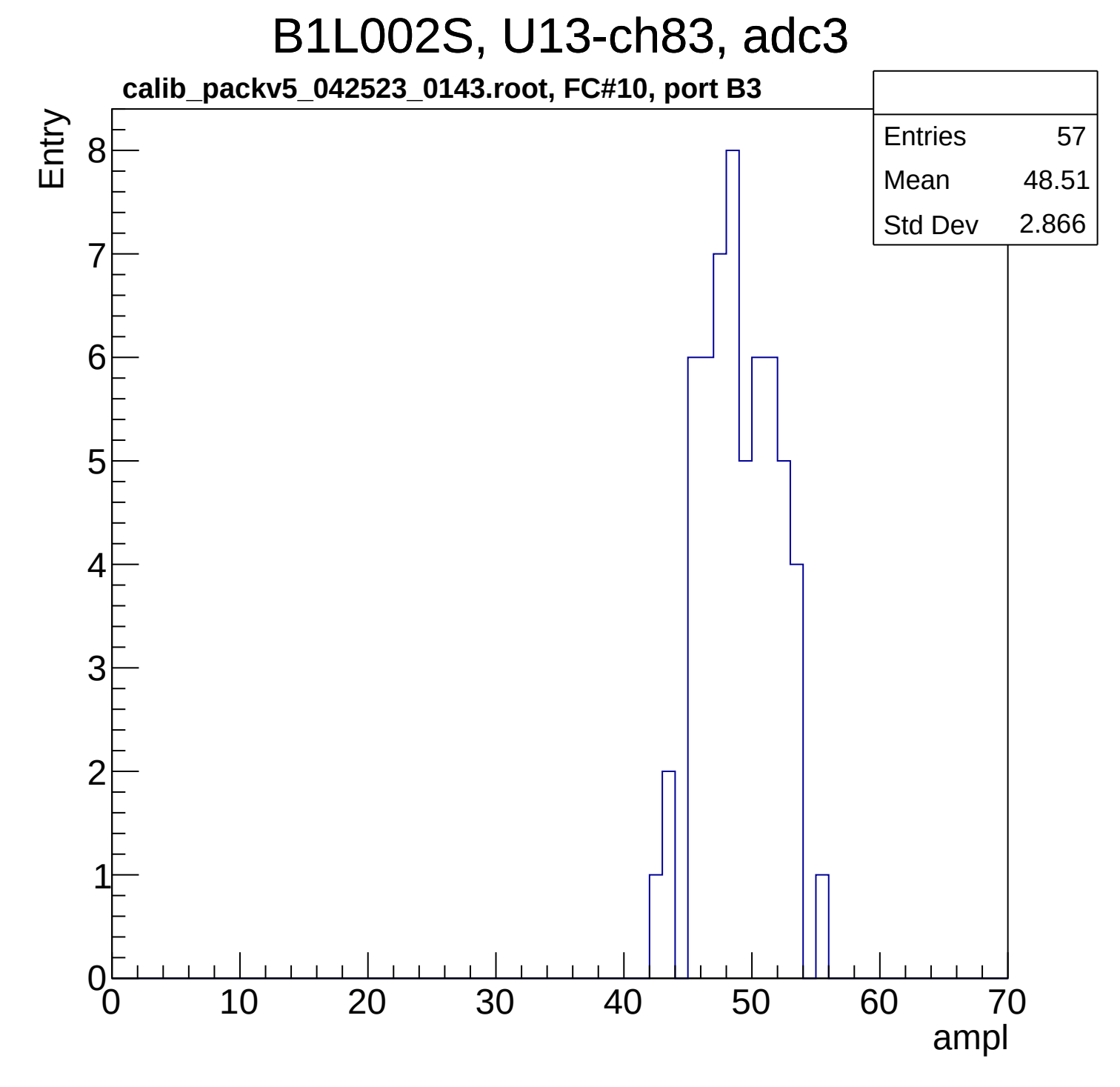
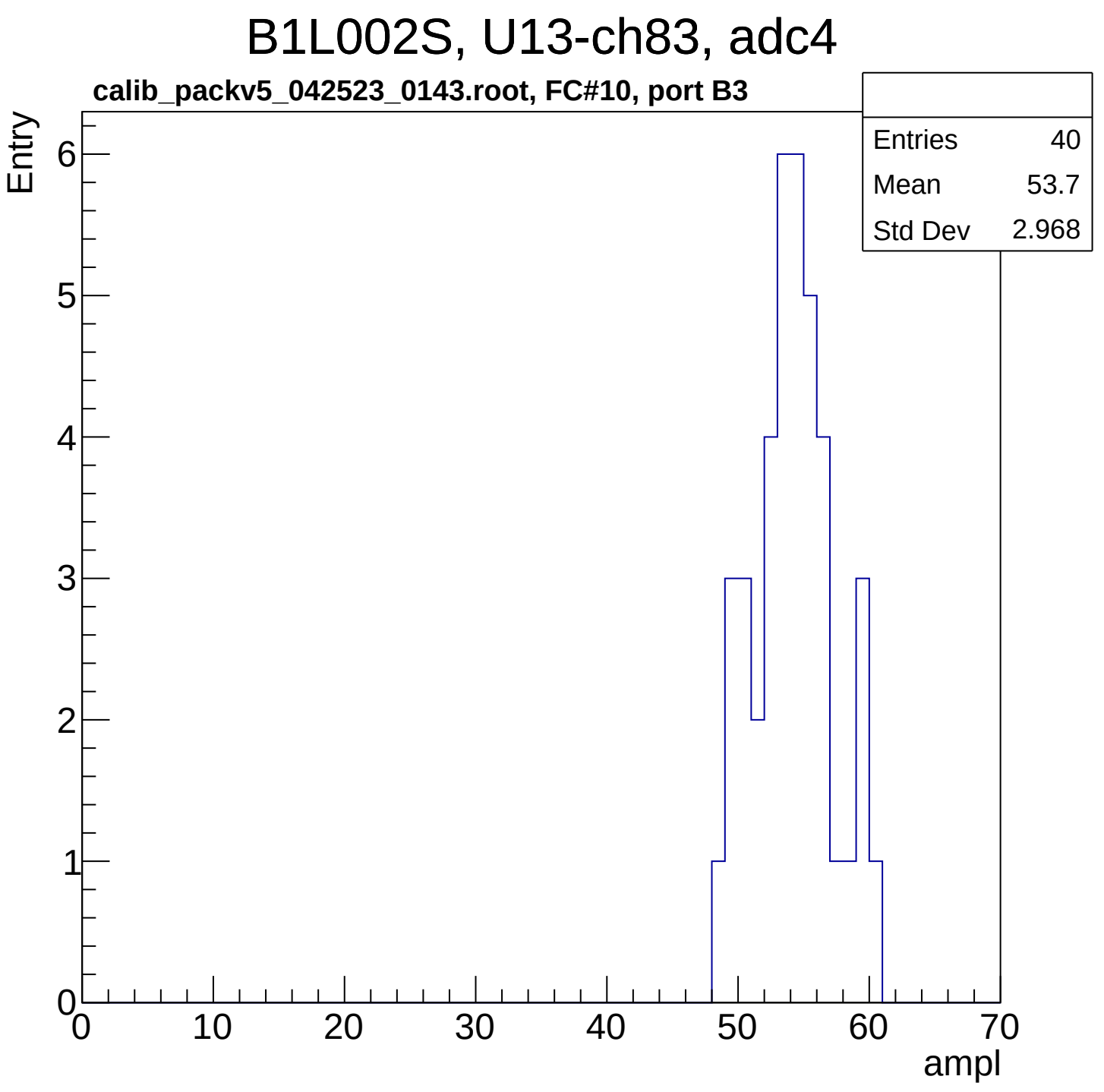
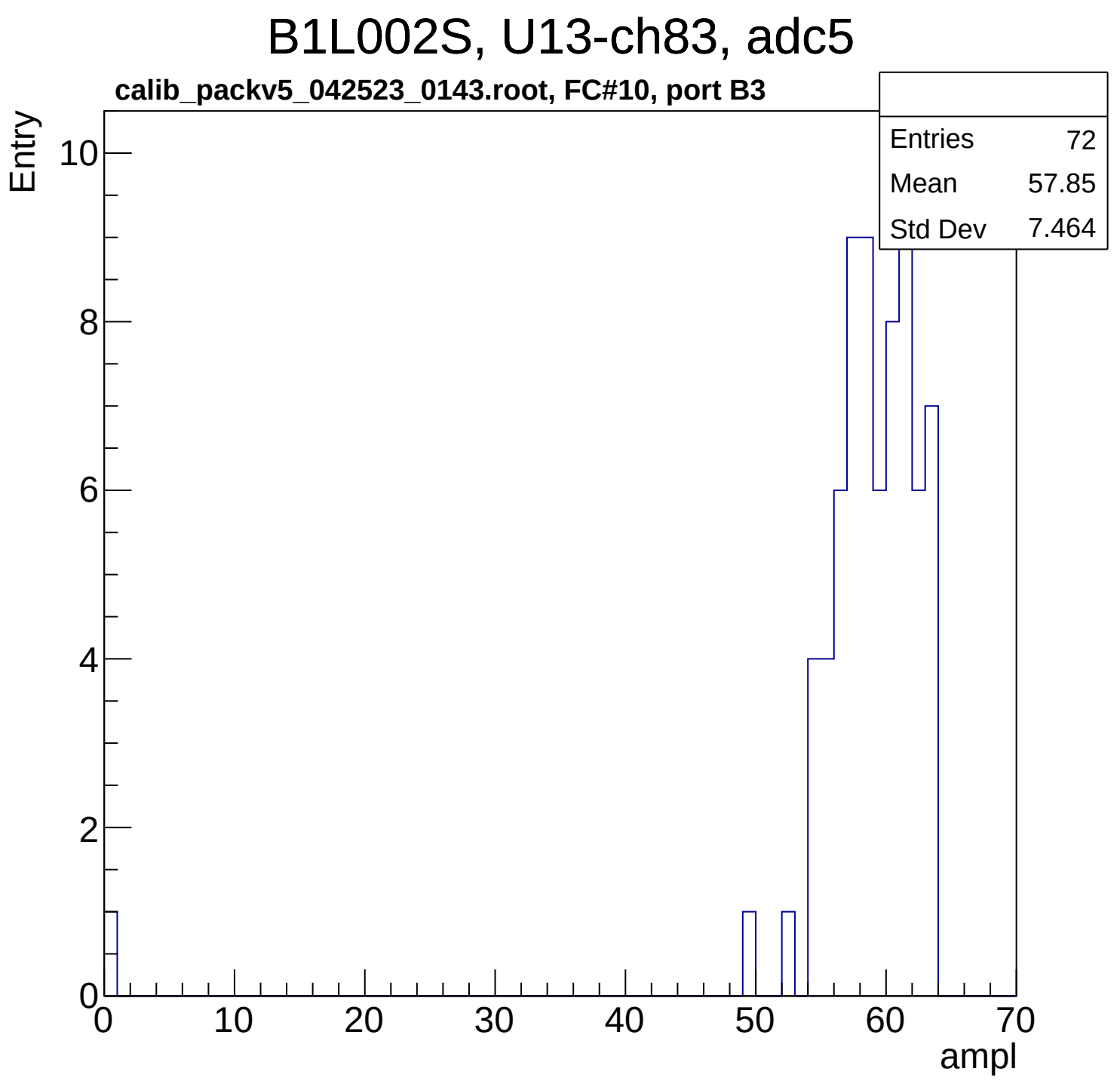

B1L002S, U13-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 35.5
Std Dev 3.489
Gaus mean : 36.0575
Gaus Width: 3.3372
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.76
Std Dev 3.303
Gaus mean : 42.8156
Gaus Width: 3.1931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.51
Std Dev 2.866
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 53.7
Std Dev 2.968
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 57.85
Std Dev 7.464
Entry
ampl
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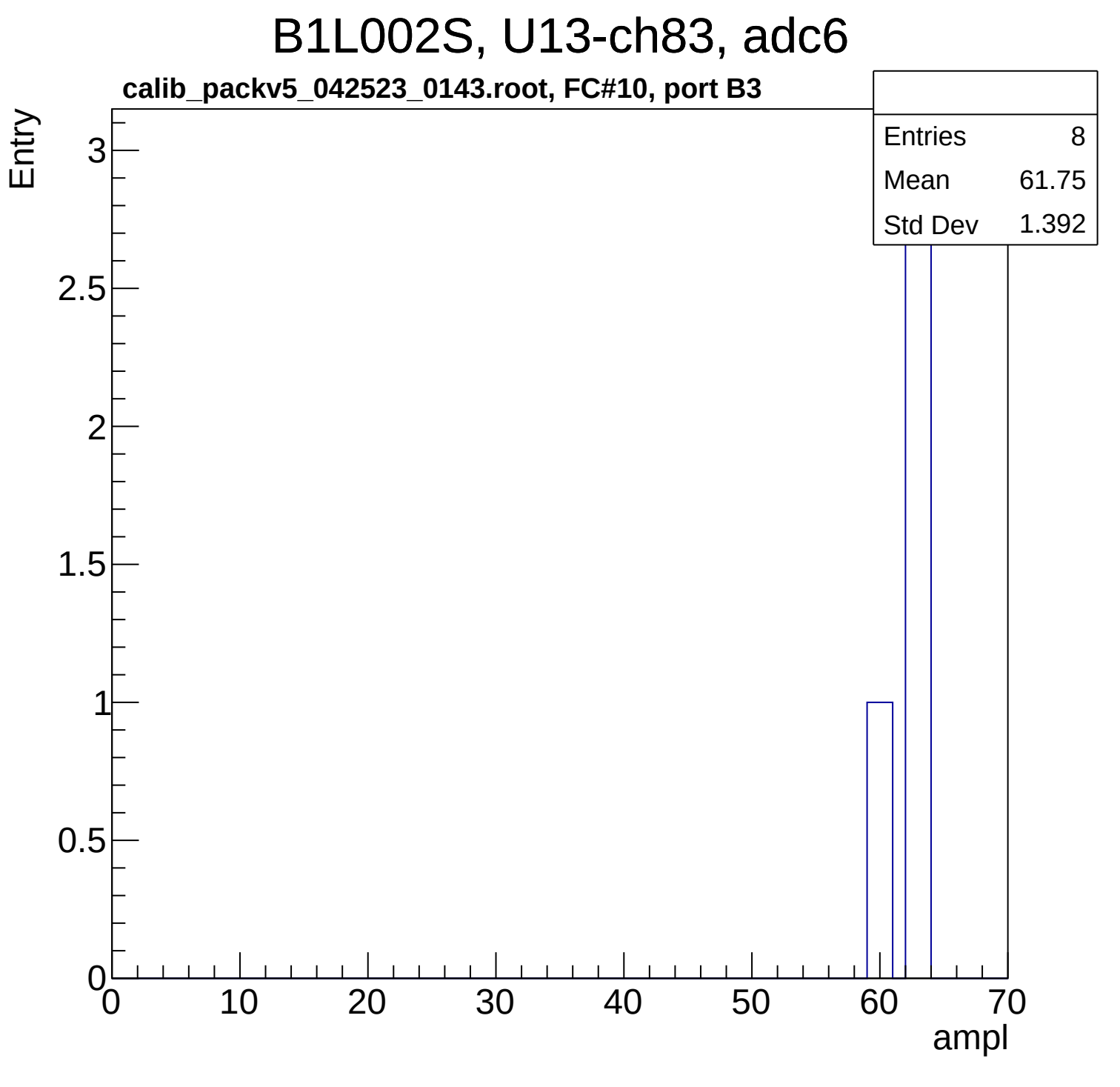
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61.75
Std Dev 1.392
Entry
ampl
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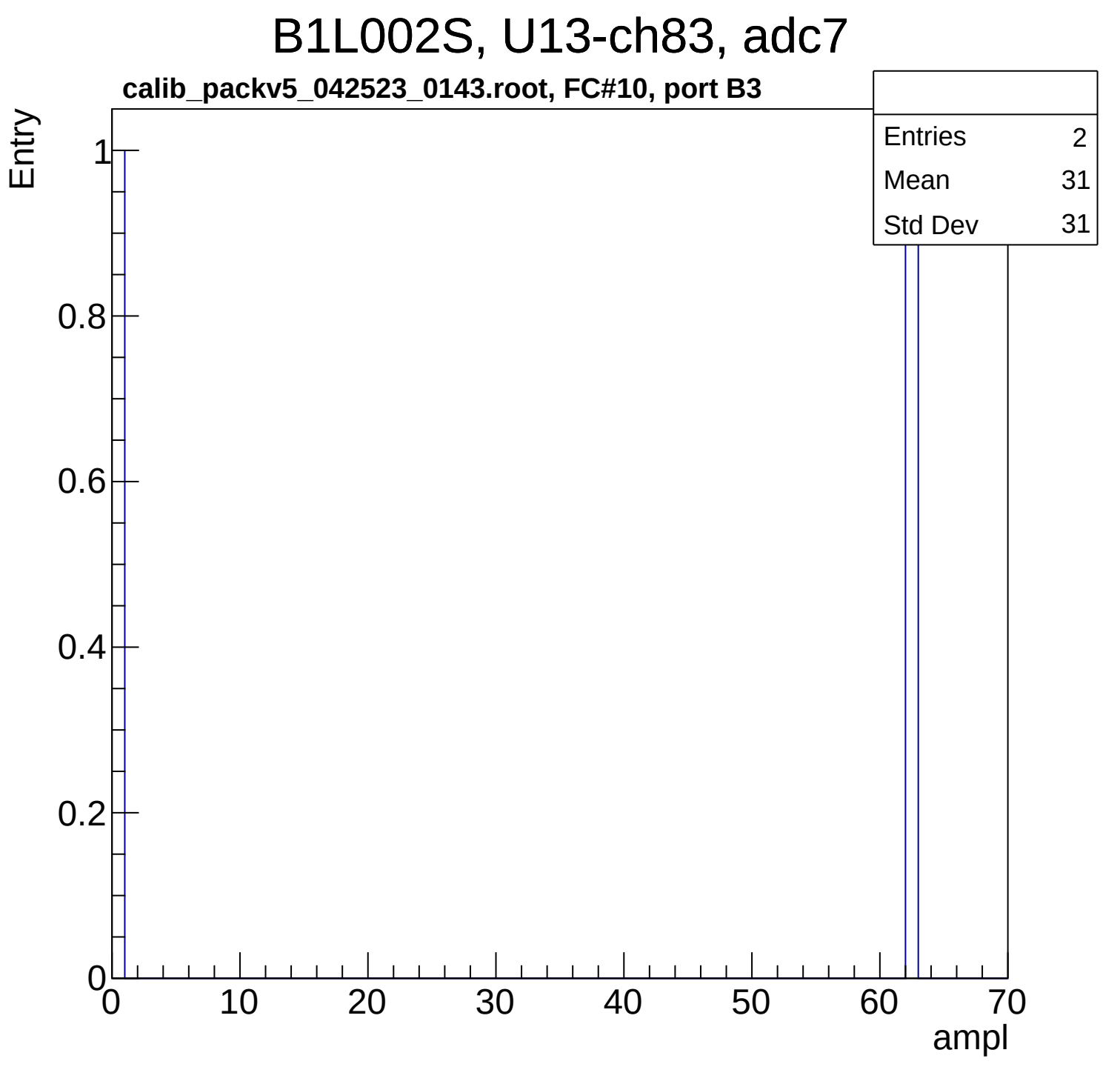
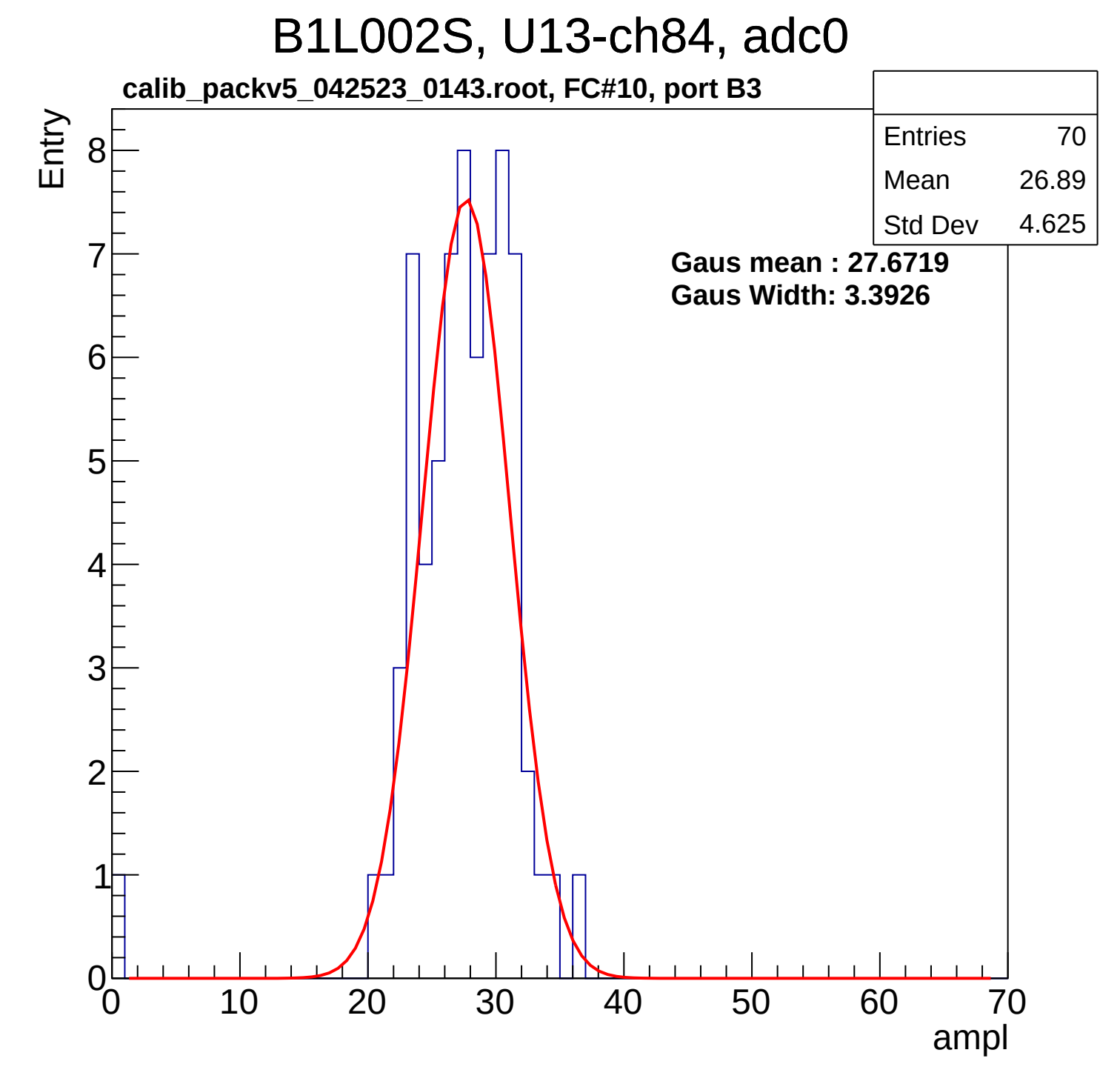
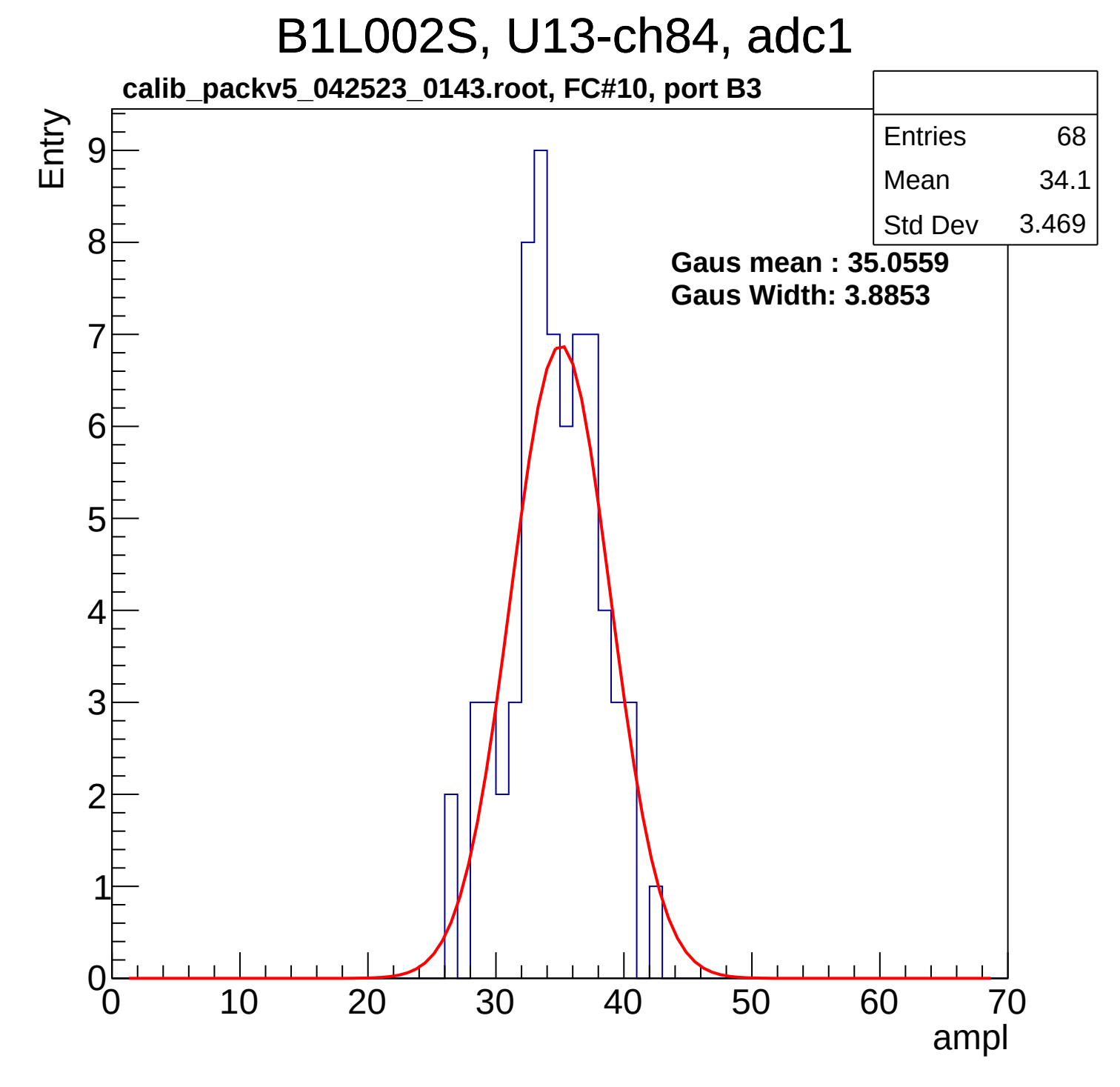
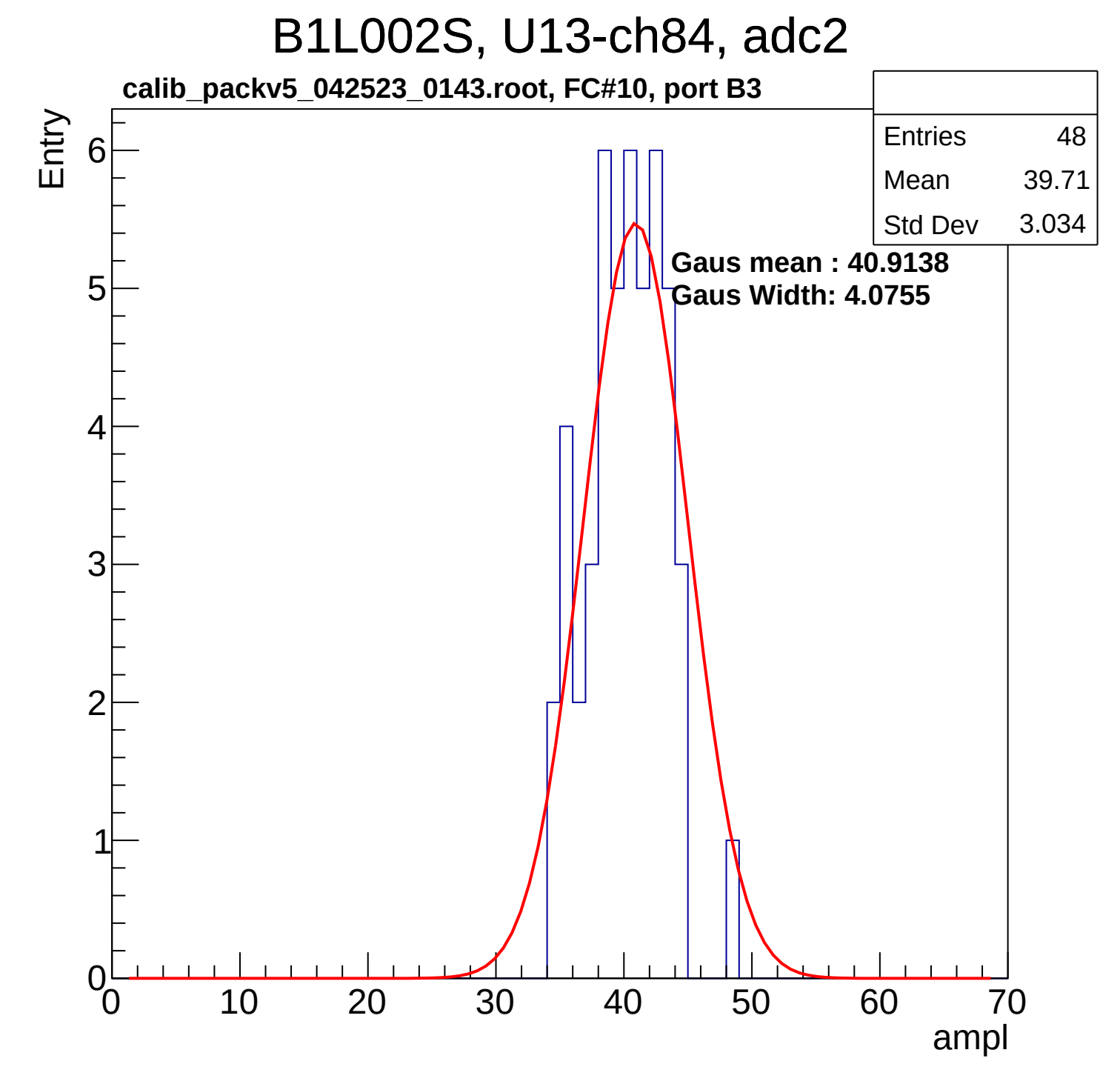
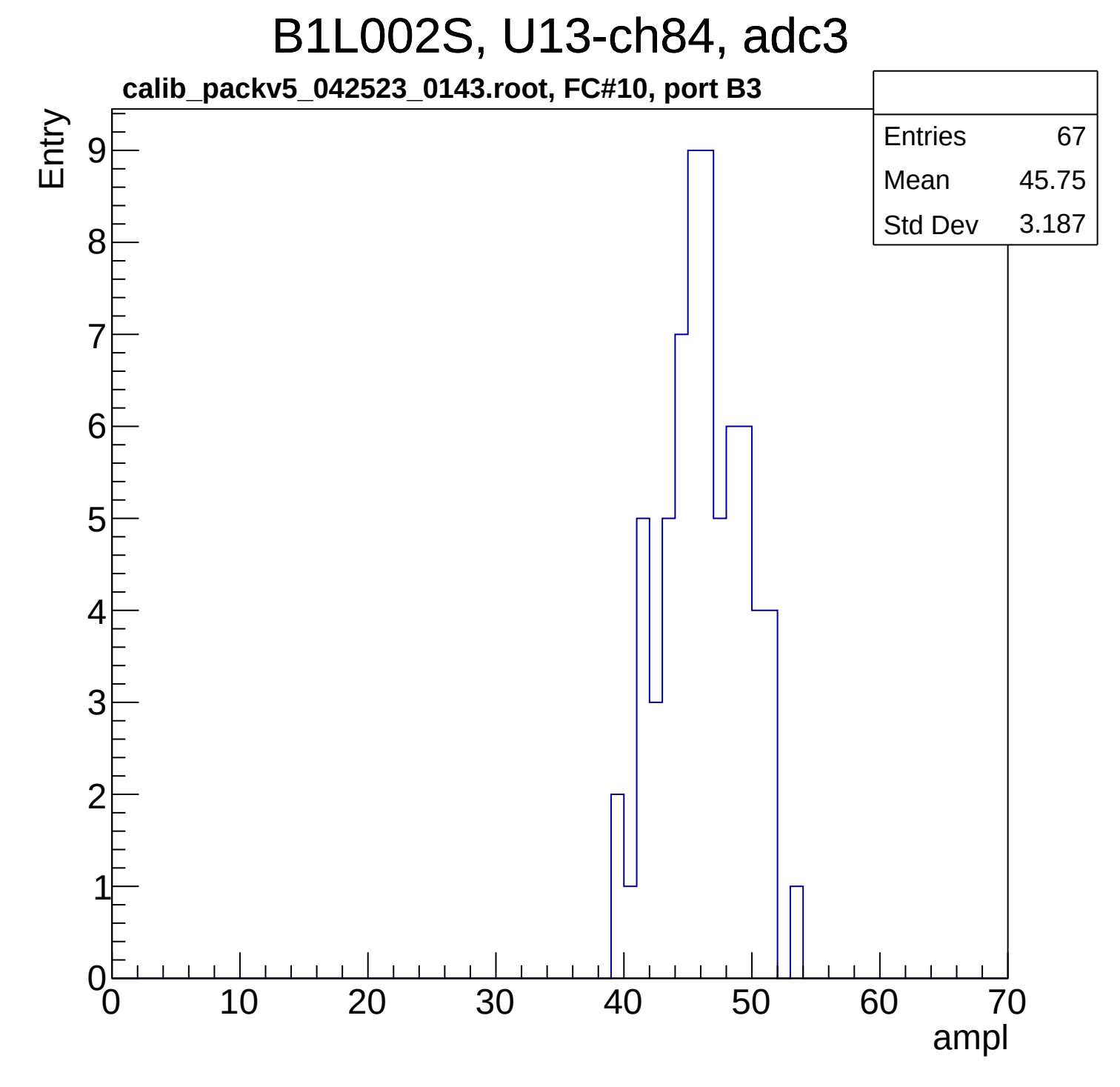
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.89
Std Dev 4.625
Gaus mean : 27.6719
Gaus Width: 3.3926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.1
Std Dev 3.469
Gaus mean : 35.0559
Gaus Width: 3.8853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 39.71
Std Dev 3.034
Gaus mean : 40.9138
Gaus Width: 4.0755
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.75
Std Dev 3.187
Entry
ampl
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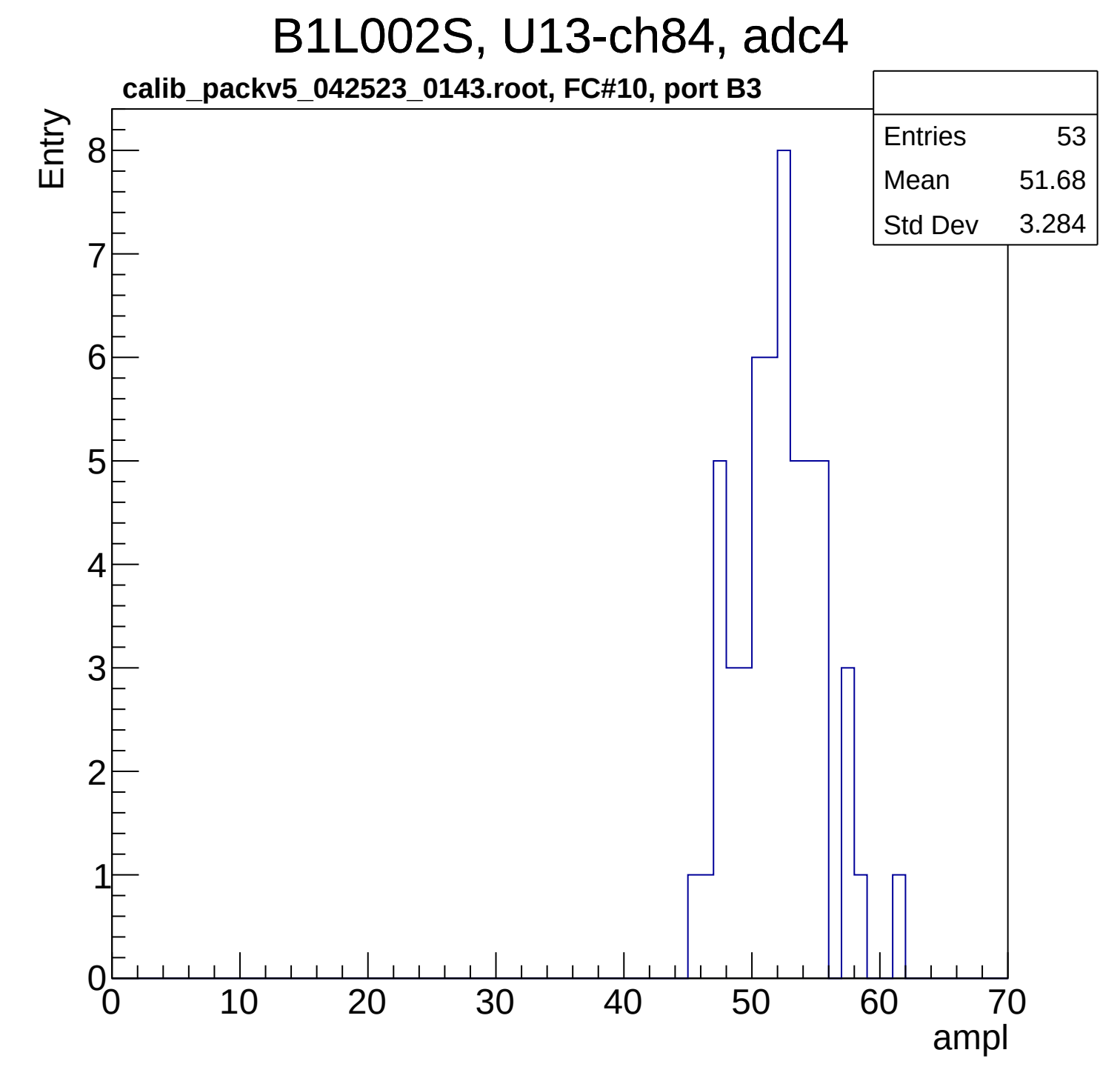
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.68
Std Dev 3.284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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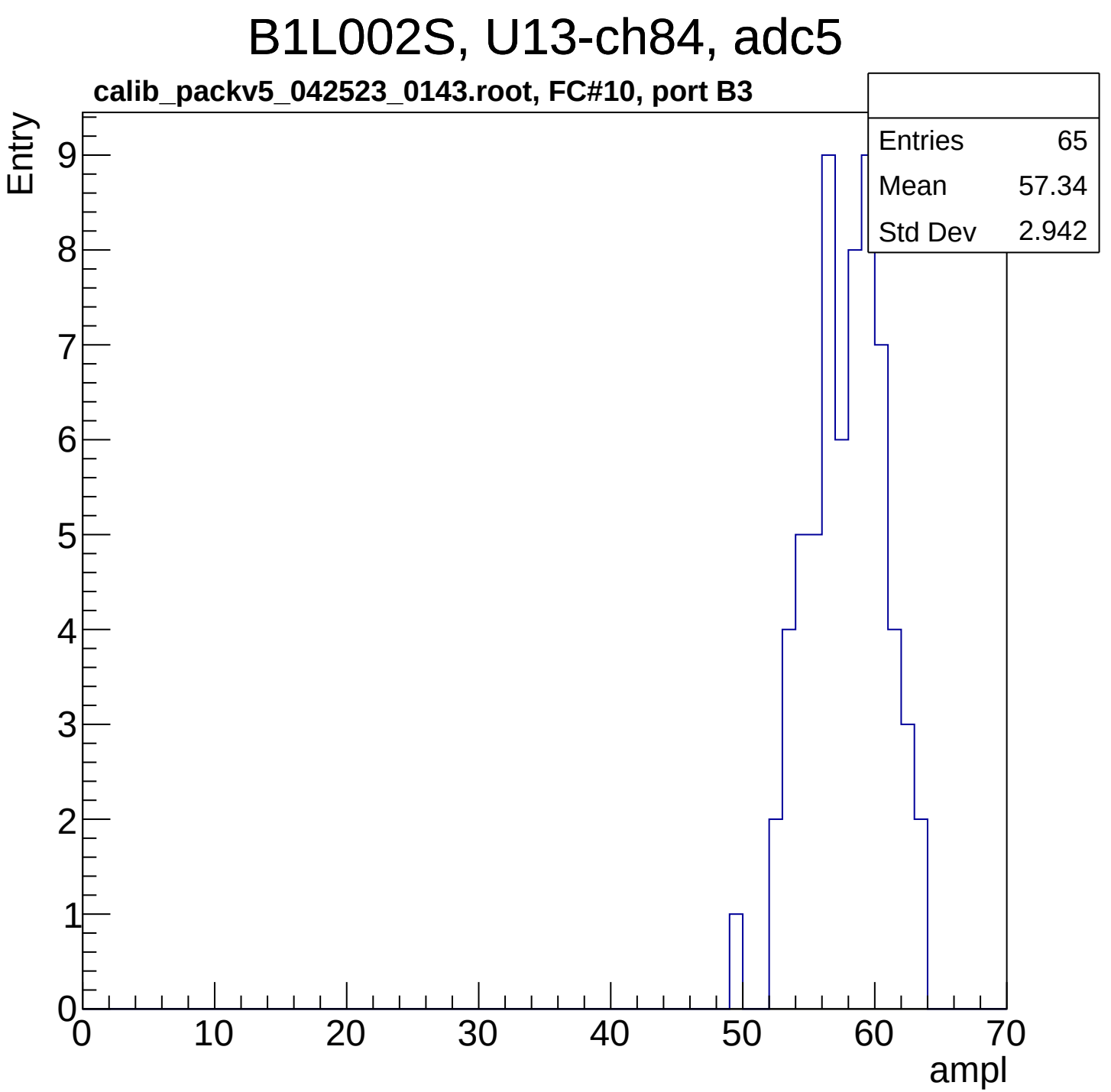
B1L002S, U13-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.34
Std Dev 2.942
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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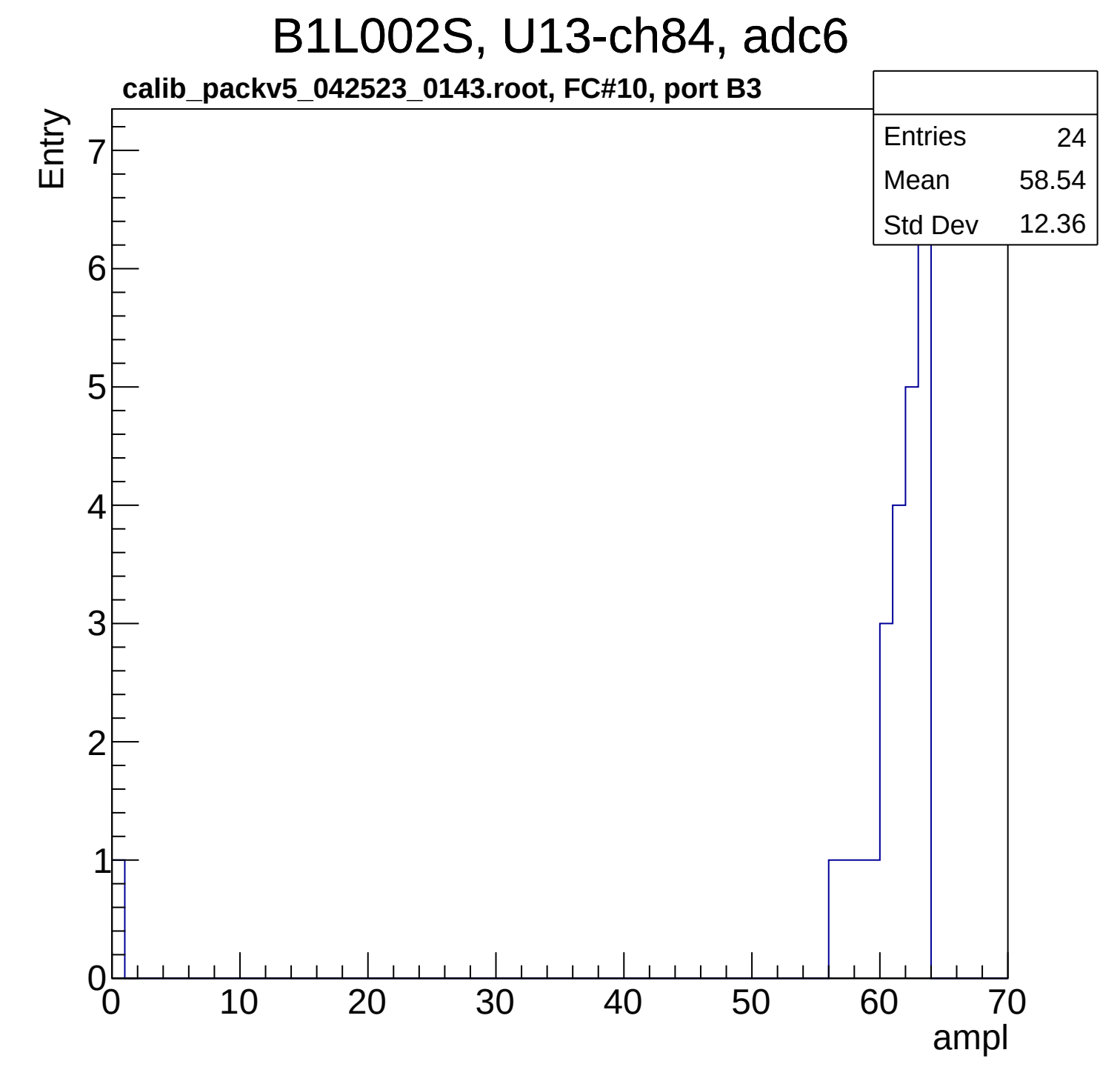
B1L002S, U13-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.54
Std Dev 12.36
Entry
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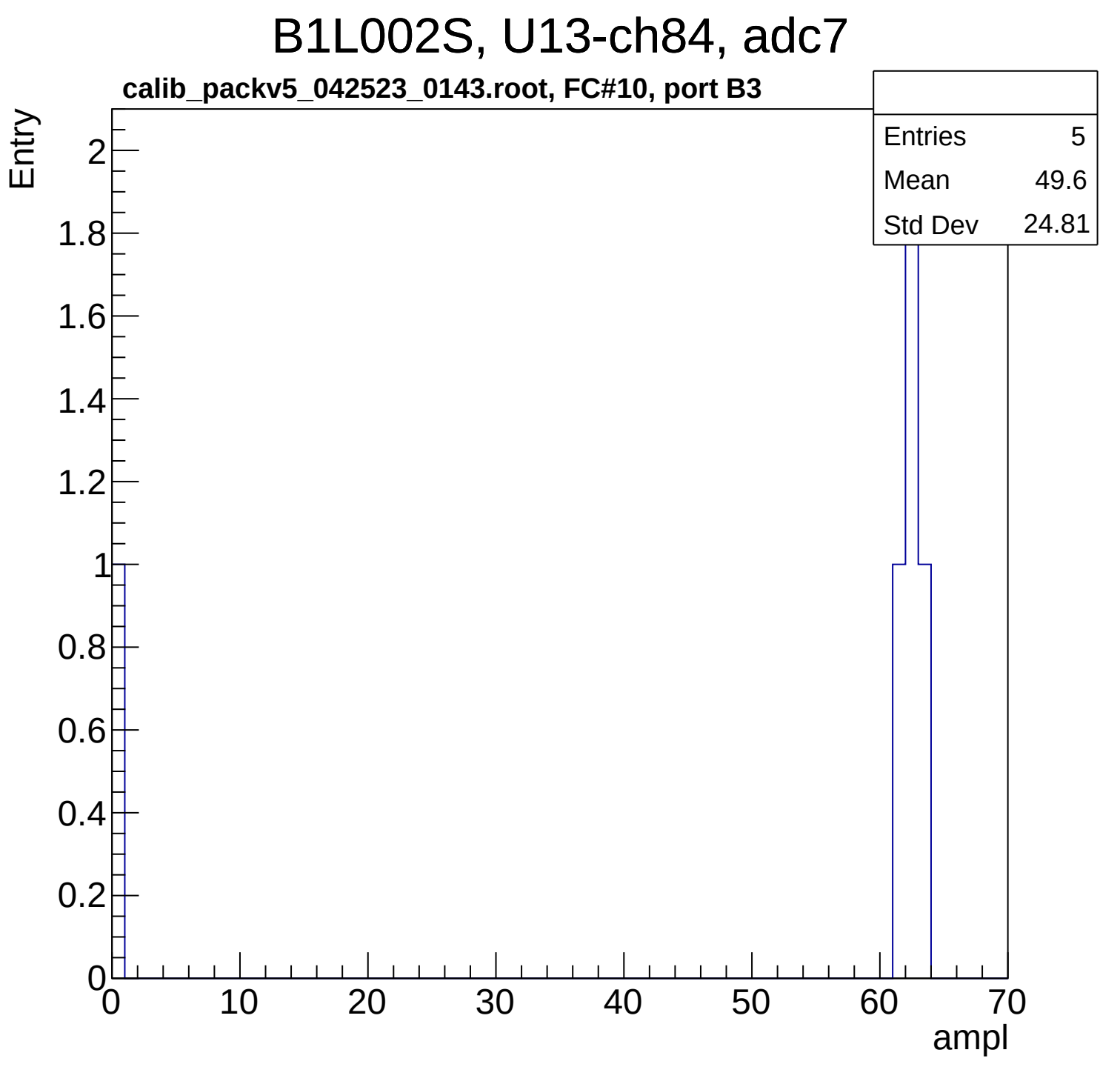
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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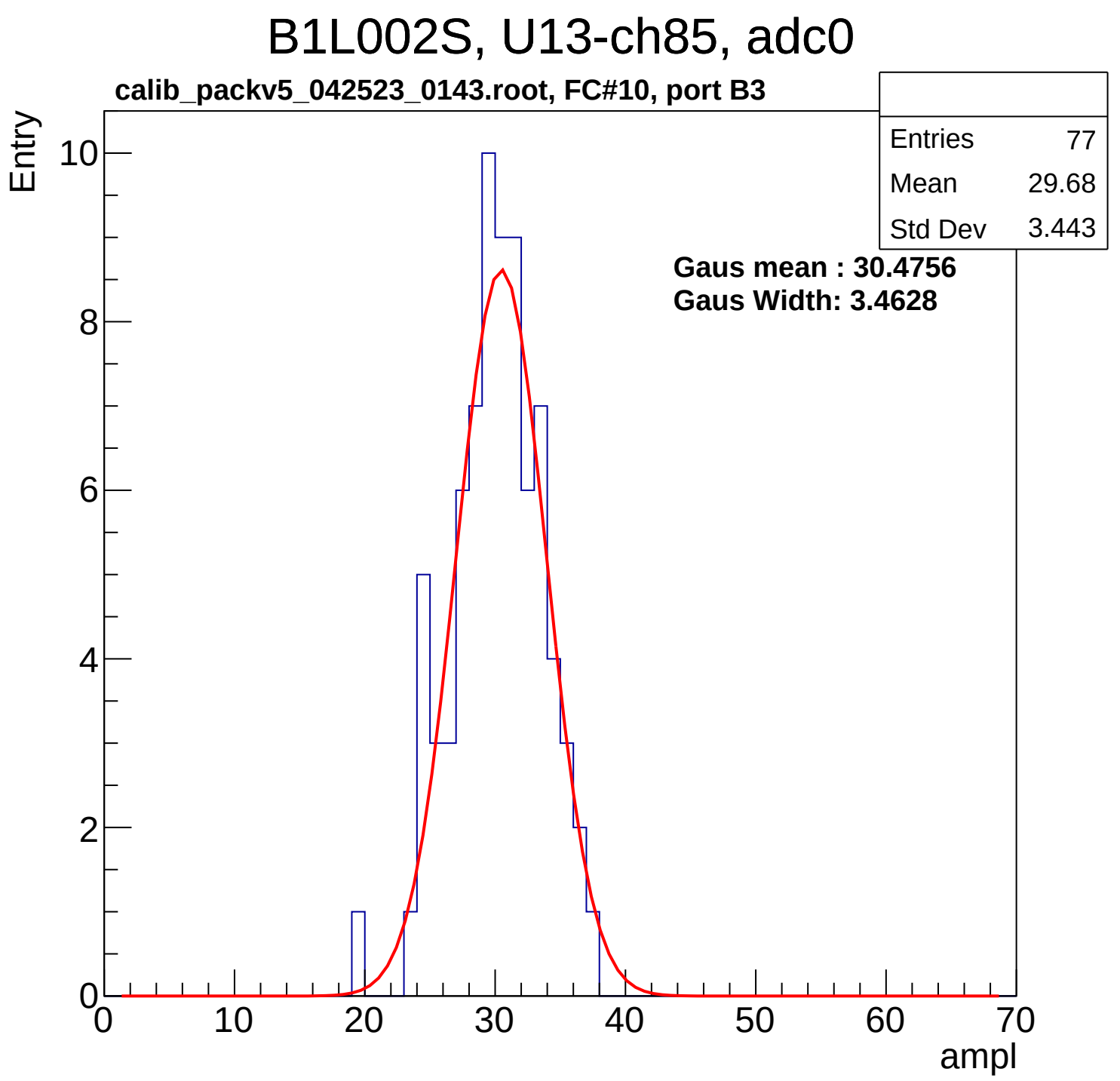
B1L002S, U13-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.68
Std Dev 3.443
Gaus mean : 30.4756
Gaus Width: 3.4628
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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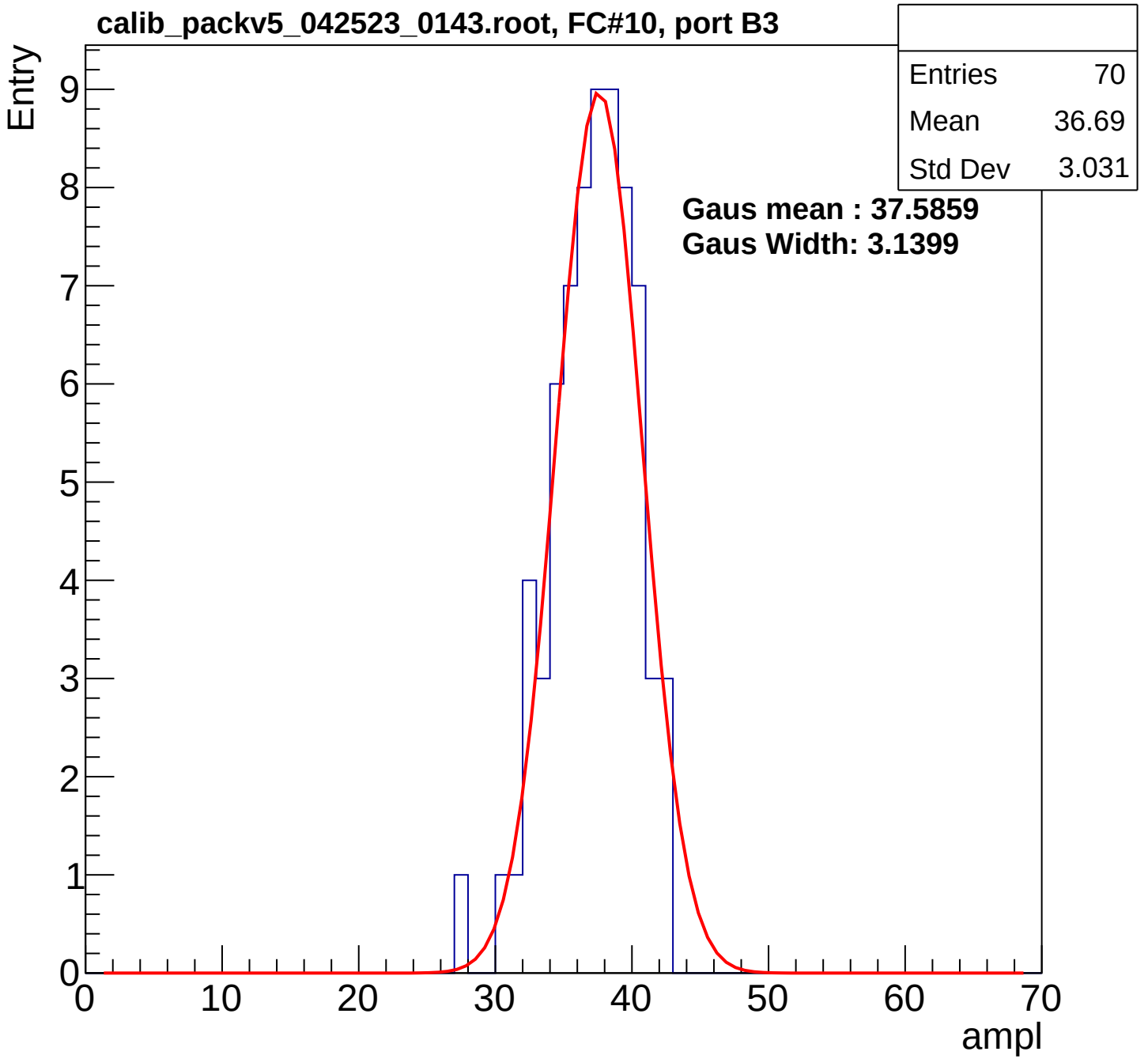
B1L002S, U13-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 36.69
Std Dev 3.031
Gaus mean : 37.5859
Gaus Width: 3.1399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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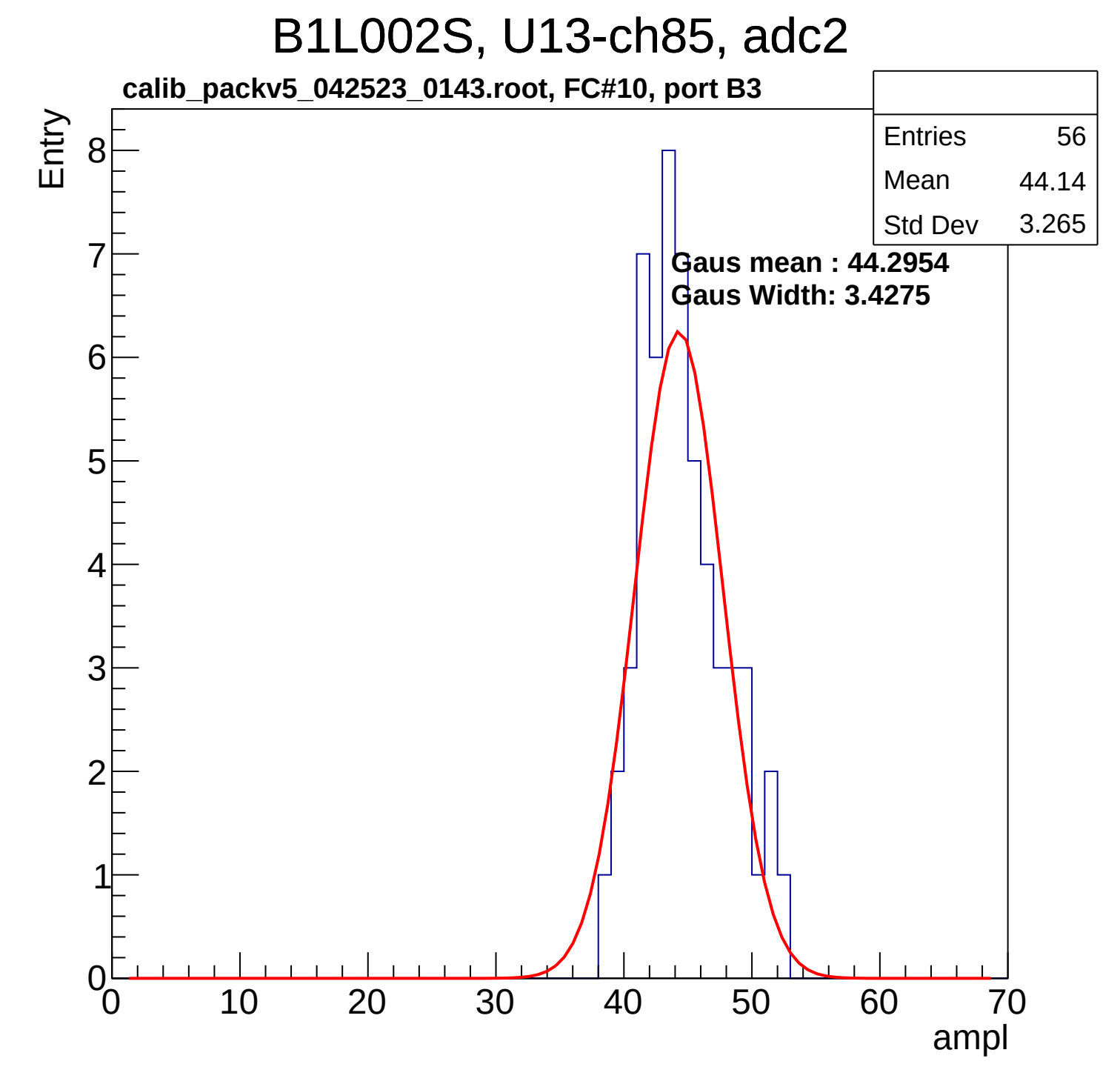
B1L002S, U13-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 44.14
Std Dev 3.265
Gaus mean : 44.2954
Gaus Width: 3.4275
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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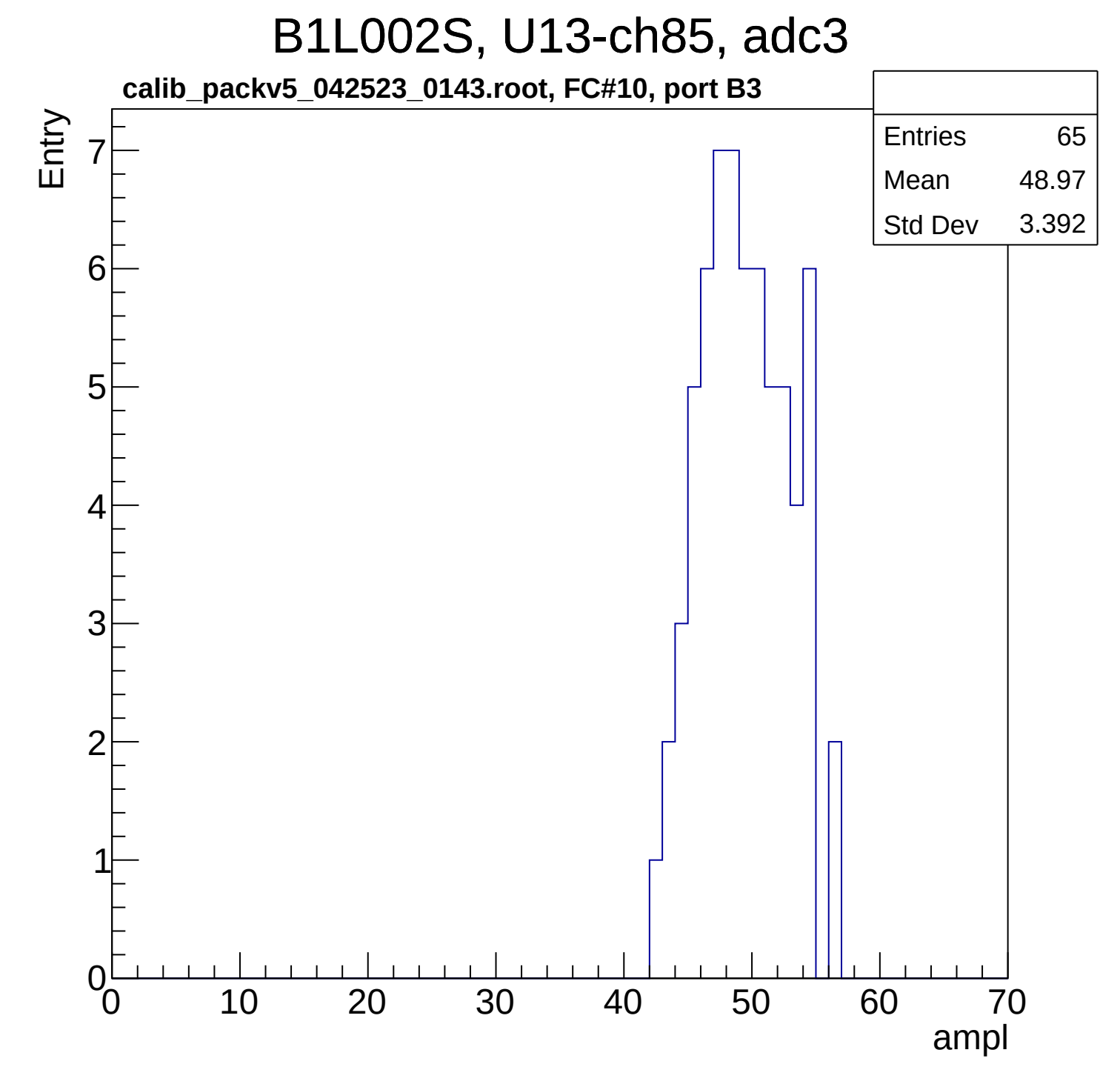
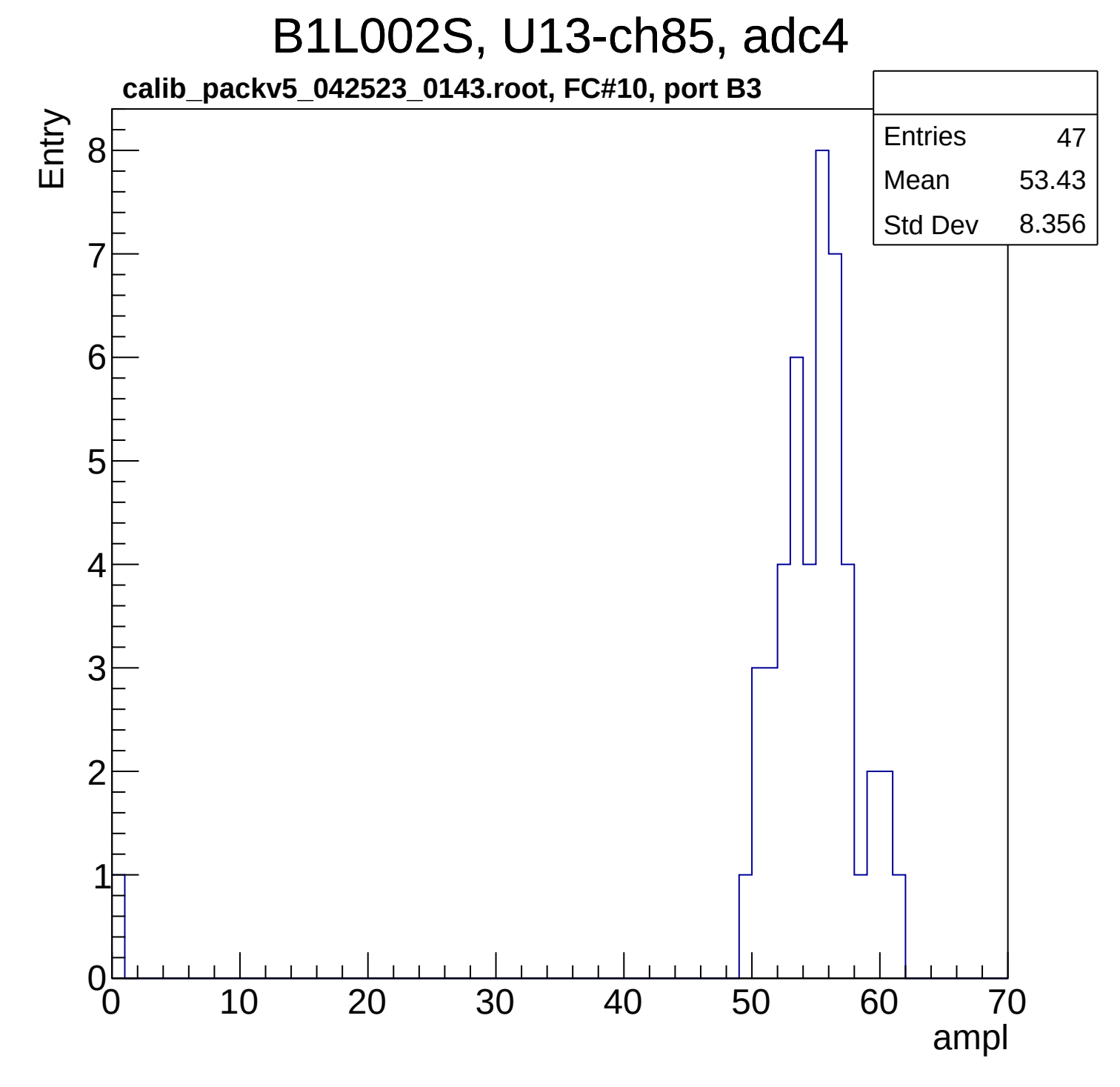

B1L002S, U13-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.97
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.43
Std Dev 8.356
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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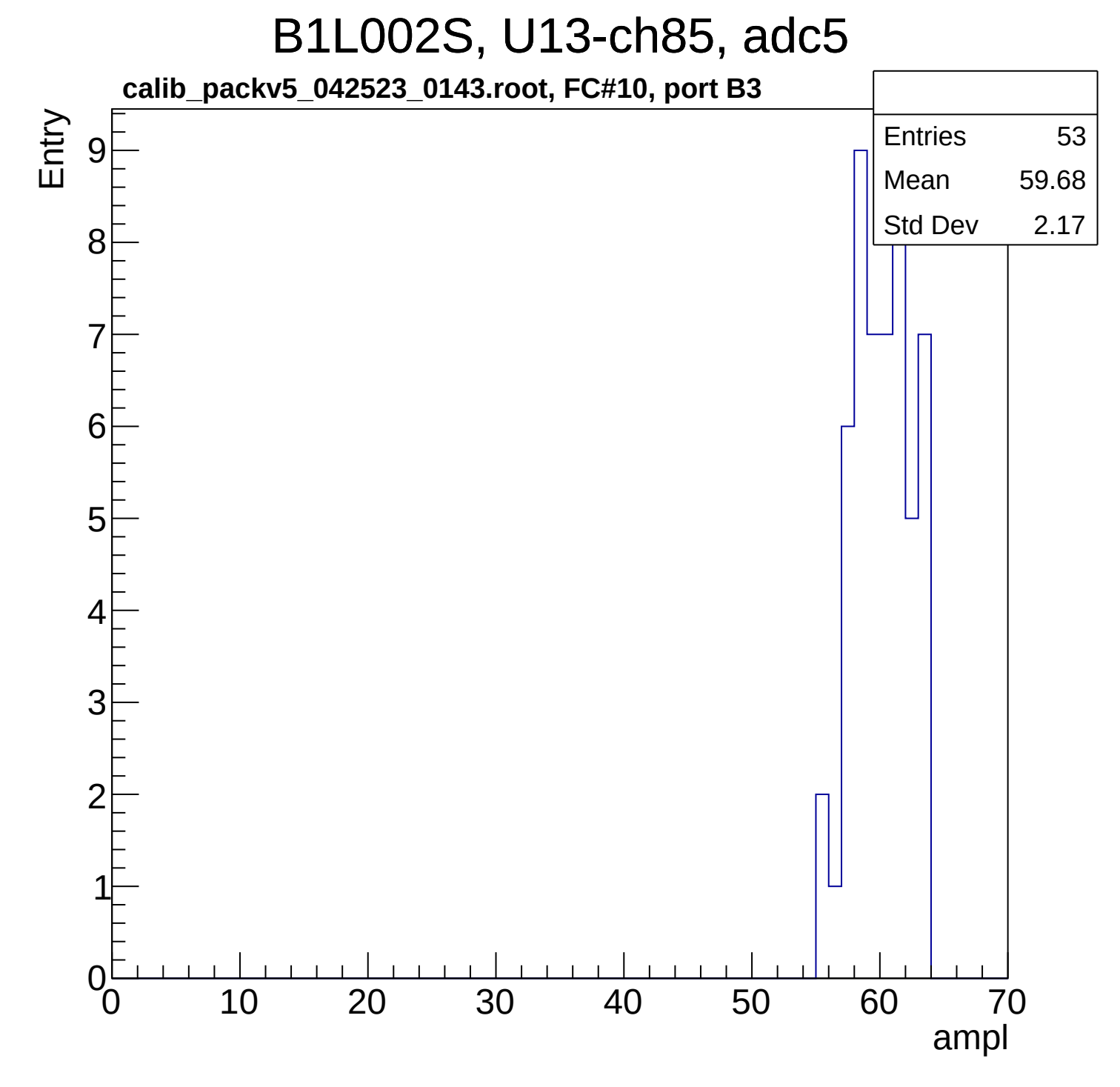
B1L002S, U13-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.68
Std Dev 2.17
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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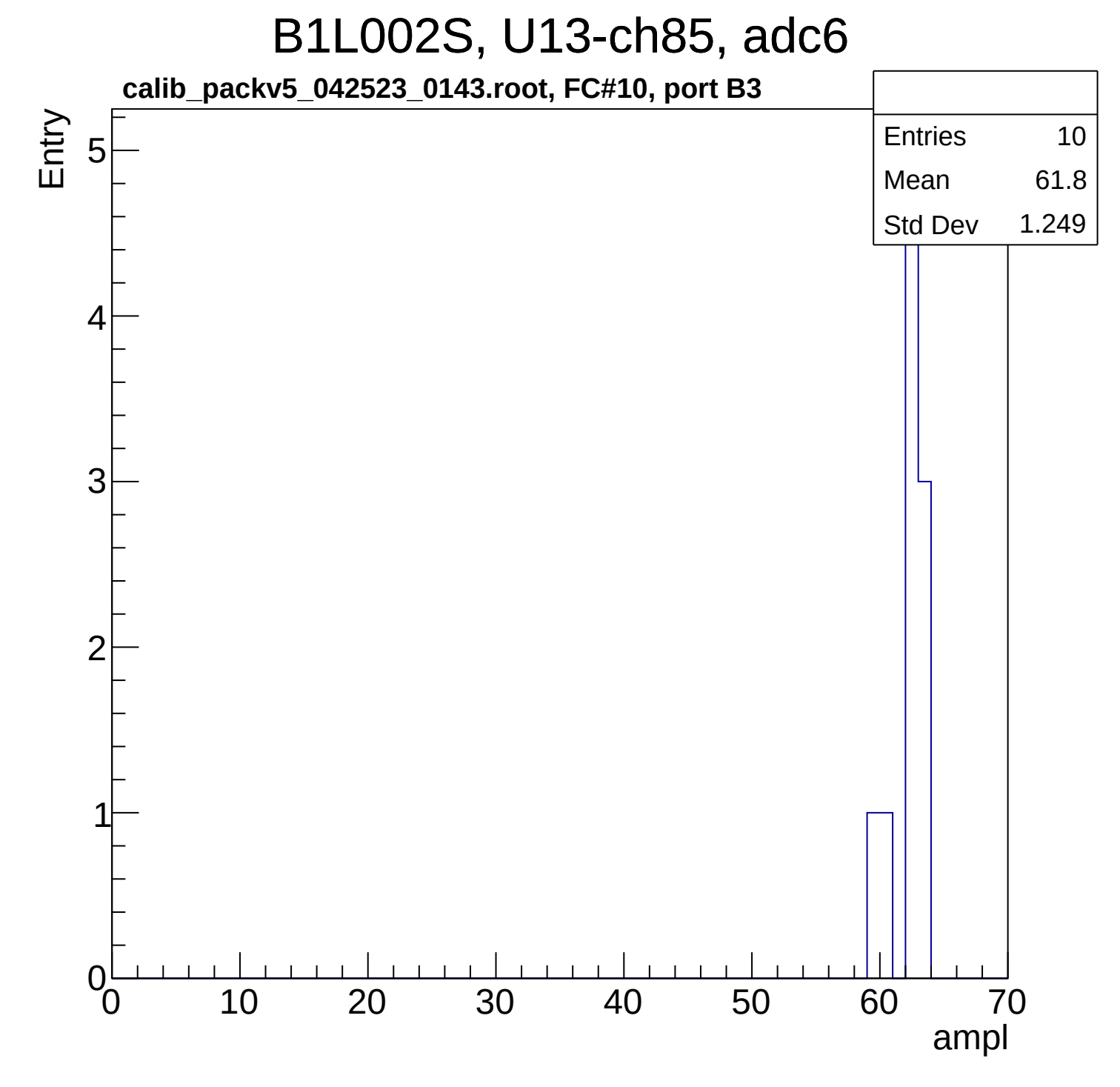
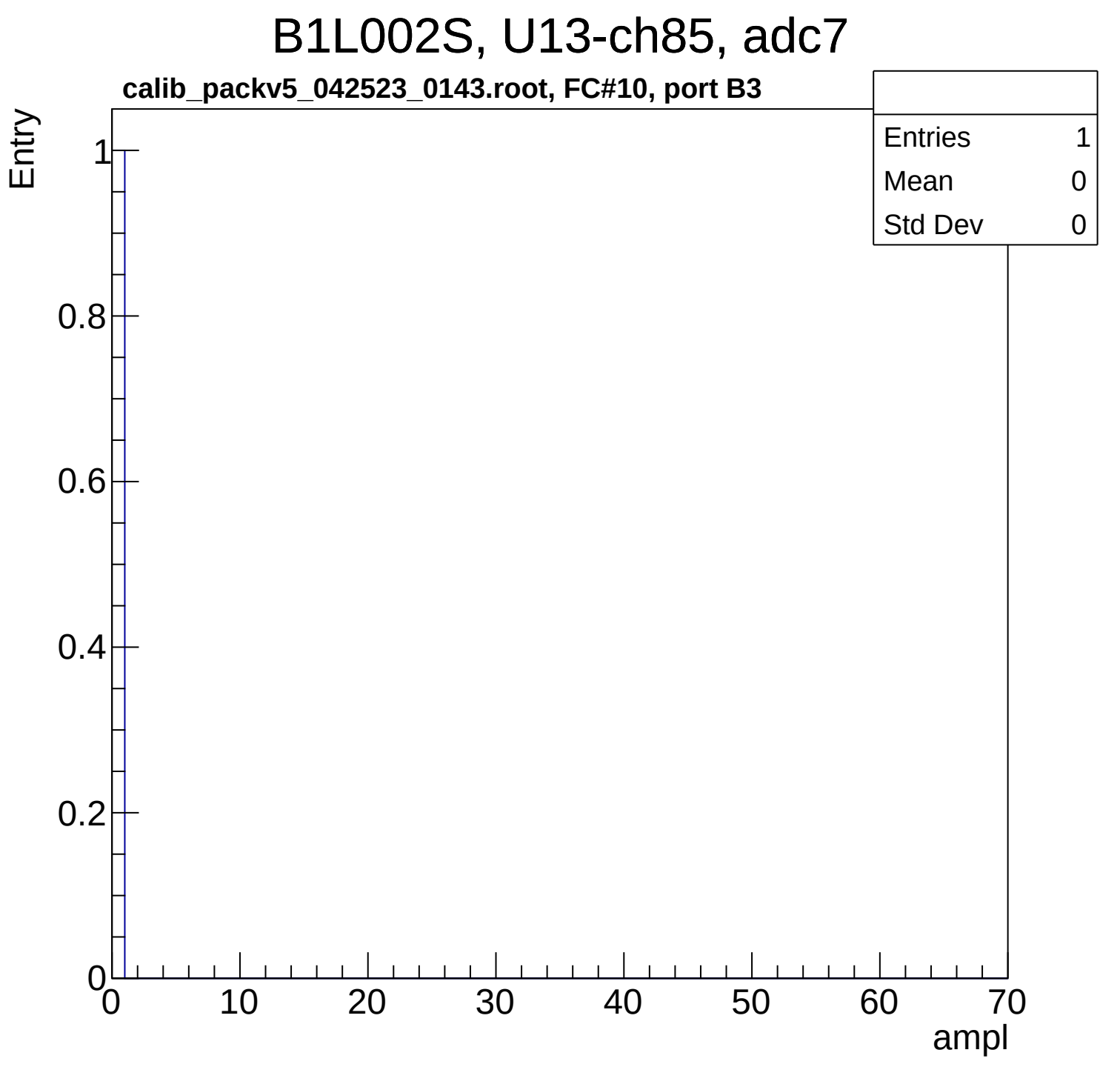
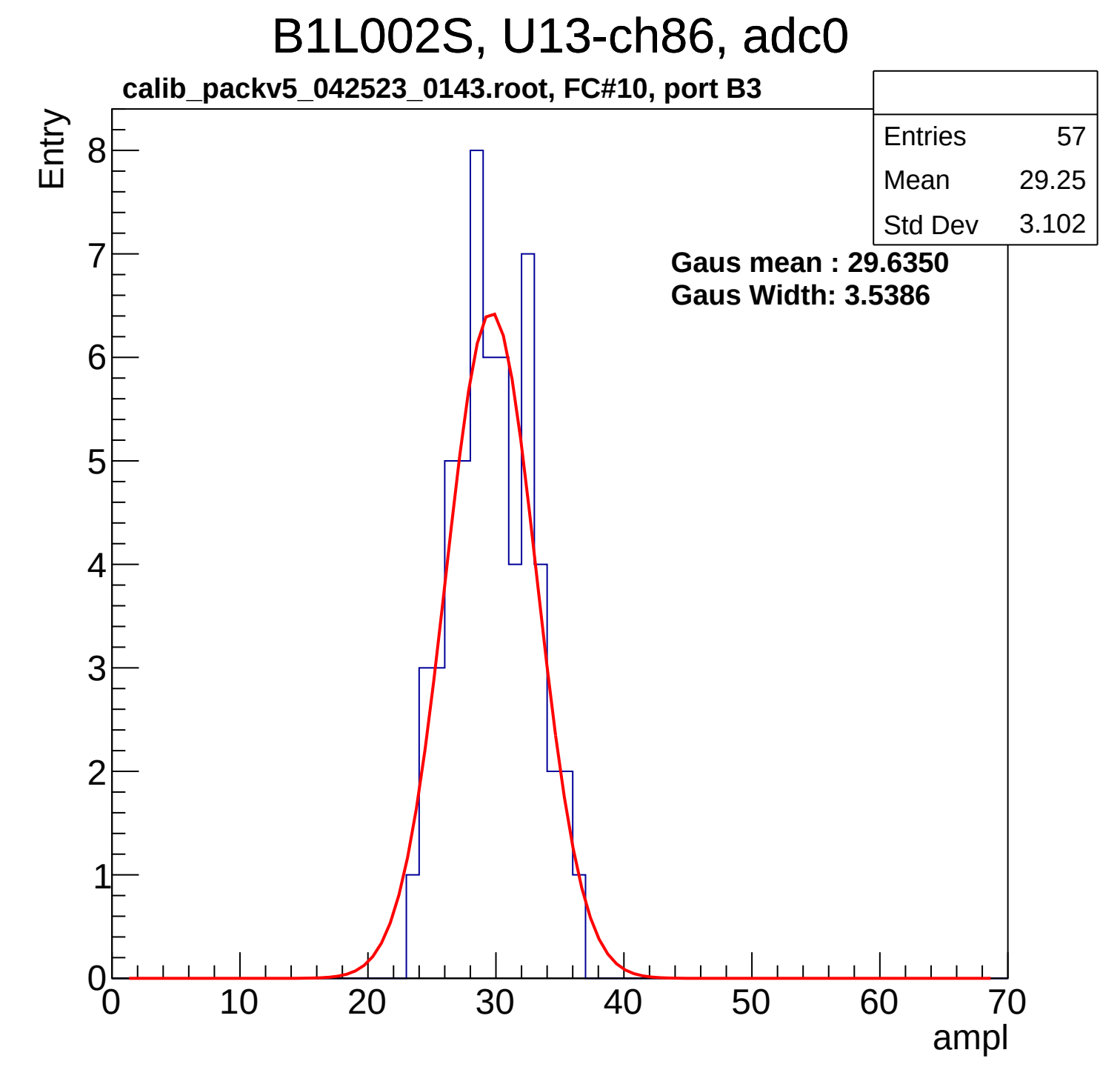
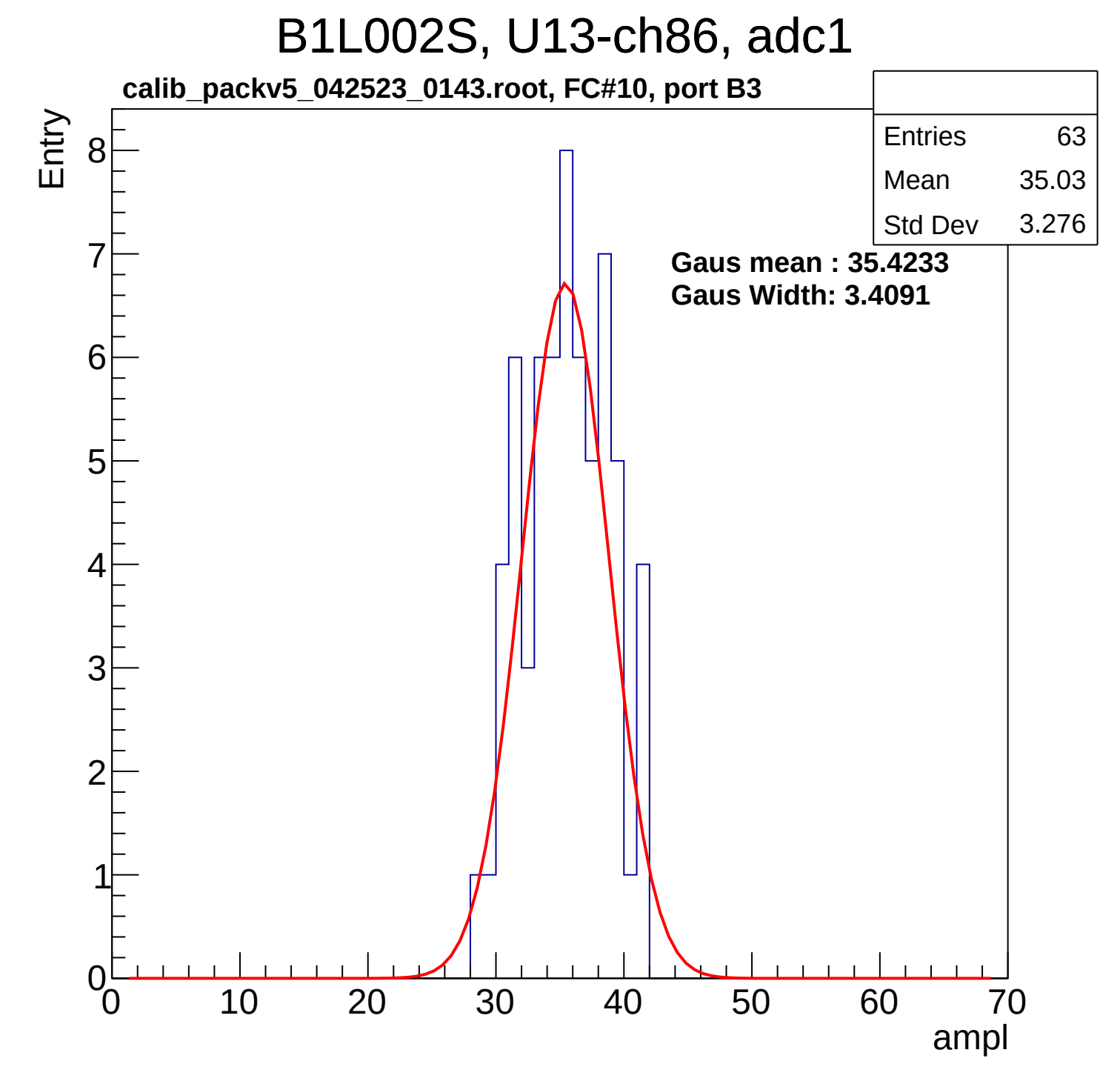
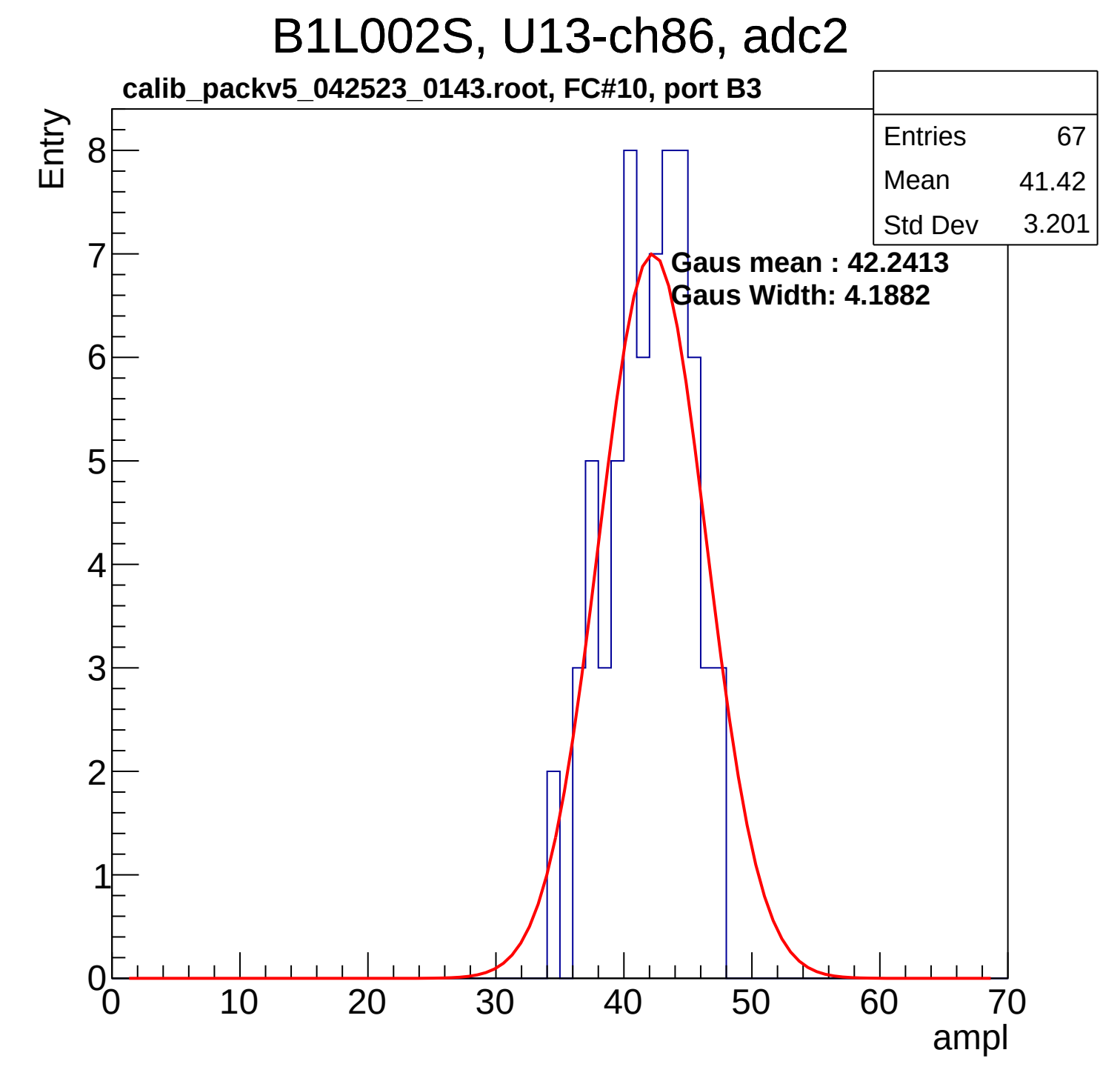

B1L002S, U13-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.8
Std Dev 1.249
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.25
Std Dev 3.102
Gaus mean : 29.6350
Gaus Width: 3.5386
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.03
Std Dev 3.276
Gaus mean : 35.4233
Gaus Width: 3.4091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.42
Std Dev 3.201
Gaus mean : 42.2413
Gaus Width: 4.1882
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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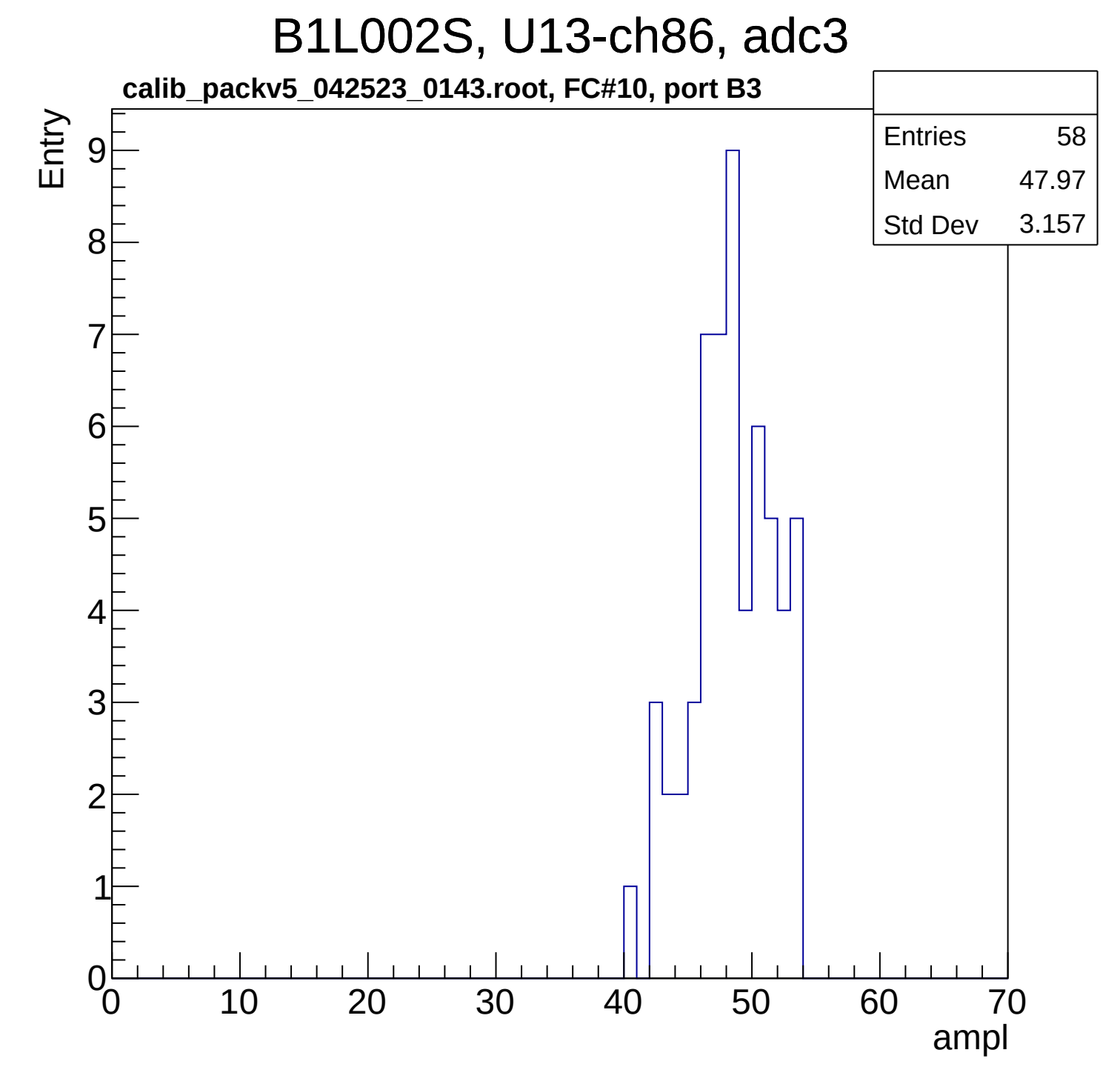
B1L002S, U13-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.97
Std Dev 3.157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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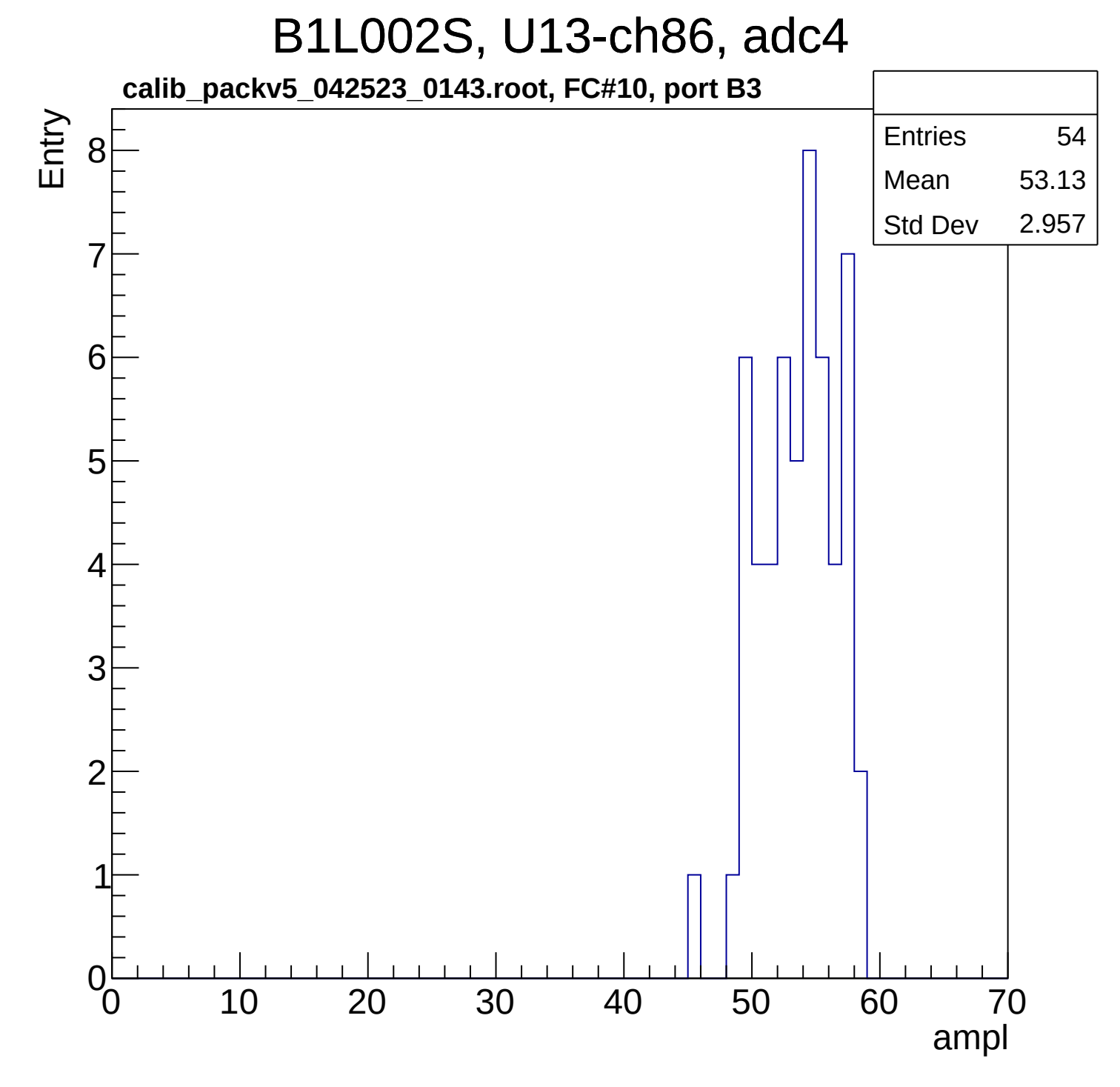
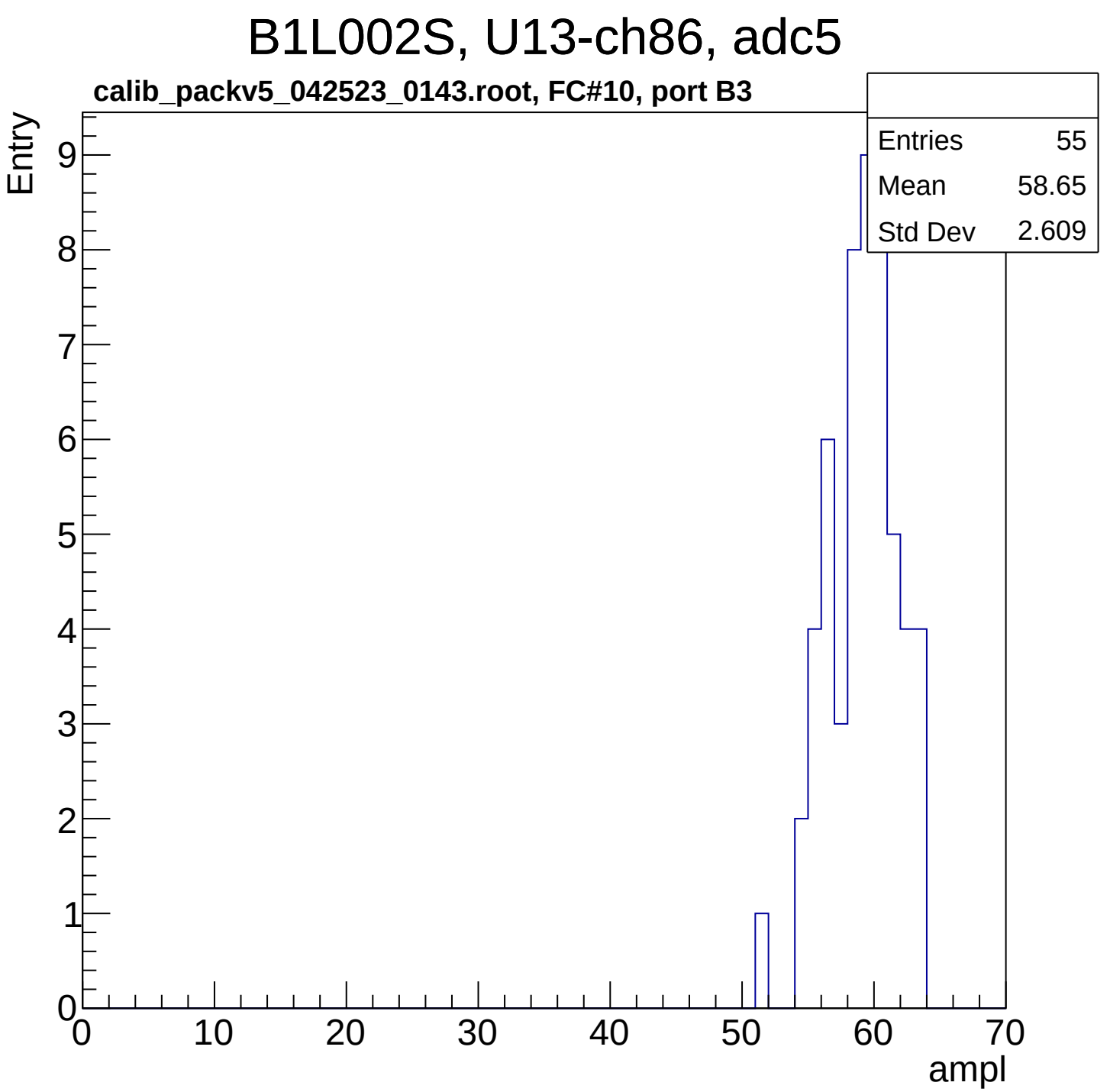

B1L002S, U13-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.13
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.65
Std Dev 2.609
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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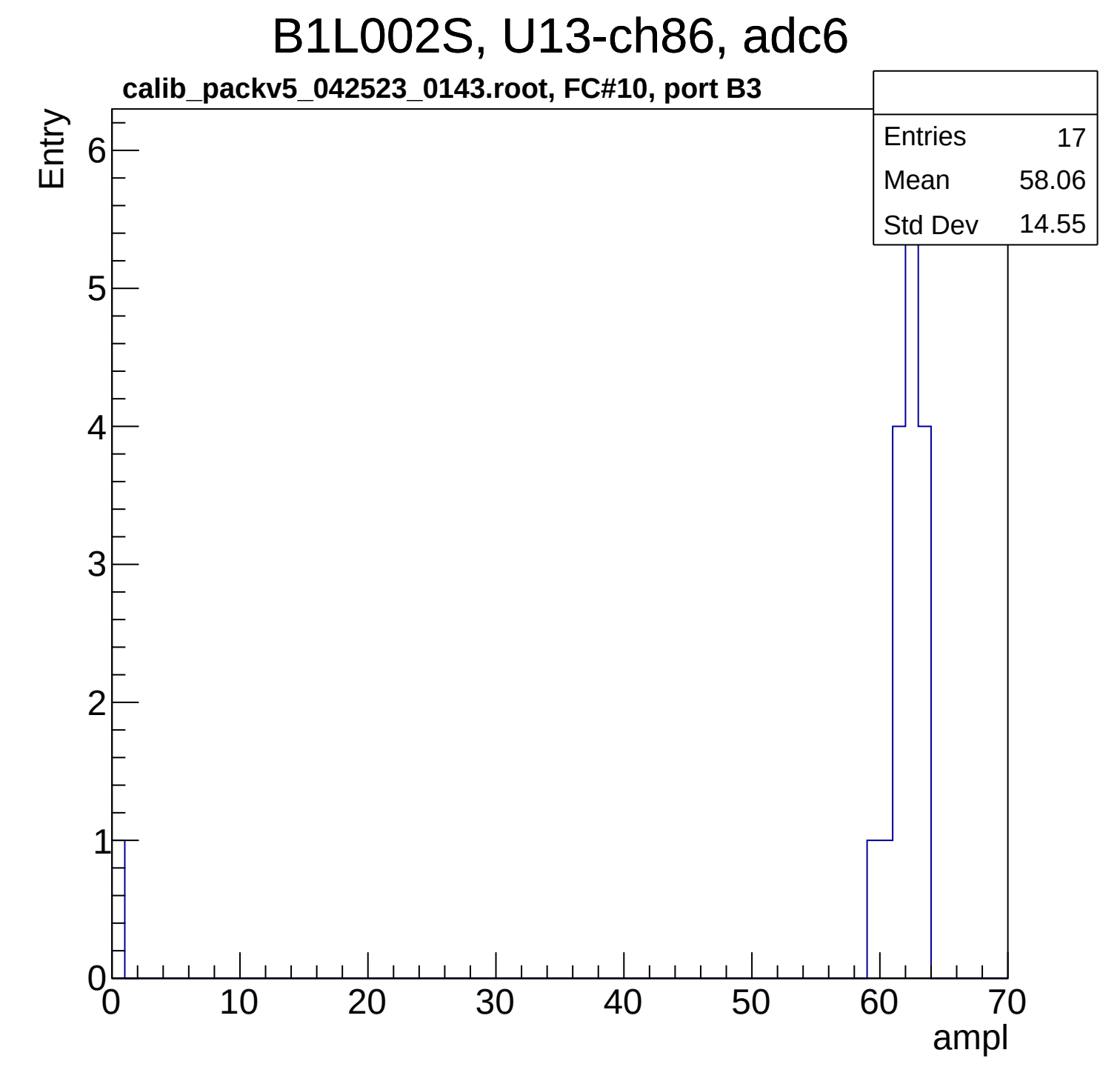
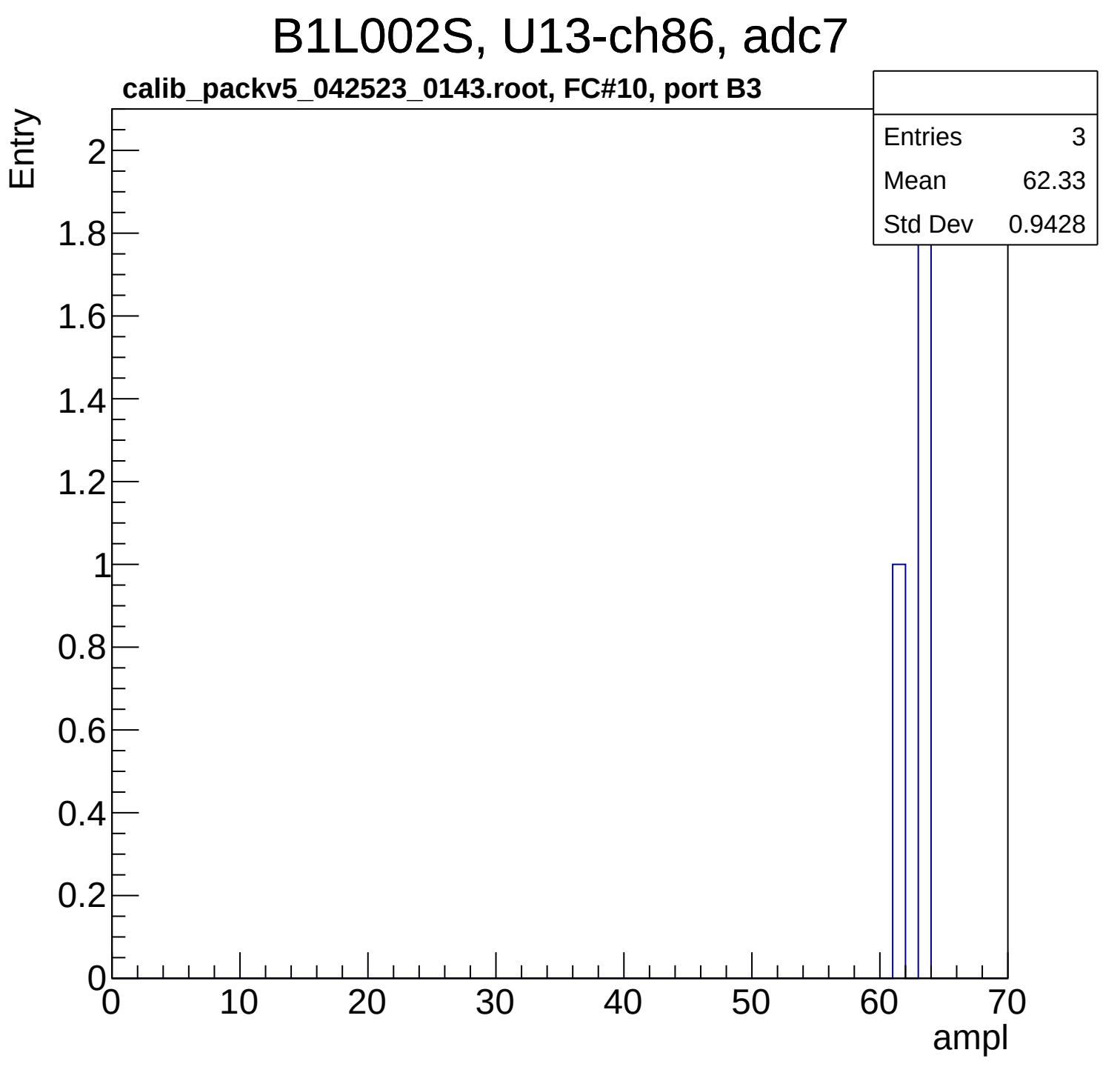
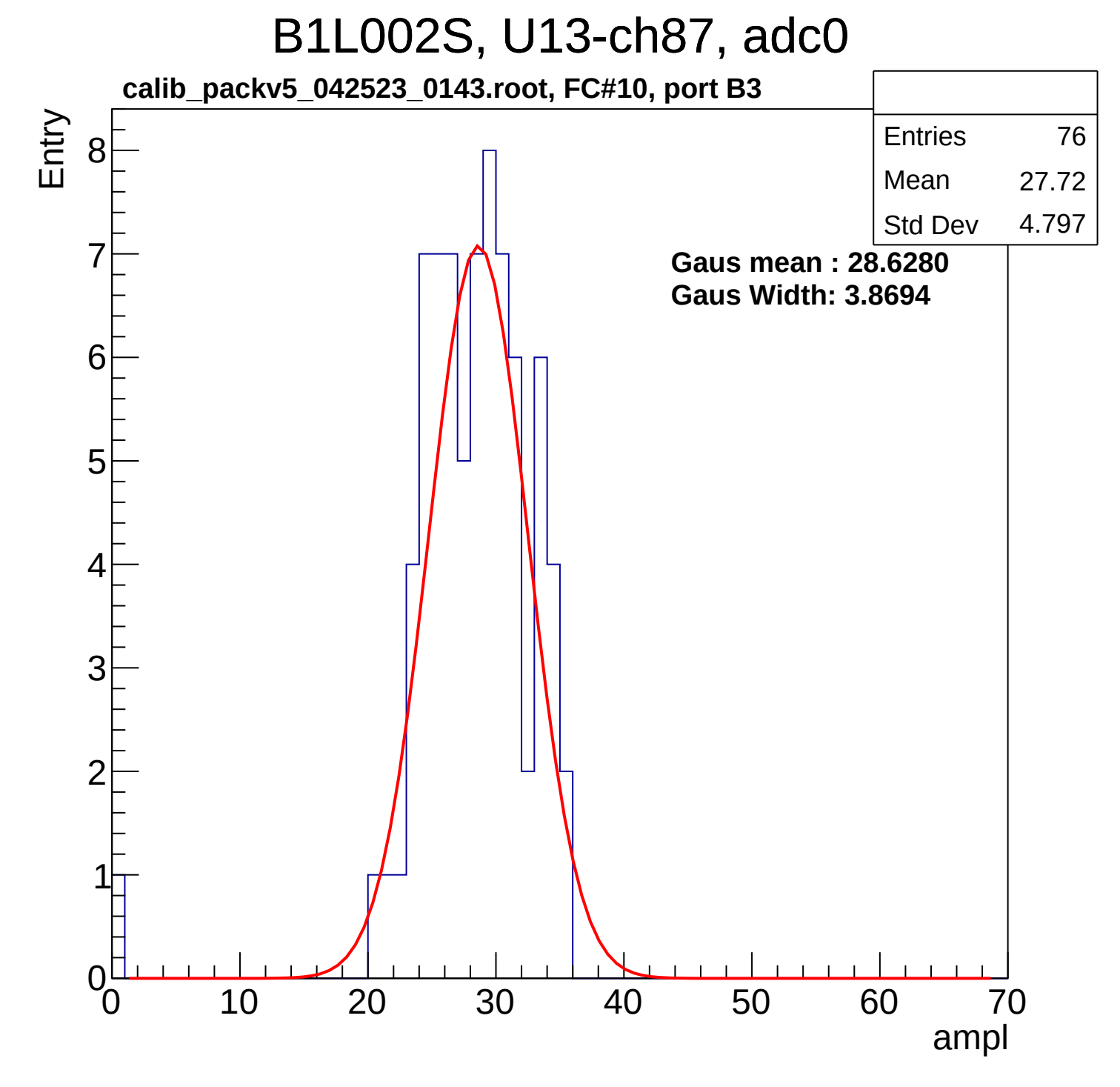
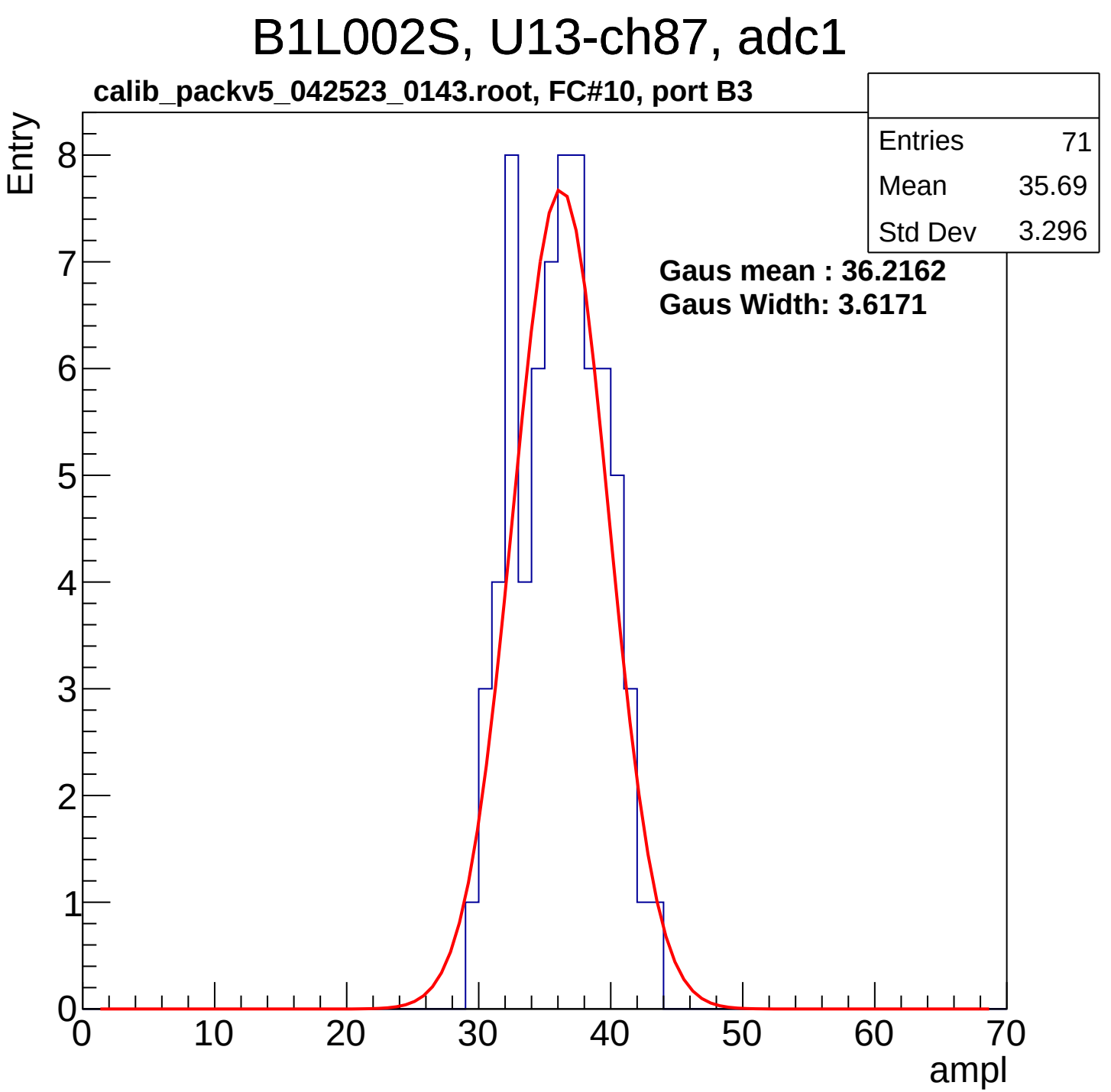

B1L002S, U13-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.06
Std Dev 14.55
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.72
Std Dev 4.797
Gaus mean : 28.6280
Gaus Width: 3.8694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.69
Std Dev 3.296
Gaus mean : 36.2162
Gaus Width: 3.6171
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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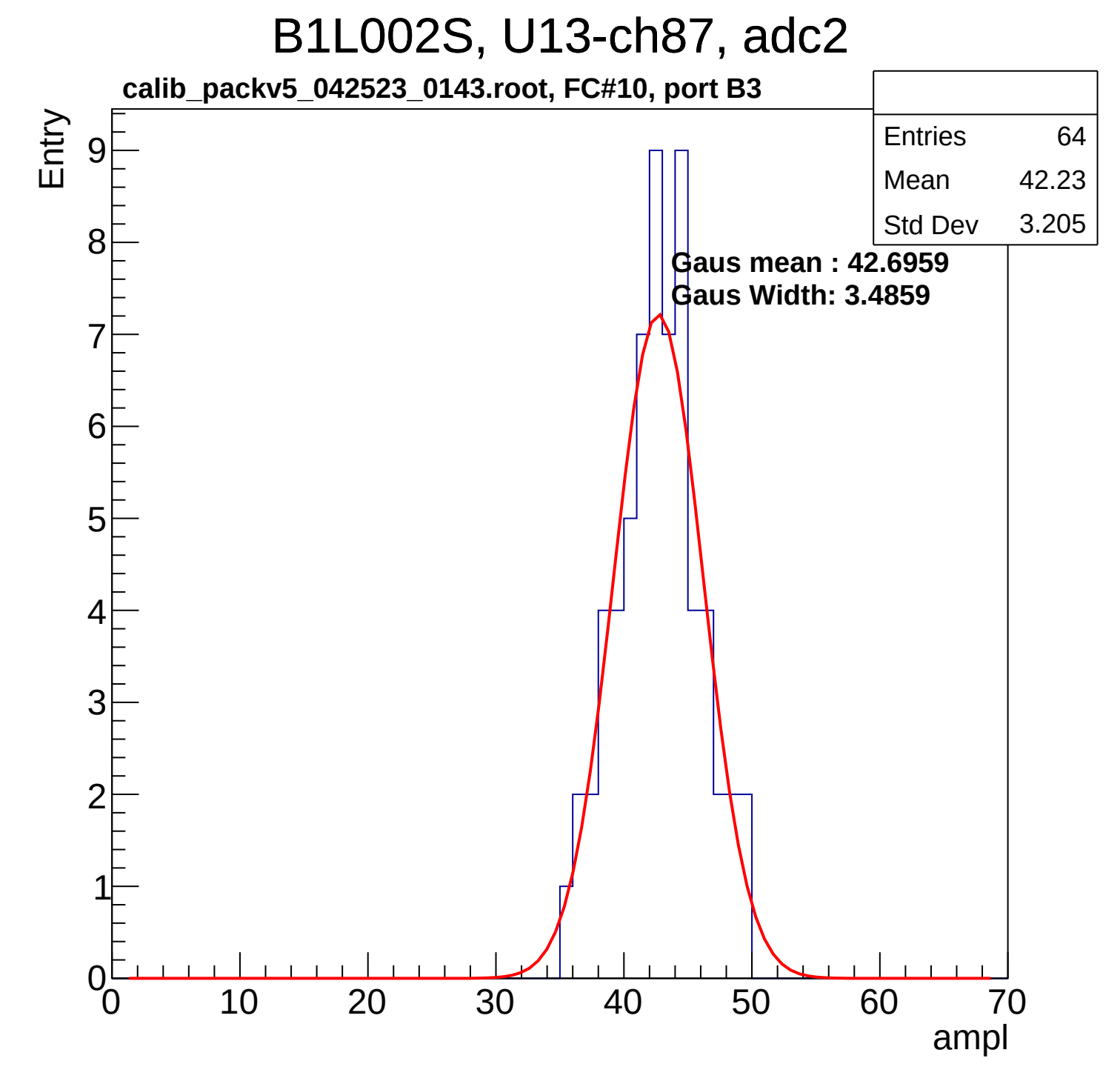
B1L002S, U13-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.23
Std Dev 3.205
Gaus mean : 42.6959
Gaus Width: 3.4859
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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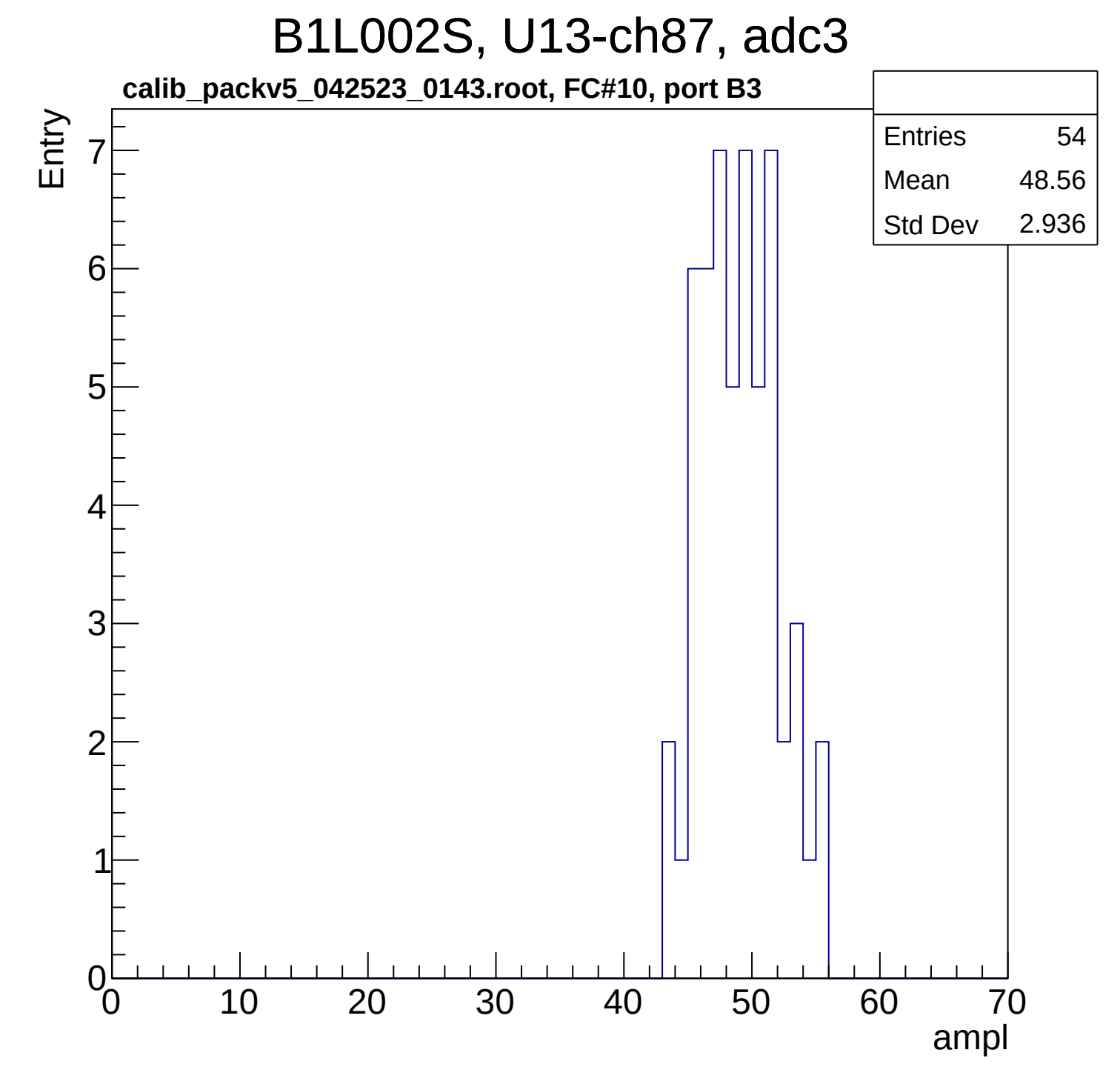
B1L002S, U13-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.56
Std Dev 2.936
Entry
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2
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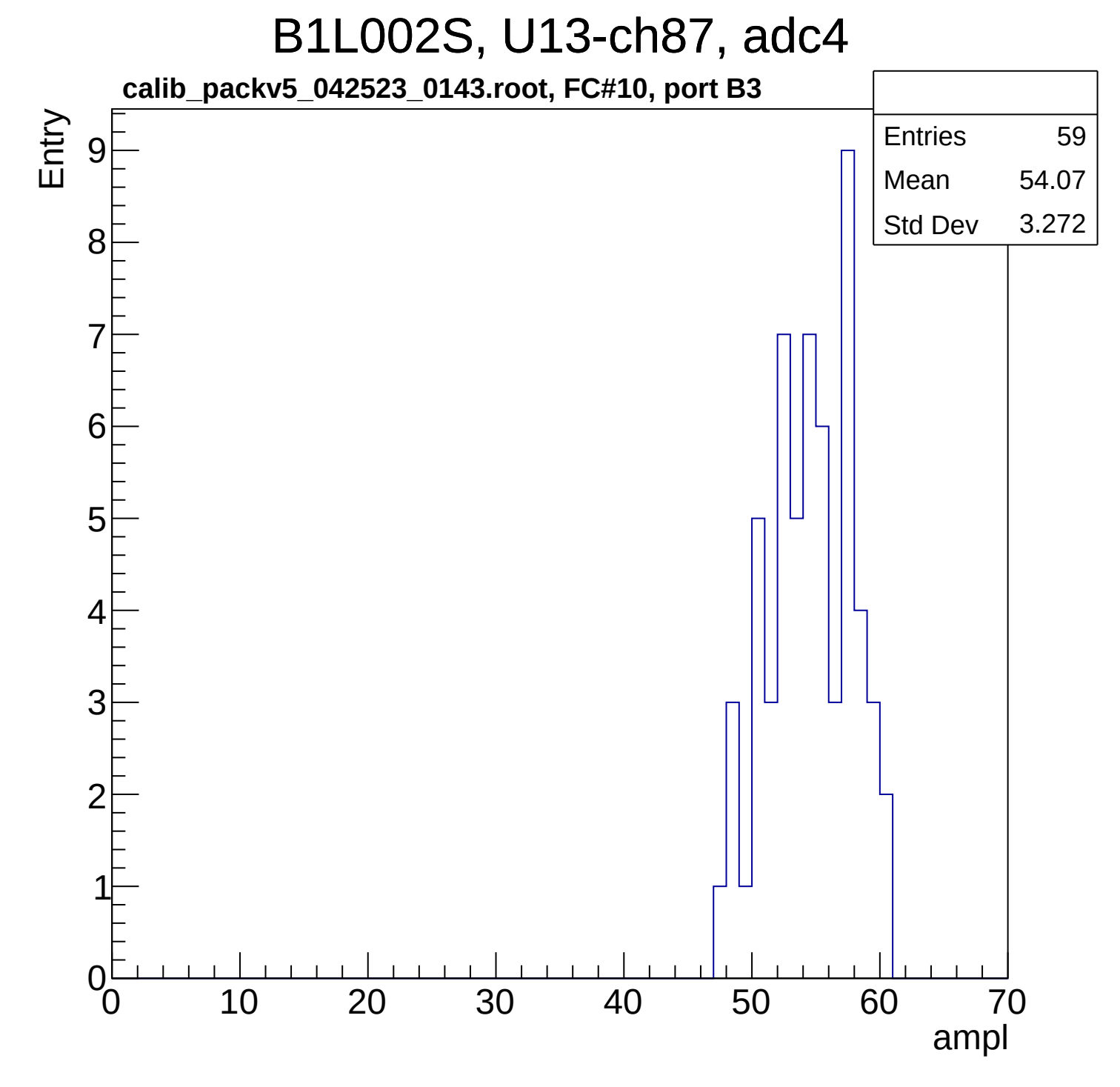
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.07
Std Dev 3.272
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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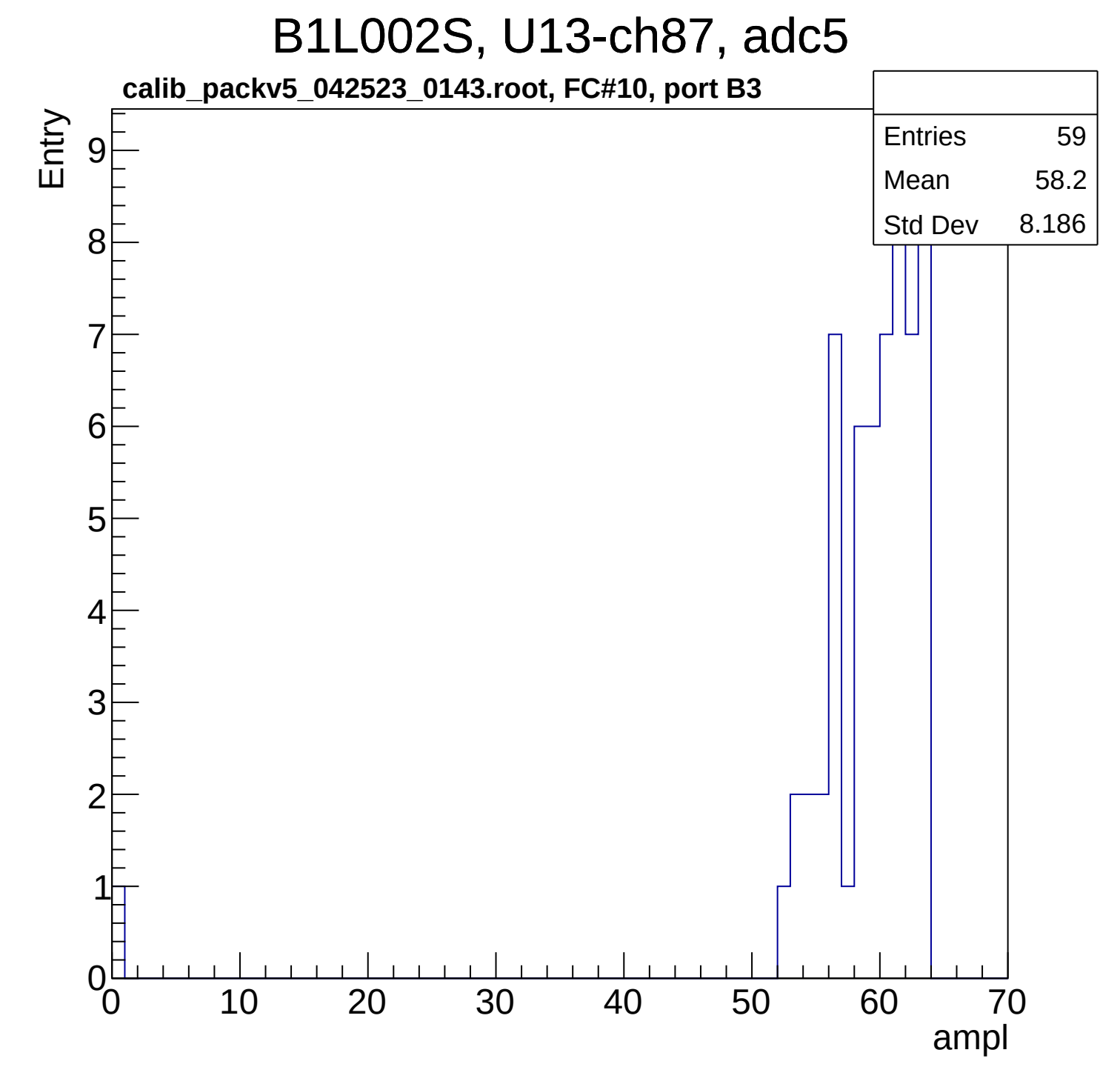
40
50
60
70


B1L002S, U13-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.2
Std Dev 8.186
Entry
ampl
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2
3
4
5
6
7
8
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0
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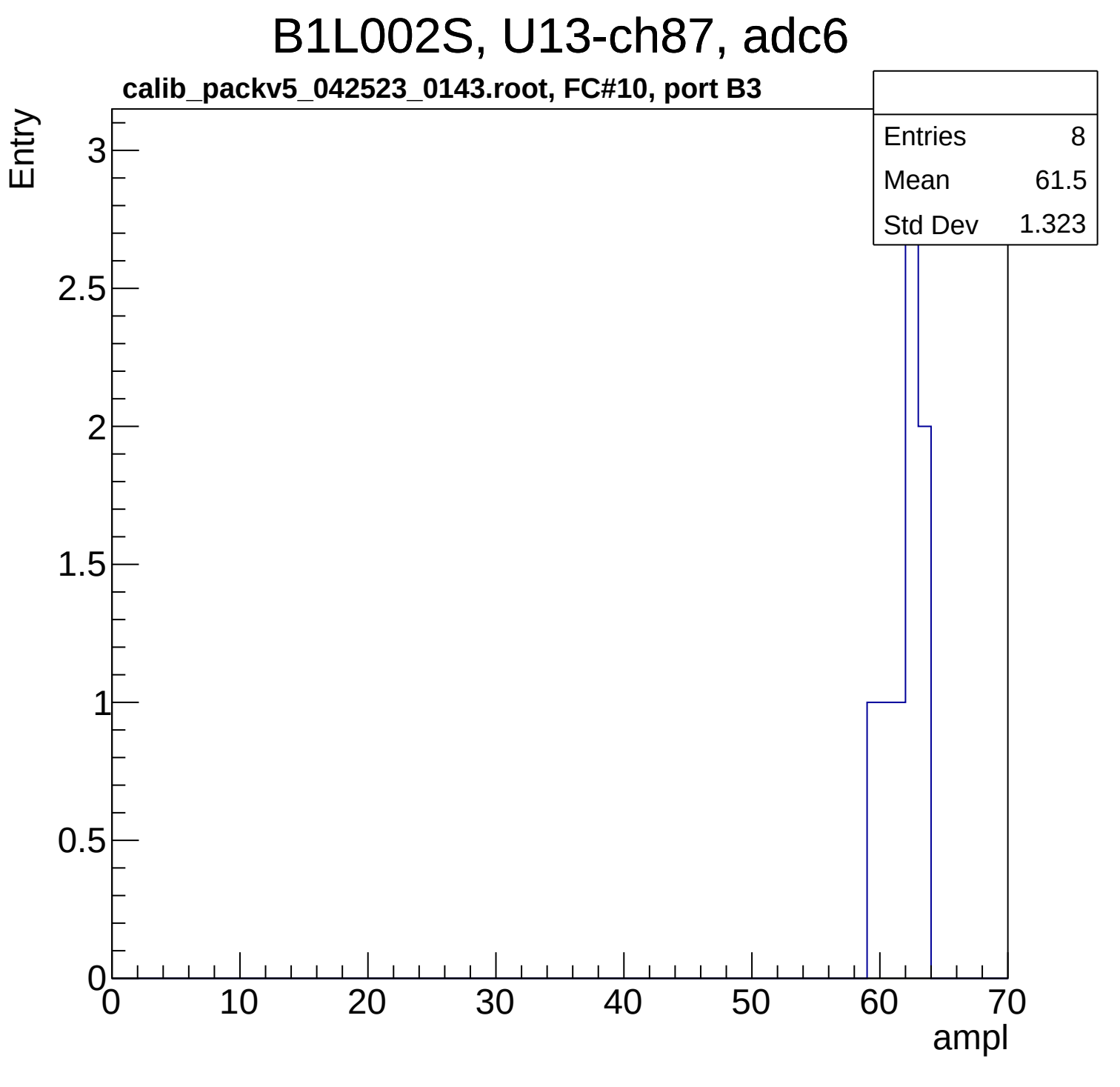
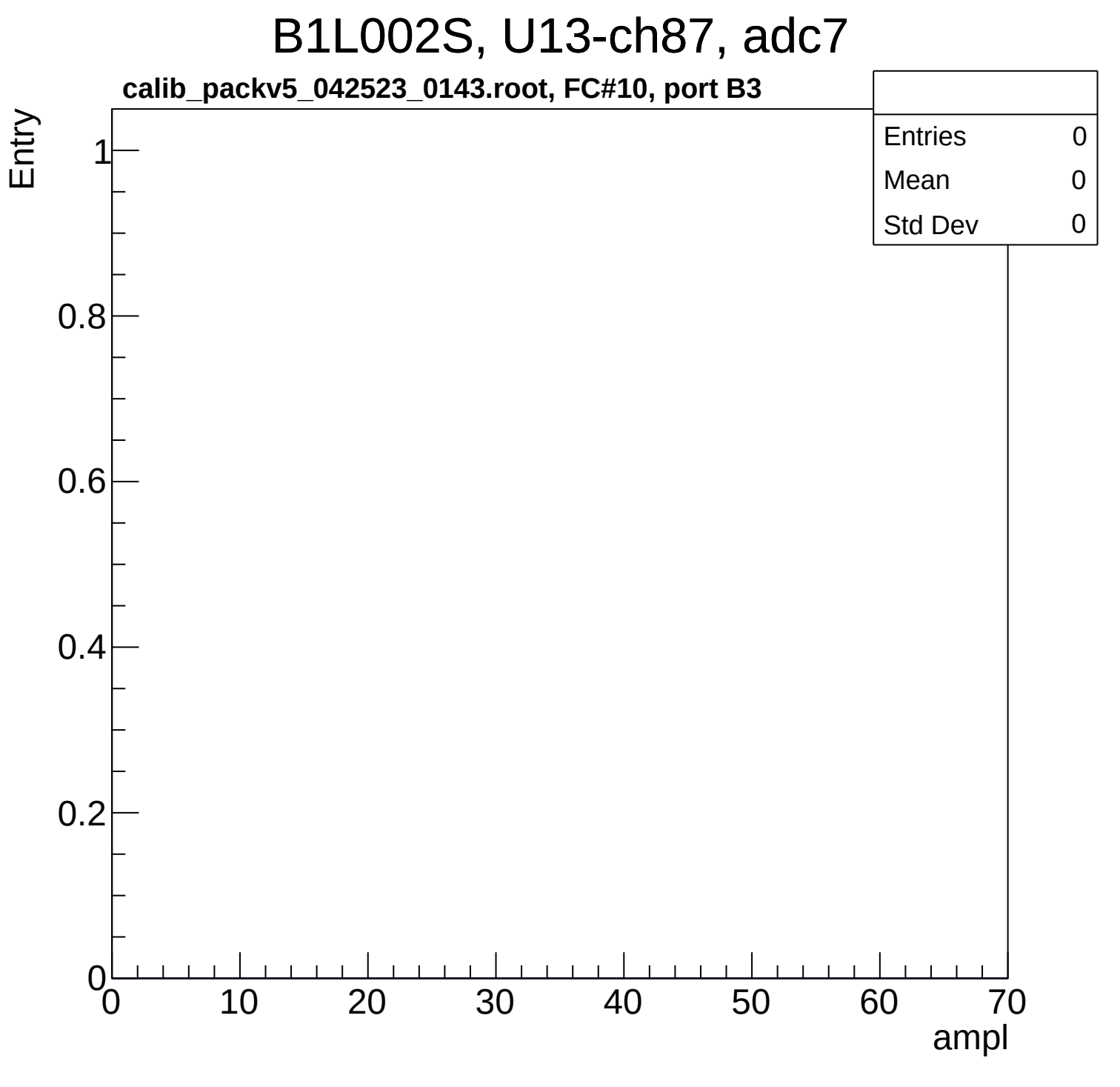
40
50
60
70


B1L002S, U13-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61.5
Std Dev 1.323
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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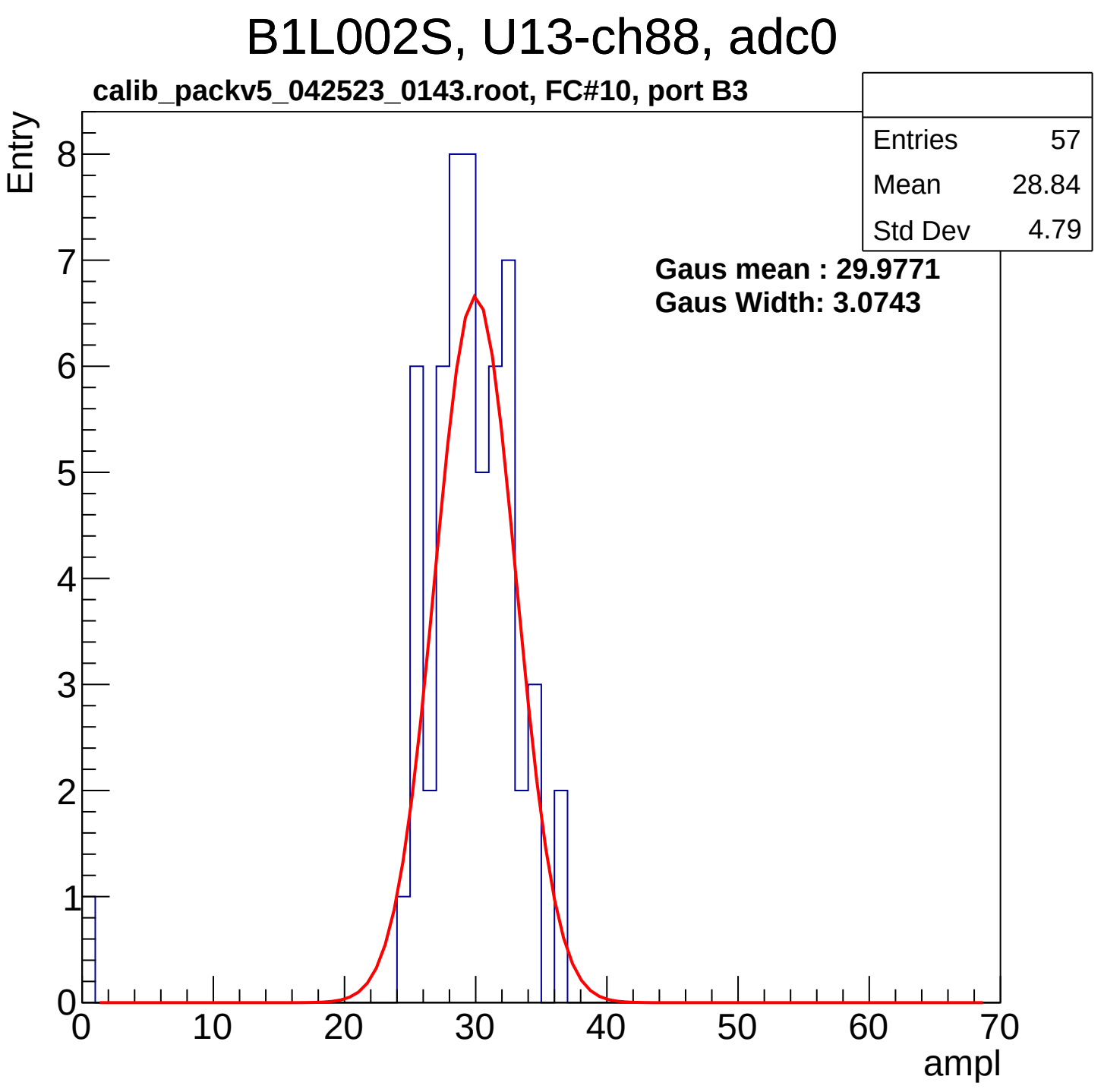
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.84
Std Dev 4.79
Gaus mean : 29.9771
Gaus Width: 3.0743
Entry
ampl
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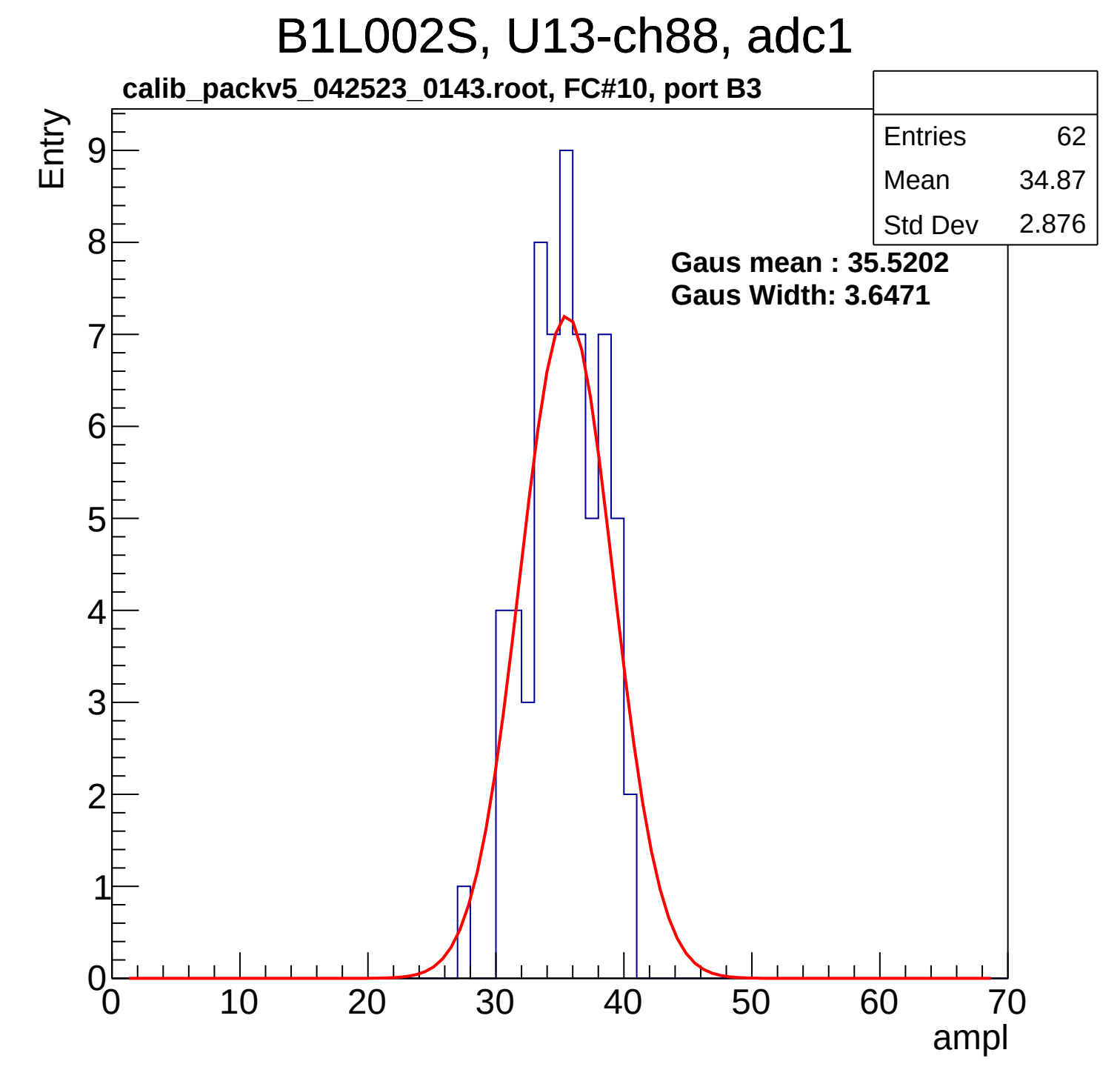
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.87
Std Dev 2.876
Gaus mean : 35.5202
Gaus Width: 3.6471
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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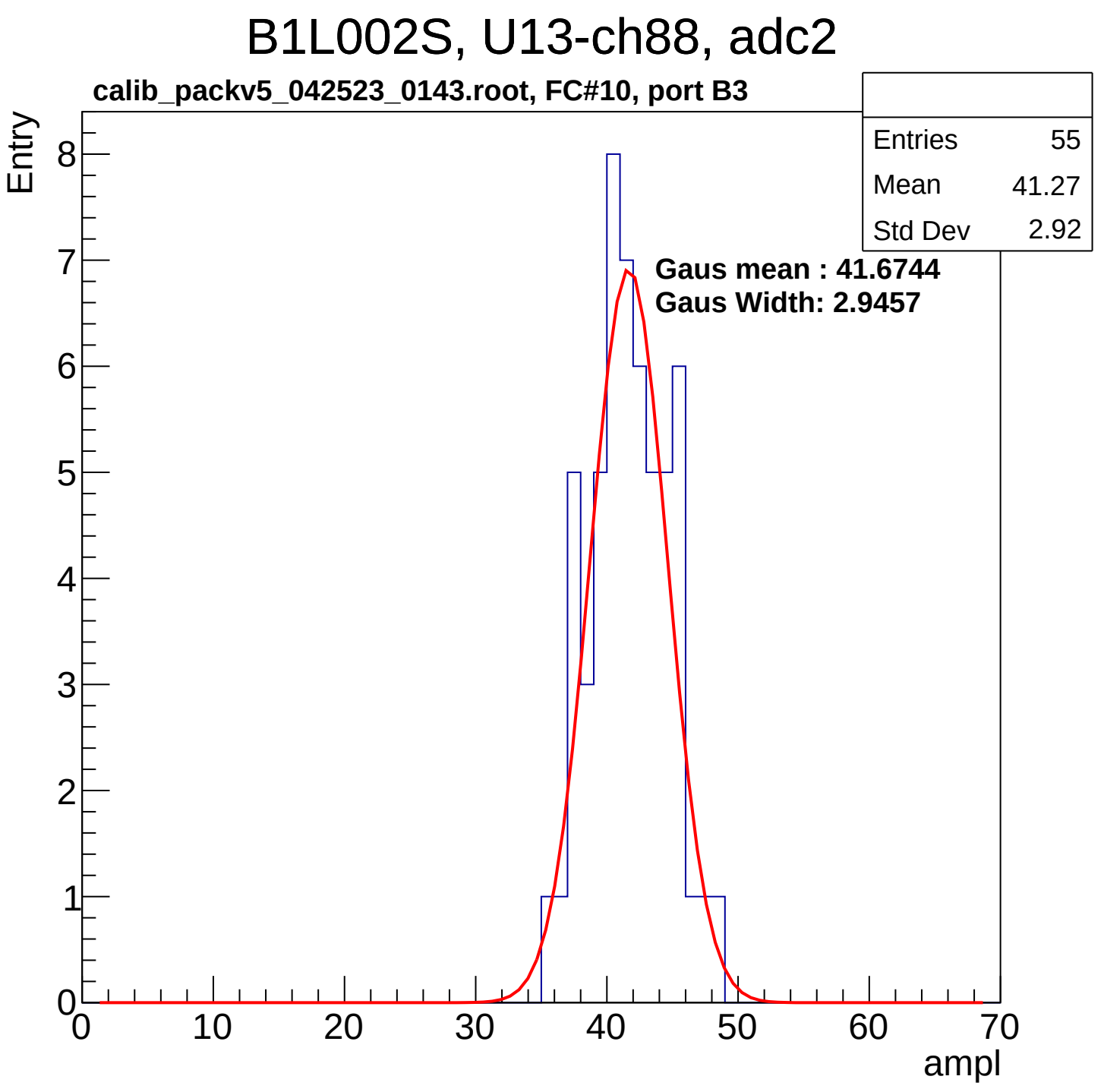
B1L002S, U13-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.27
Std Dev 2.92
Gaus mean : 41.6744
Gaus Width: 2.9457
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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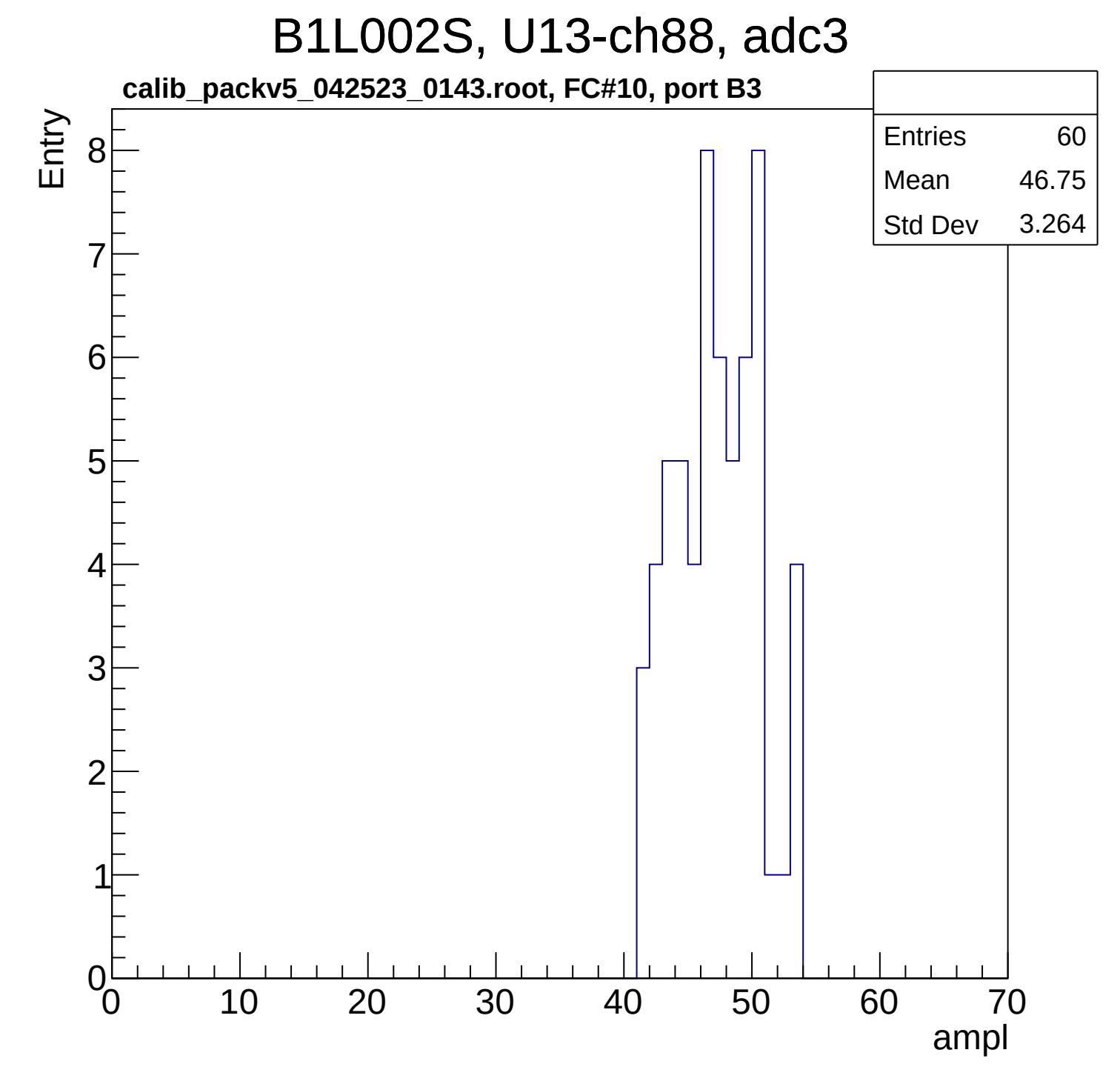
40
50
60
70


B1L002S, U13-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.75
Std Dev 3.264
Entry
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2
3
4
5
6
7
8
0
10
20
30
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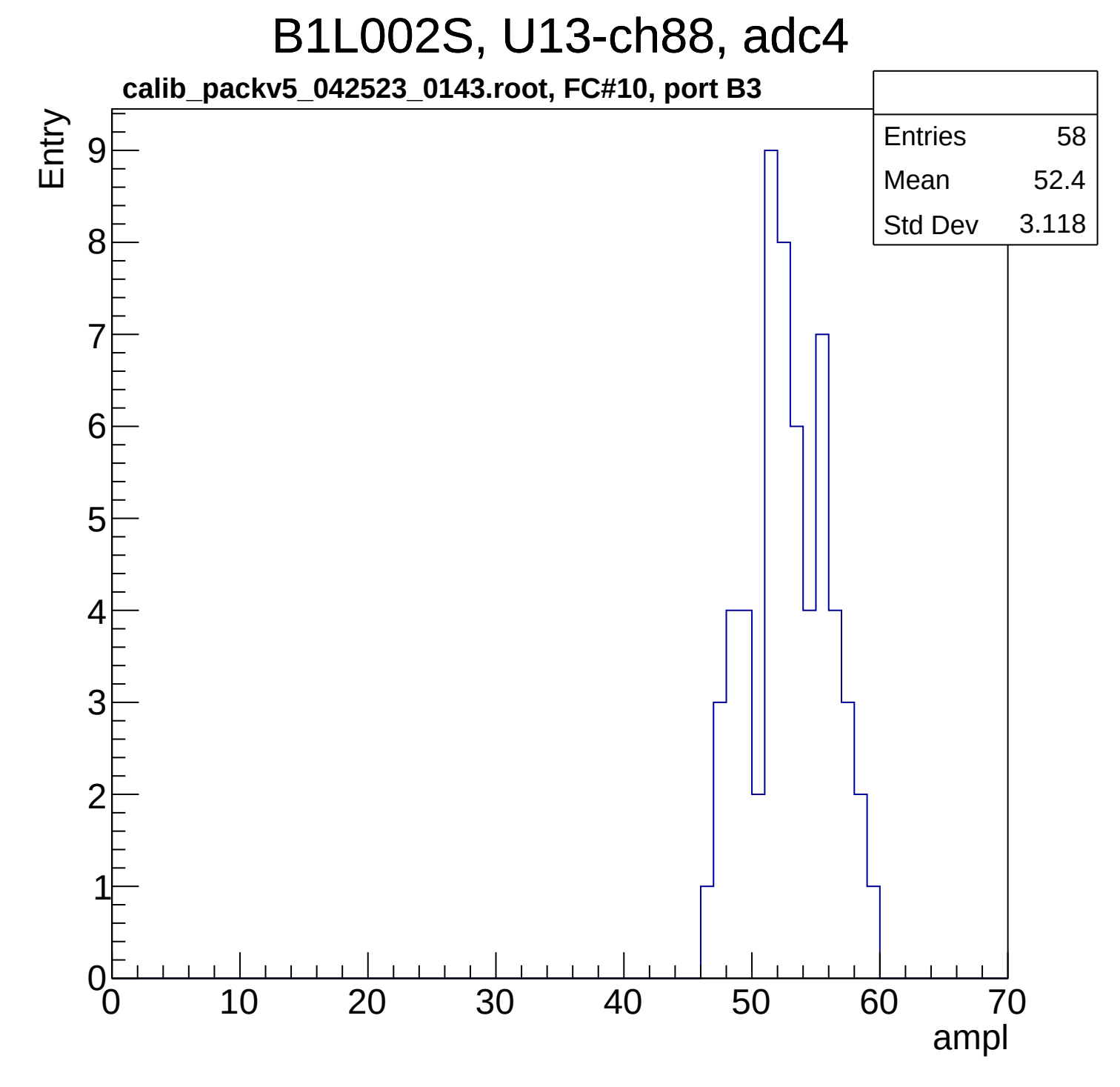
50
60
70
ampl


B1L002S, U13-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.4
Std Dev 3.118
Entry
ampl
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2
3
4
5
6
7
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0
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40
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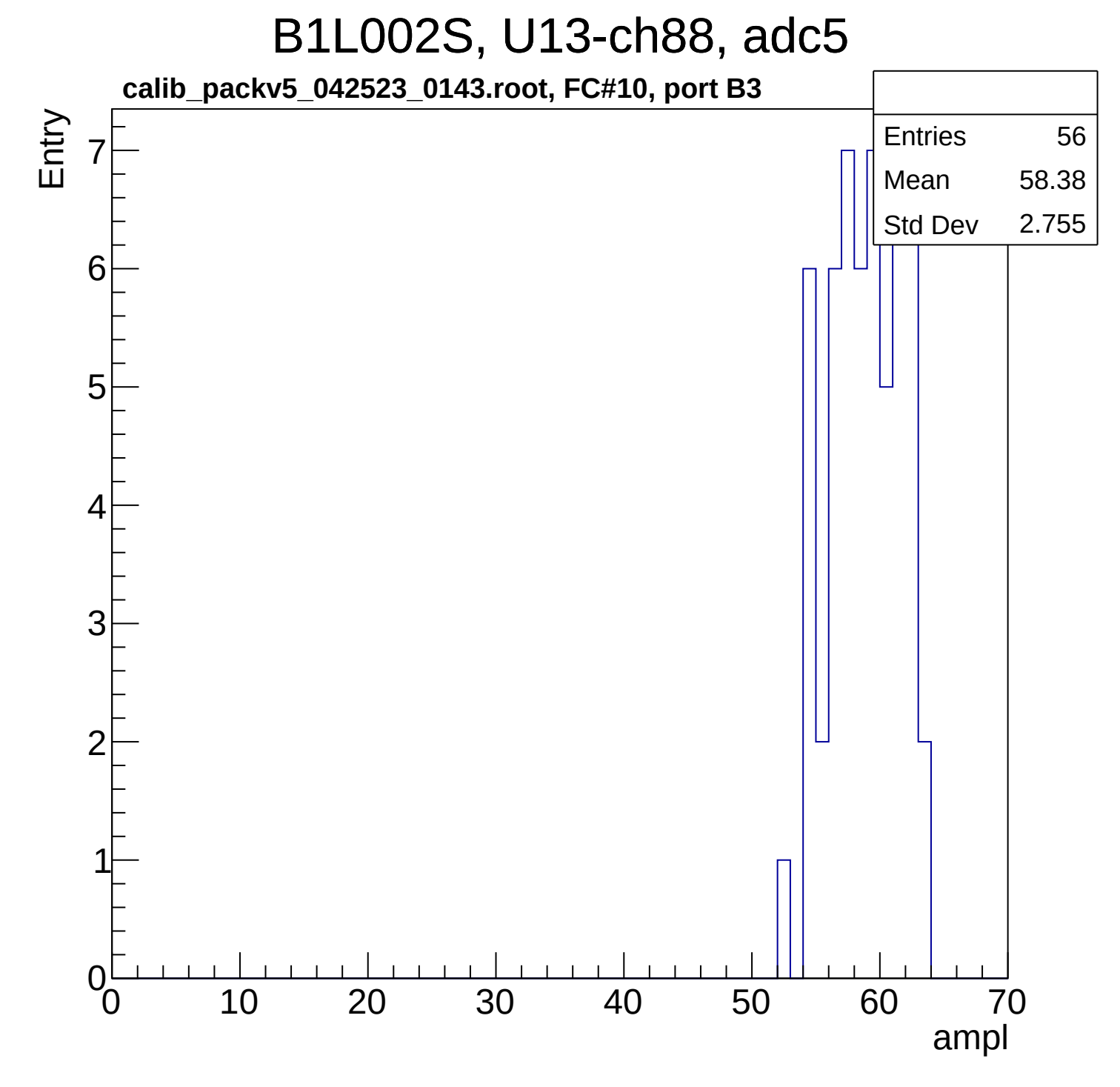
B1L002S, U13-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.38
Std Dev 2.755
Entry
ampl
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3
4
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60
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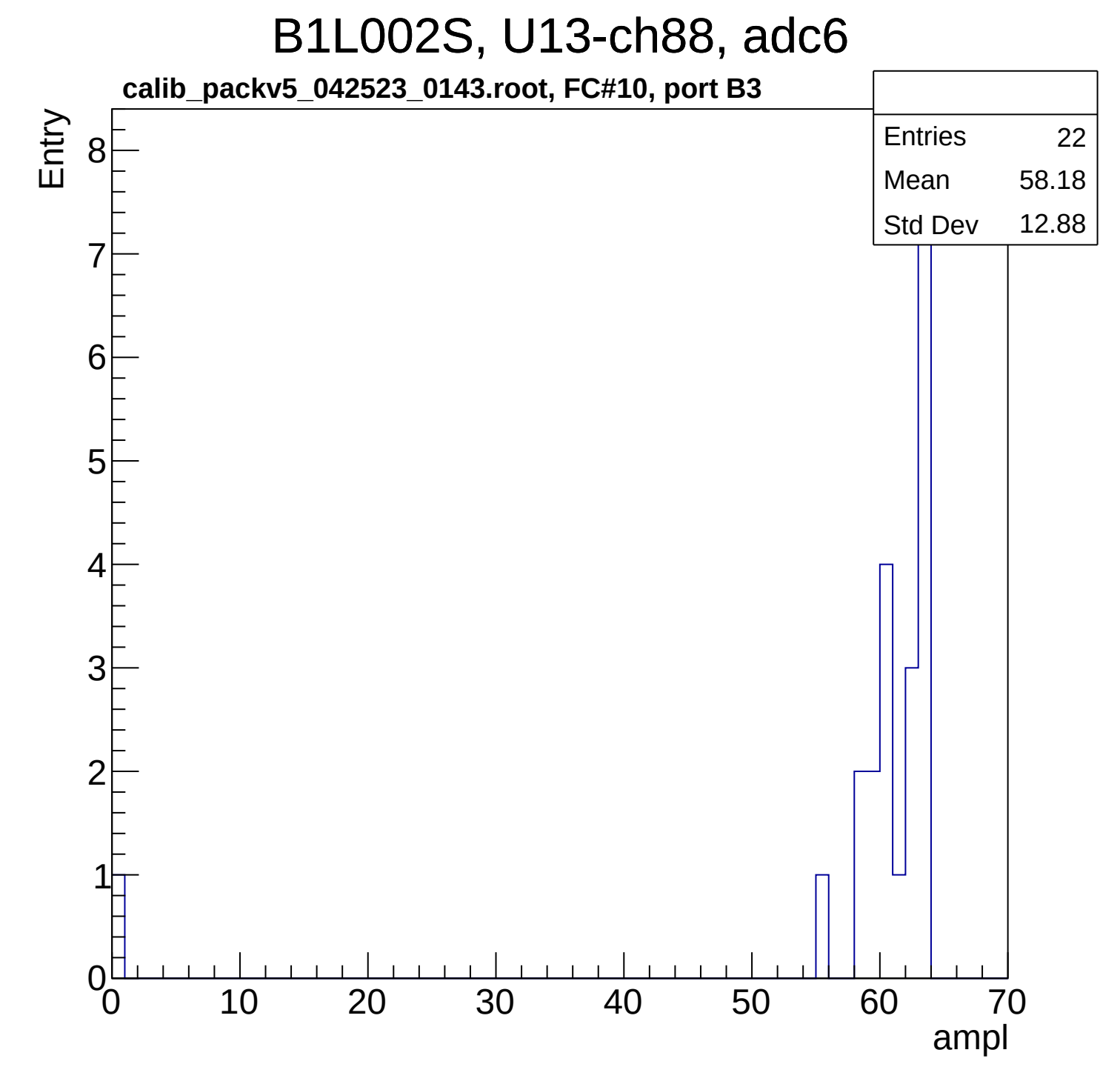
B1L002S, U13-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.18
Std Dev 12.88
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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60
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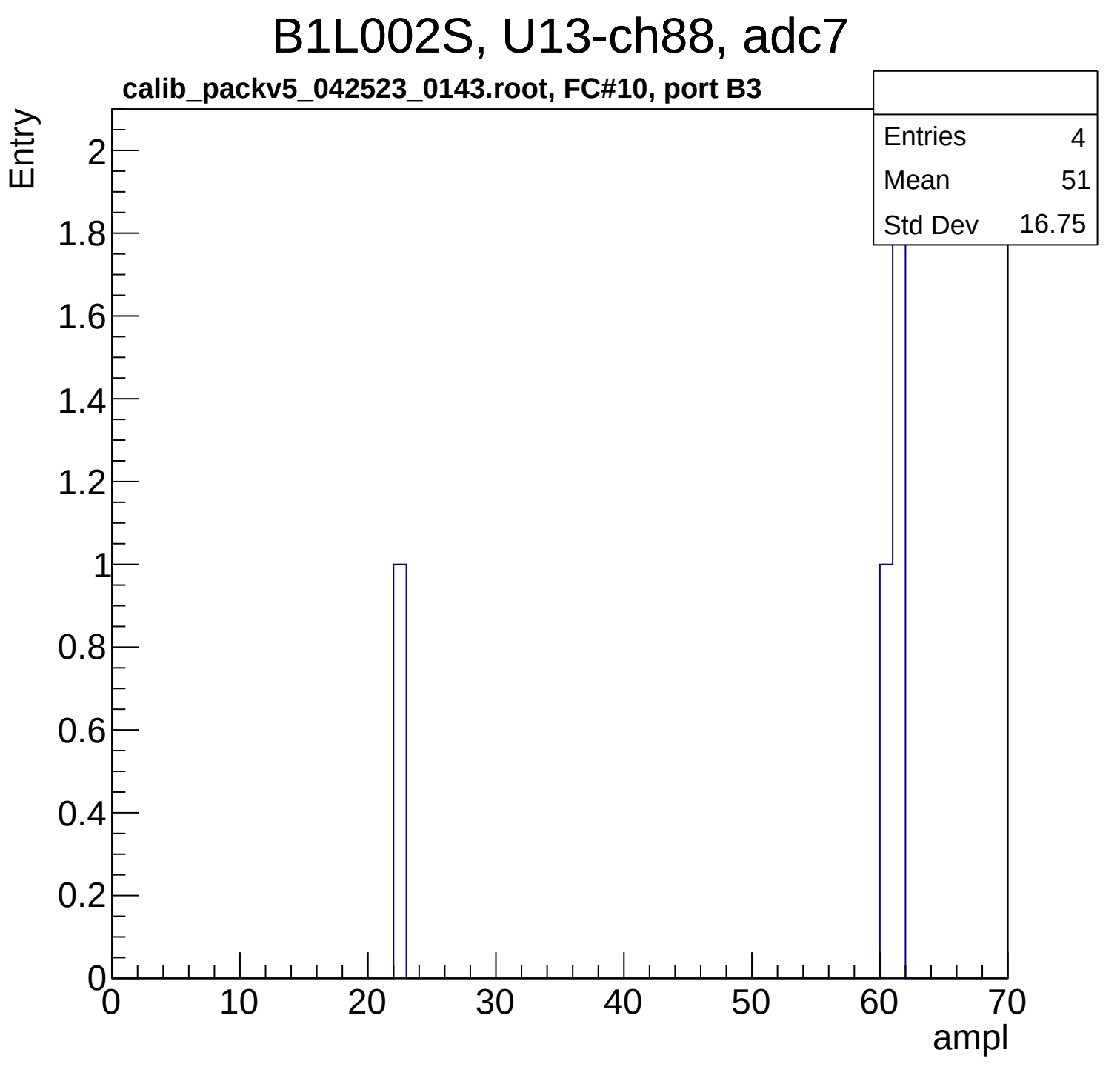
B1L002S, U13-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51
Std Dev 16.75
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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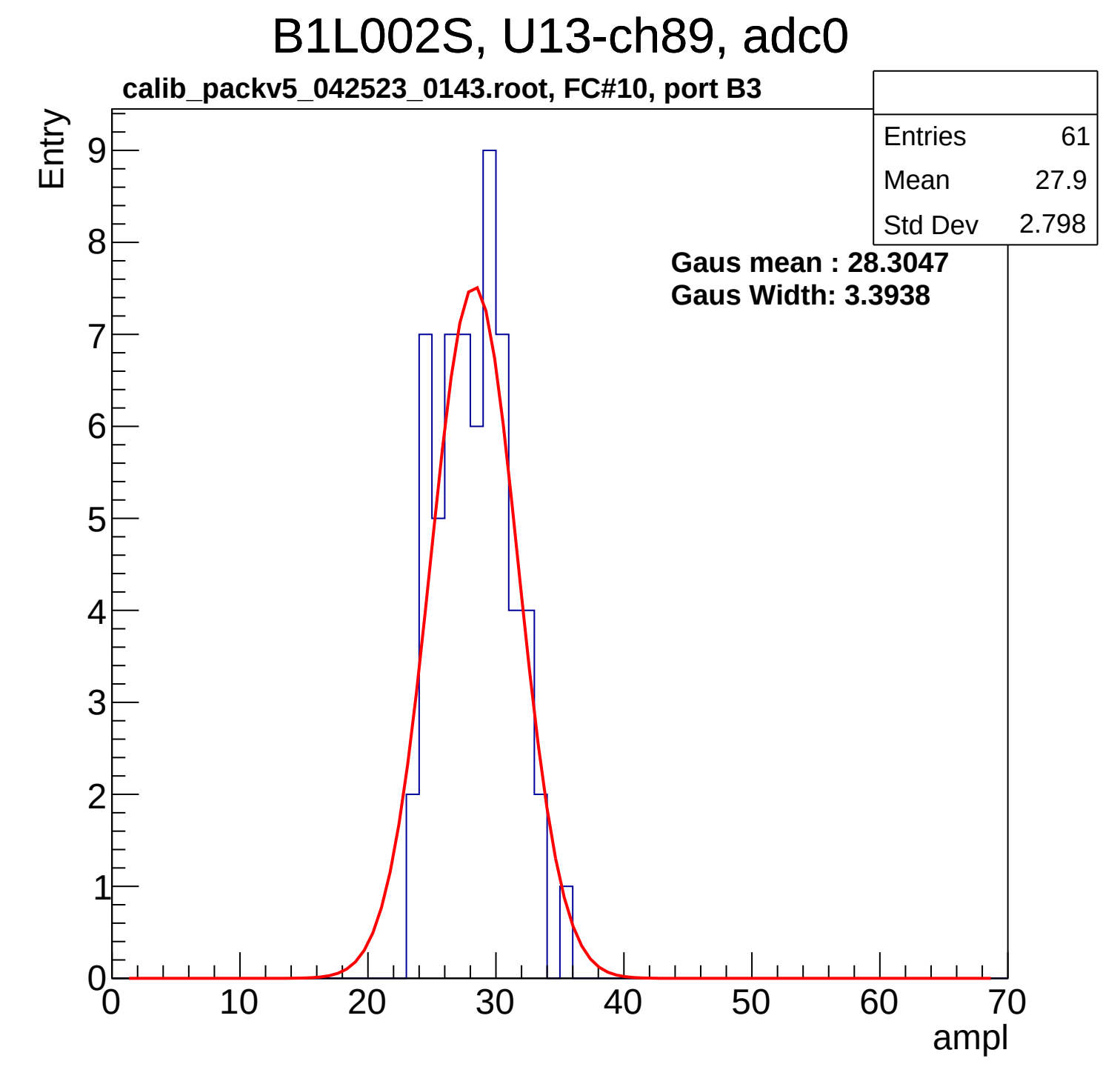
B1L002S, U13-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.9
Std Dev 2.798
Gaus mean : 28.3047
Gaus Width: 3.3938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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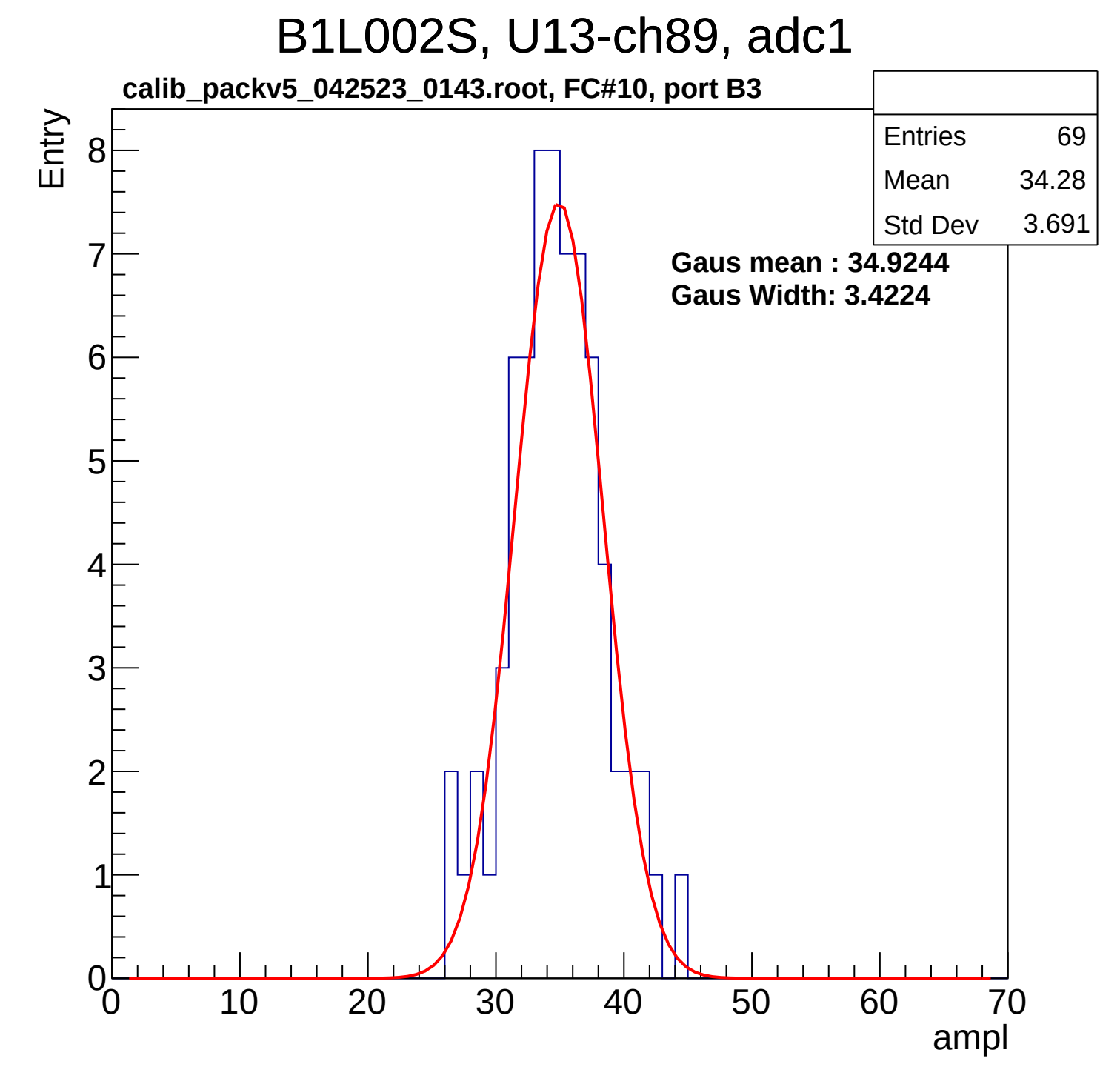
B1L002S, U13-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.28
Std Dev 3.691
Gaus mean : 34.9244
Gaus Width: 3.4224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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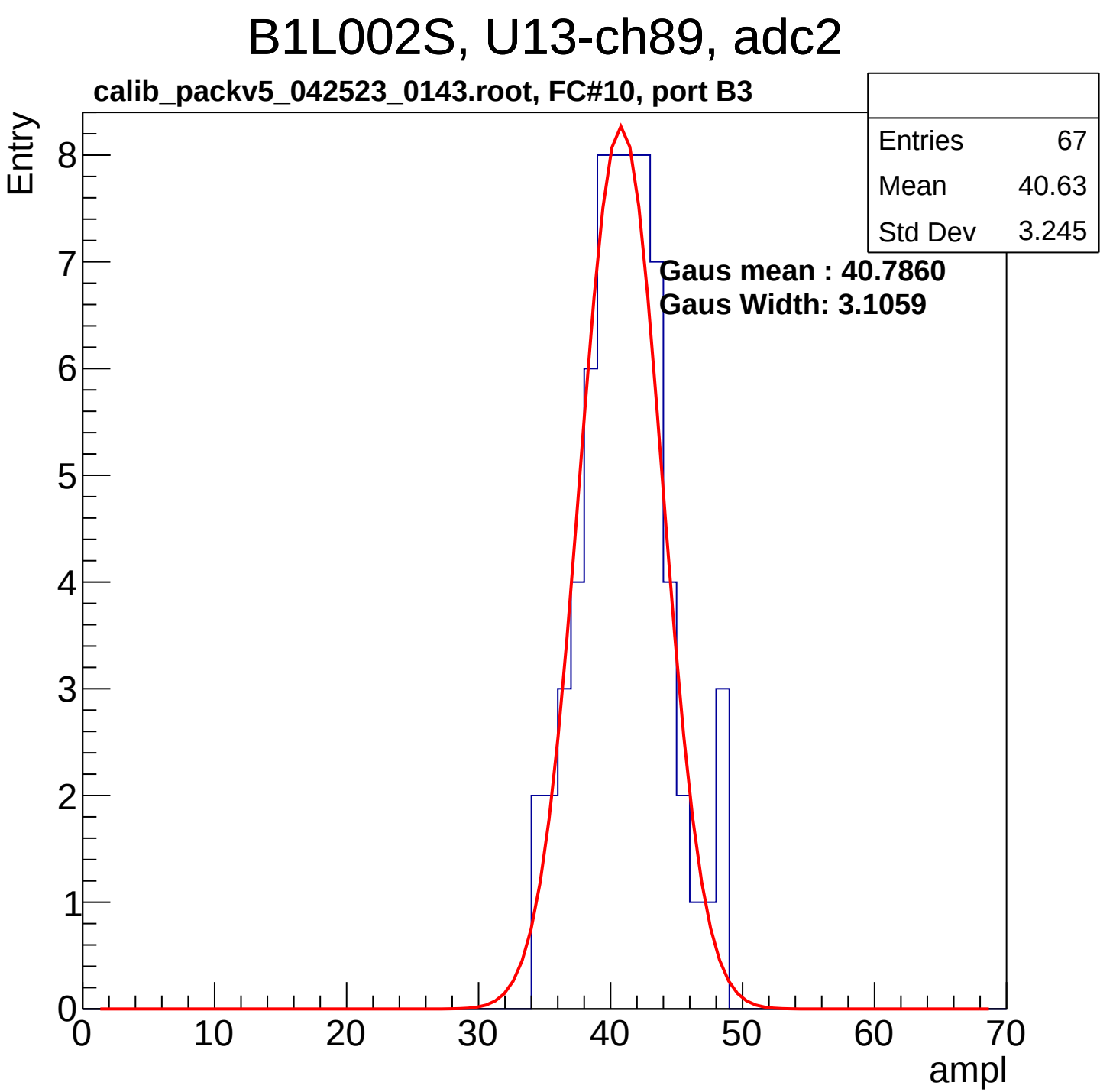
B1L002S, U13-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.63
Std Dev 3.245
Gaus mean : 40.7860
Gaus Width: 3.1059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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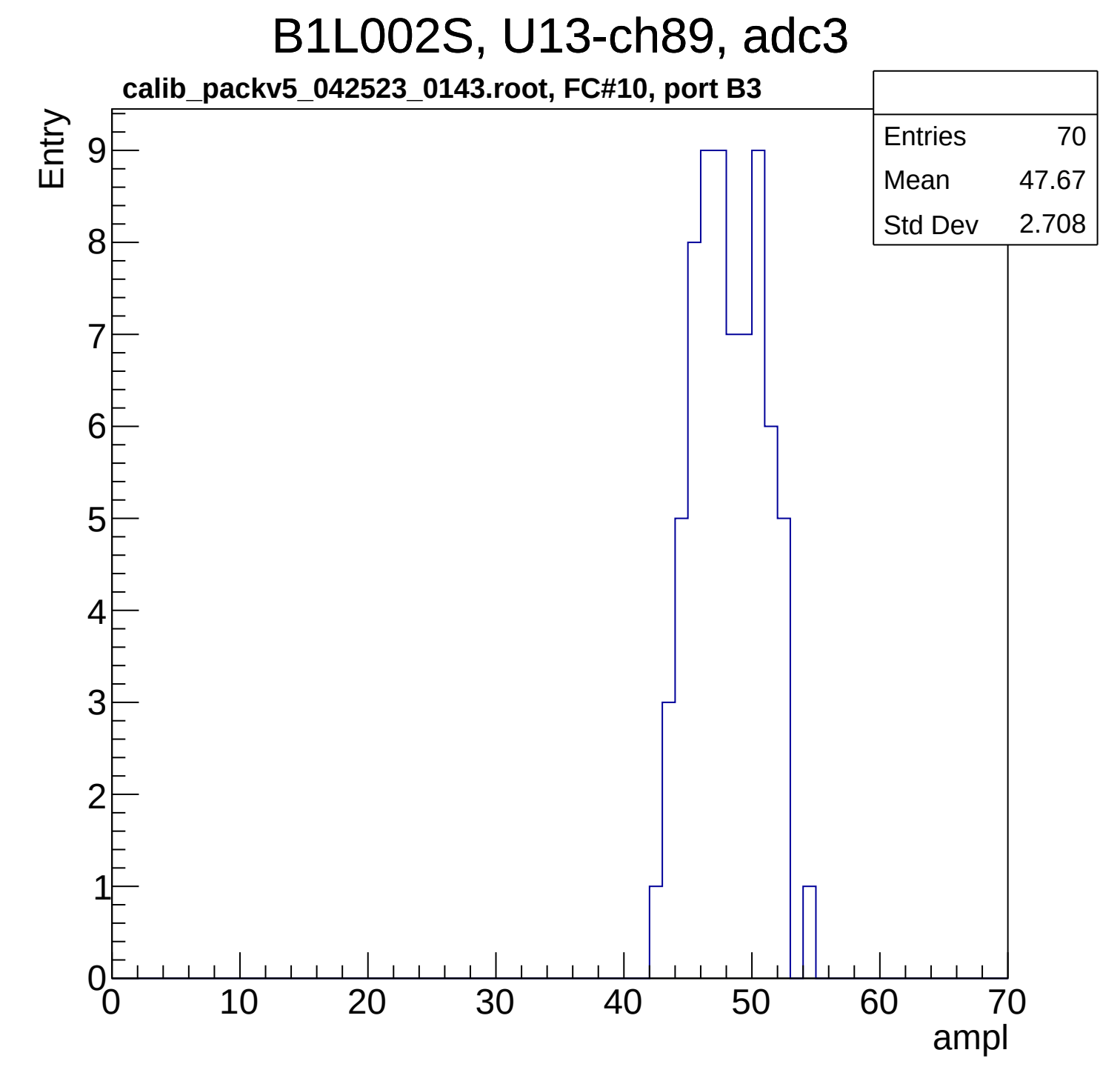
B1L002S, U13-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.67
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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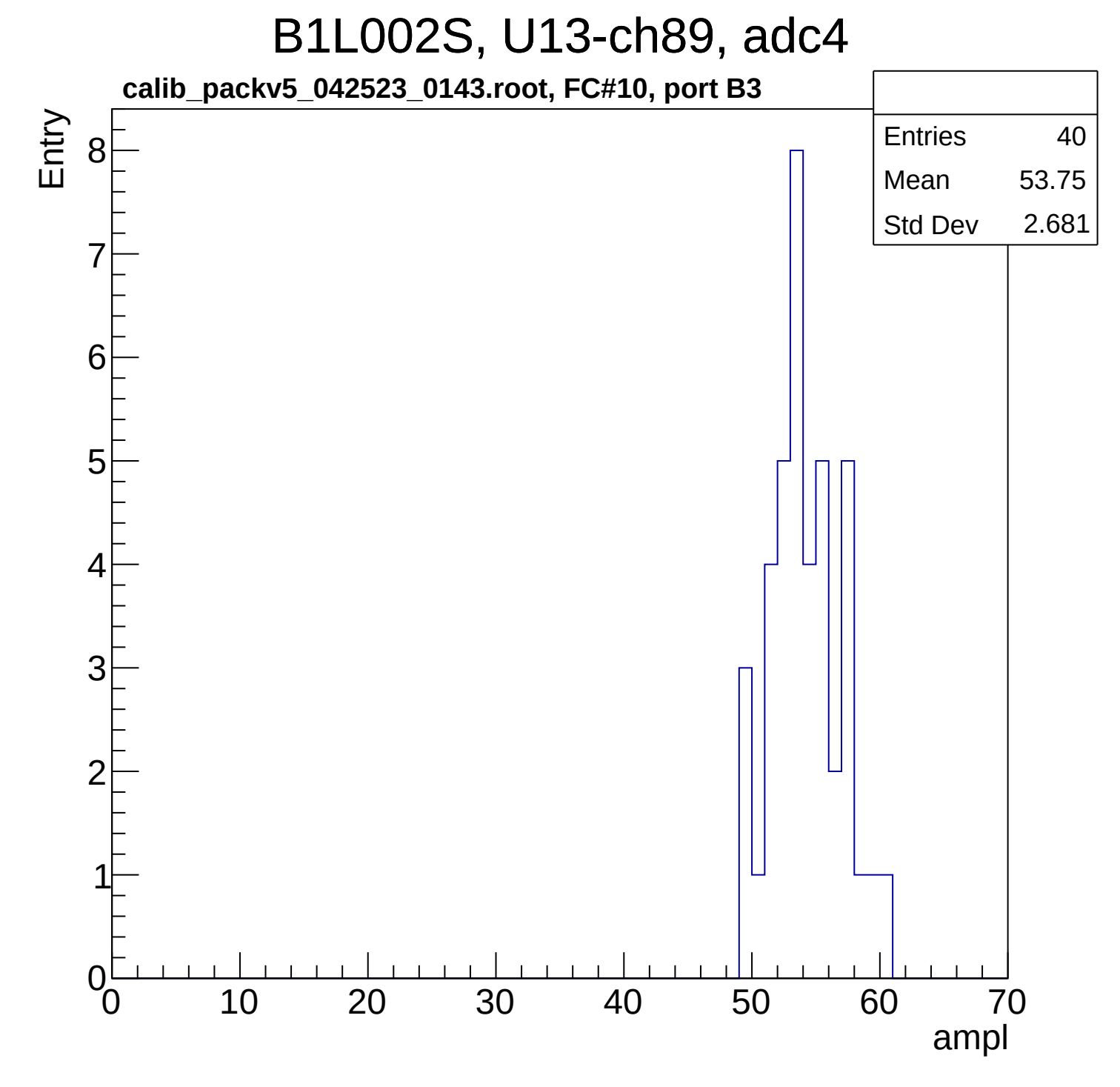
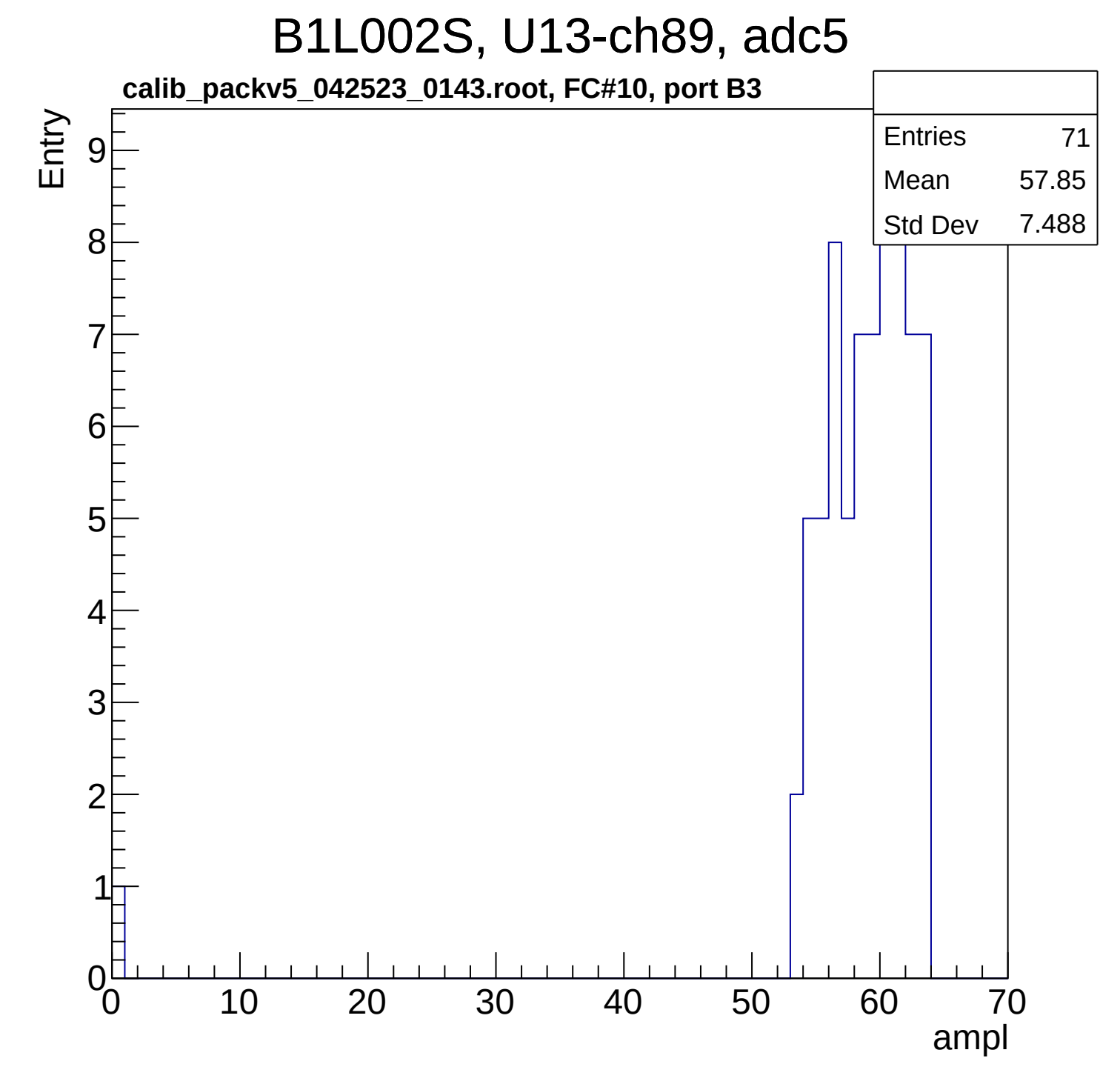

B1L002S, U13-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 53.75
Std Dev 2.681
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.85
Std Dev 7.488
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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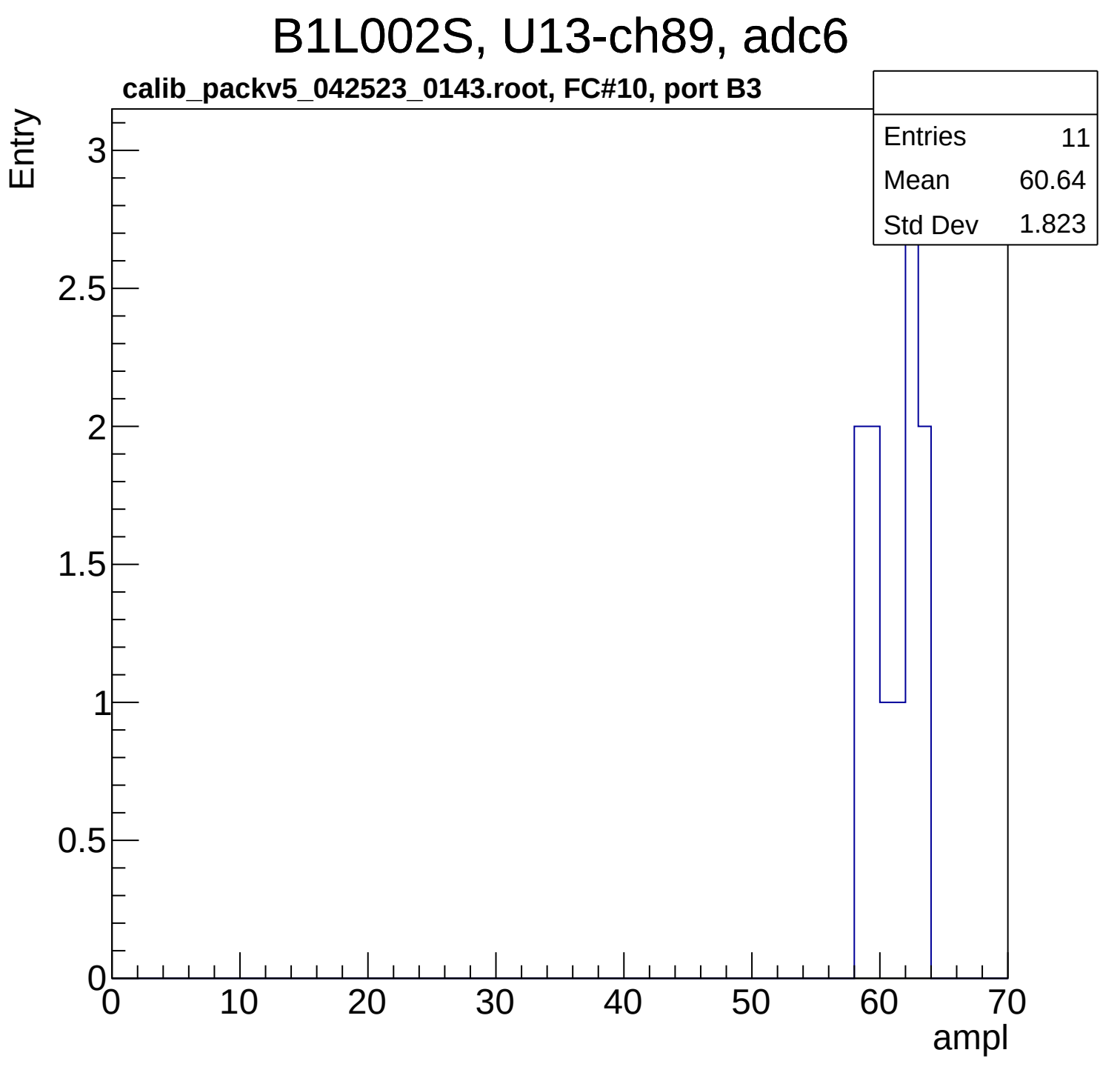
B1L002S, U13-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 60.64
Std Dev 1.823
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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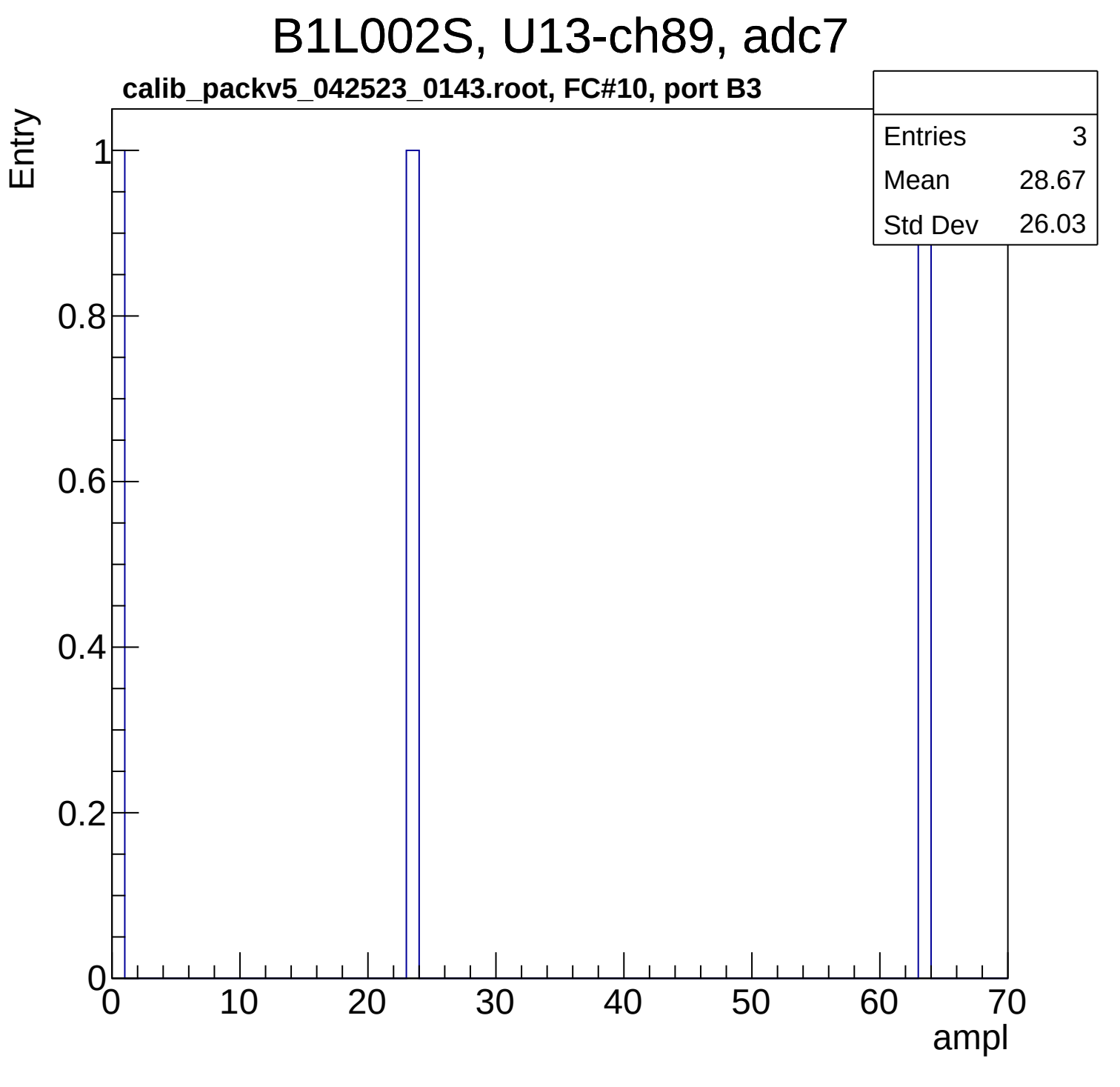
B1L002S, U13-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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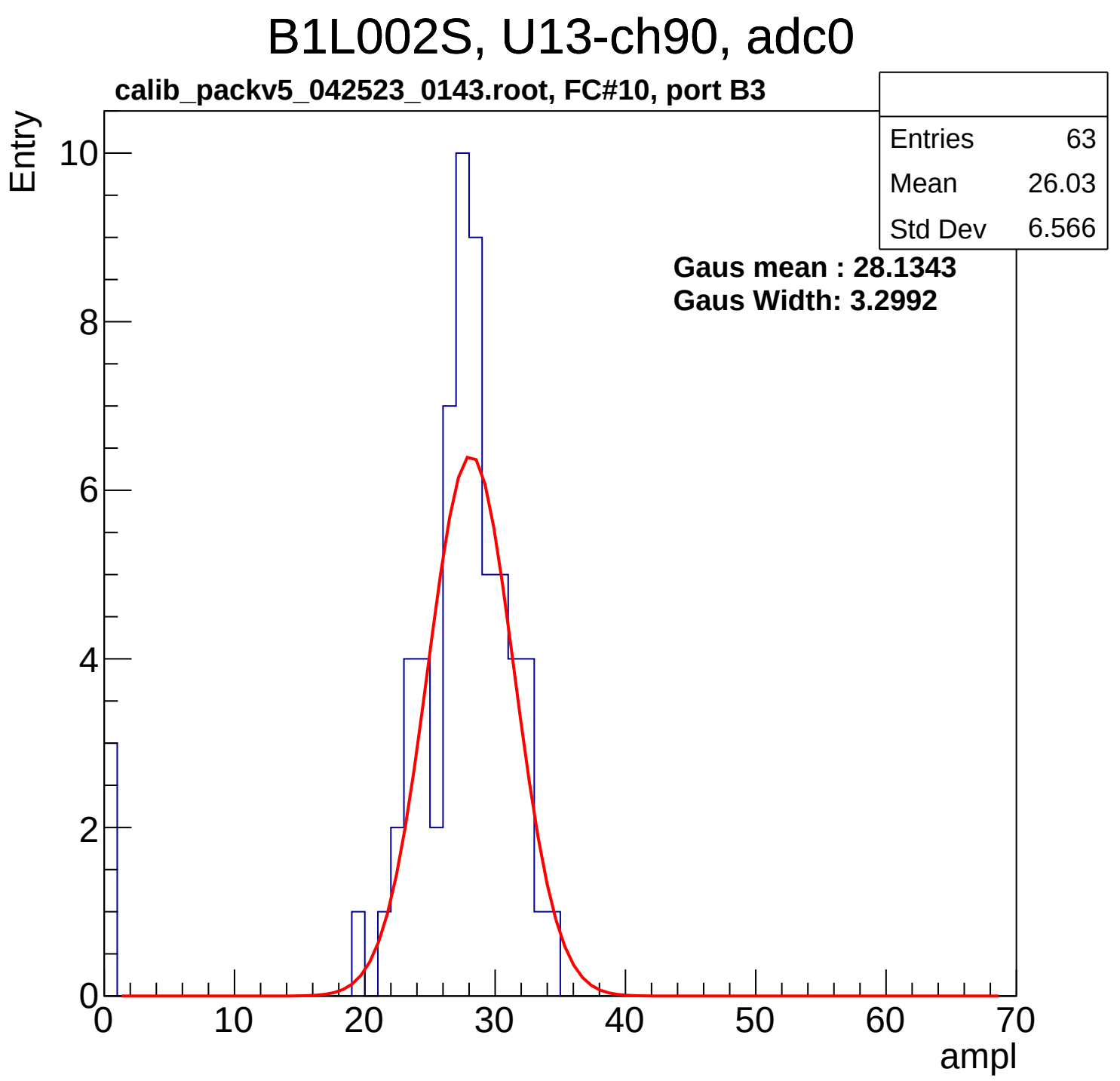
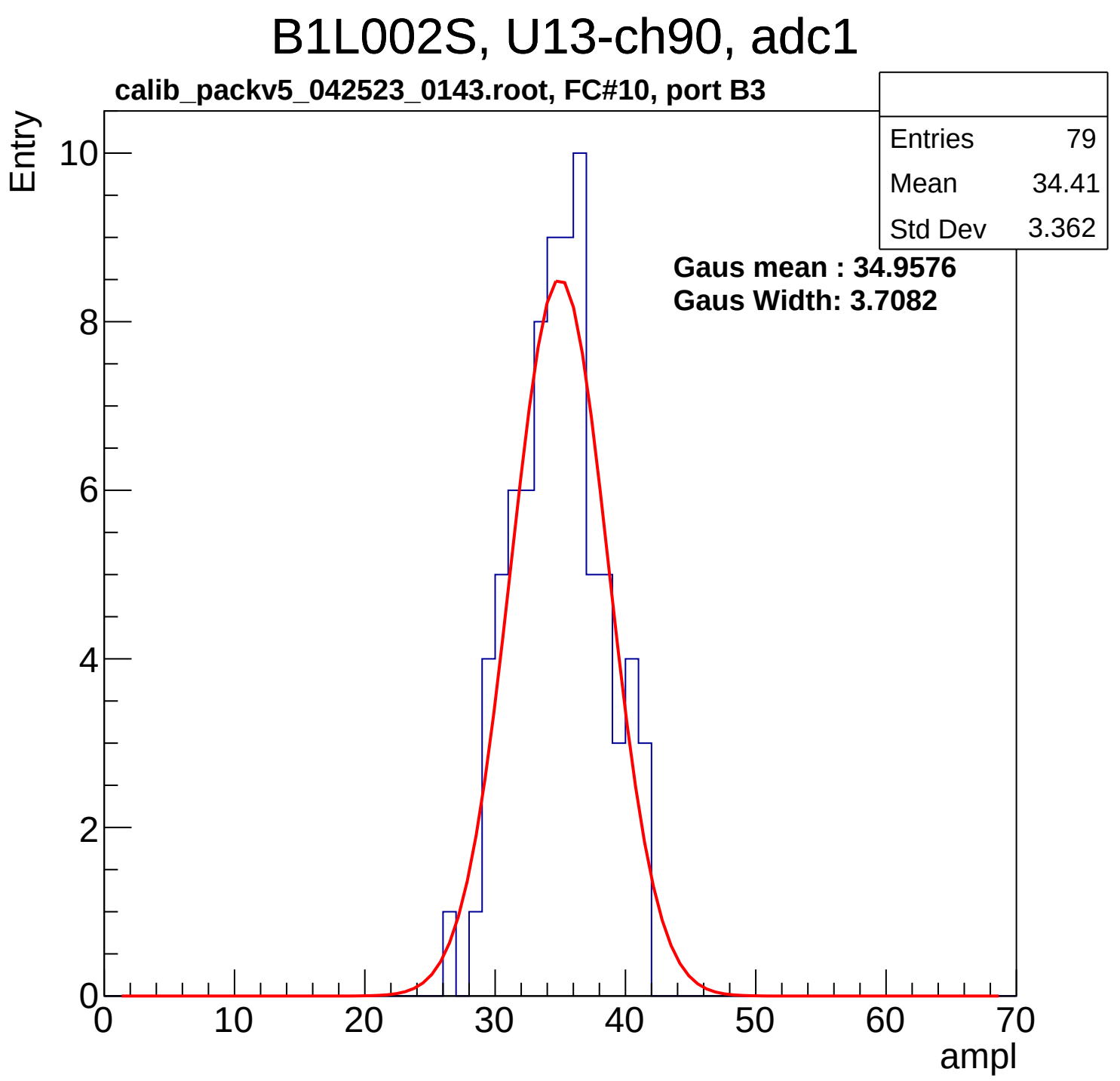
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U13-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 26.03
Std Dev 6.566
Gaus mean : 28.1343
Gaus Width: 3.2992
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.41
Std Dev 3.362
Gaus mean : 34.9576
Gaus Width: 3.7082
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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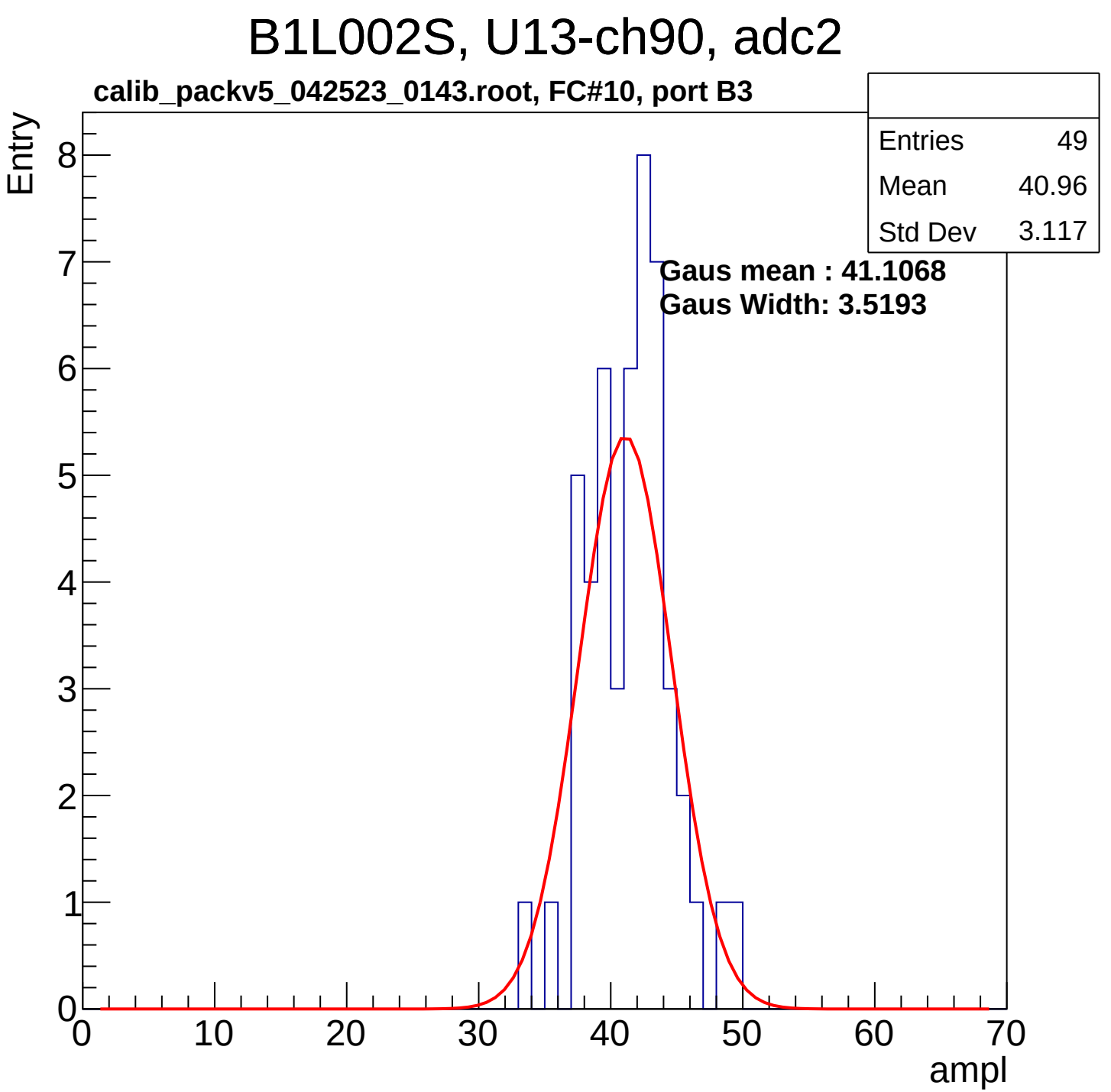
B1L002S, U13-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.96
Std Dev 3.117
Gaus mean : 41.1068
Gaus Width: 3.5193
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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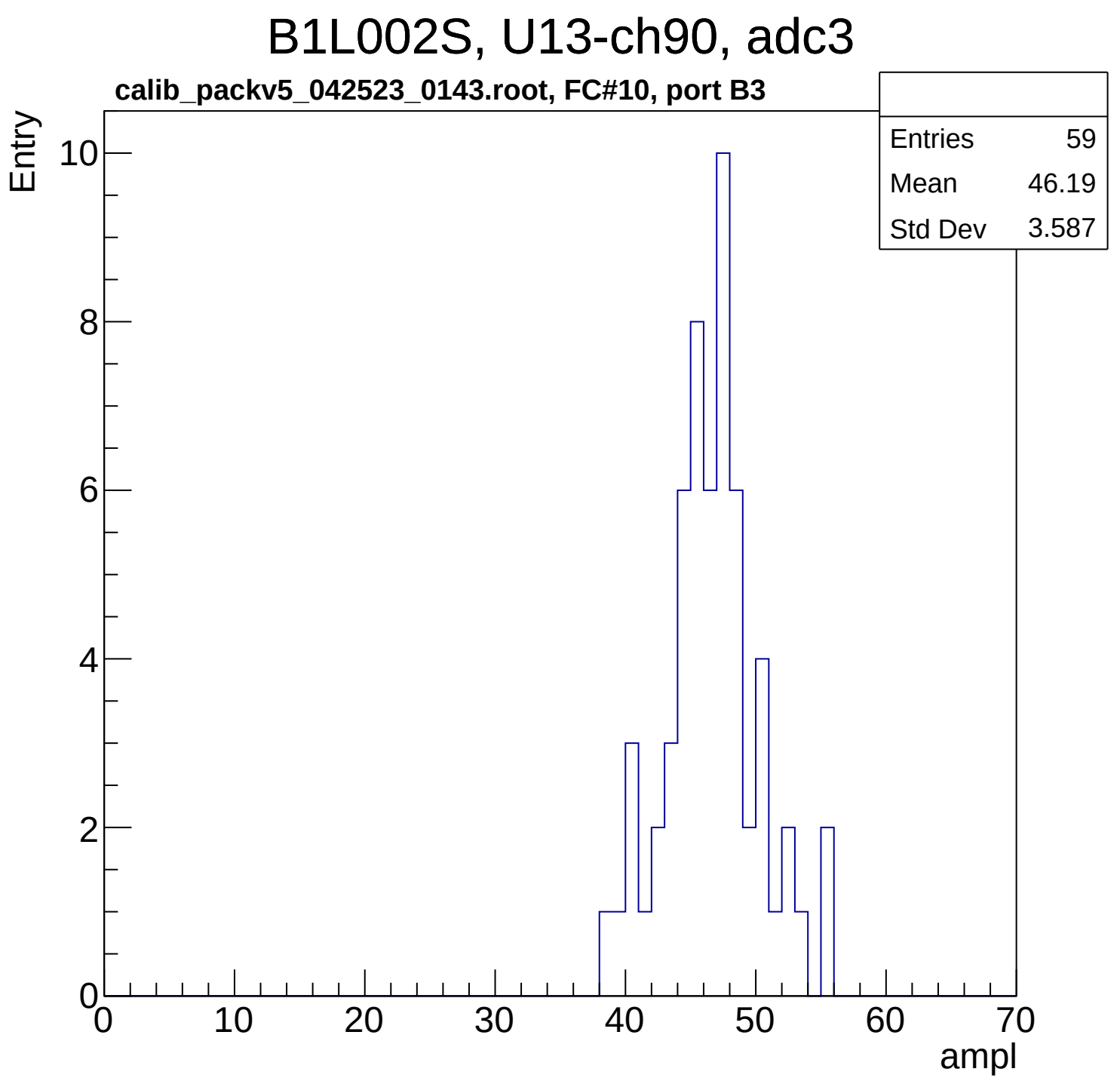
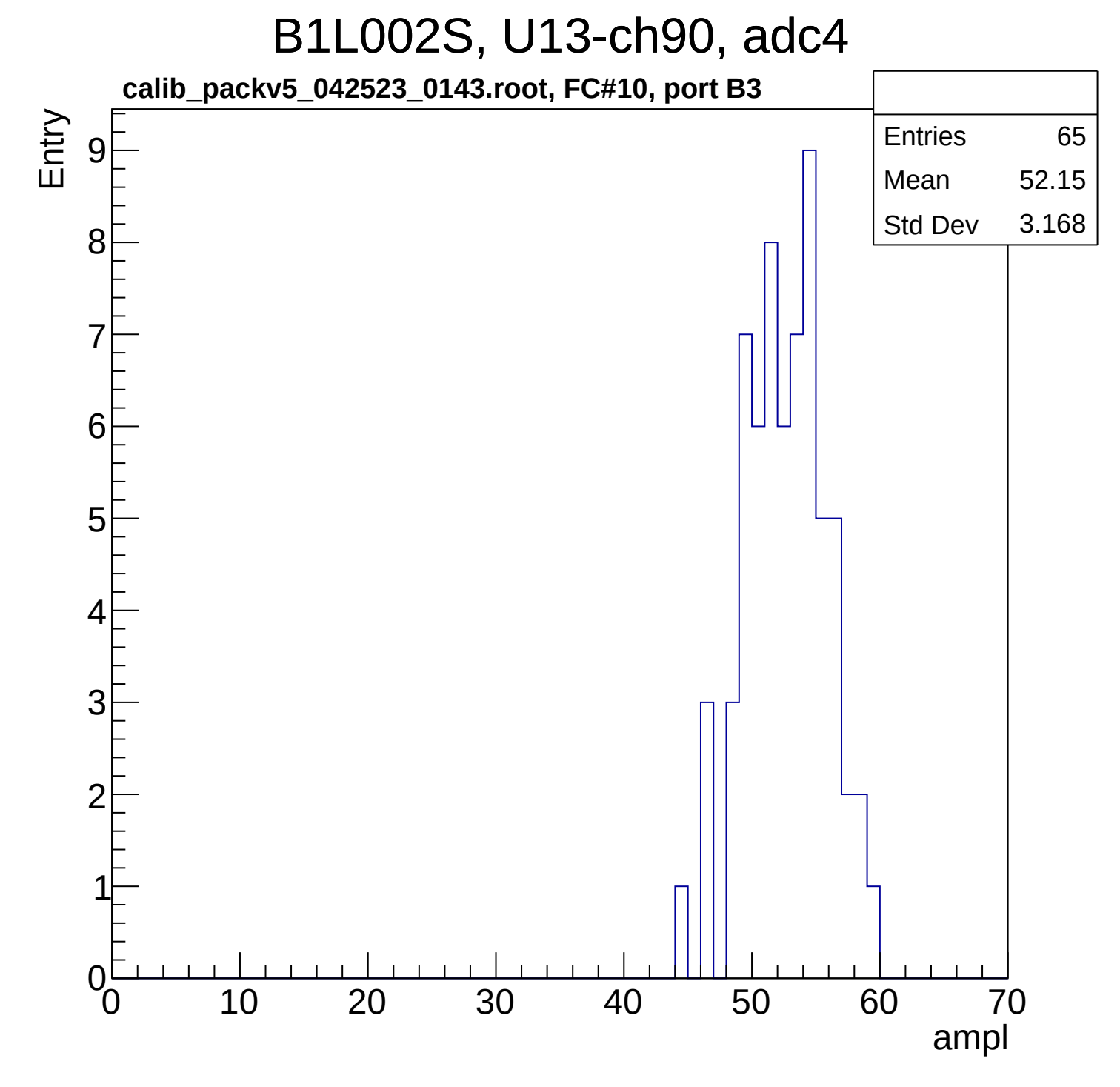

B1L002S, U13-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.19
Std Dev 3.587
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.15
Std Dev 3.168
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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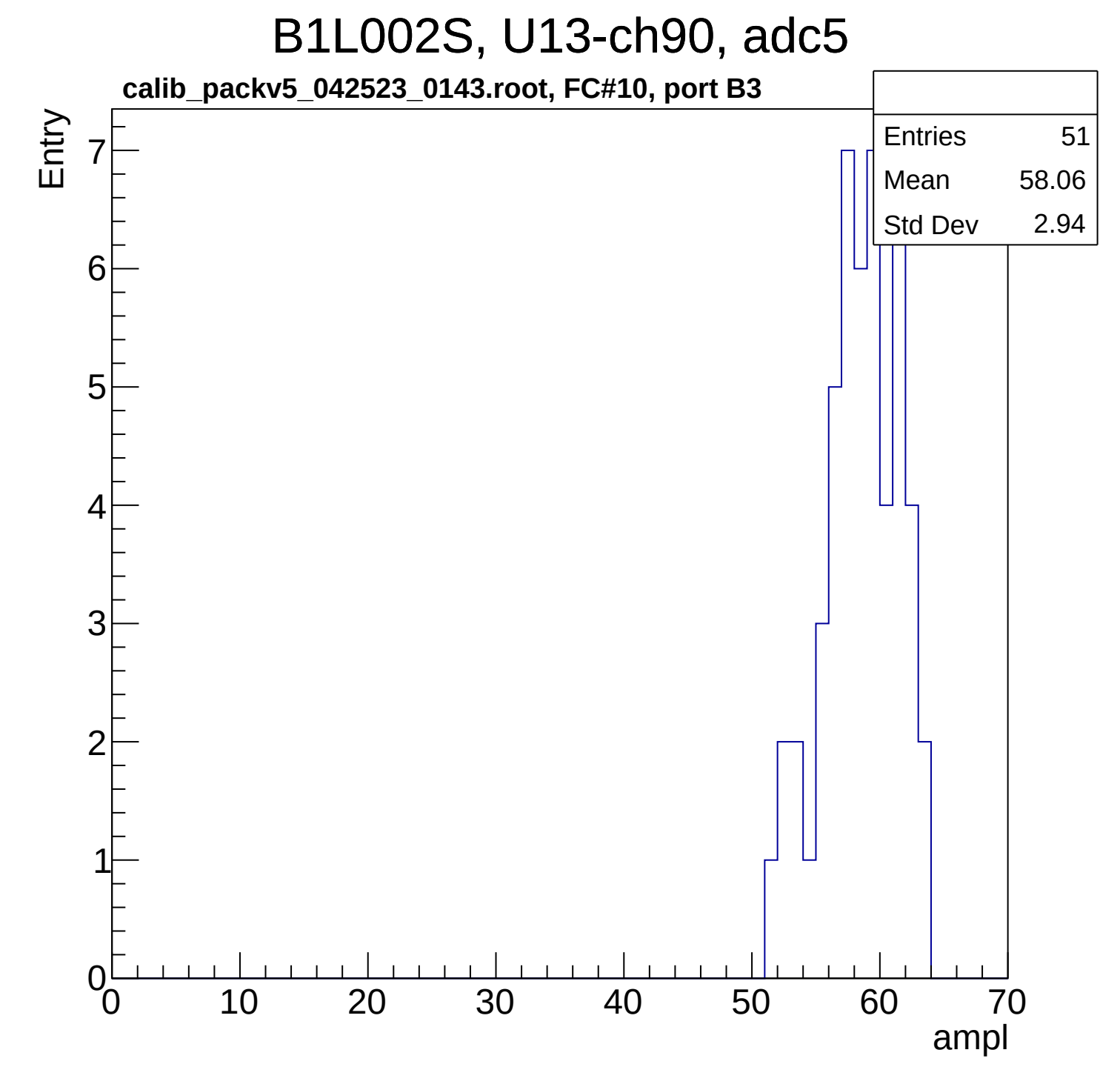
B1L002S, U13-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.06
Std Dev 2.94
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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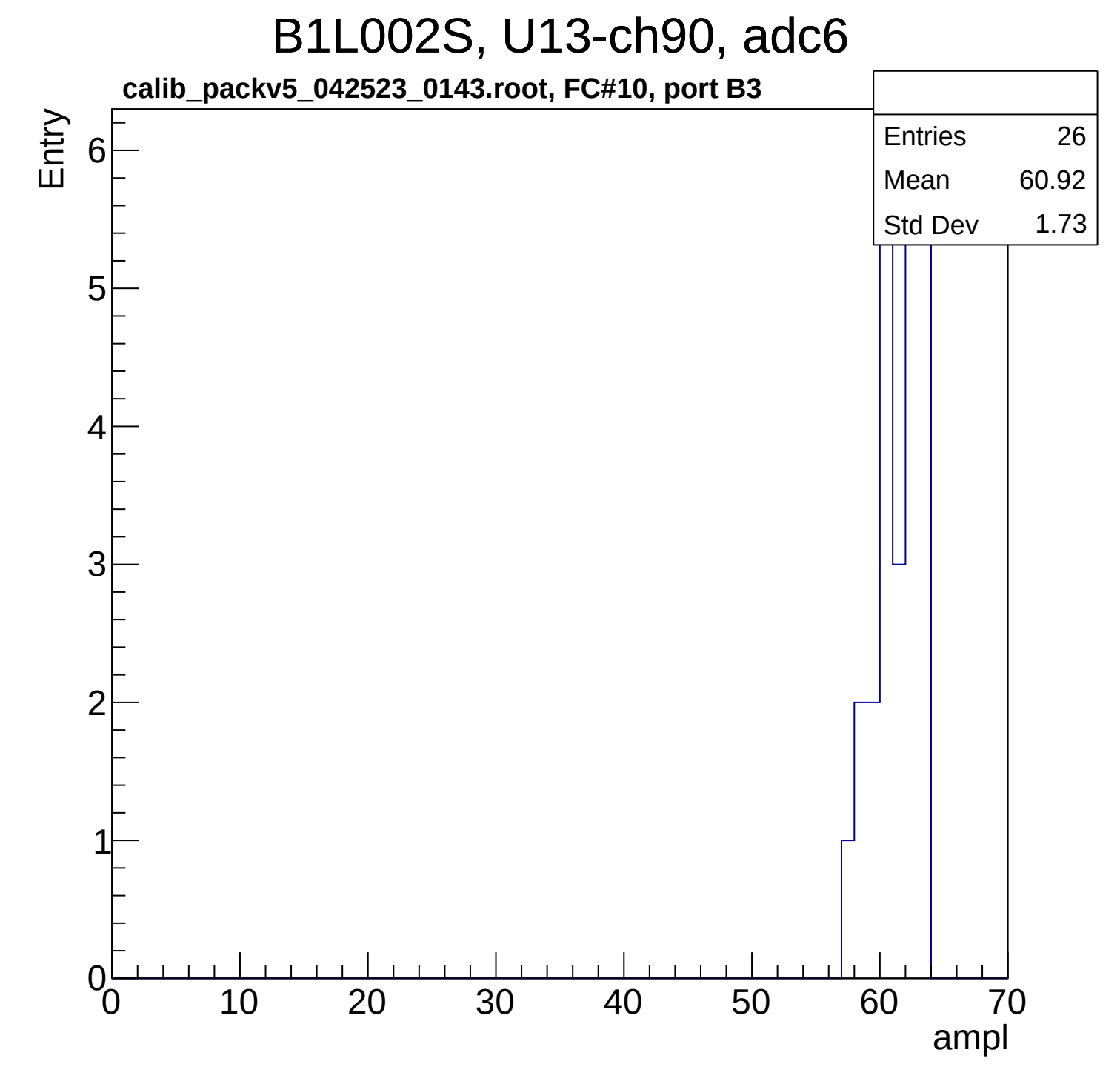
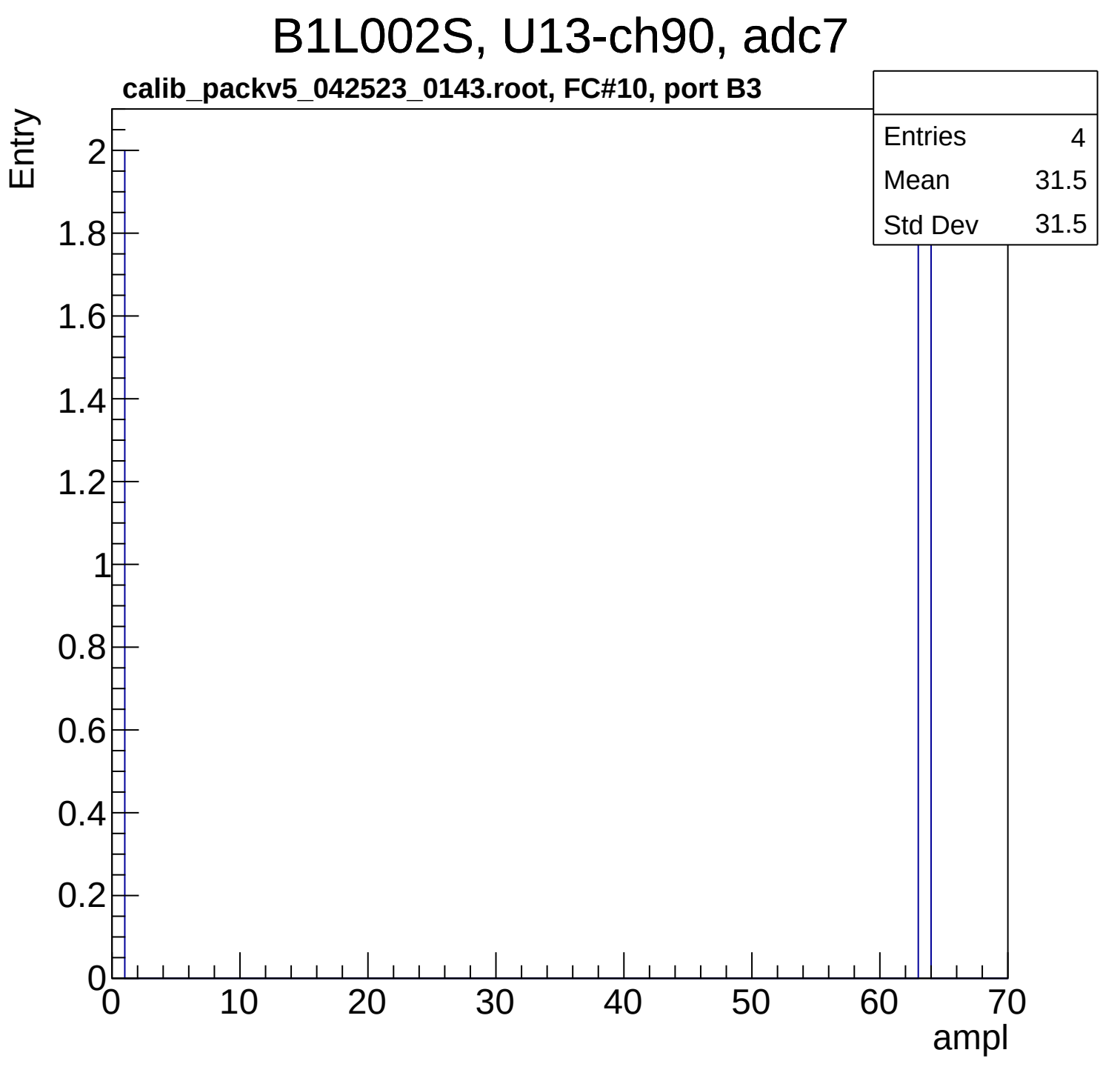

B1L002S, U13-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 60.92
Std Dev 1.73
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
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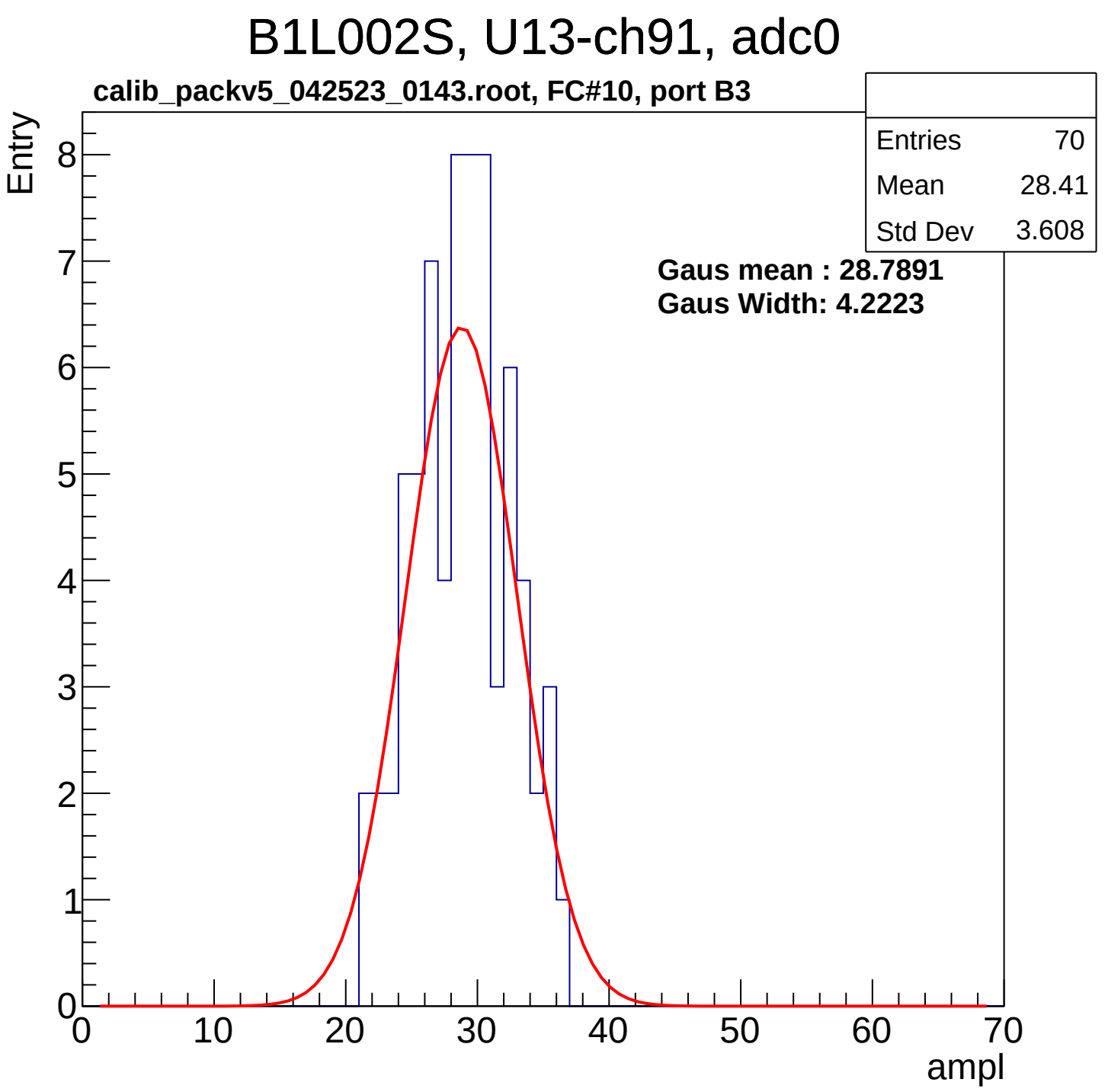
60
70


B1L002S, U13-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.41
Std Dev 3.608
Gaus mean : 28.7891
Gaus Width: 4.2223
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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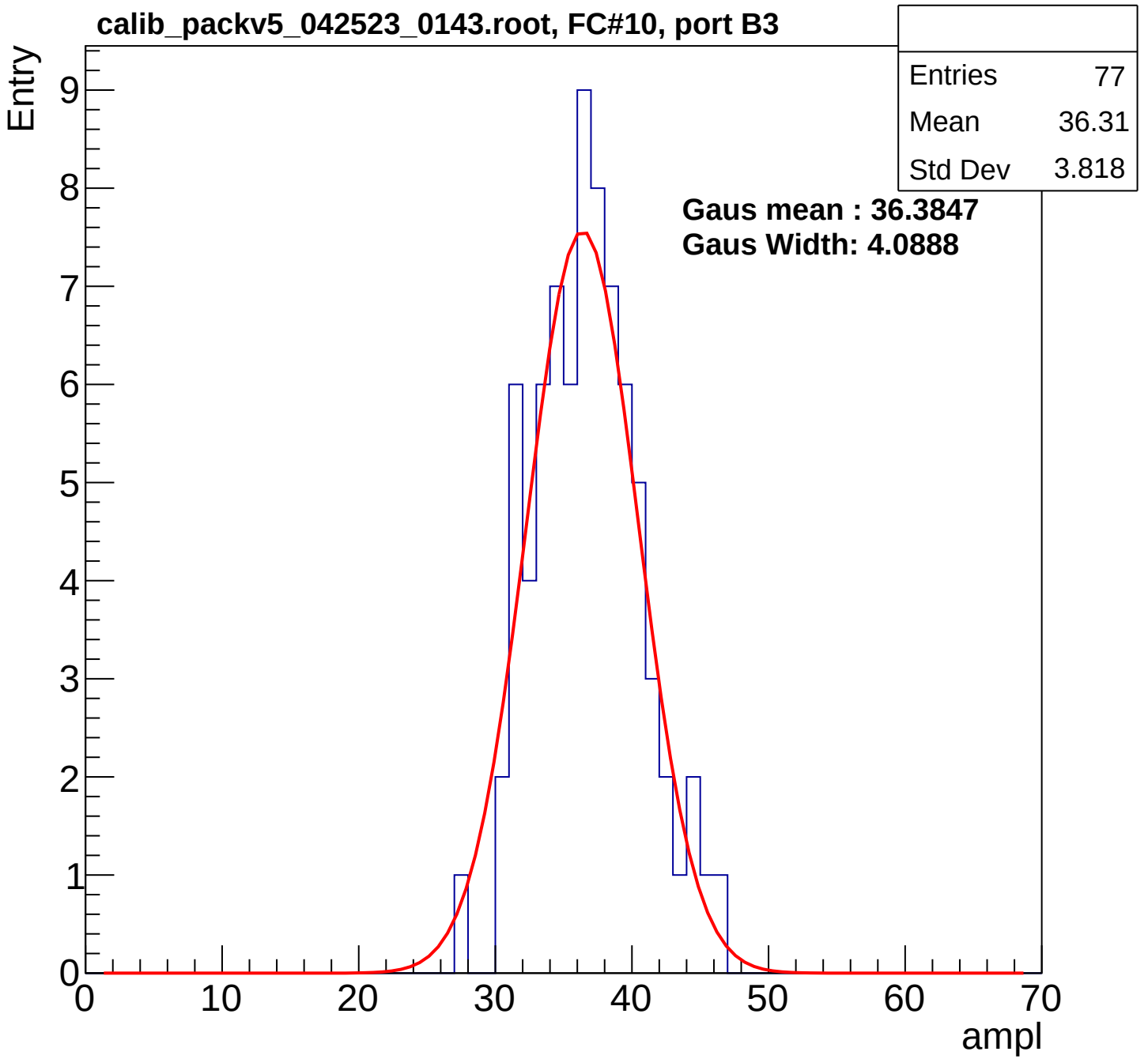
B1L002S, U13-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 36.31
Std Dev 3.818
Gaus mean : 36.3847
Gaus Width: 4.0888
Entry
ampl
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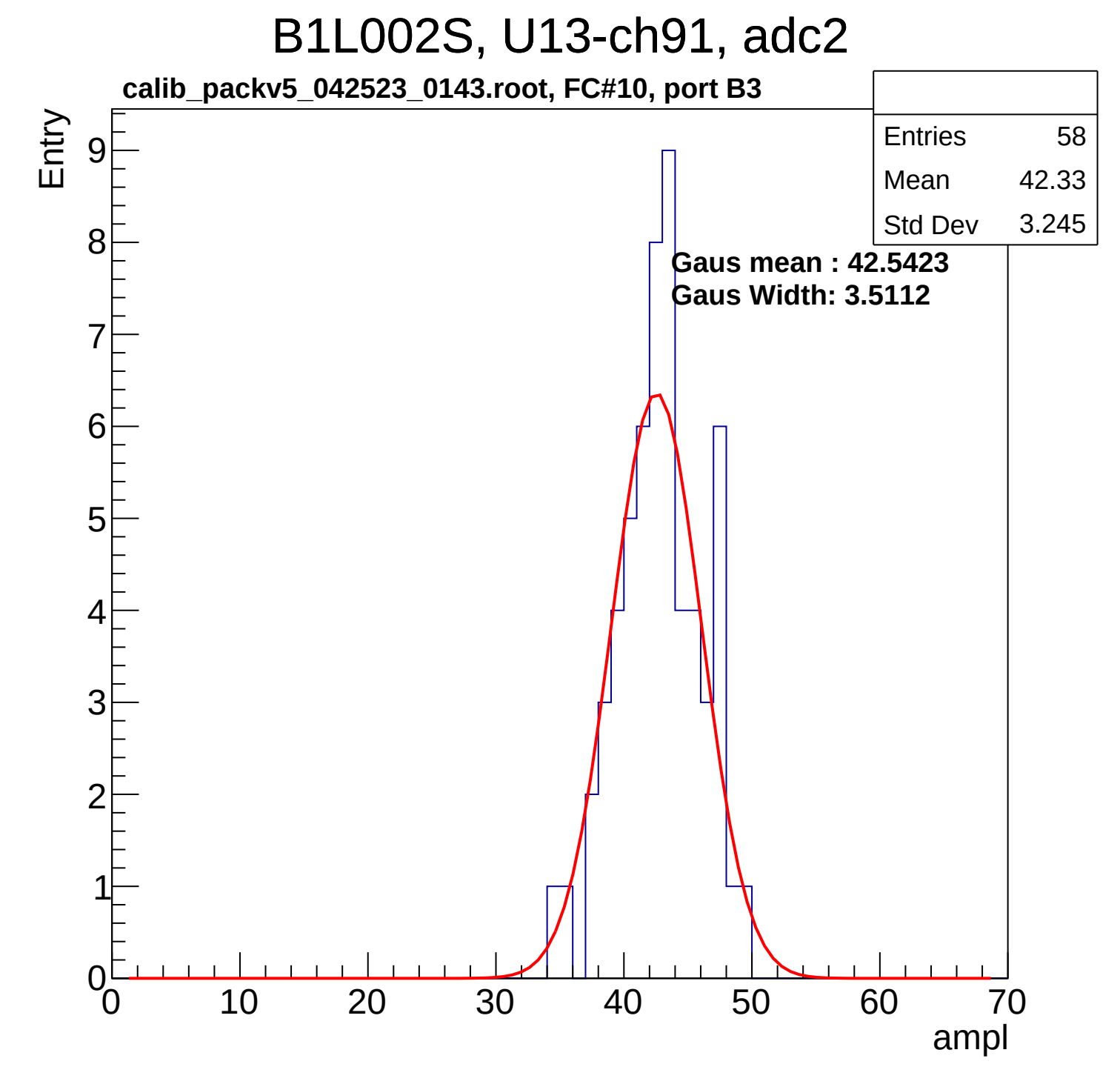
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.33
Std Dev 3.245
Gaus mean : 42.5423
Gaus Width: 3.5112
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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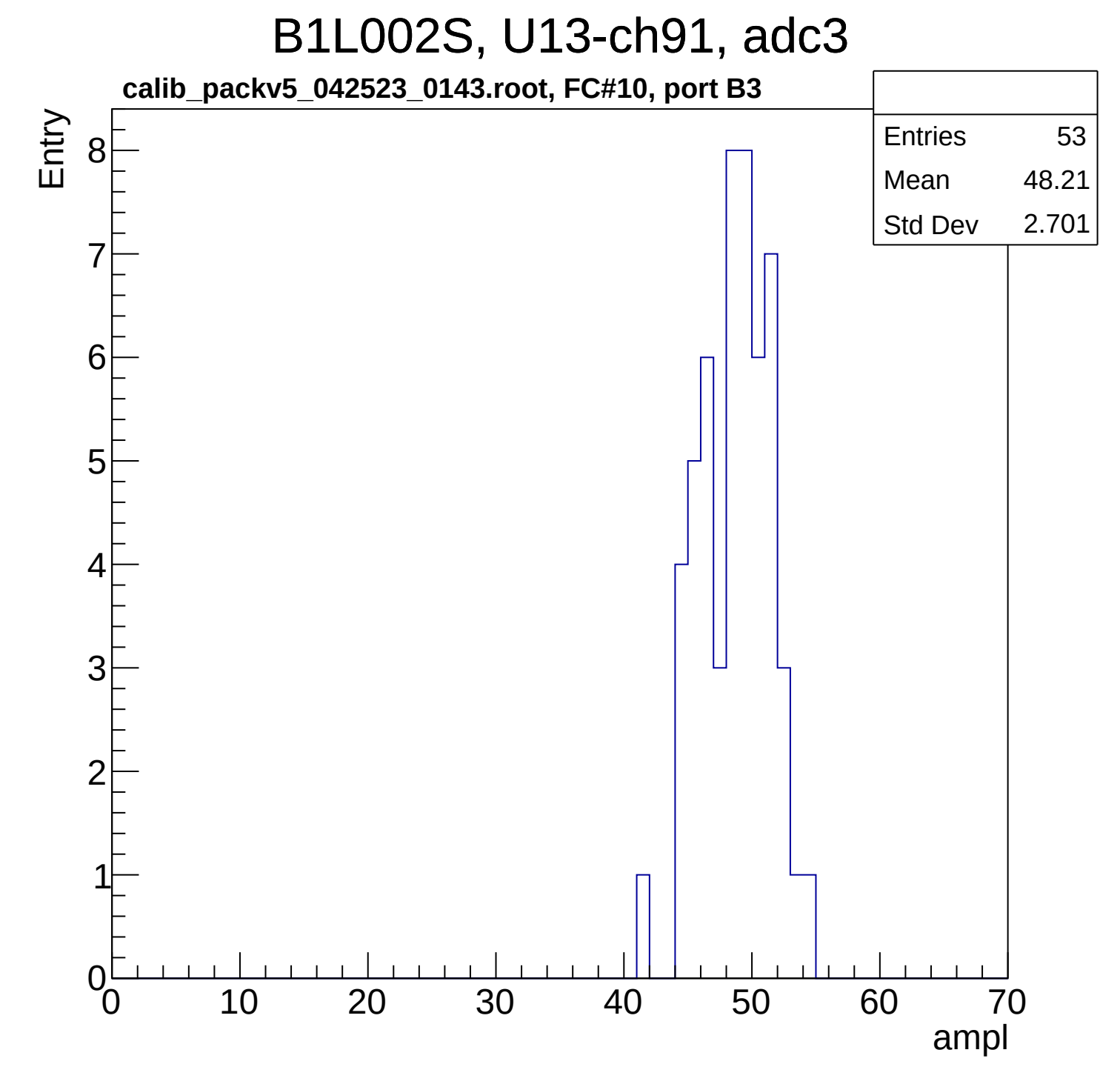
B1L002S, U13-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.21
Std Dev 2.701
Entry
ampl
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1
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4
5
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7
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0
10
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40
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60
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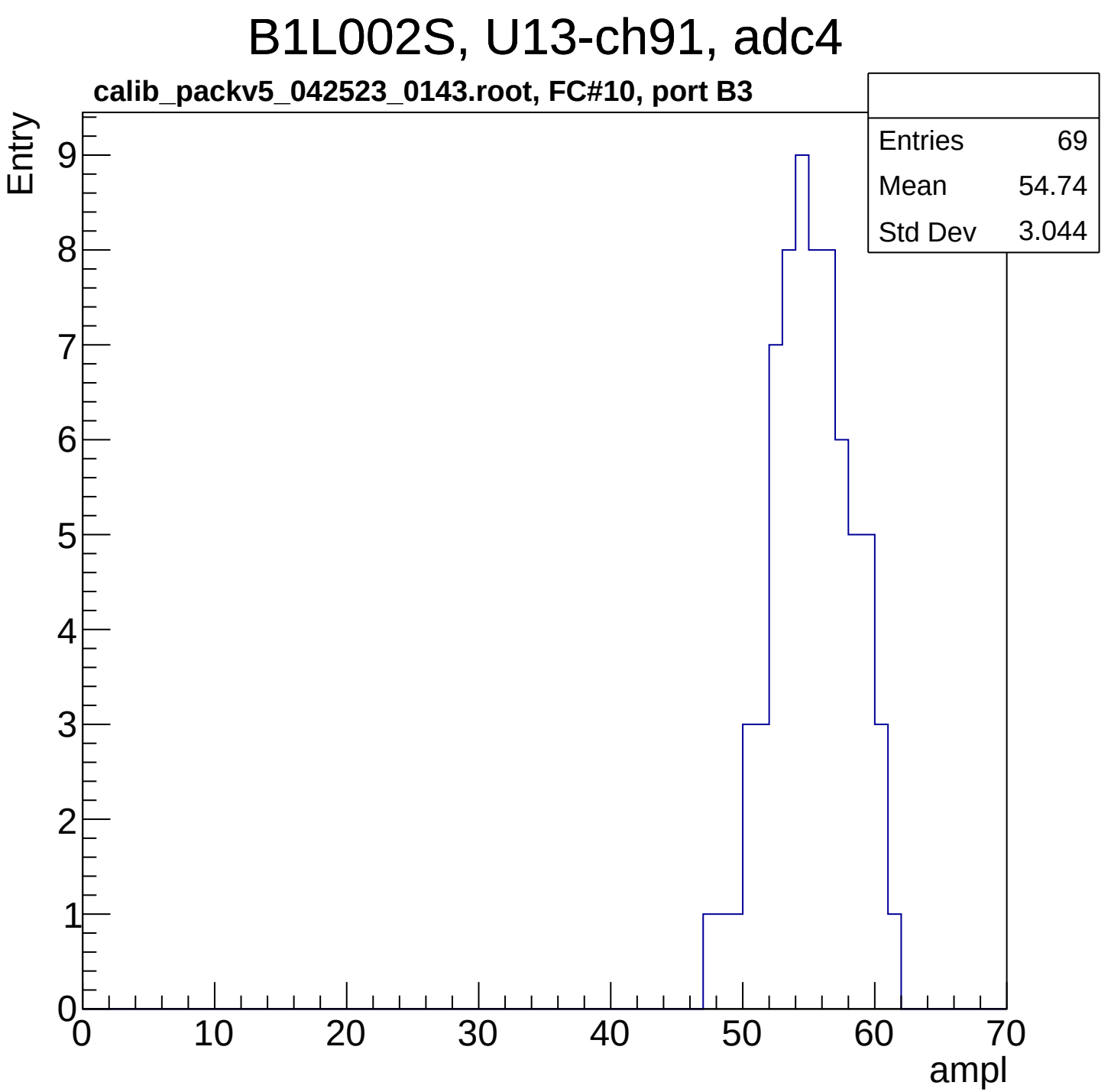
B1L002S, U13-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 54.74
Std Dev 3.044
Entry
ampl
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3
4
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6
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0
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60
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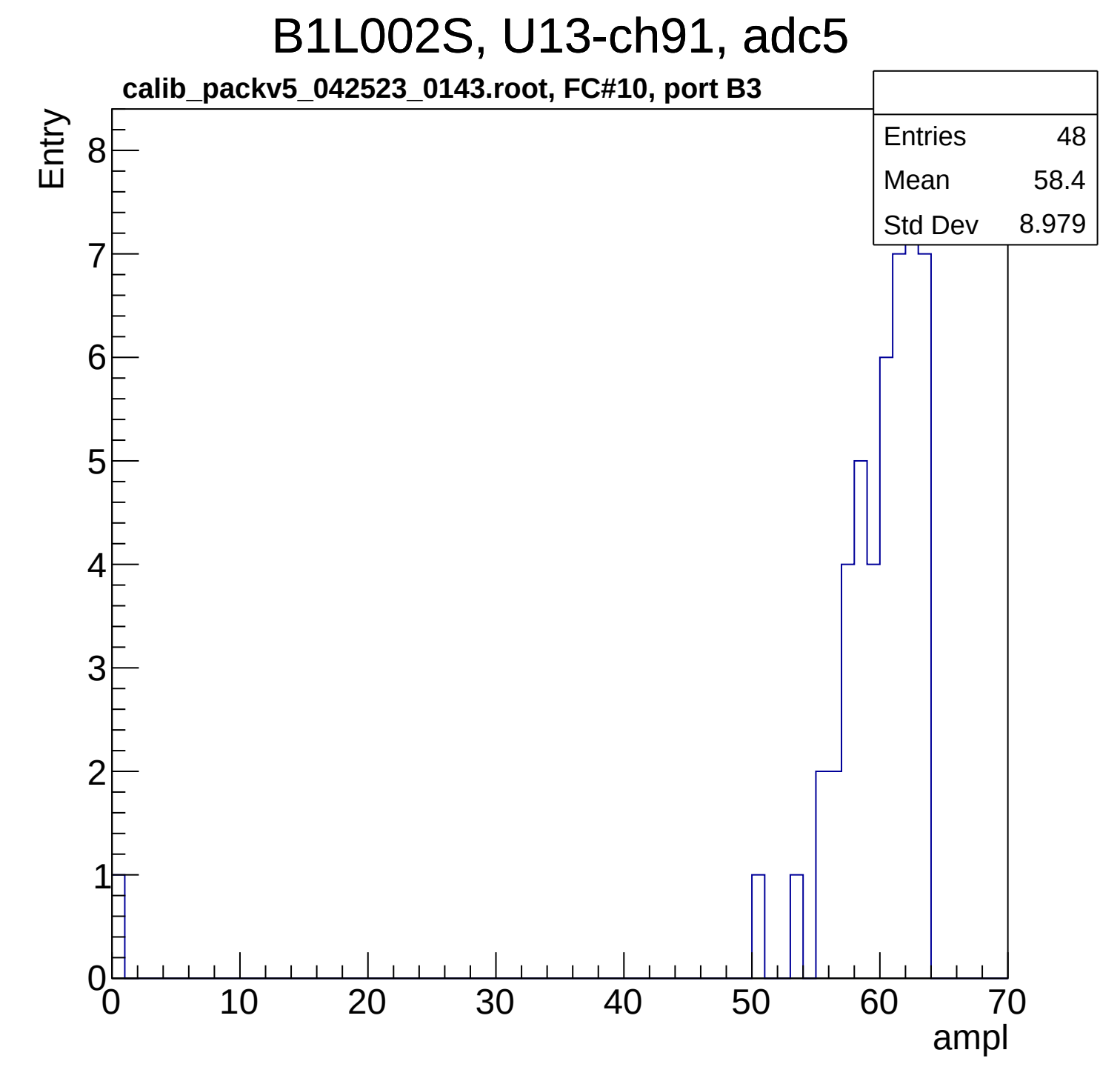
B1L002S, U13-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.4
Std Dev 8.979
Entry
ampl
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2
3
4
5
6
7
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0
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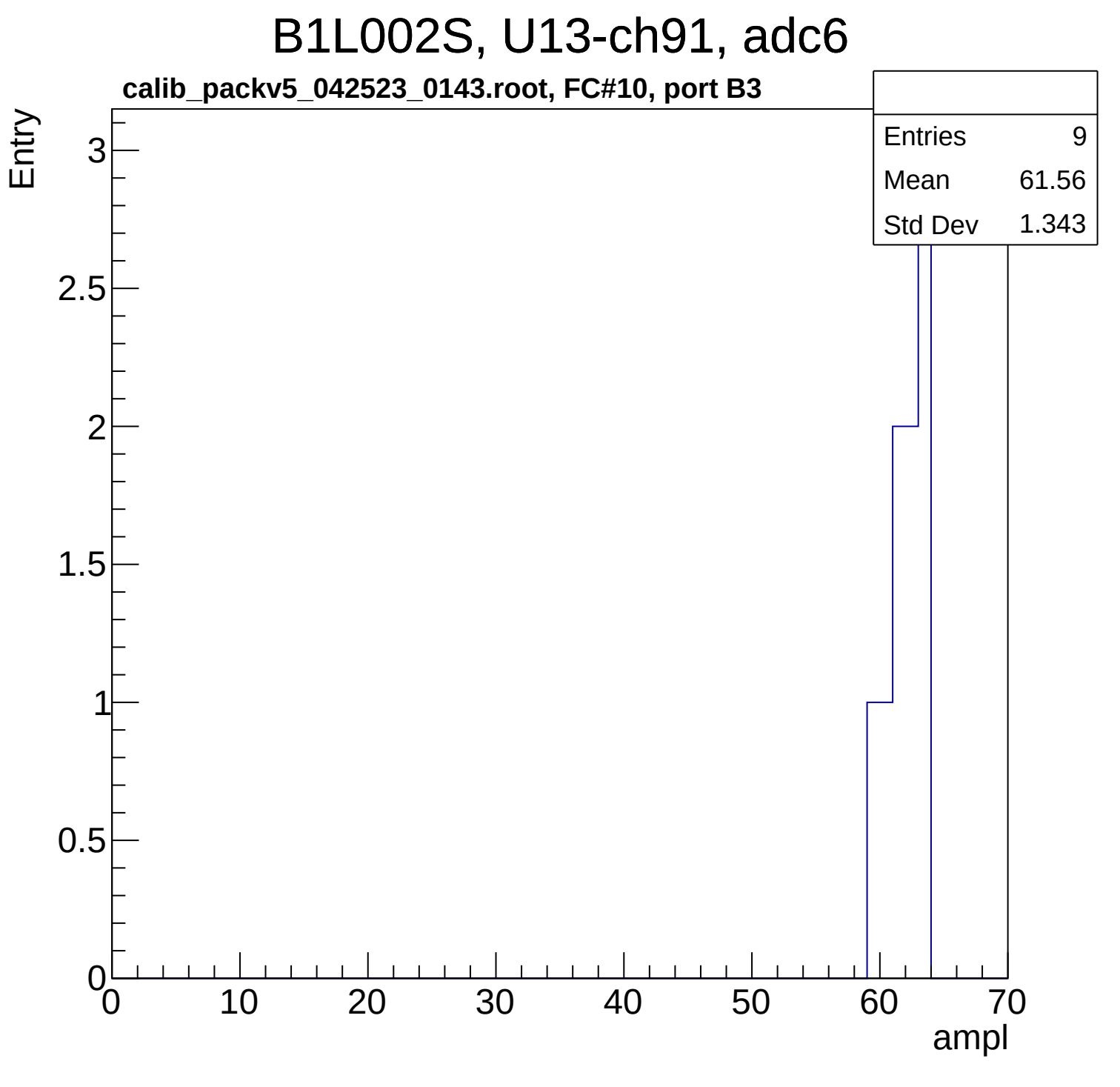
B1L002S, U13-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.56
Std Dev 1.343
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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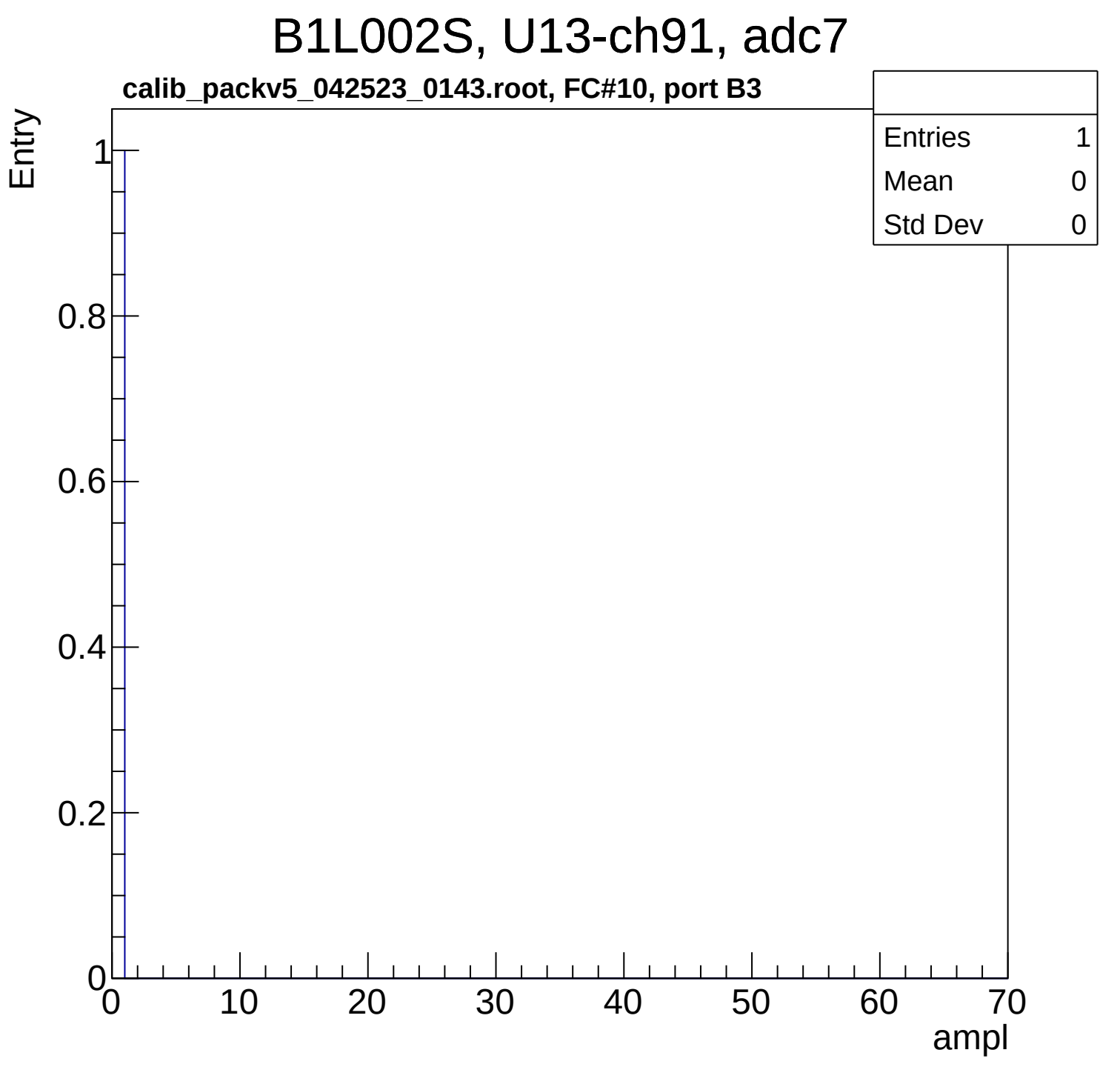
B1L002S, U13-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
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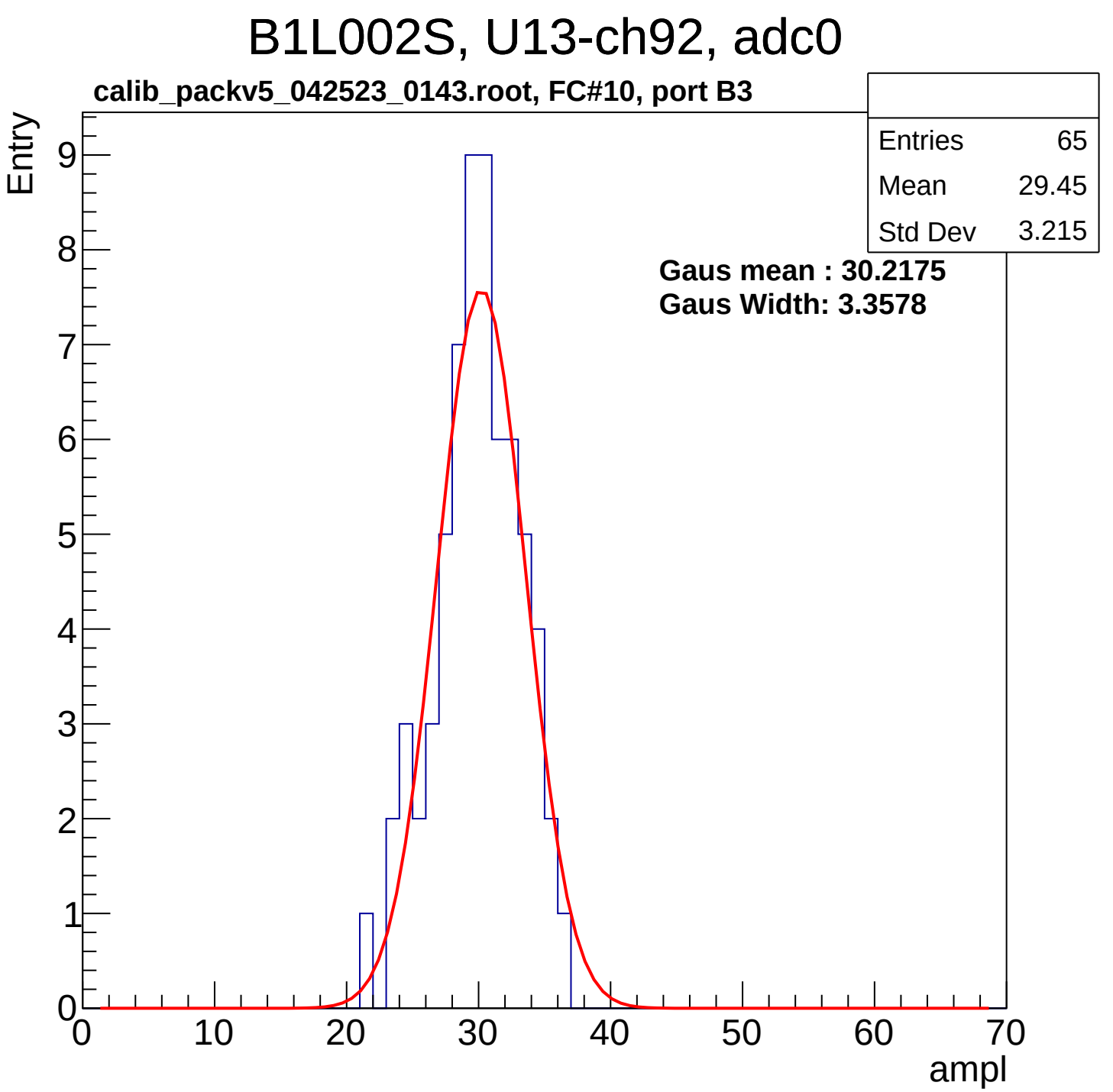
50
60
70
ampl


B1L002S, U13-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.45
Std Dev 3.215
Gaus mean : 30.2175
Gaus Width: 3.3578
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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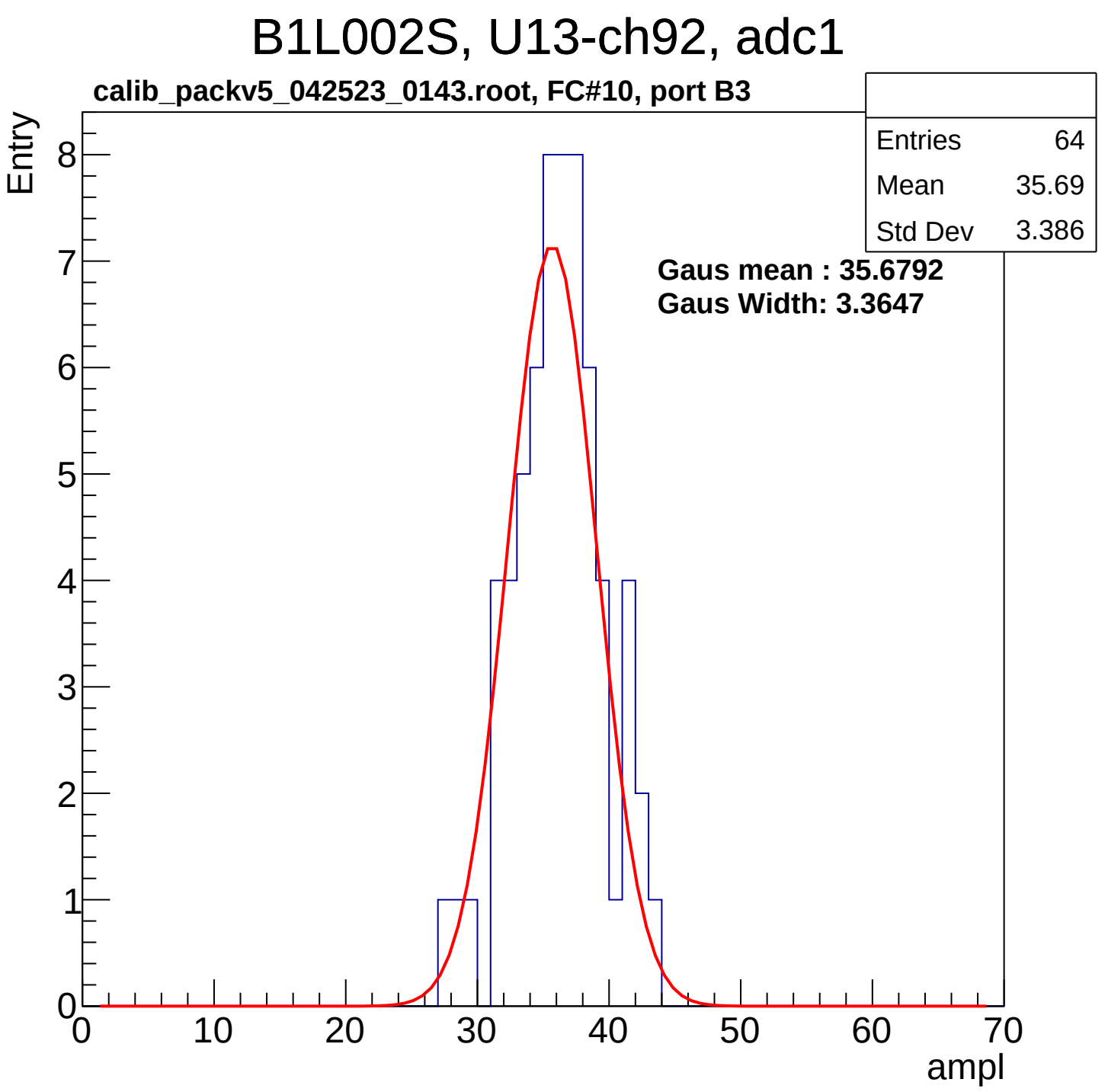
B1L002S, U13-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.69
Std Dev 3.386
Gaus mean : 35.6792
Gaus Width: 3.3647
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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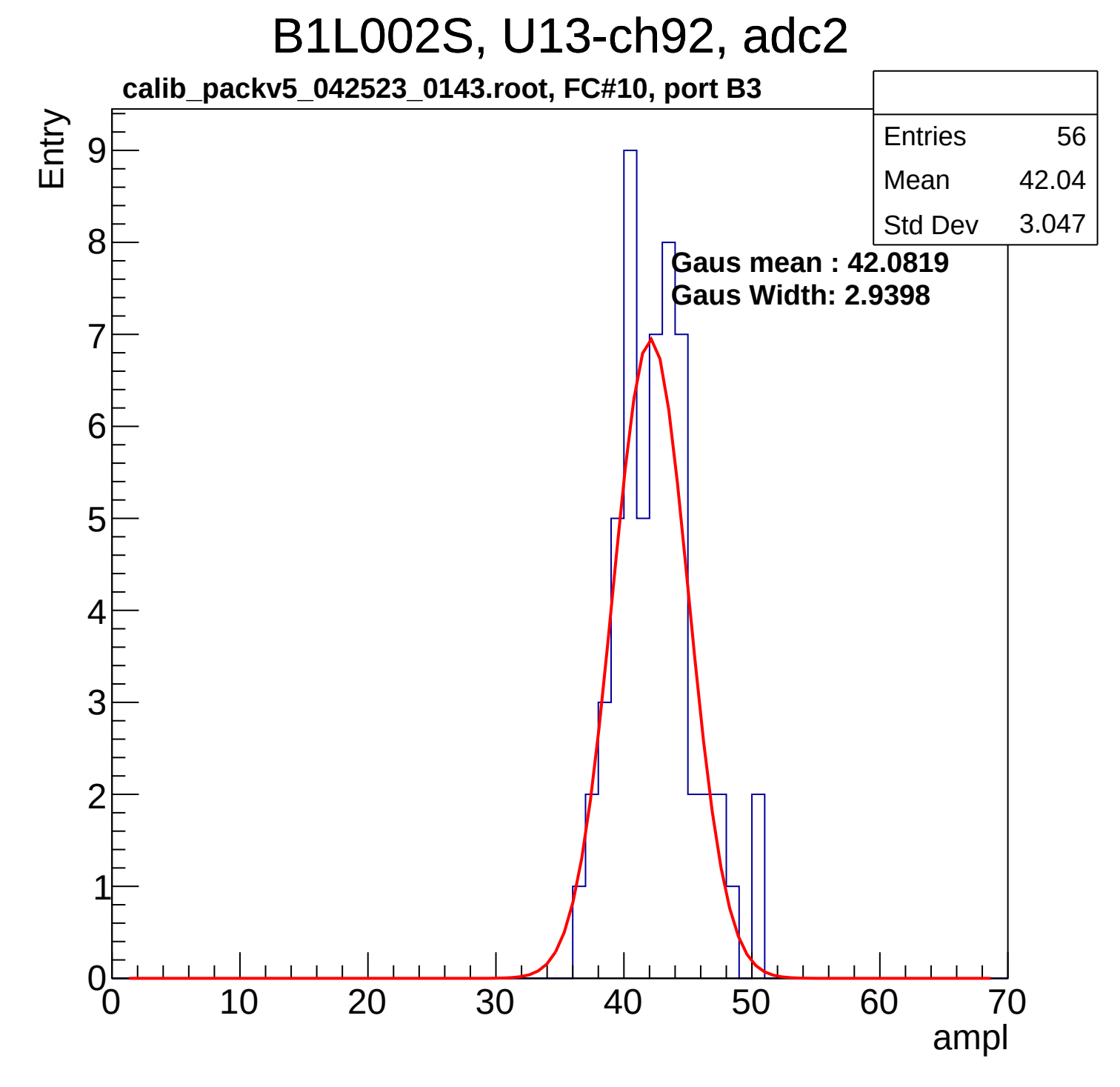
B1L002S, U13-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.04
Std Dev 3.047
Gaus mean : 42.0819
Gaus Width: 2.9398
Entry
ampl
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3
4
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0
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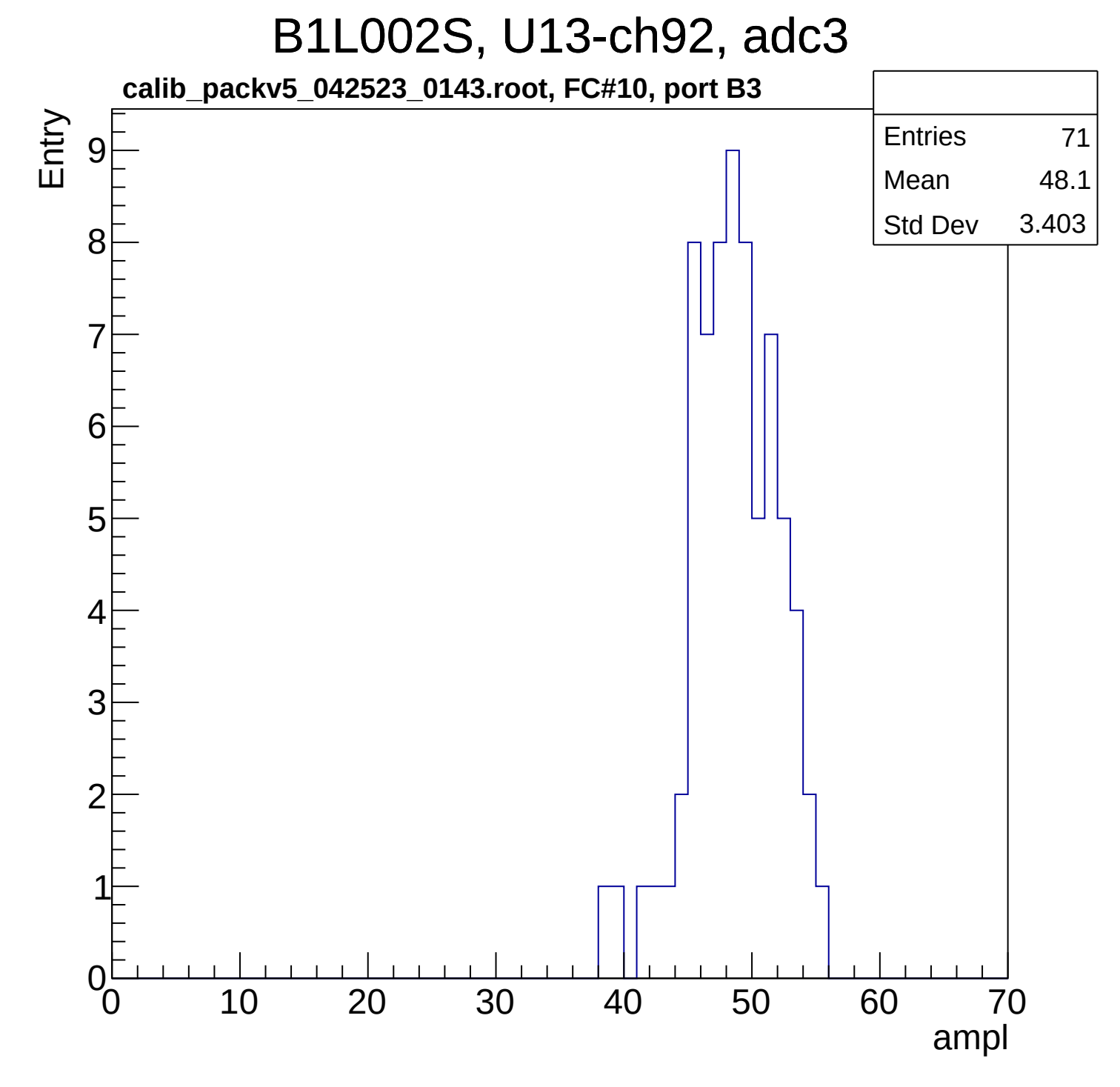
B1L002S, U13-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 48.1
Std Dev 3.403
Entry
ampl
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4
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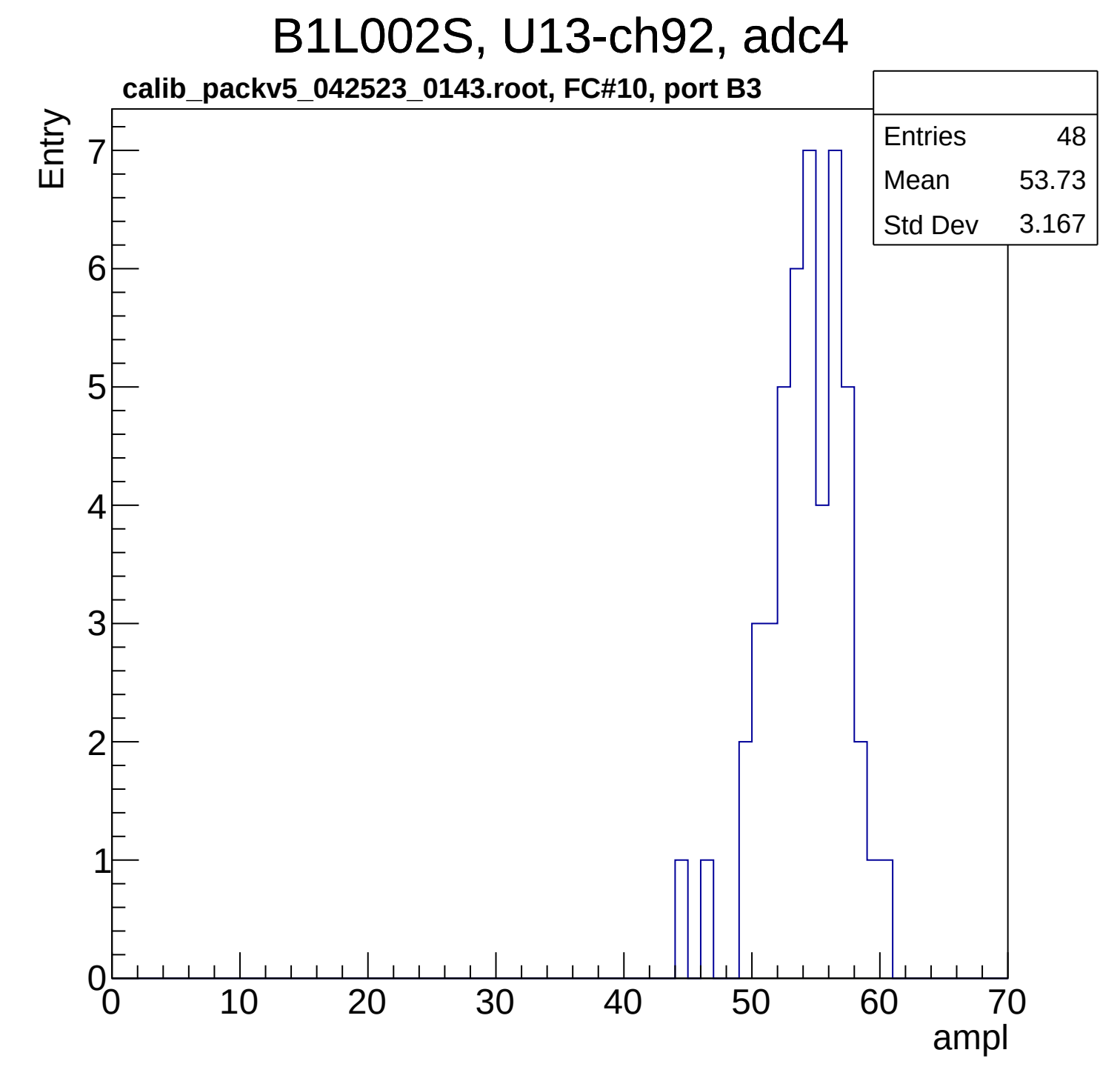
B1L002S, U13-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.73
Std Dev 3.167
Entry
ampl
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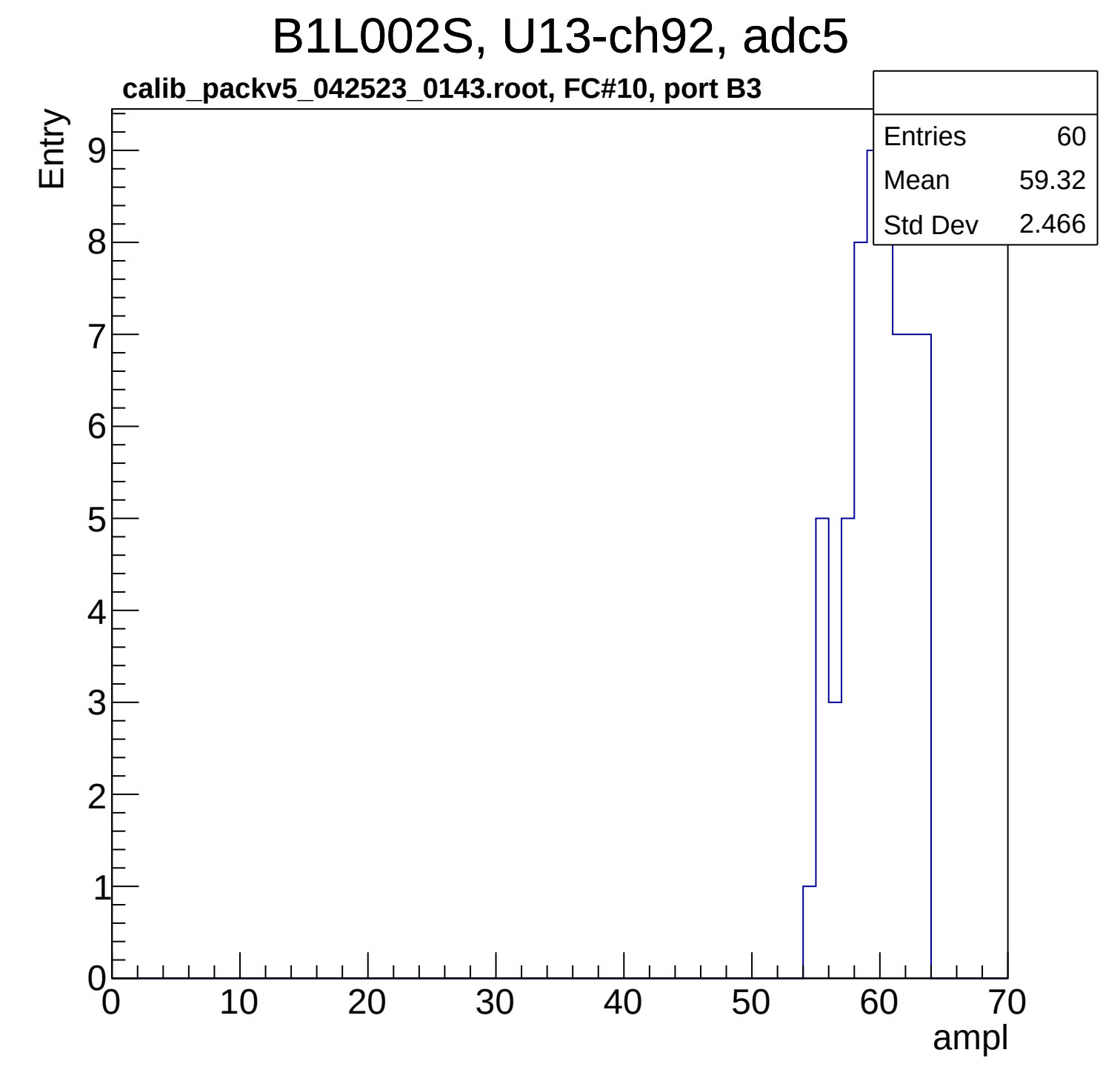
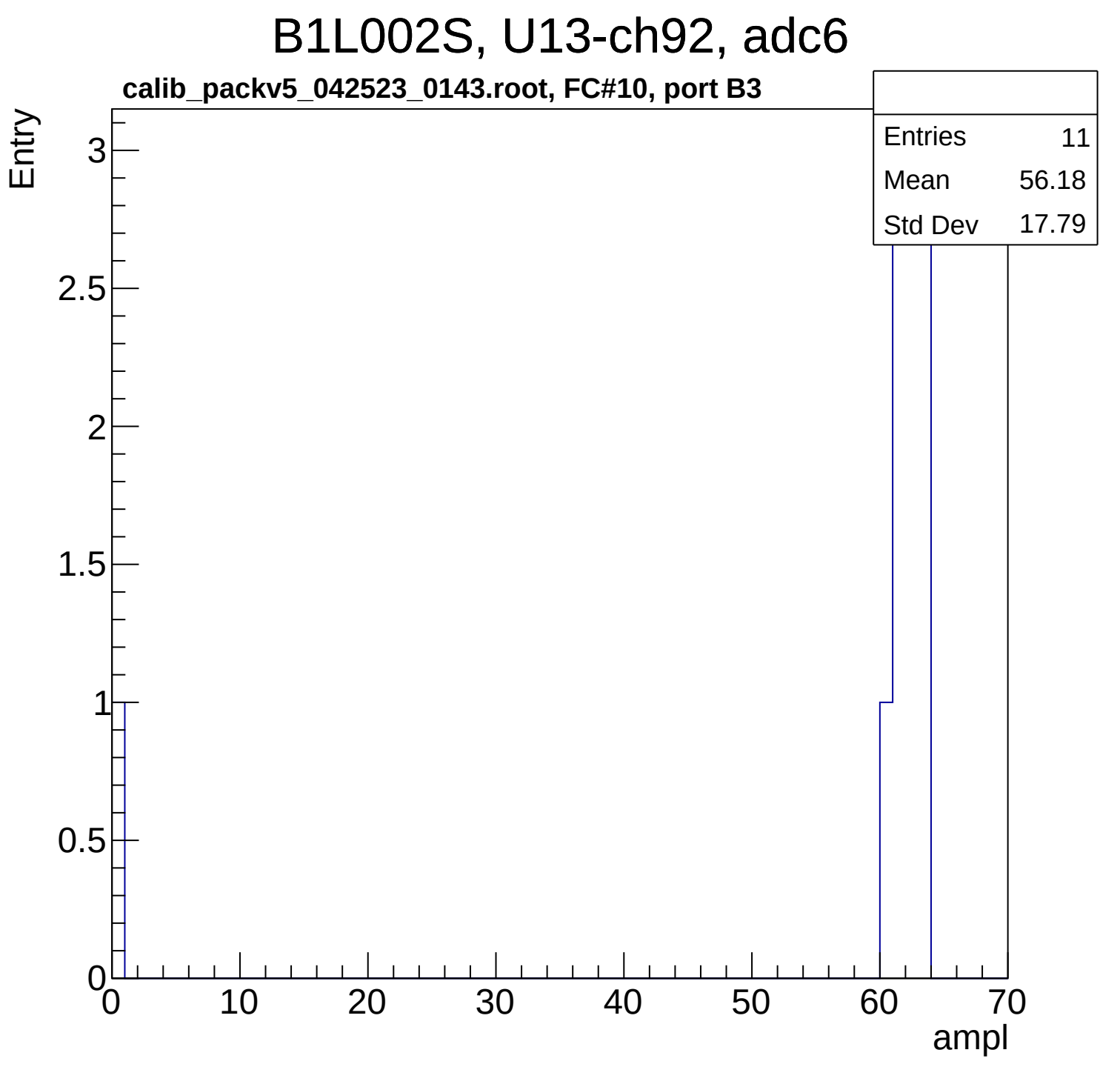
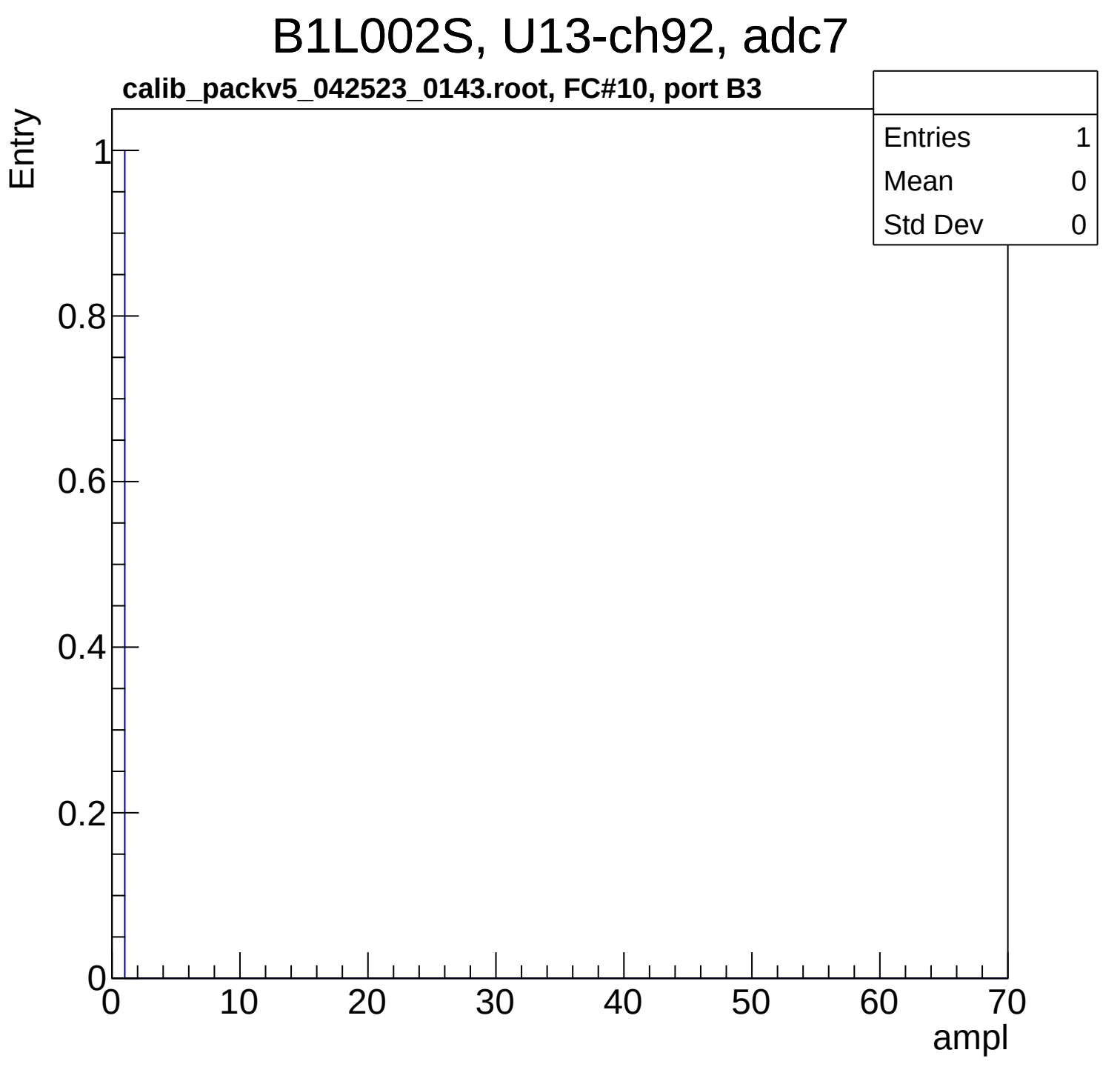
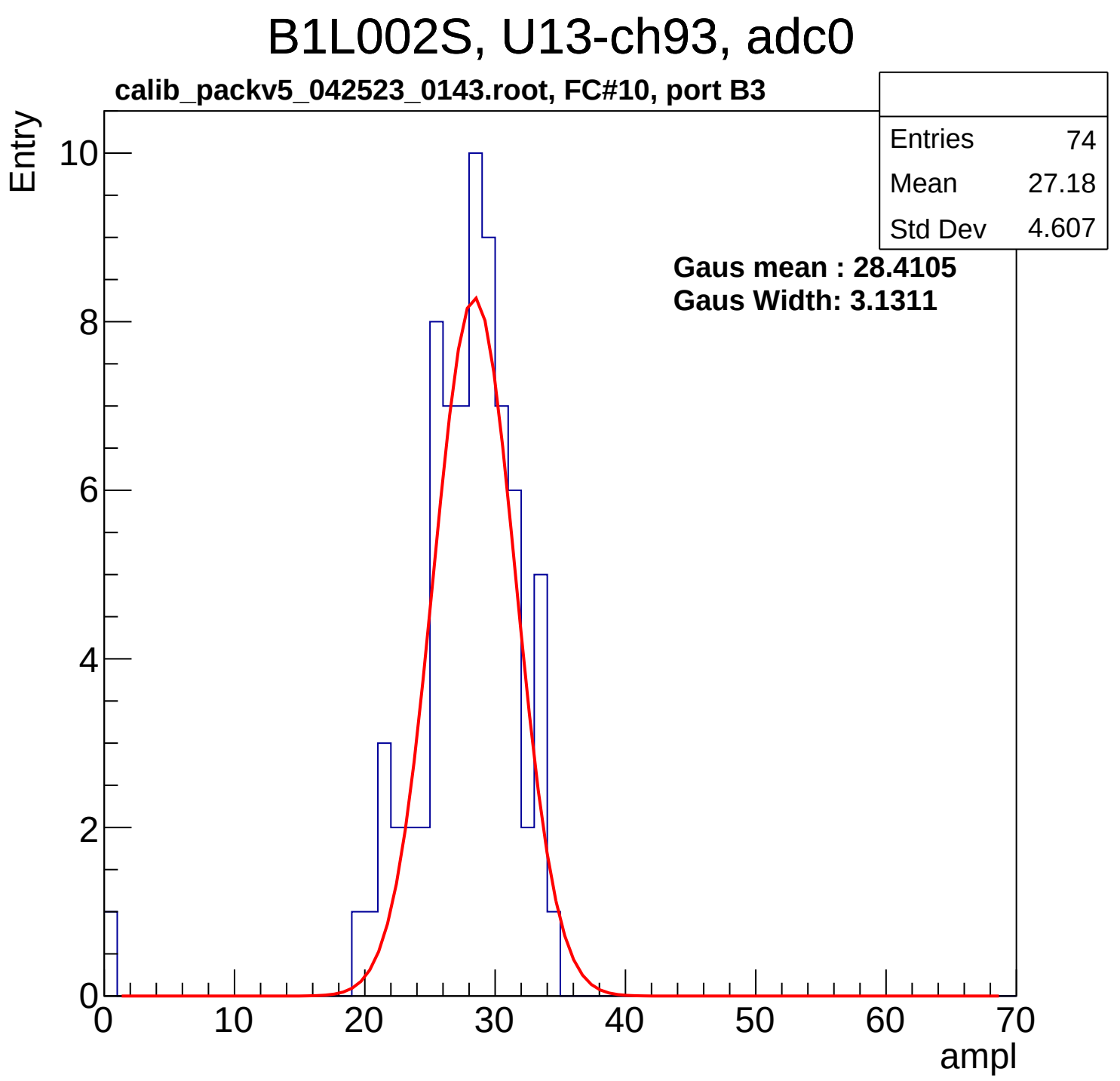
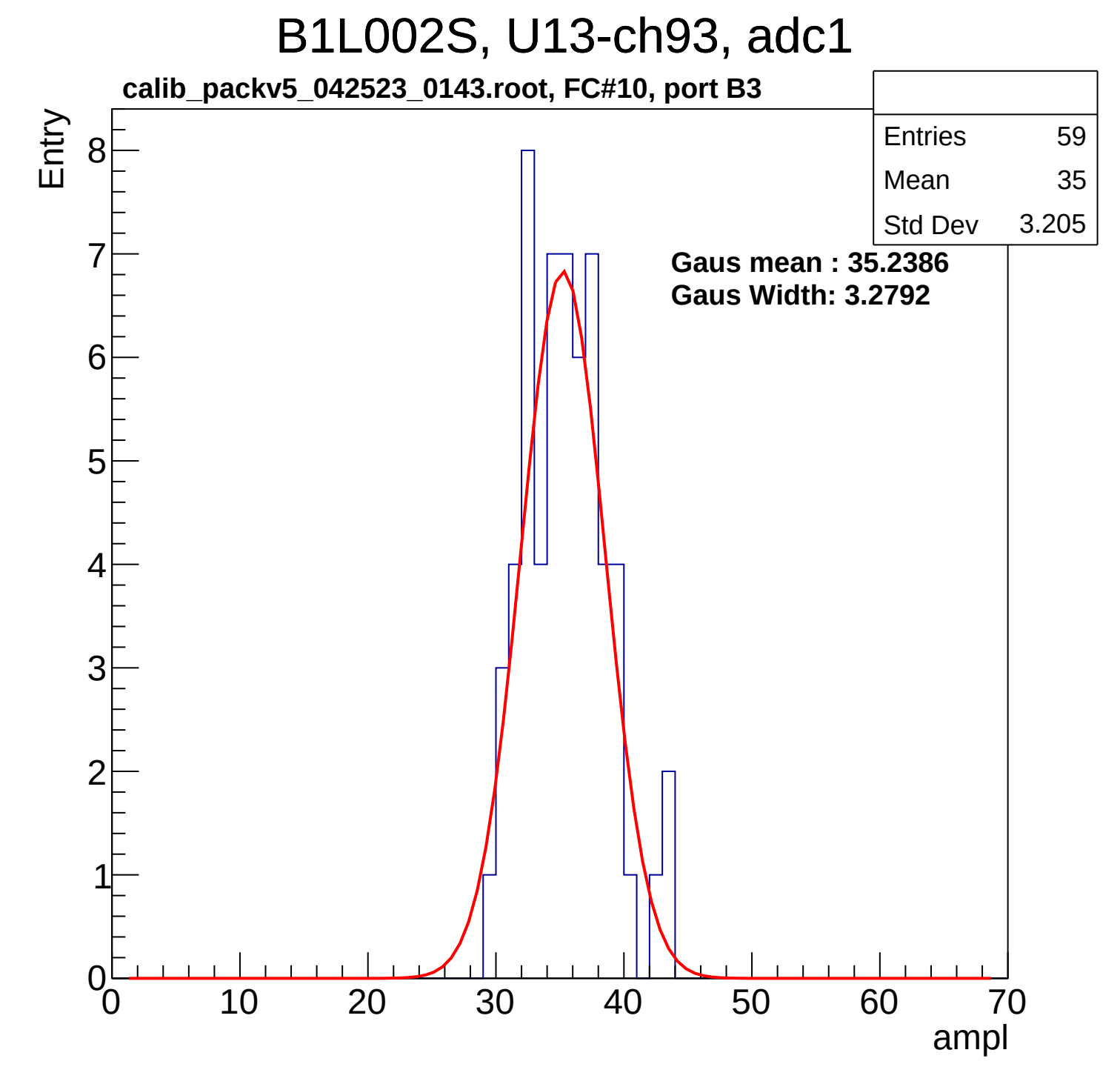

B1L002S, U13-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 59.32
Std Dev 2.466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.18
Std Dev 17.79
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.18
Std Dev 4.607
Gaus mean : 28.4105
Gaus Width: 3.1311
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35
Std Dev 3.205
Gaus mean : 35.2386
Gaus Width: 3.2792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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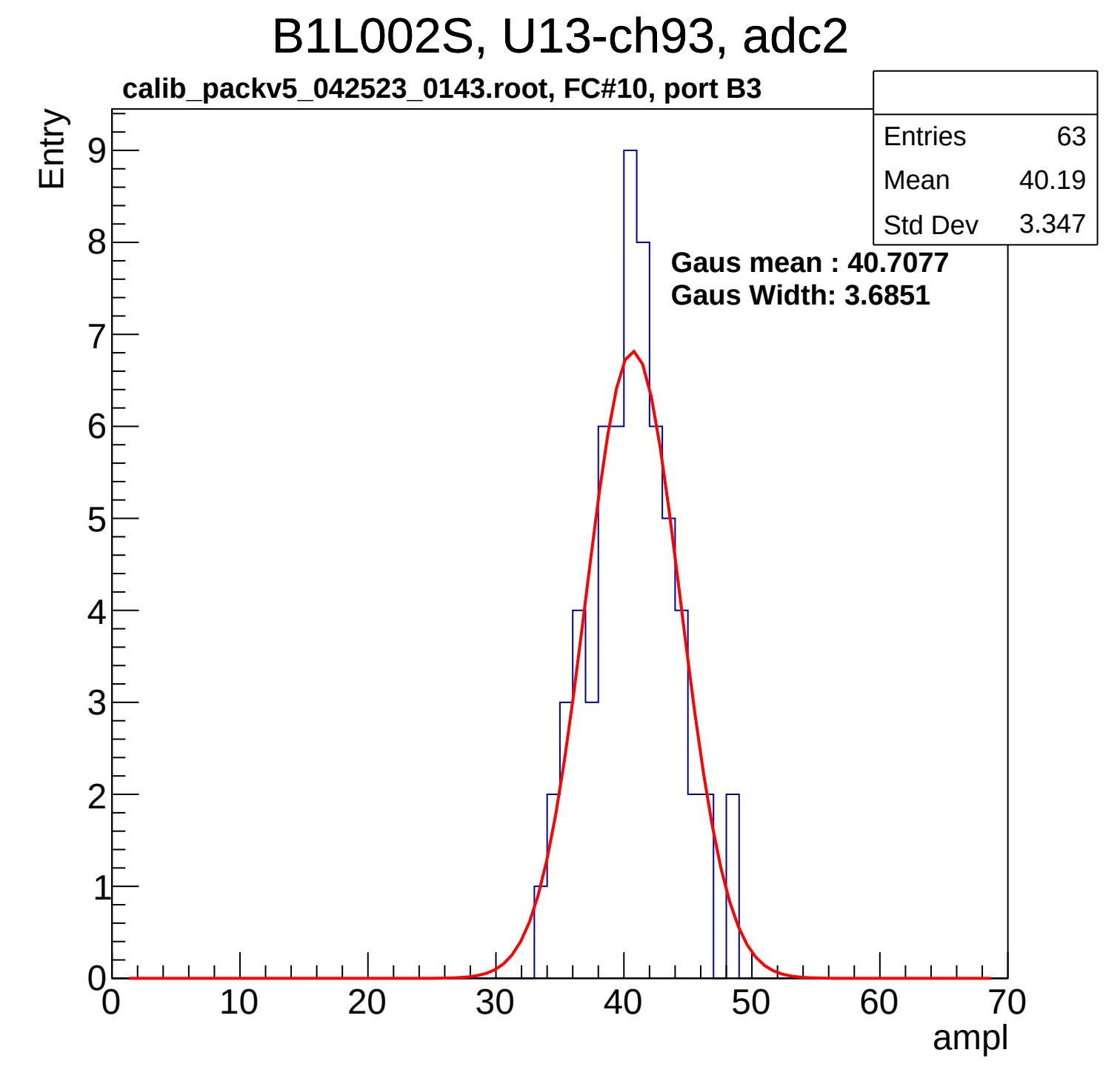
B1L002S, U13-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.19
Std Dev 3.347
Gaus mean : 40.7077
Gaus Width: 3.6851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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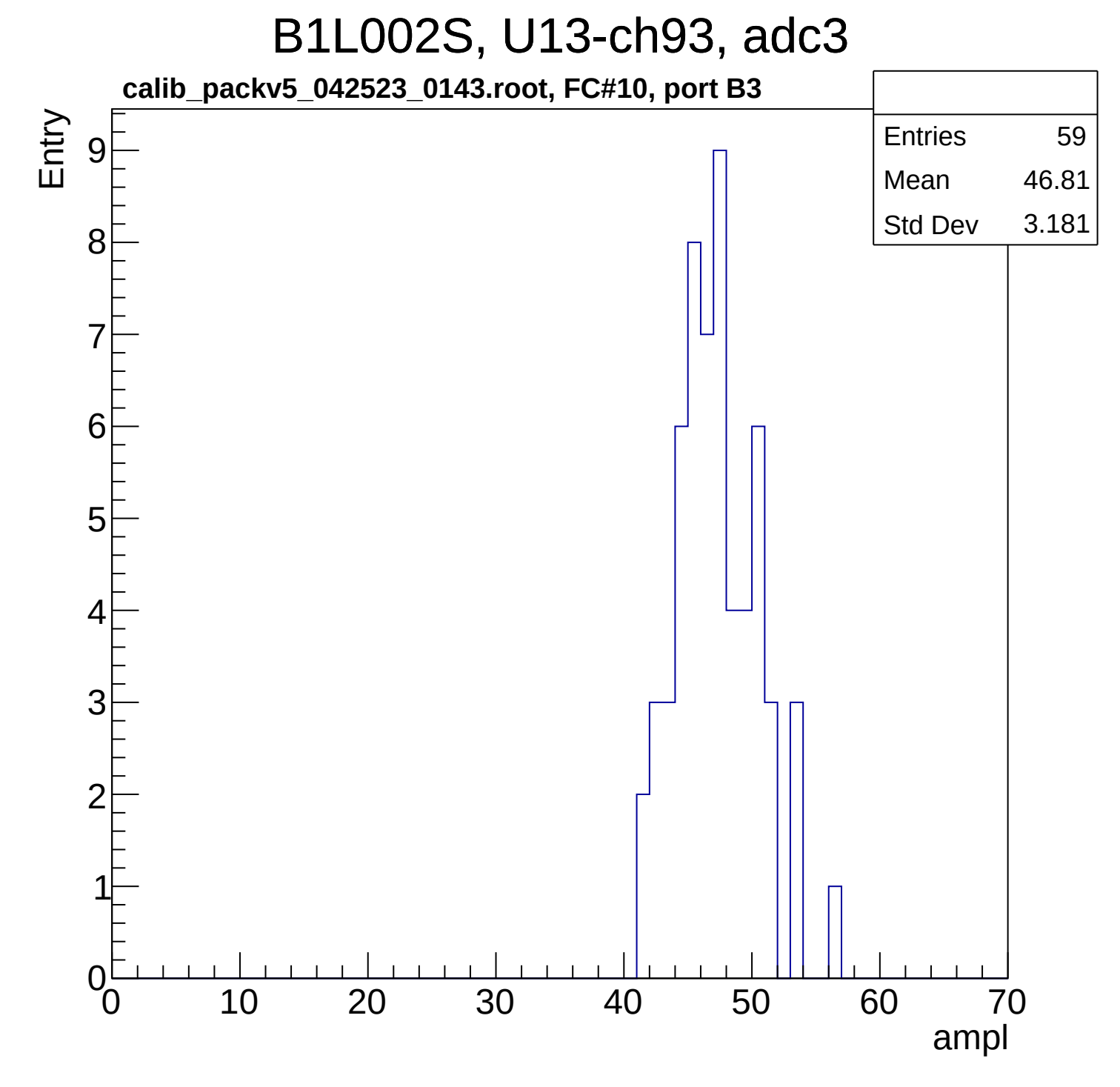
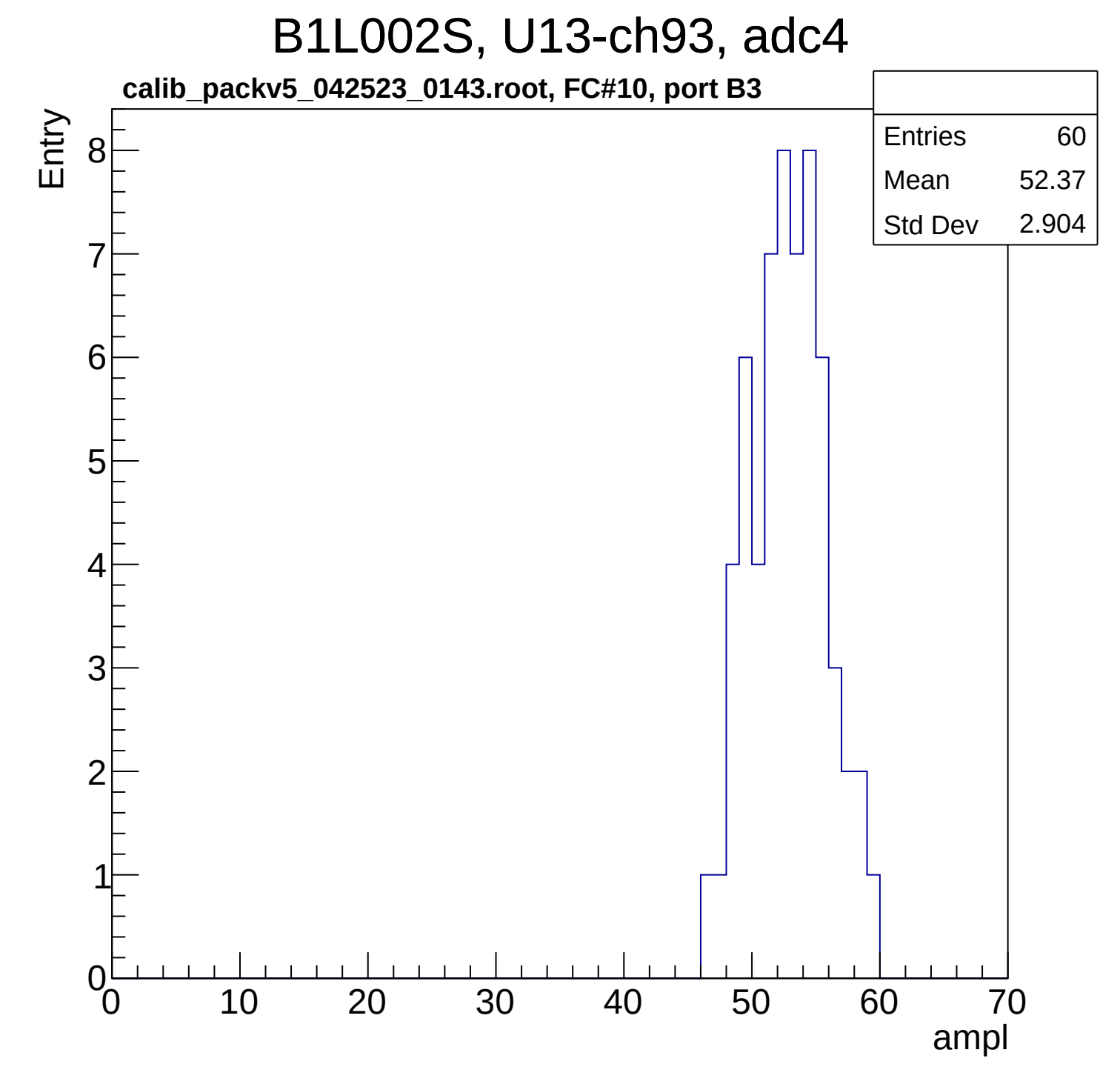
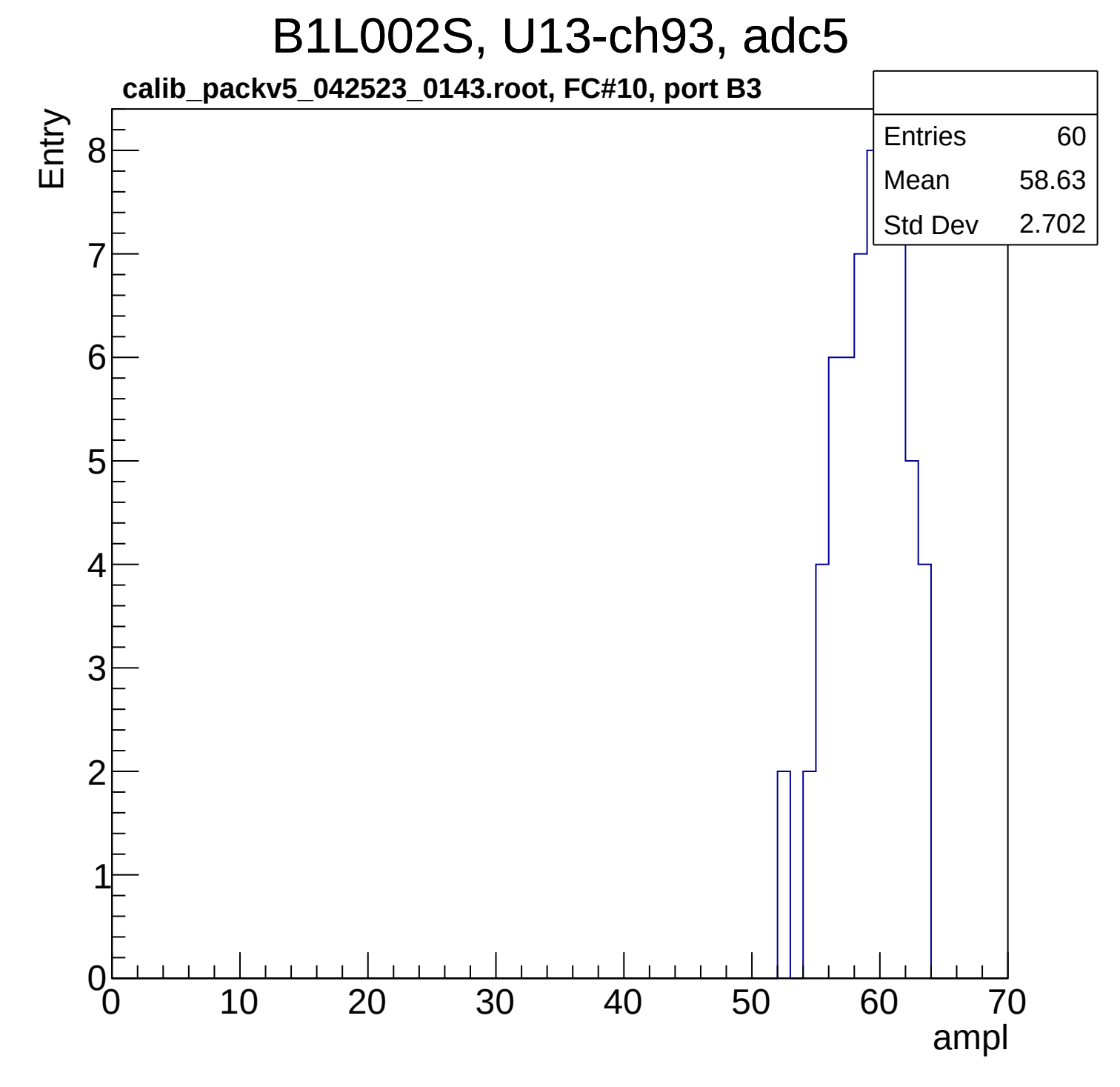
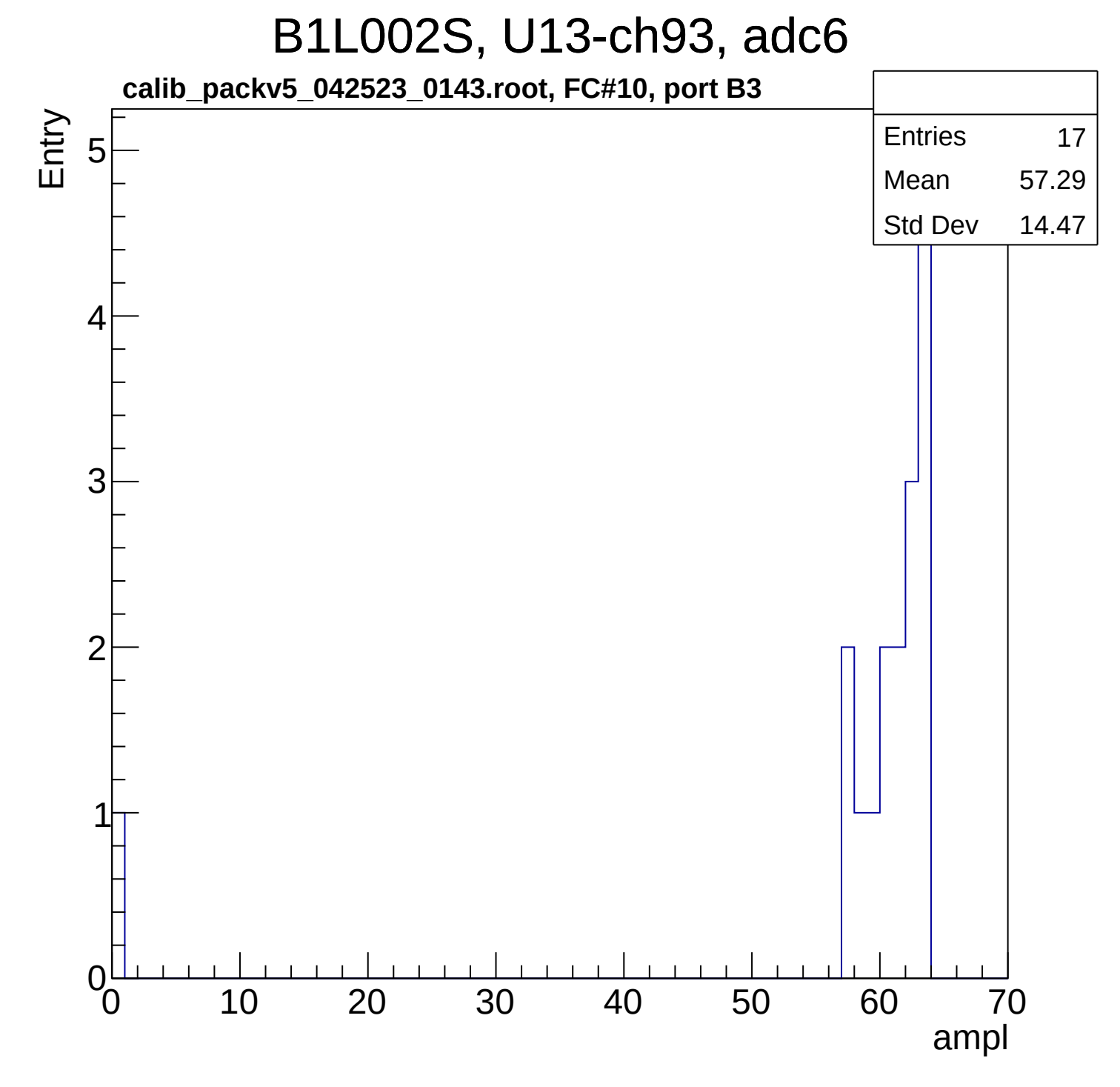
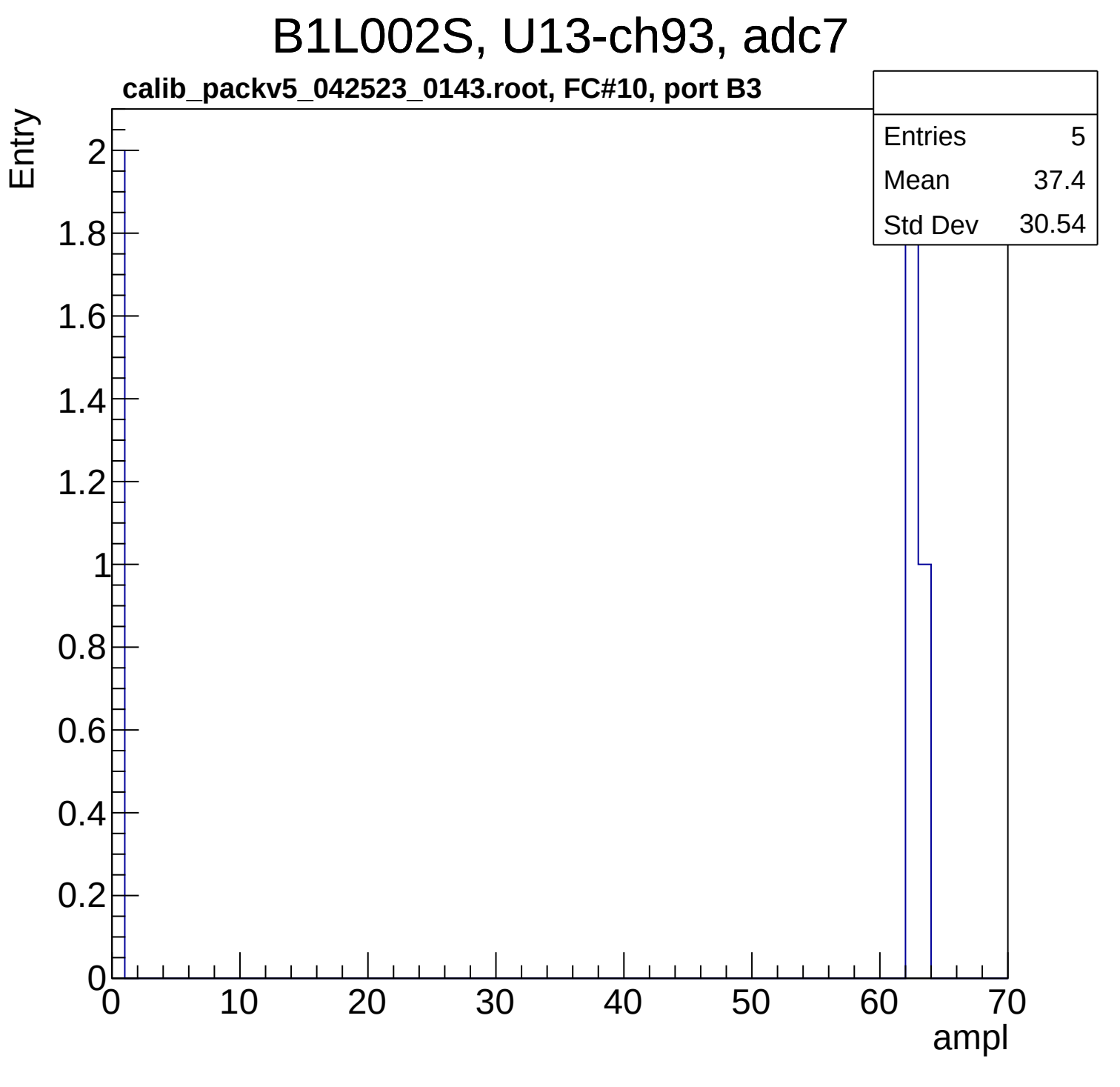
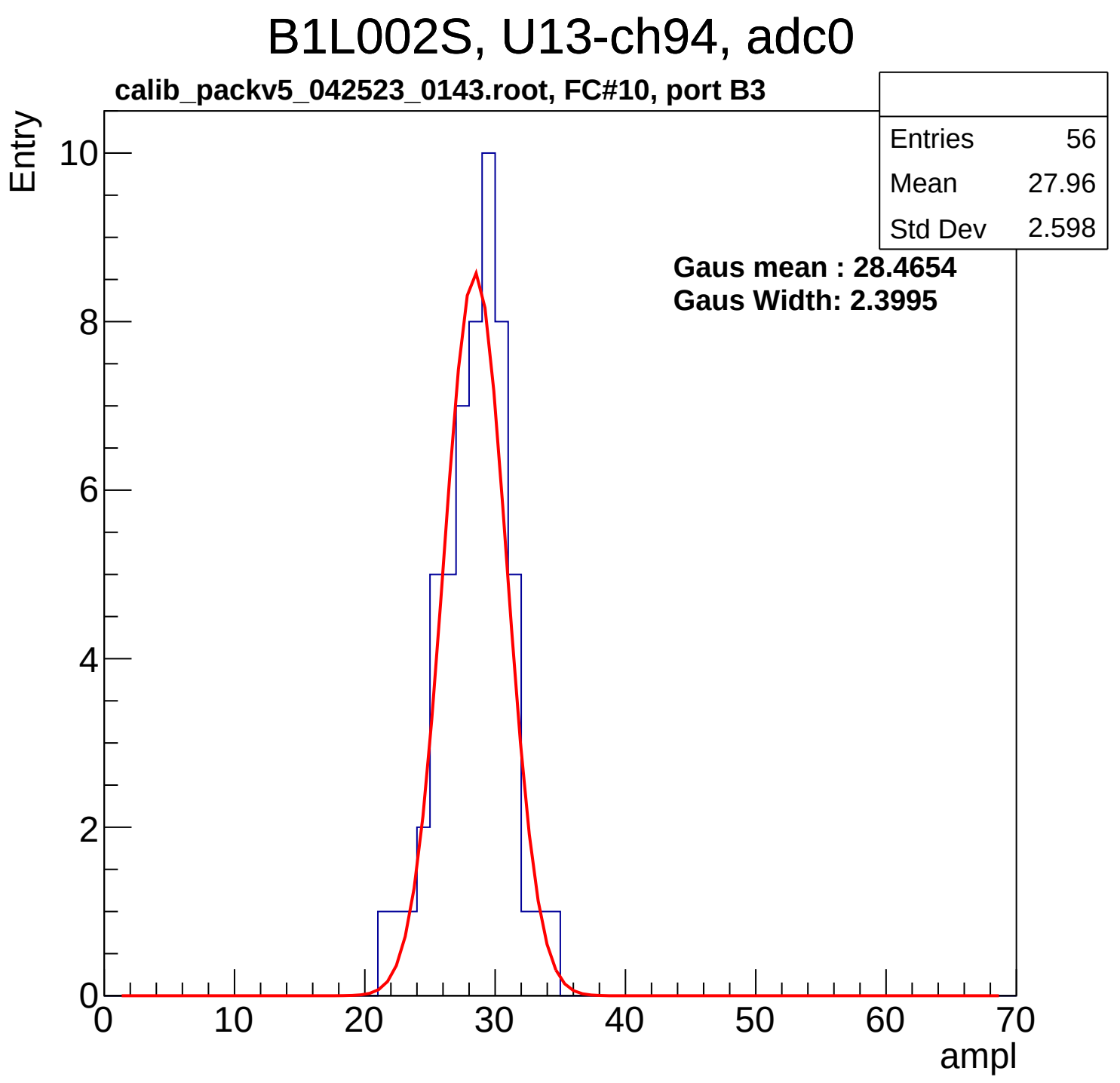

B1L002S, U13-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.81
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

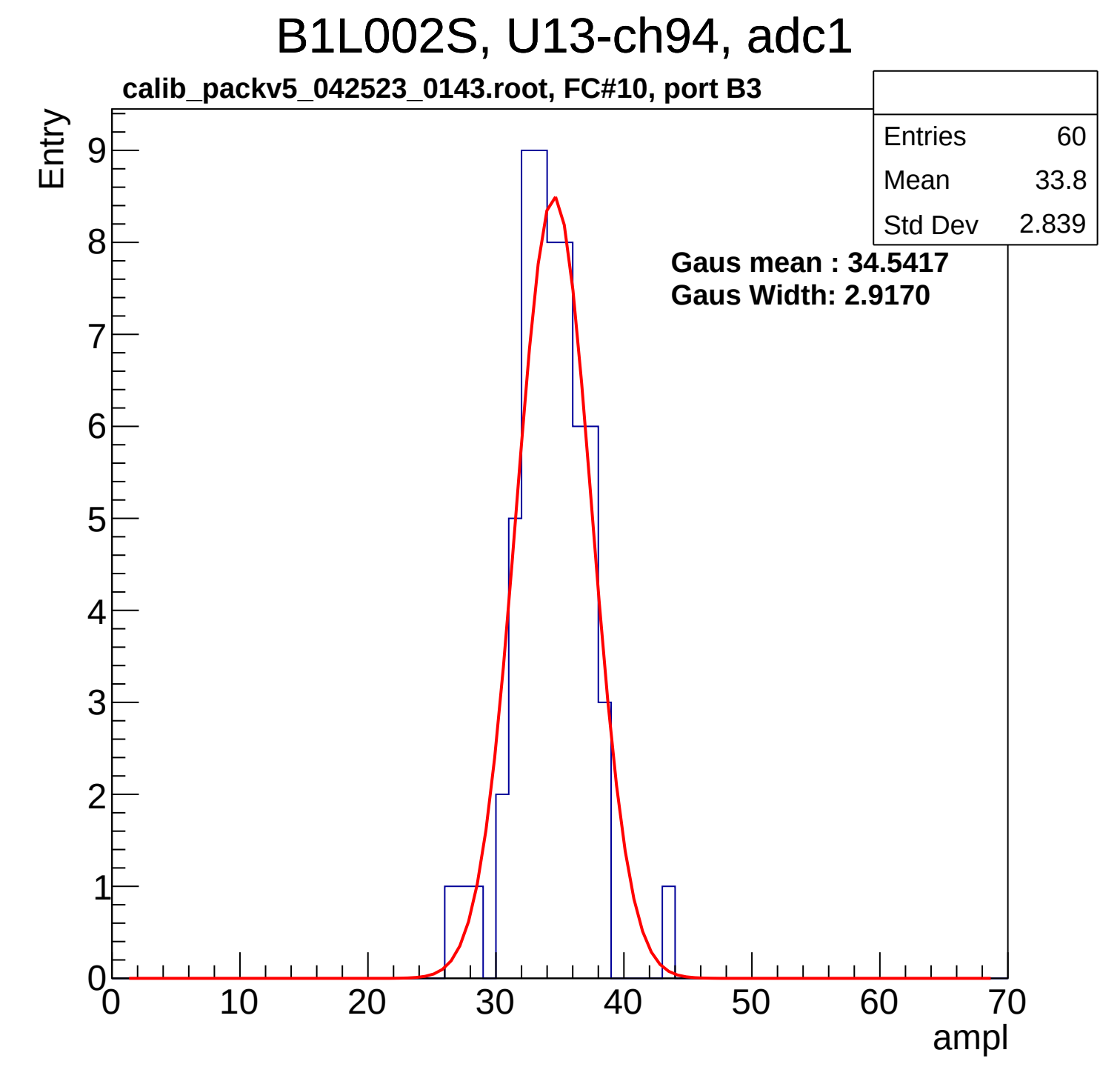

B1L002S, U13-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.37
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

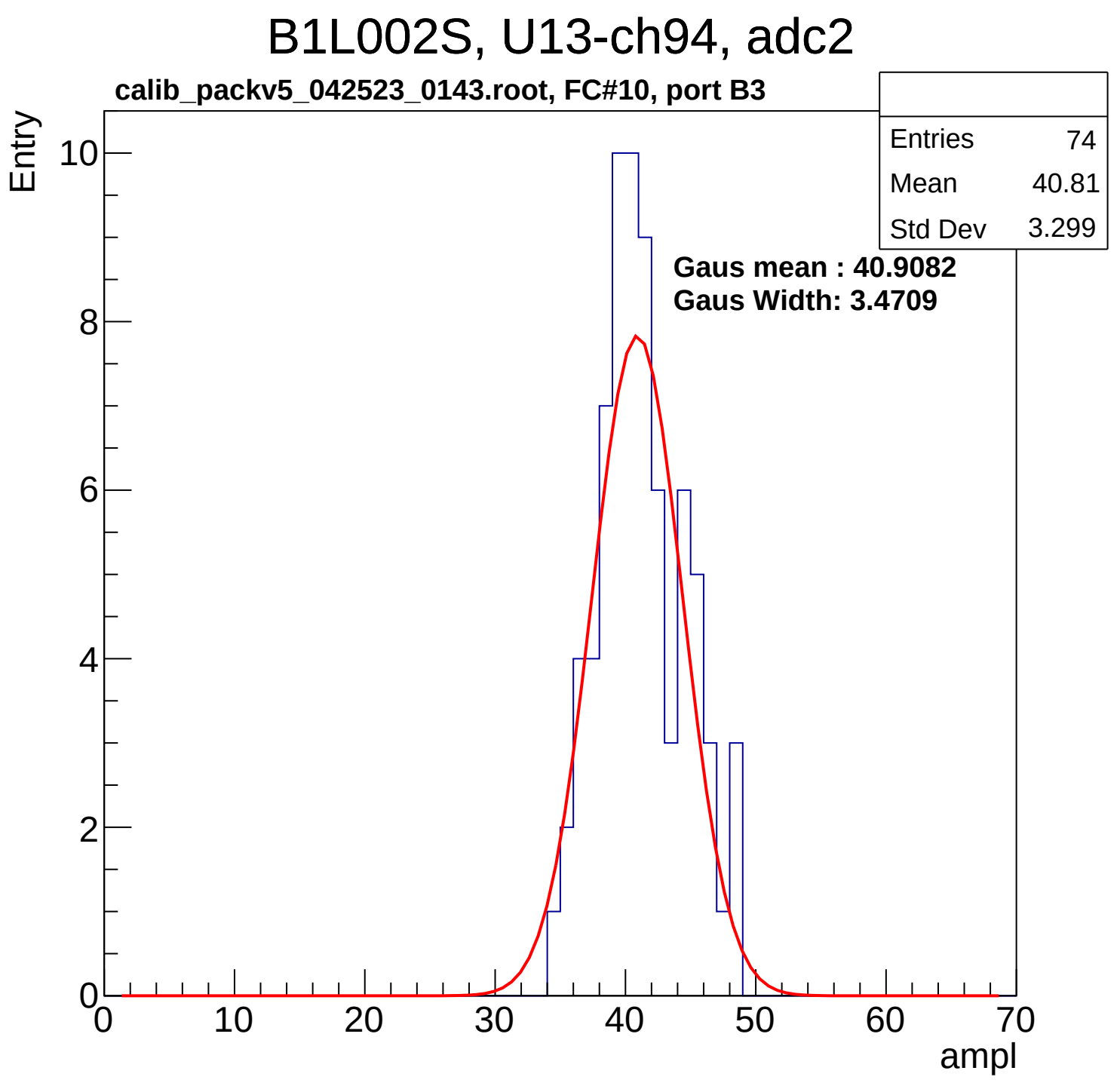

B1L002S, U13-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.63
Std Dev 2.702
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.29
Std Dev 14.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.96
Std Dev 2.598
Gaus mean : 28.4654
Gaus Width: 2.3995
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 33.8
Std Dev 2.839
Gaus mean : 34.5417
Gaus Width: 2.9170
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.81
Std Dev 3.299
Gaus mean : 40.9082
Gaus Width: 3.4709
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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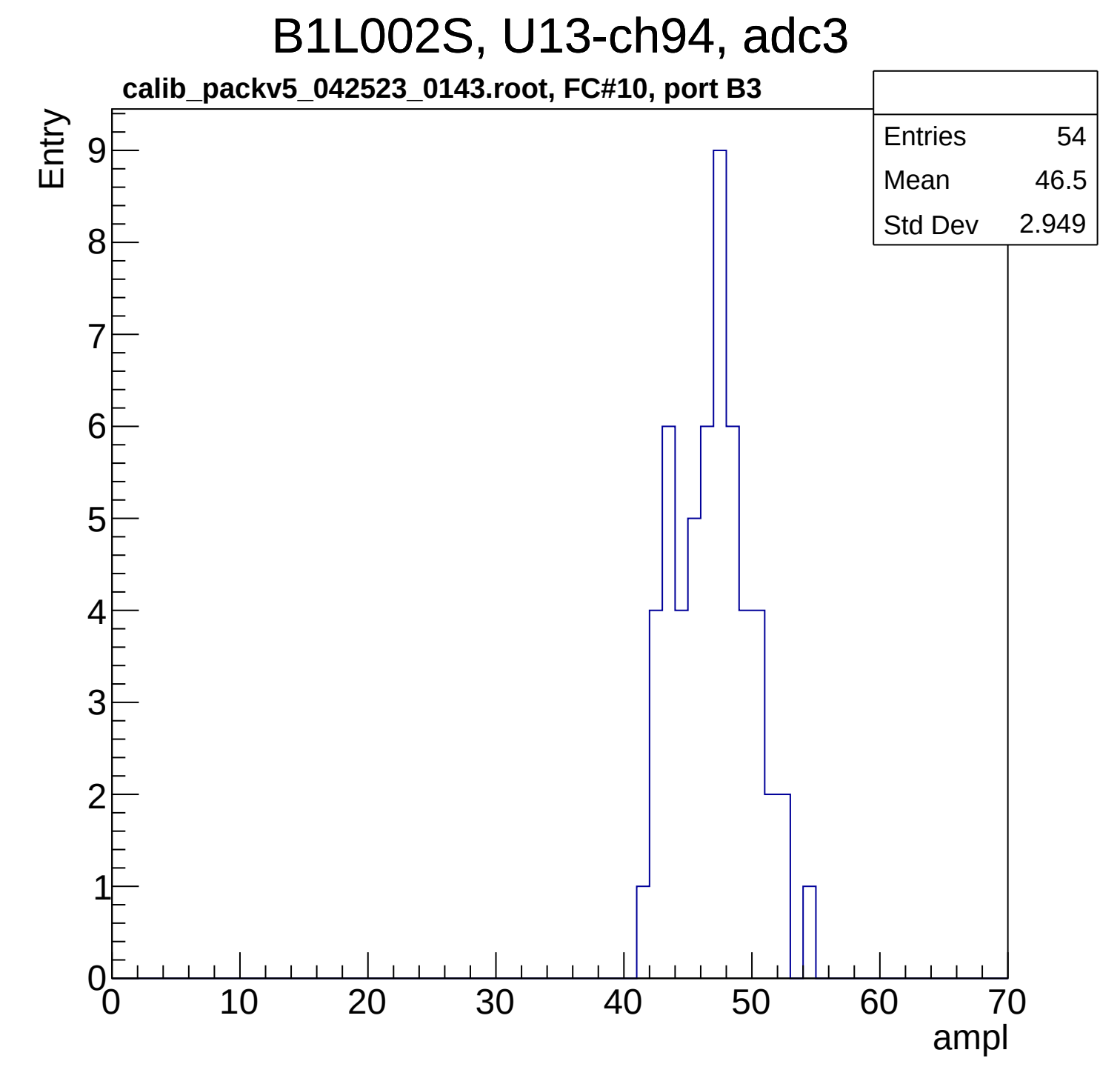
B1L002S, U13-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.5
Std Dev 2.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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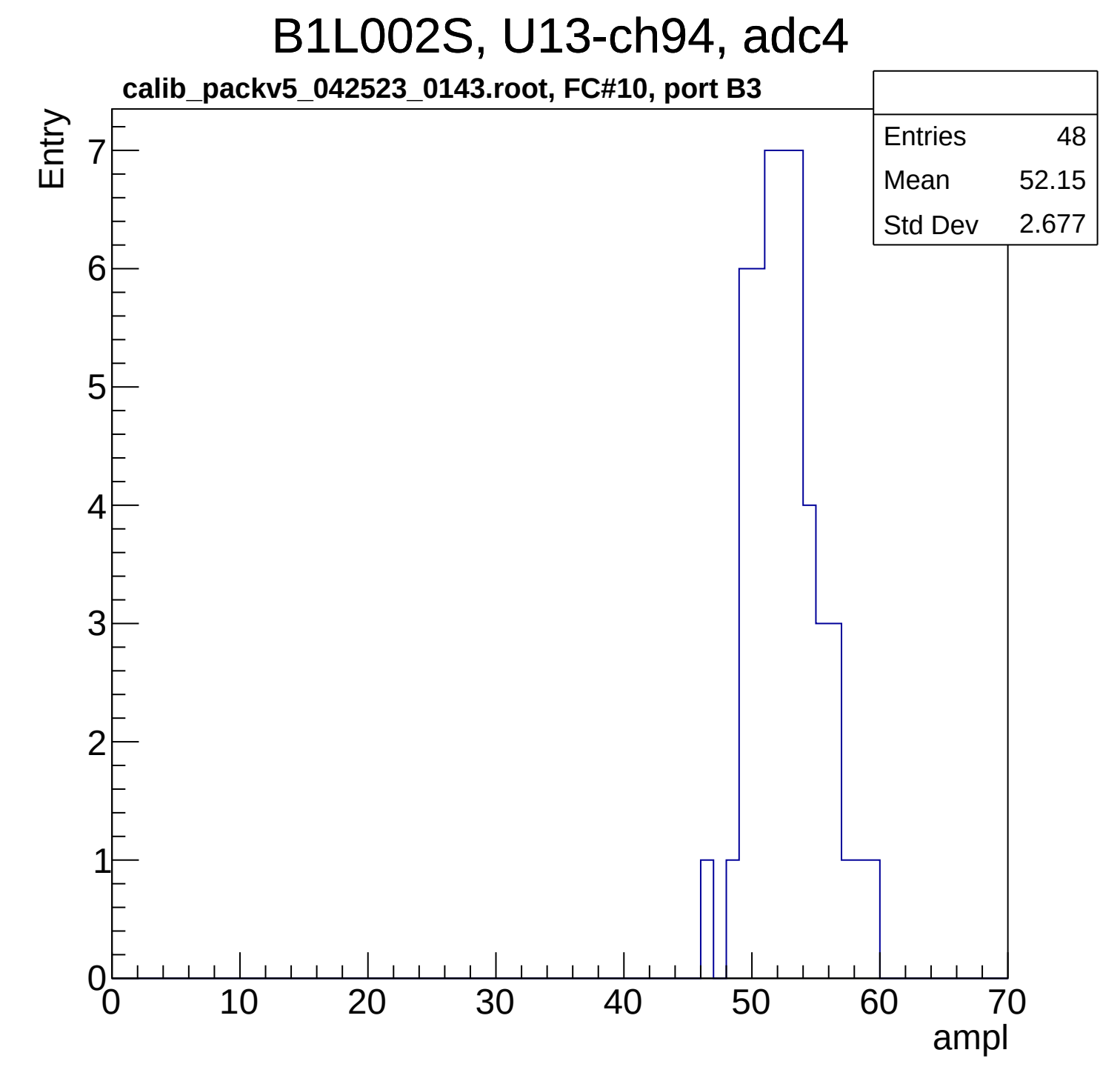
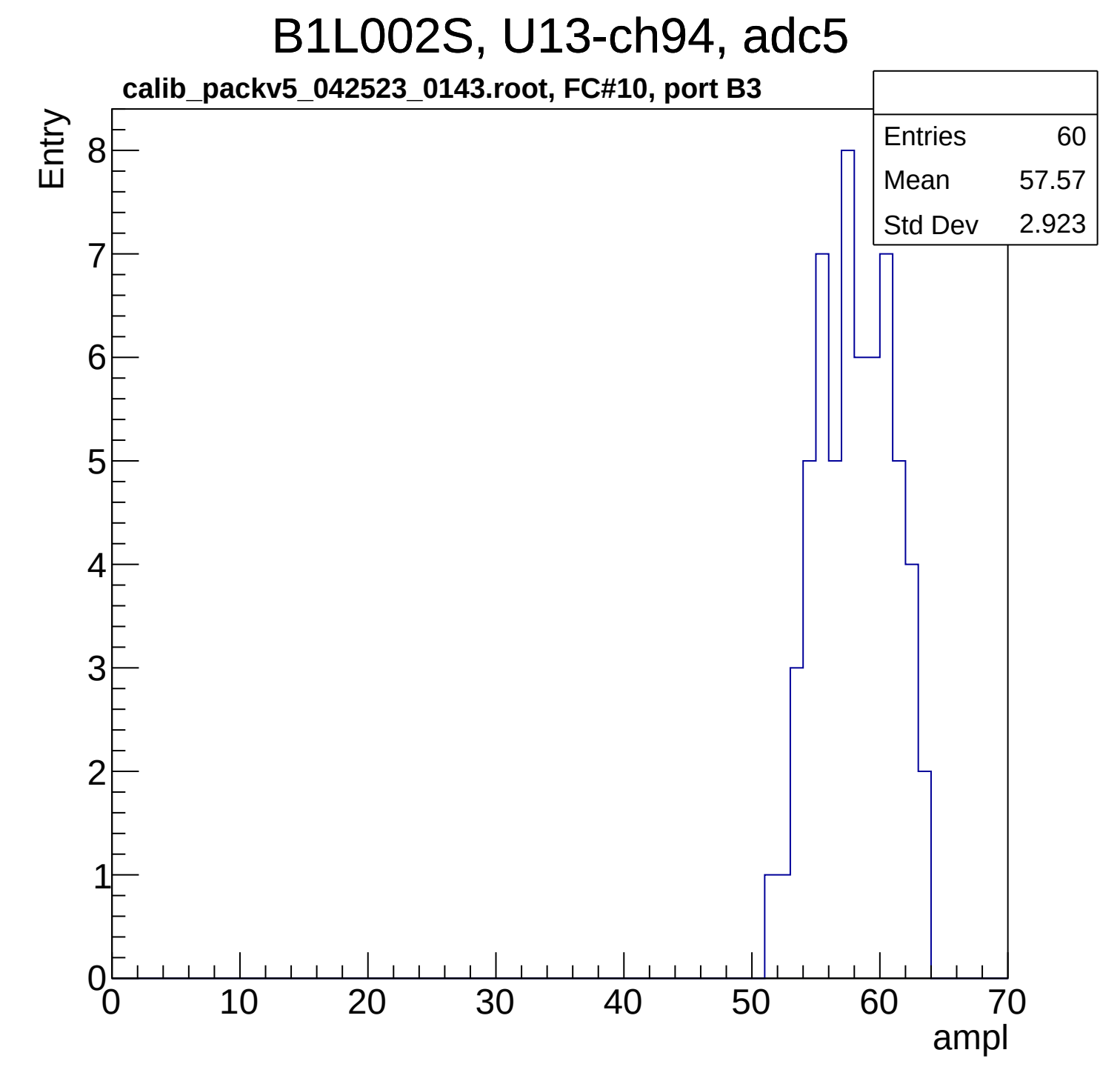
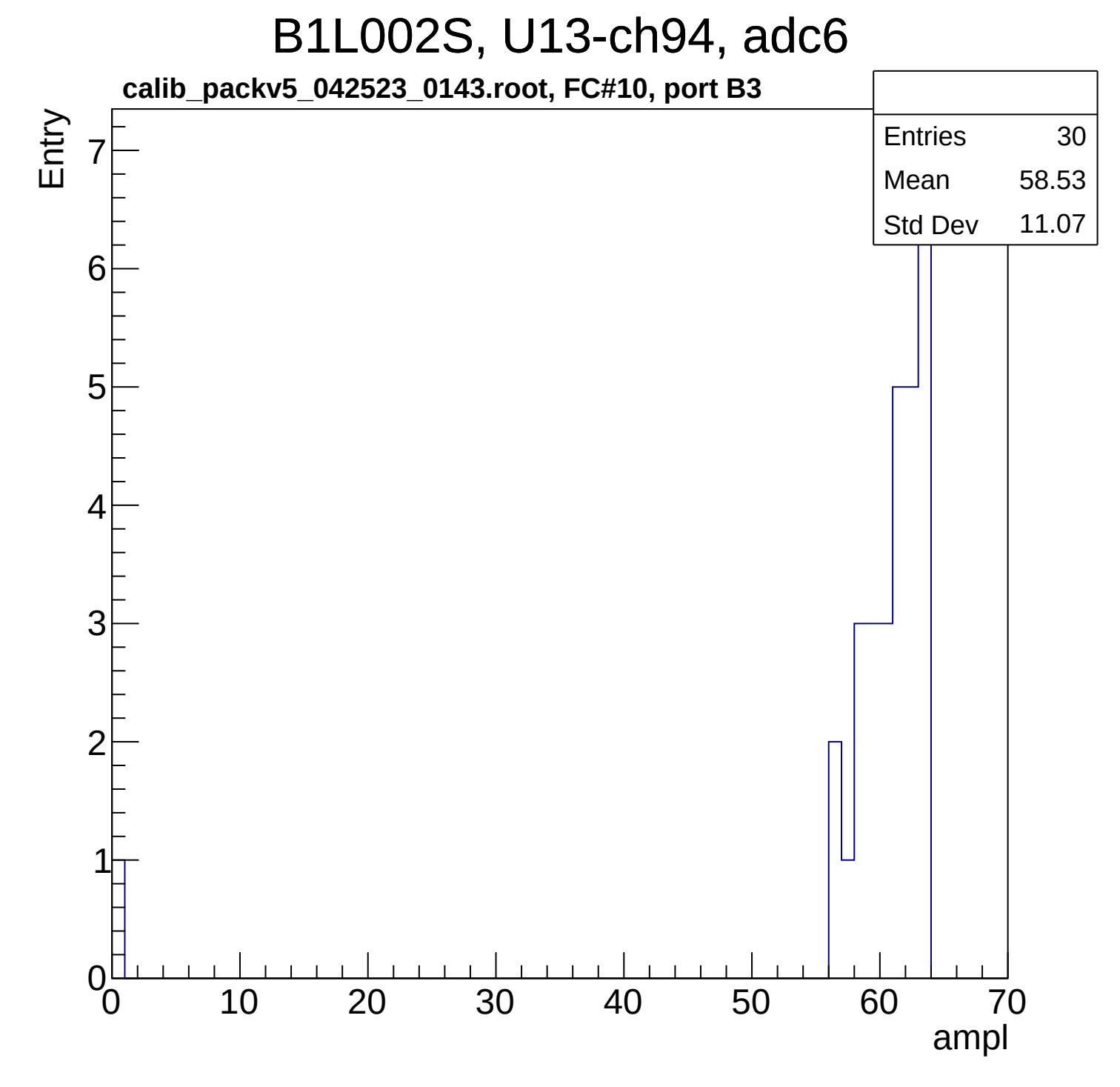
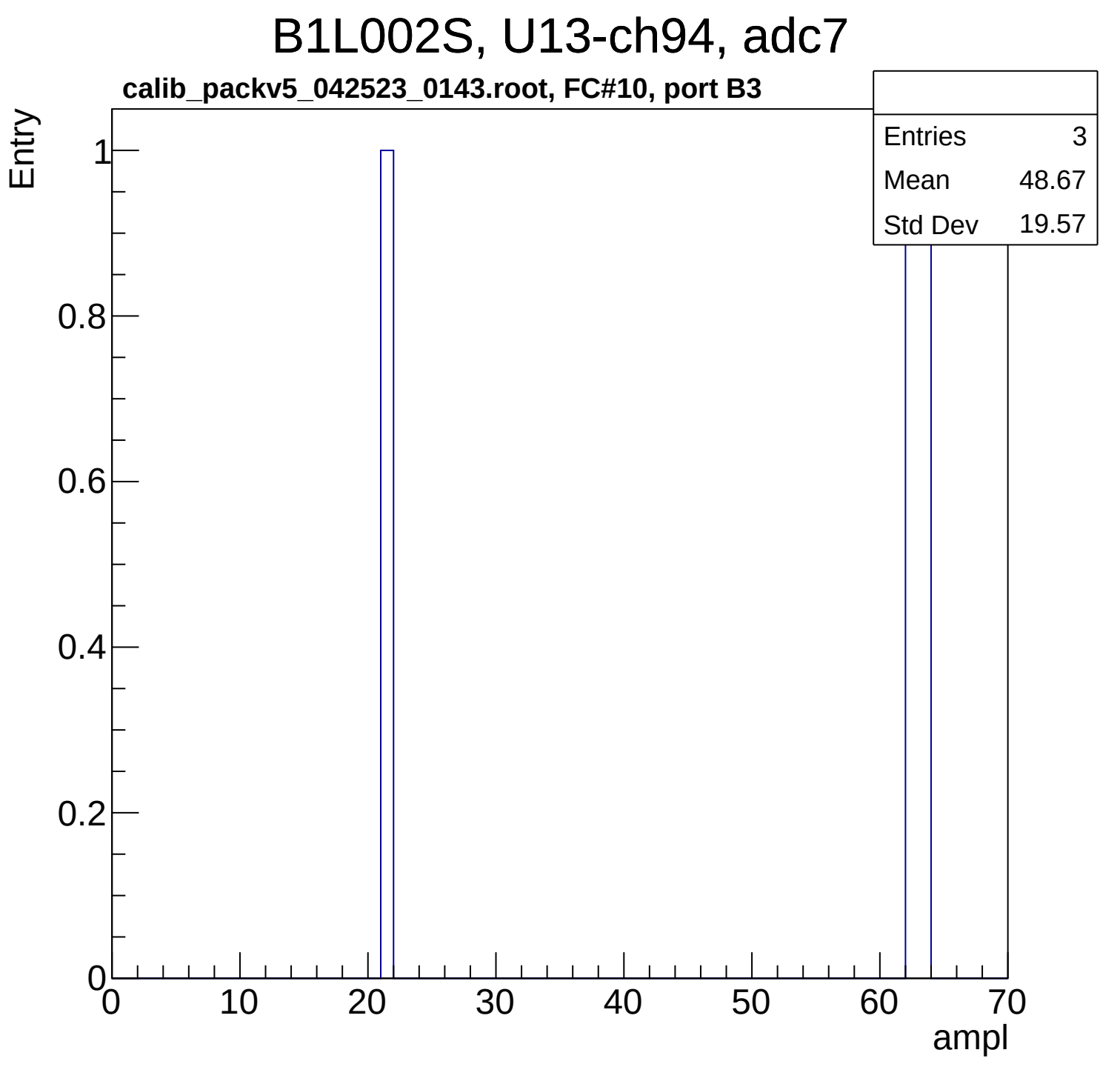
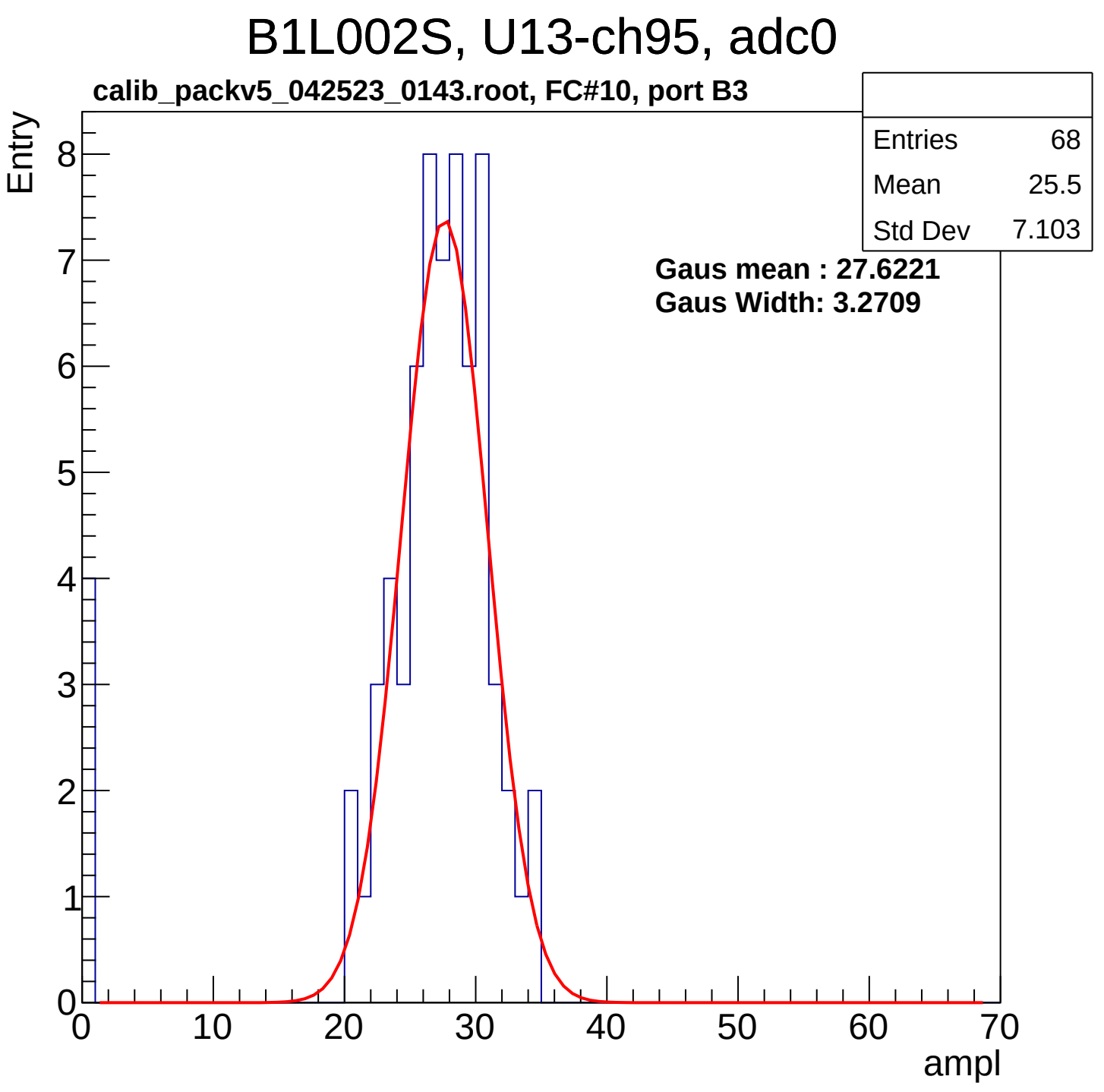

B1L002S, U13-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.15
Std Dev 2.677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.57
Std Dev 2.923
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 58.53
Std Dev 11.07
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.67
Std Dev 19.57
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 25.5
Std Dev 7.103
Gaus mean : 27.6221
Gaus Width: 3.2709
Entry
ampl
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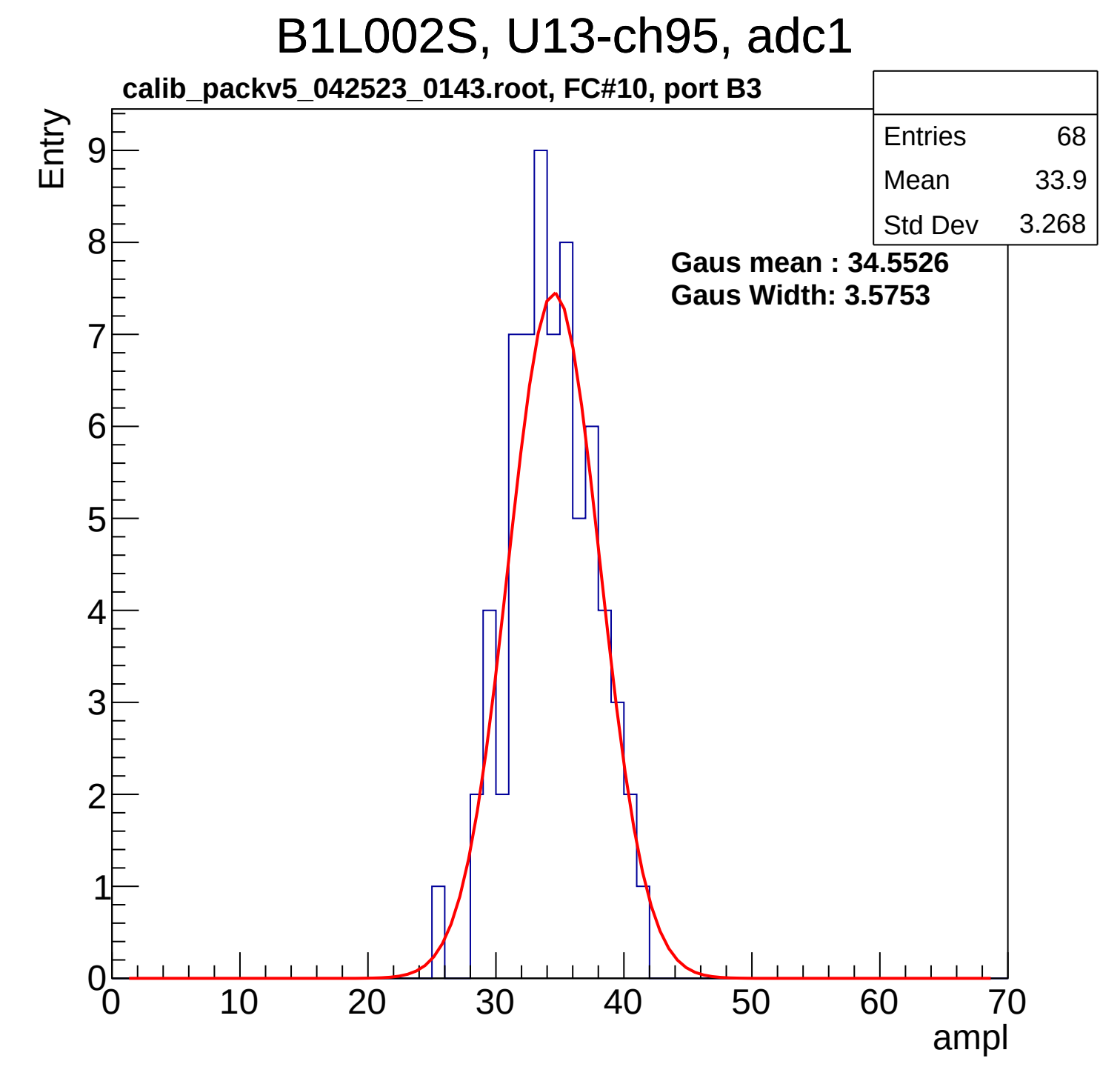
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.9
Std Dev 3.268
Gaus mean : 34.5526
Gaus Width: 3.5753
Entry
ampl
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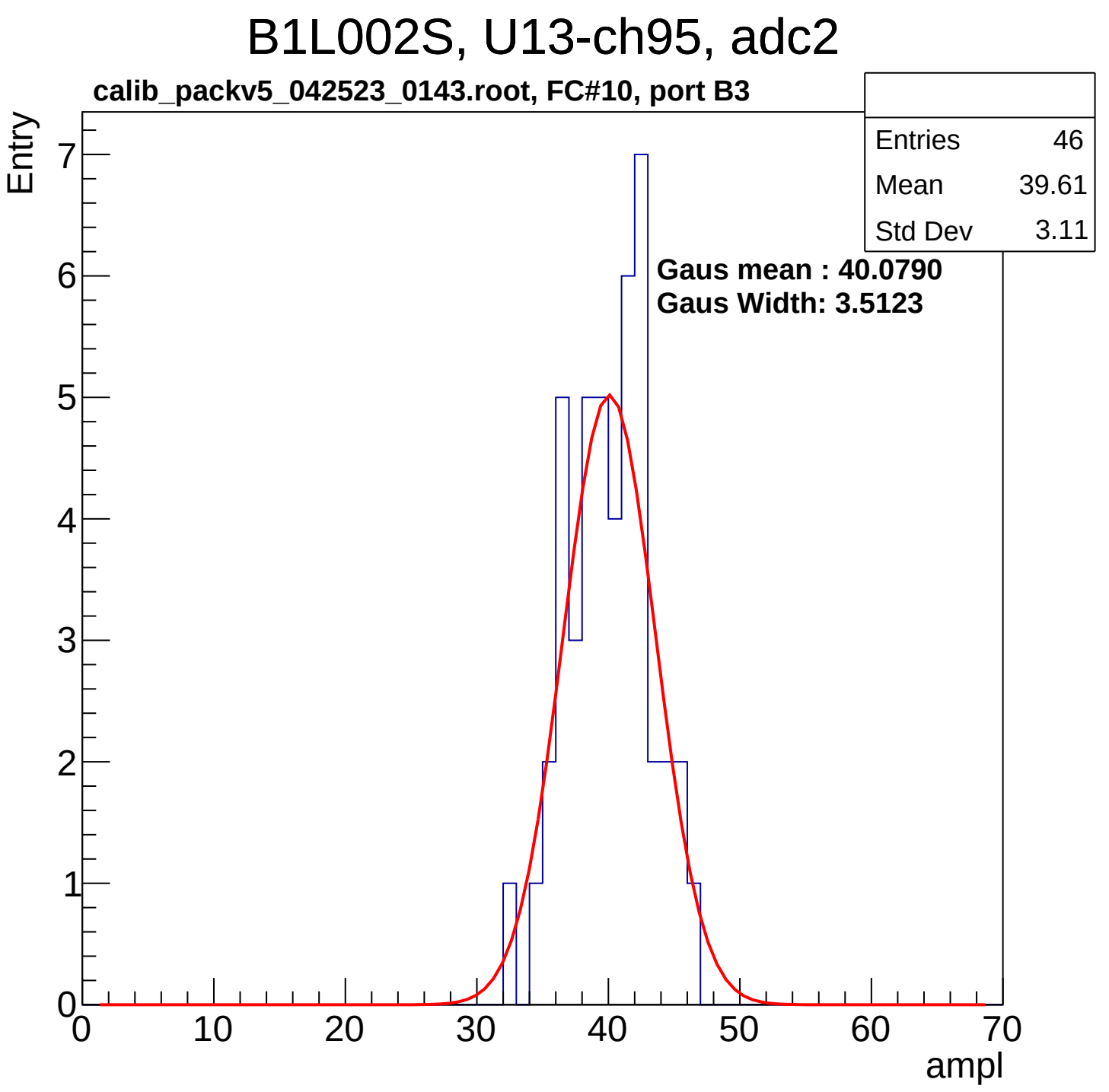
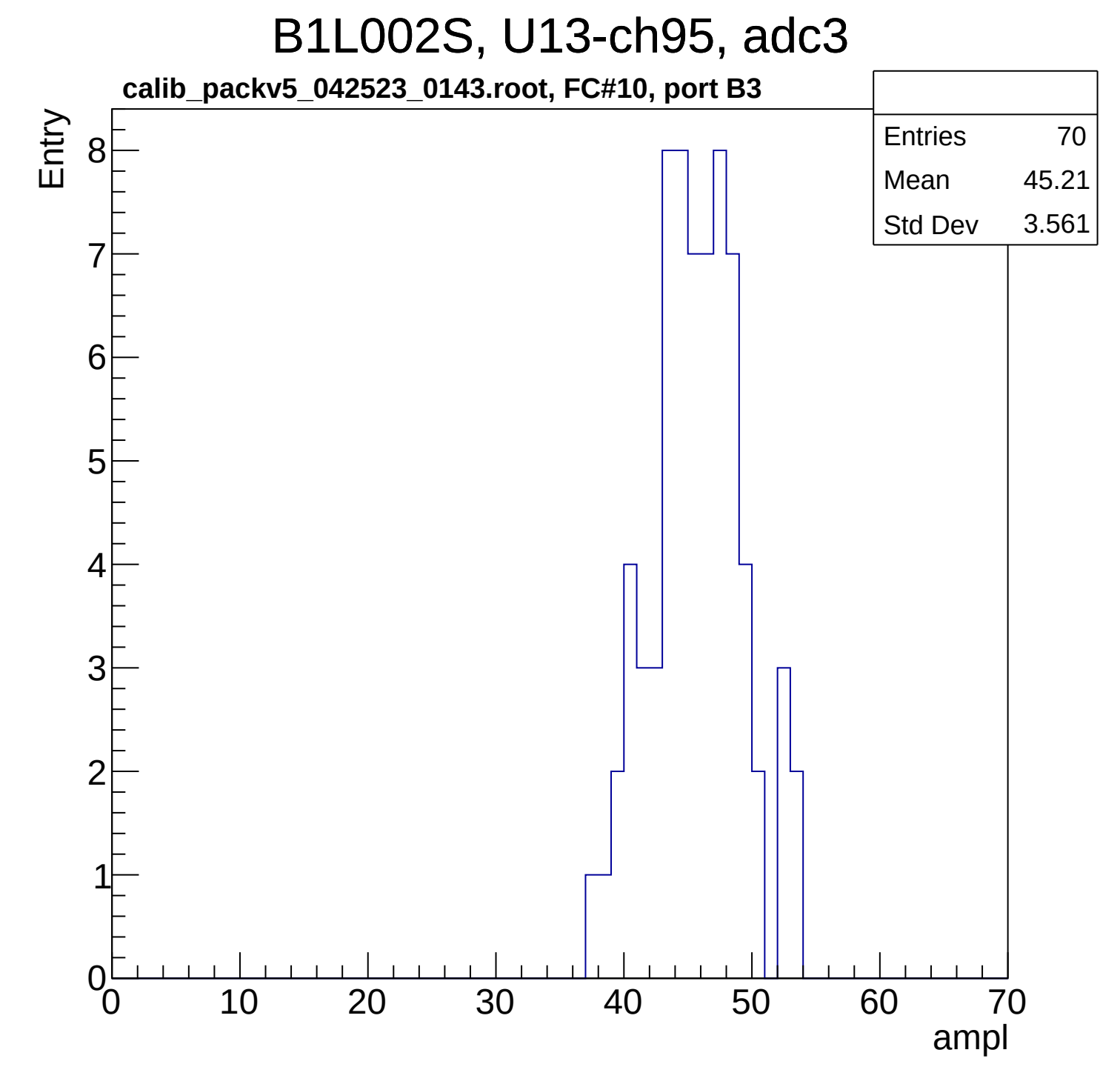
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 39.61
Std Dev 3.11
Gaus mean : 40.0790
Gaus Width: 3.5123
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 45.21
Std Dev 3.561
Entry
ampl
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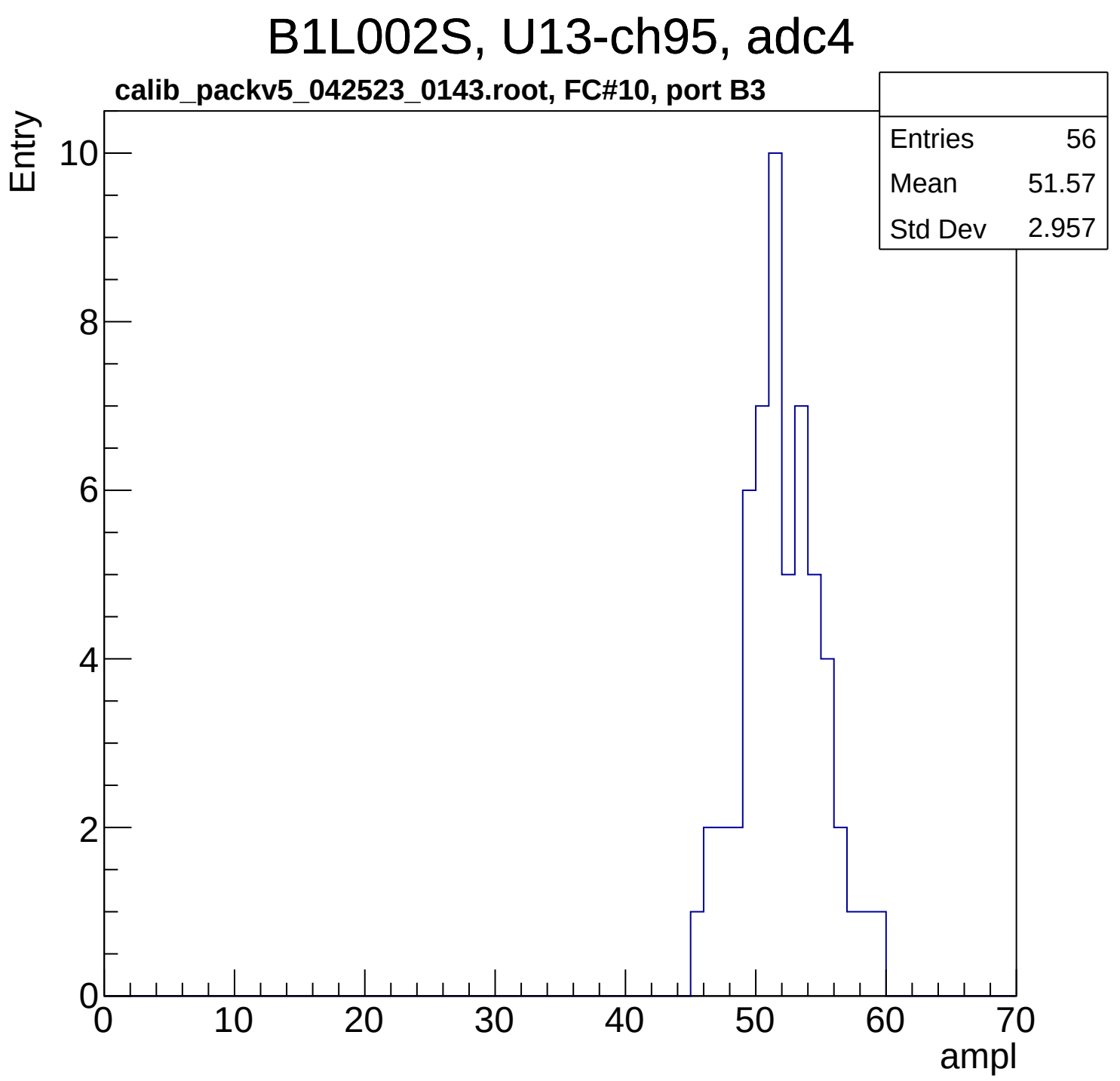
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.57
Std Dev 2.957
Entry
0
2
4
6
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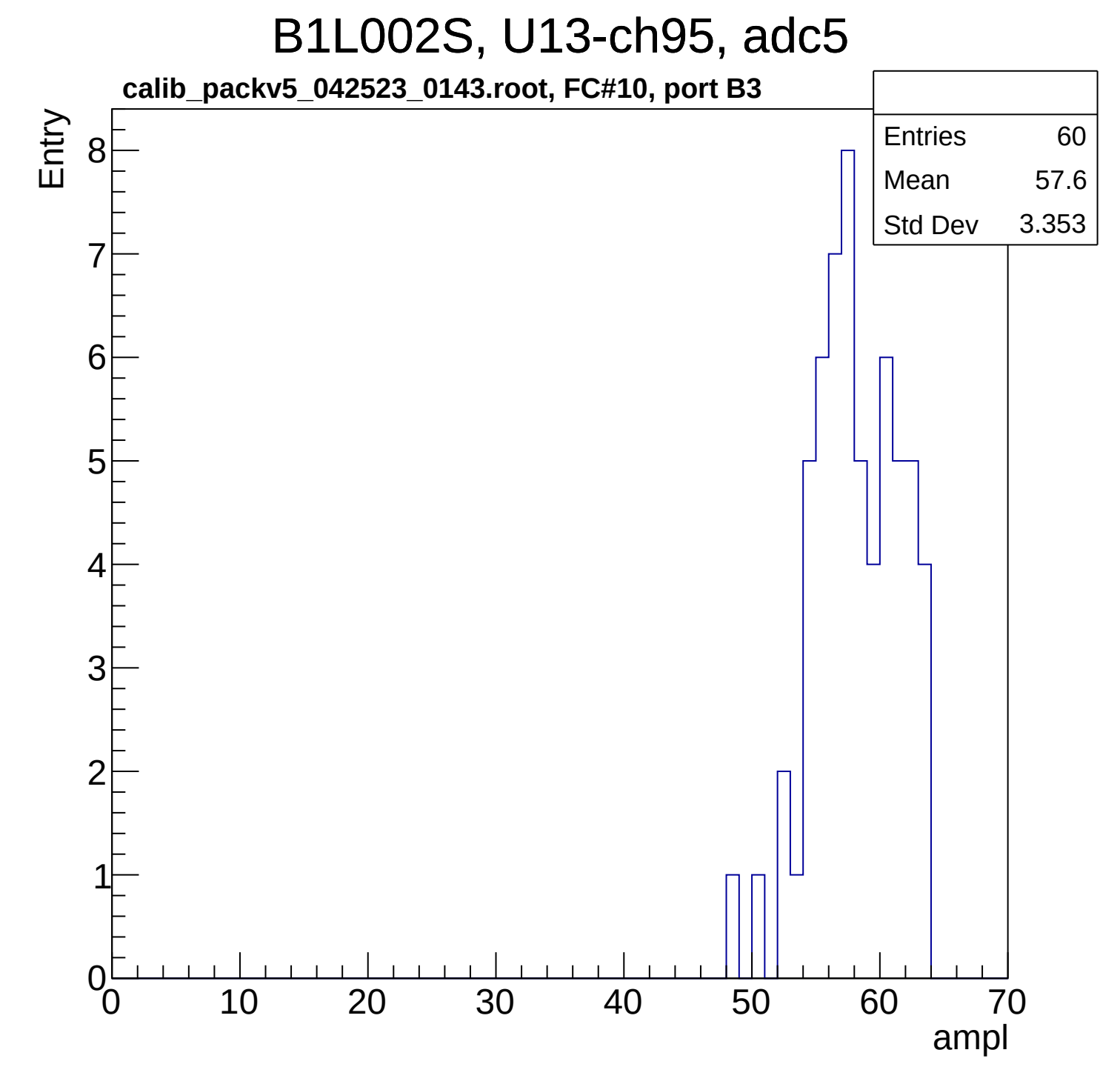
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.6
Std Dev 3.353
Entry
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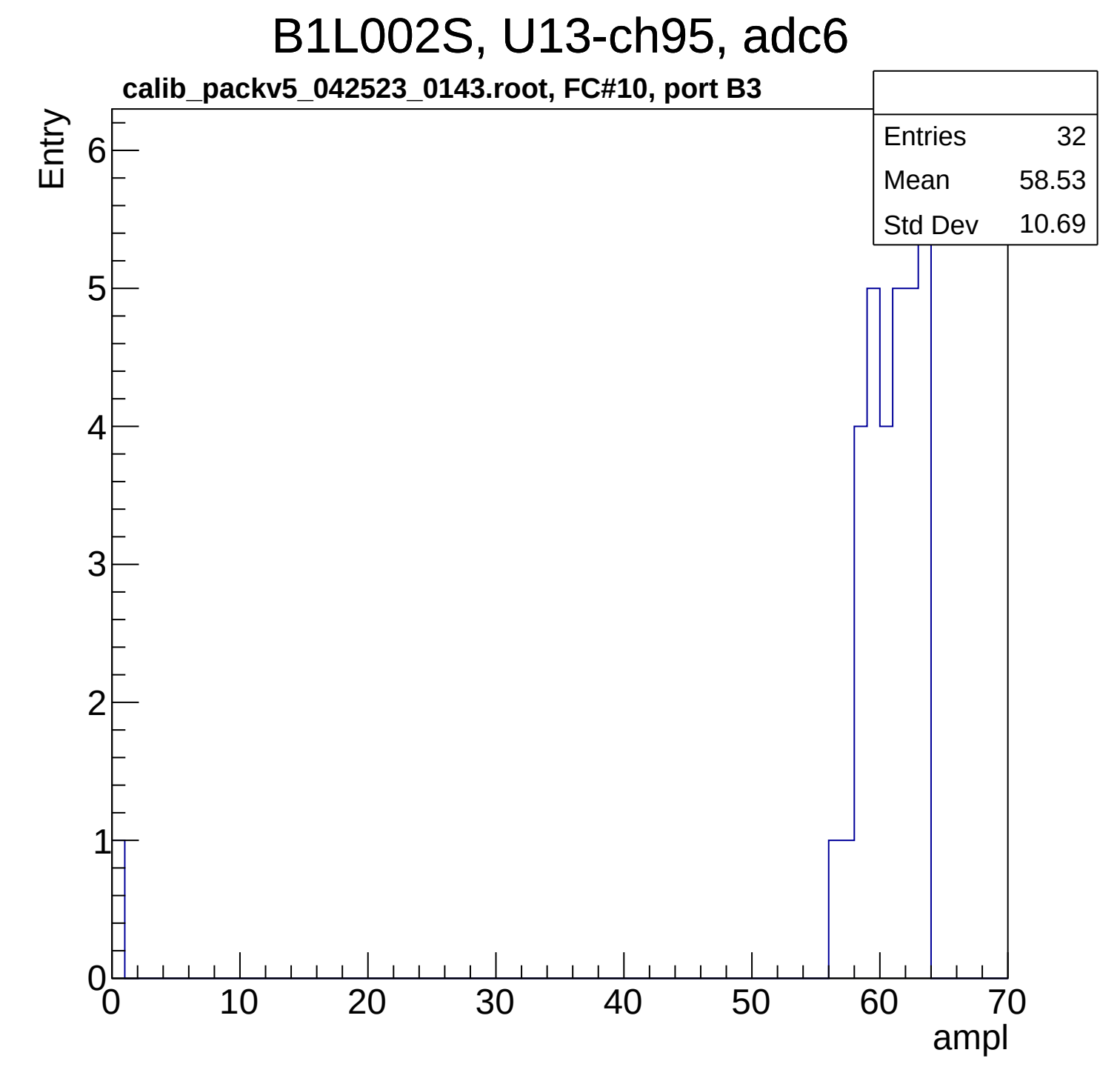
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.53
Std Dev 10.69
Entry
0
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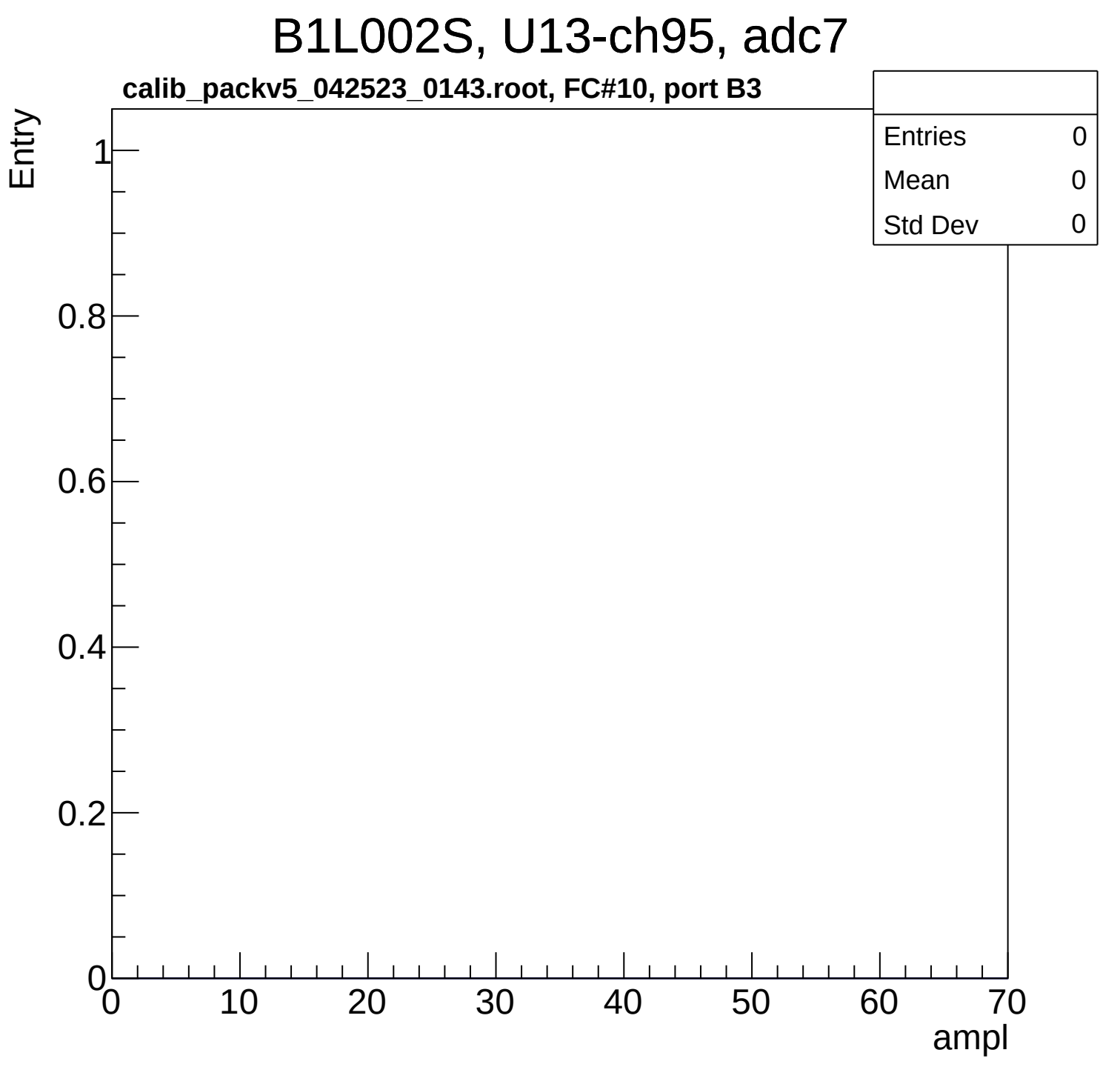
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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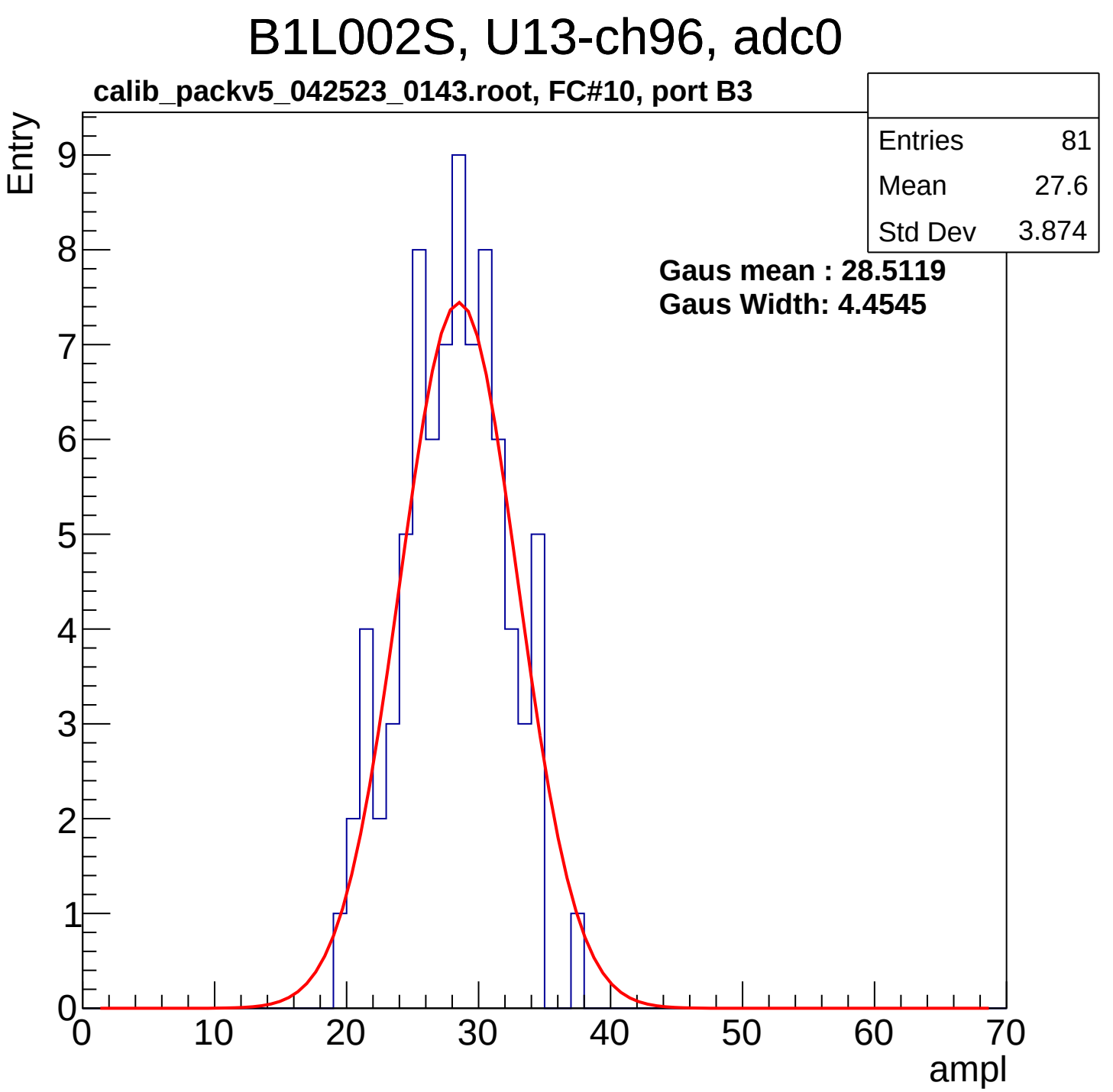
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 27.6
Std Dev 3.874
Gaus mean : 28.5119
Gaus Width: 4.4545
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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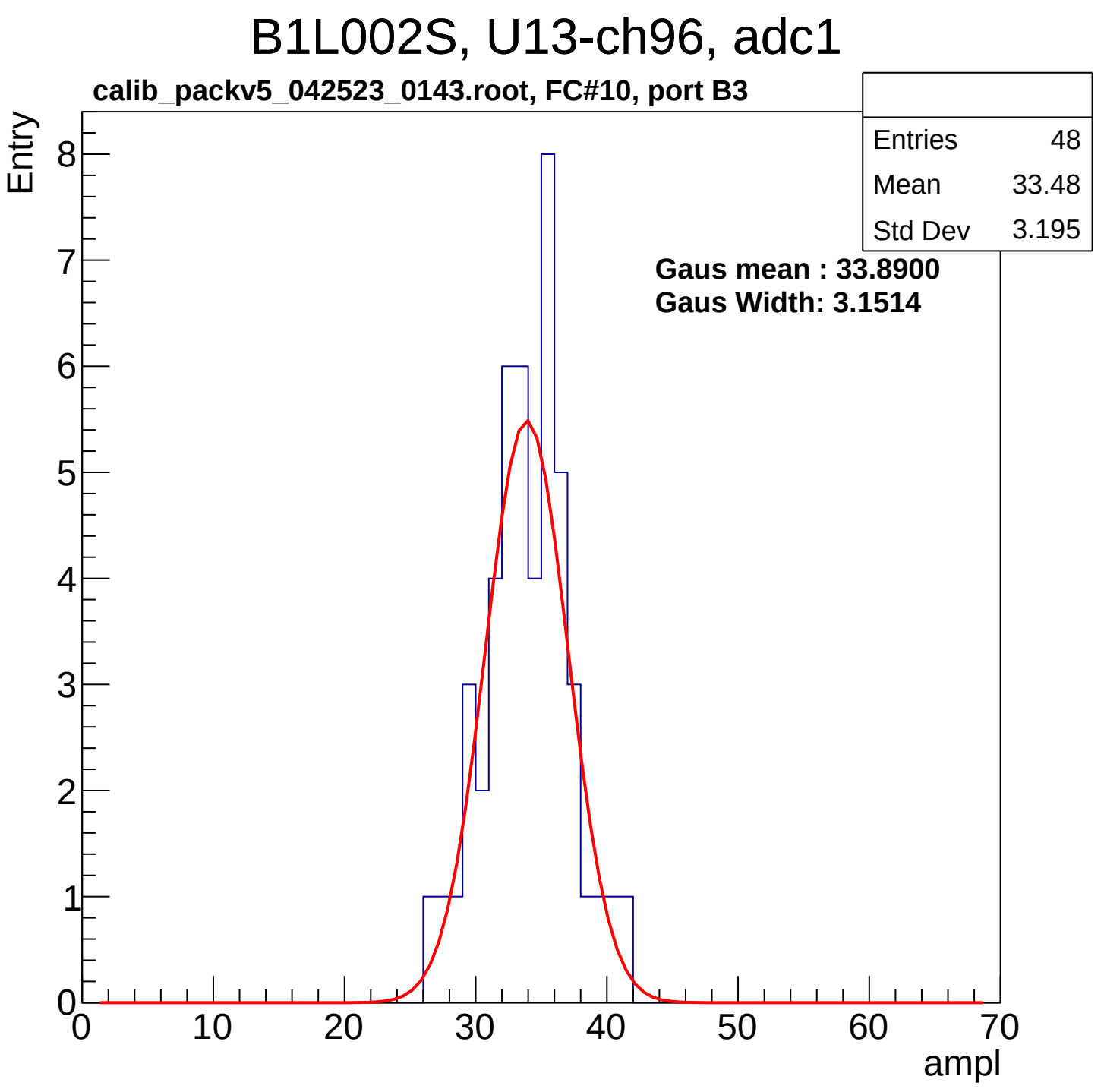
B1L002S, U13-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 33.48
Std Dev 3.195
Gaus mean : 33.8900
Gaus Width: 3.1514
Entry
ampl
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5
6
7
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0
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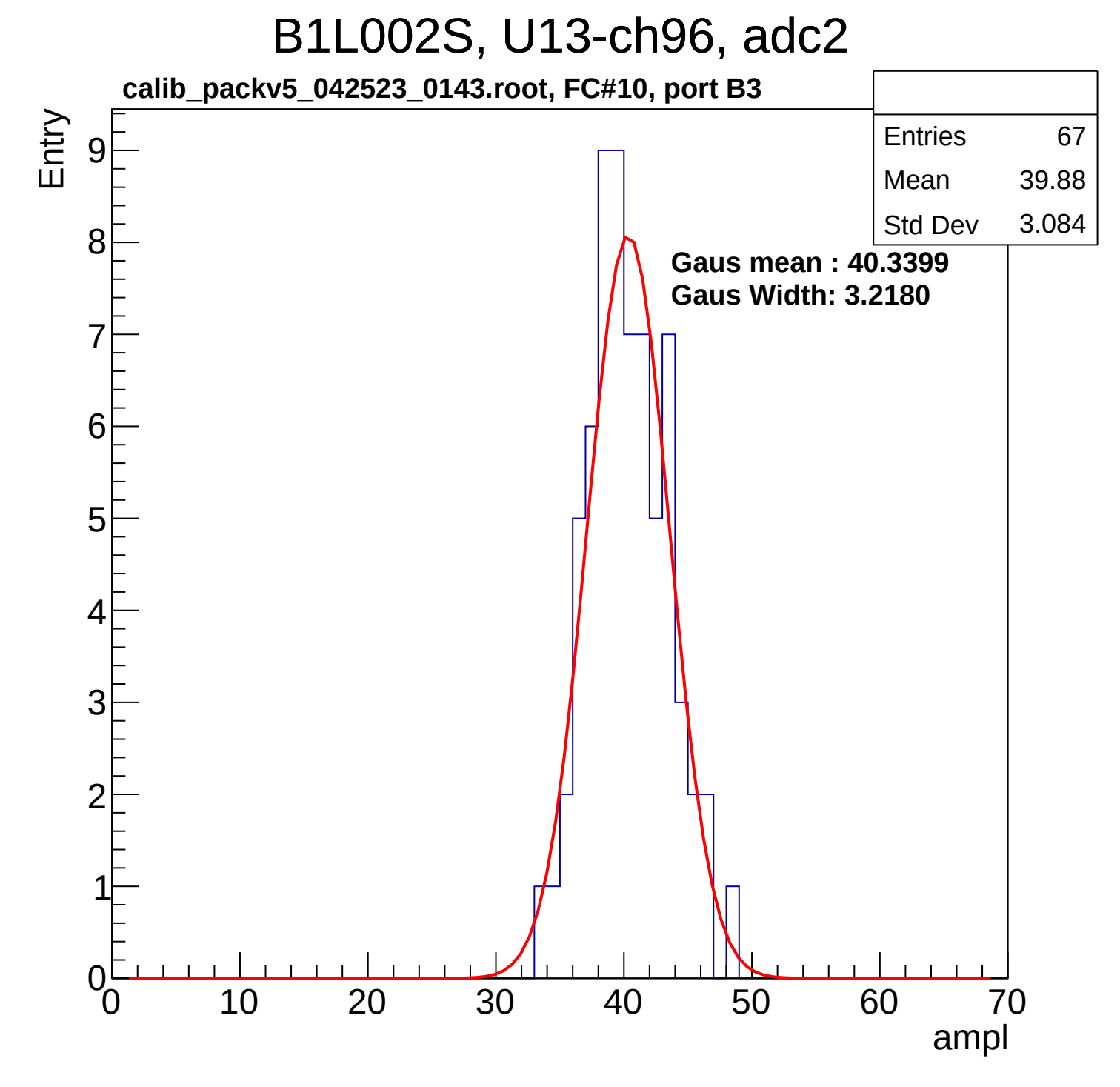
B1L002S, U13-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 39.88
Std Dev 3.084
Gaus mean : 40.3399
Gaus Width: 3.2180
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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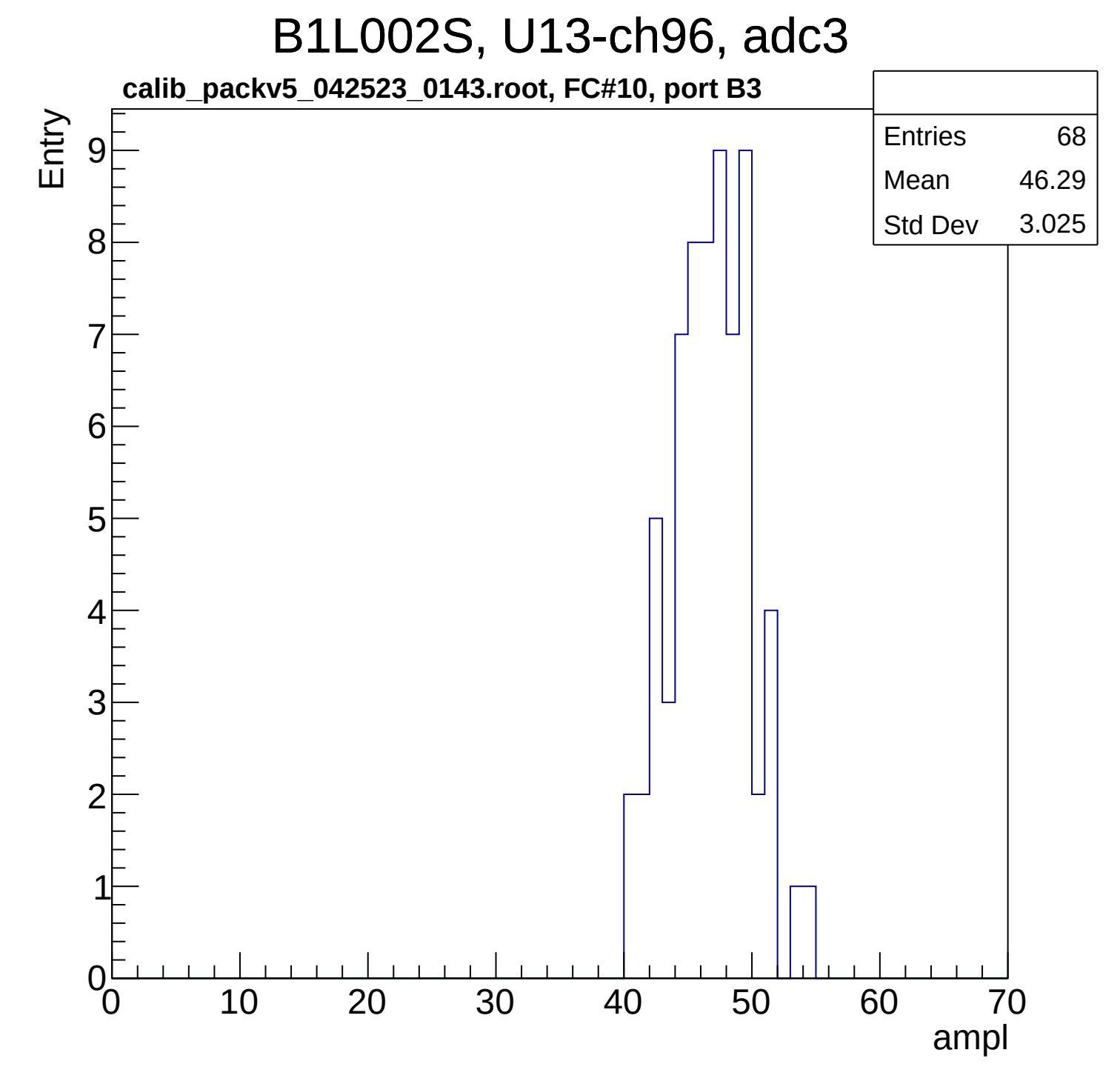
B1L002S, U13-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.29
Std Dev 3.025
Entry
ampl
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3
4
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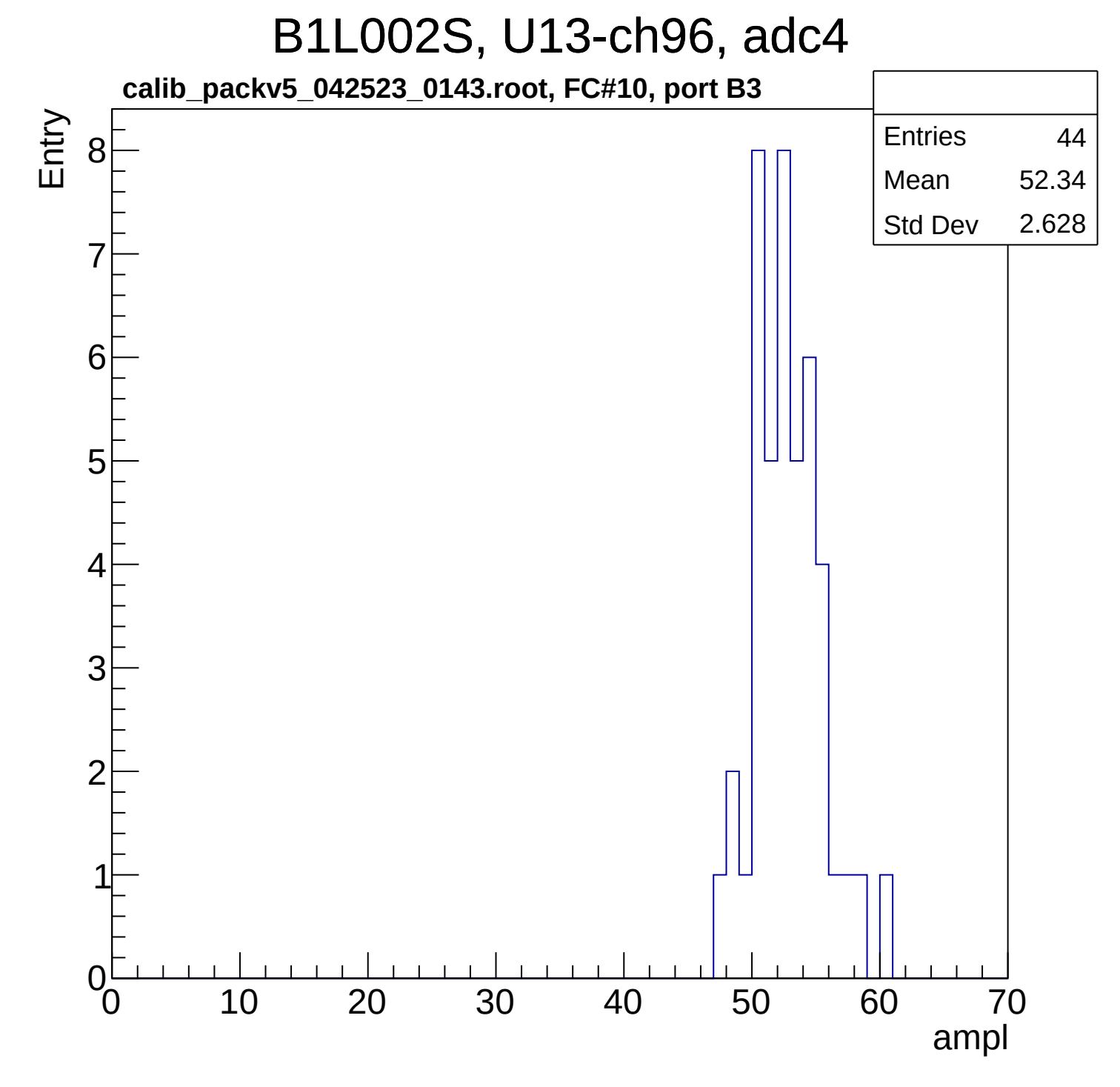
B1L002S, U13-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.34
Std Dev 2.628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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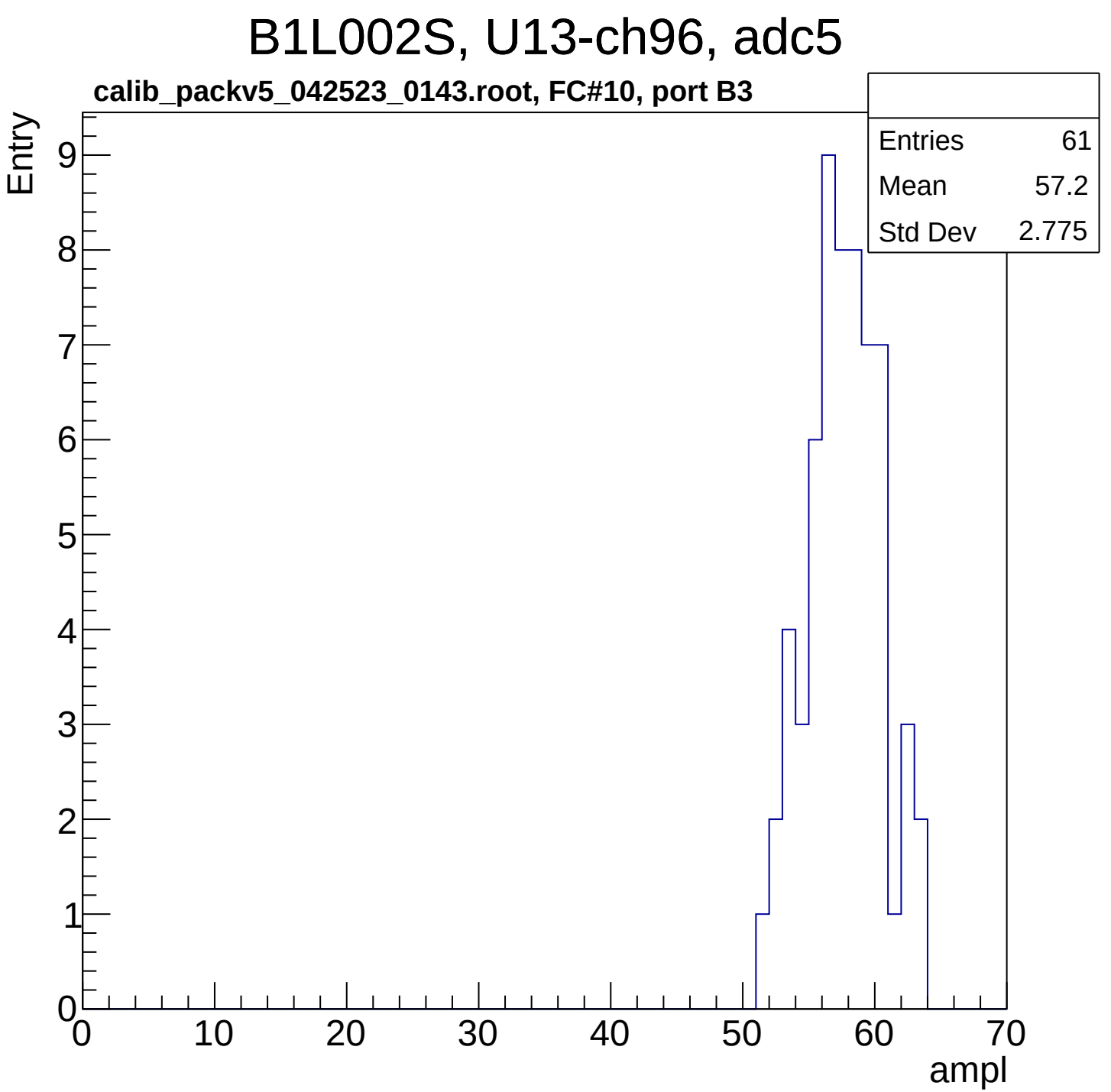
B1L002S, U13-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.2
Std Dev 2.775
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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60
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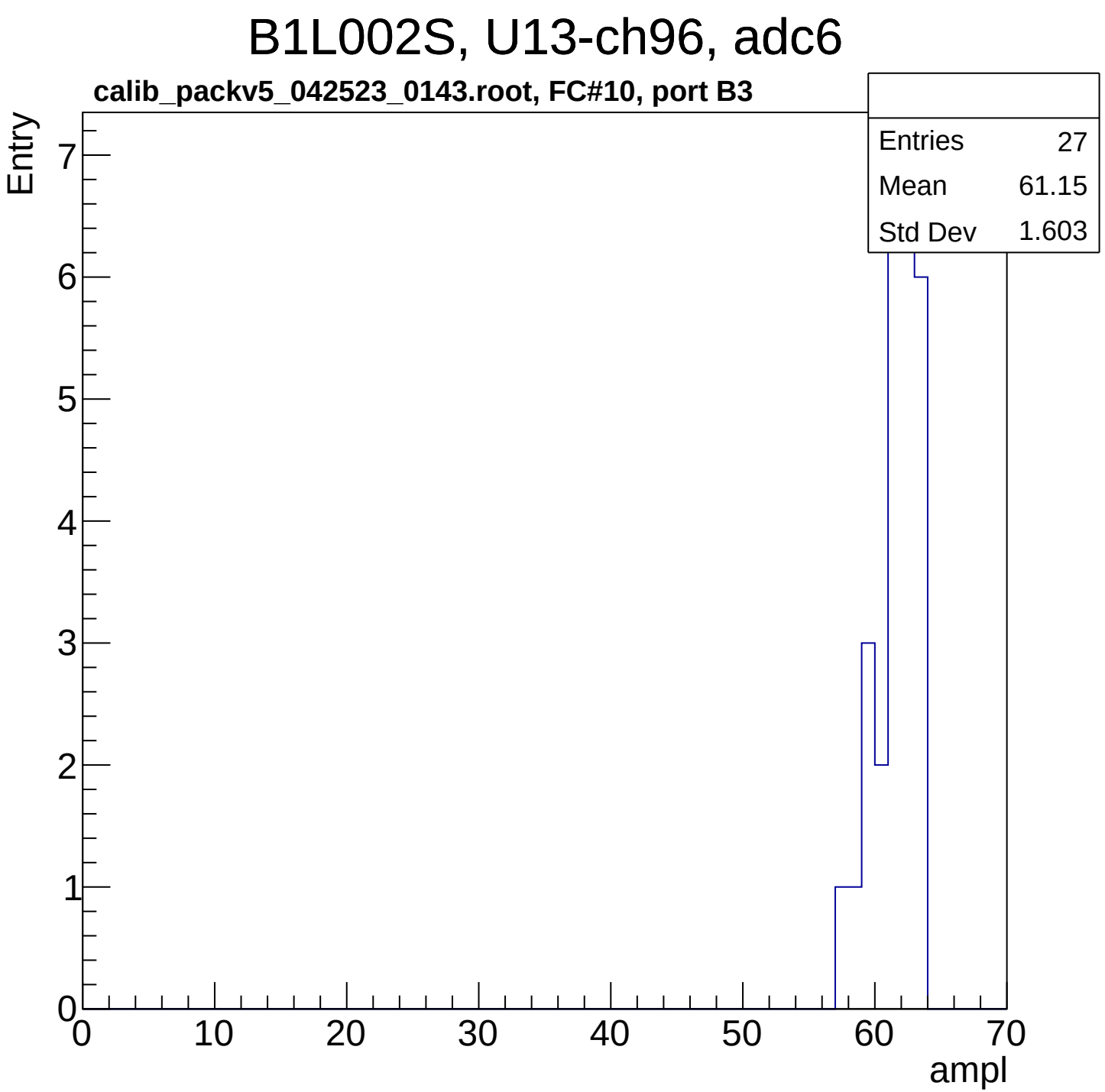
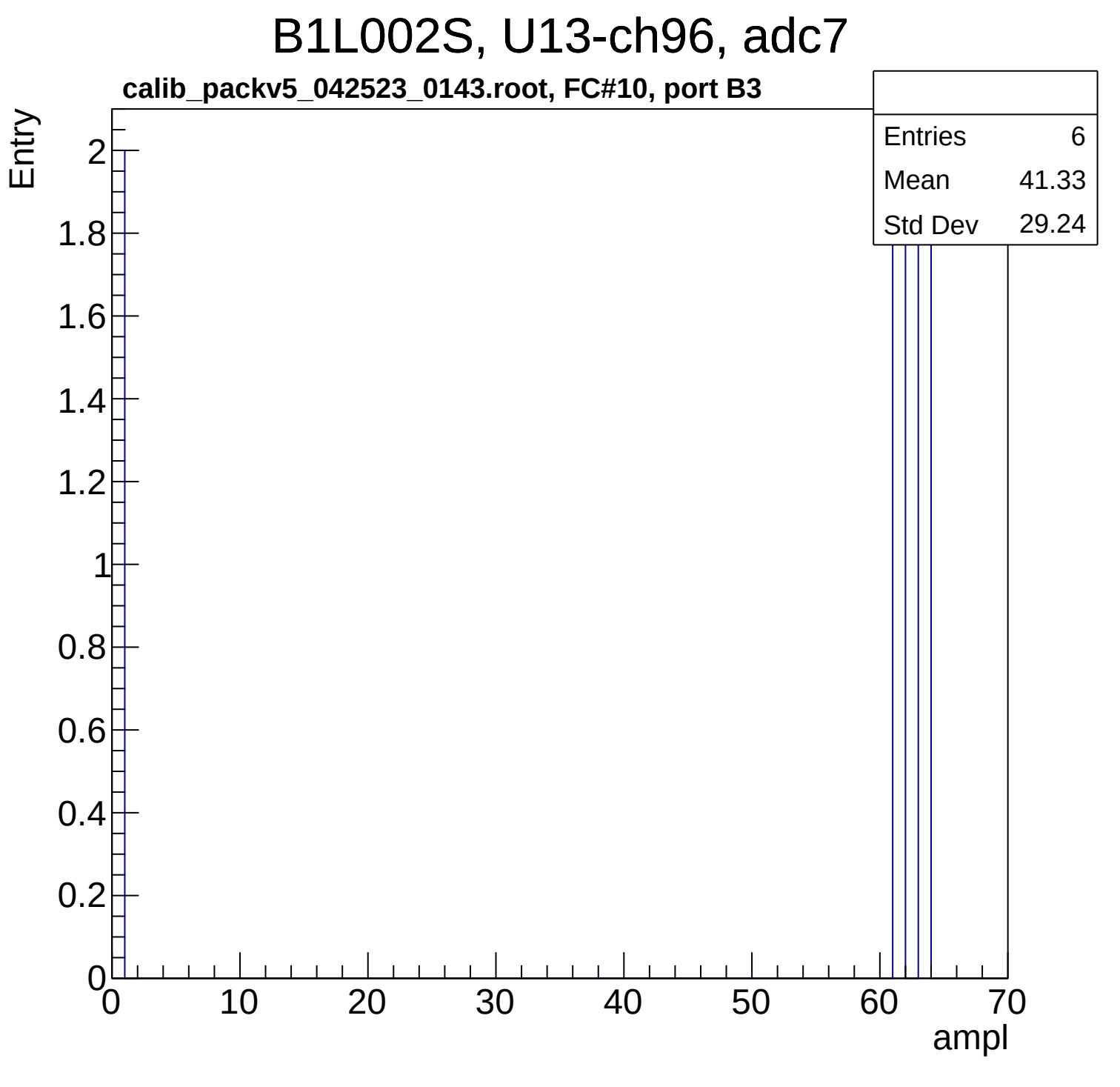
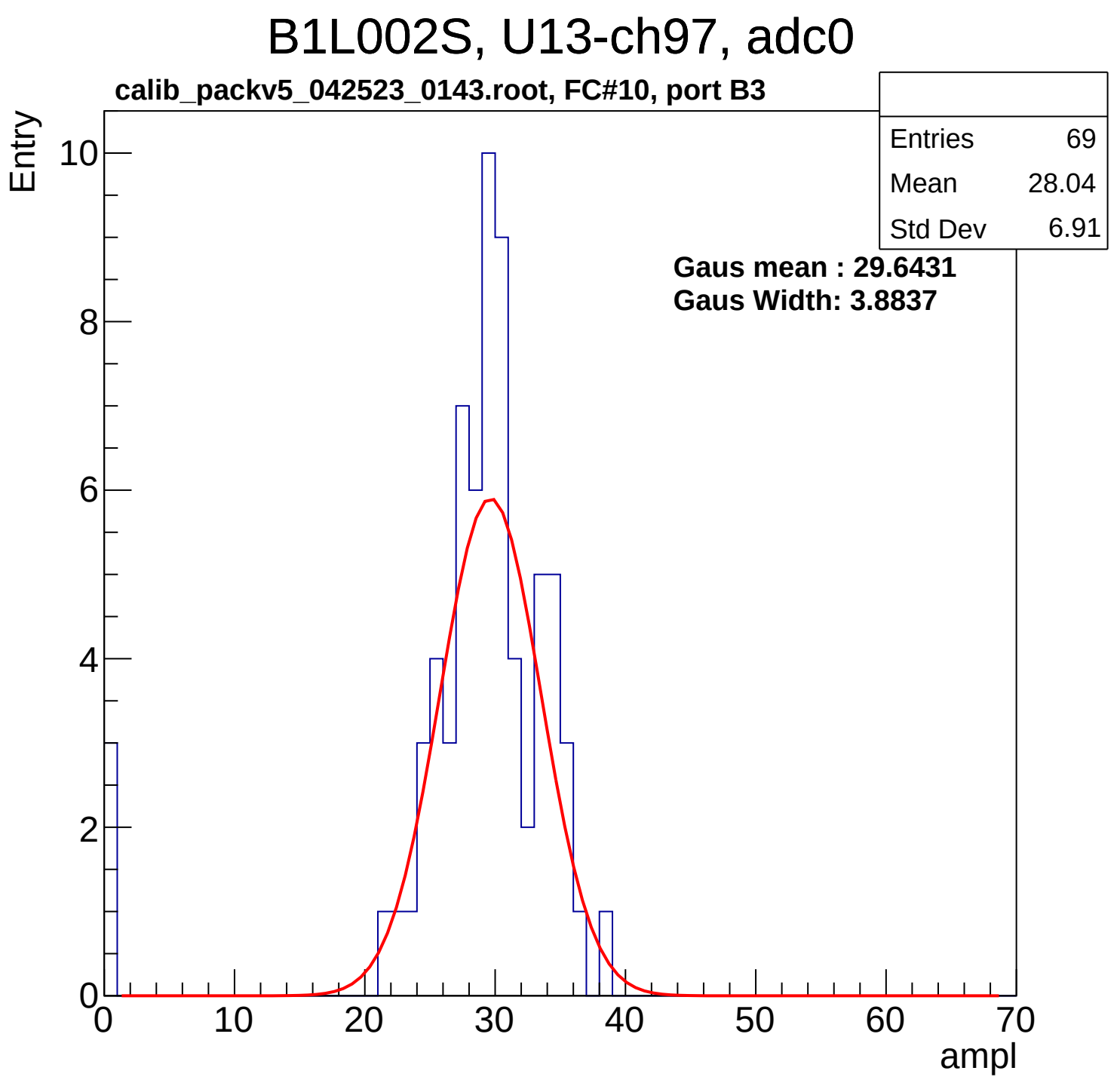
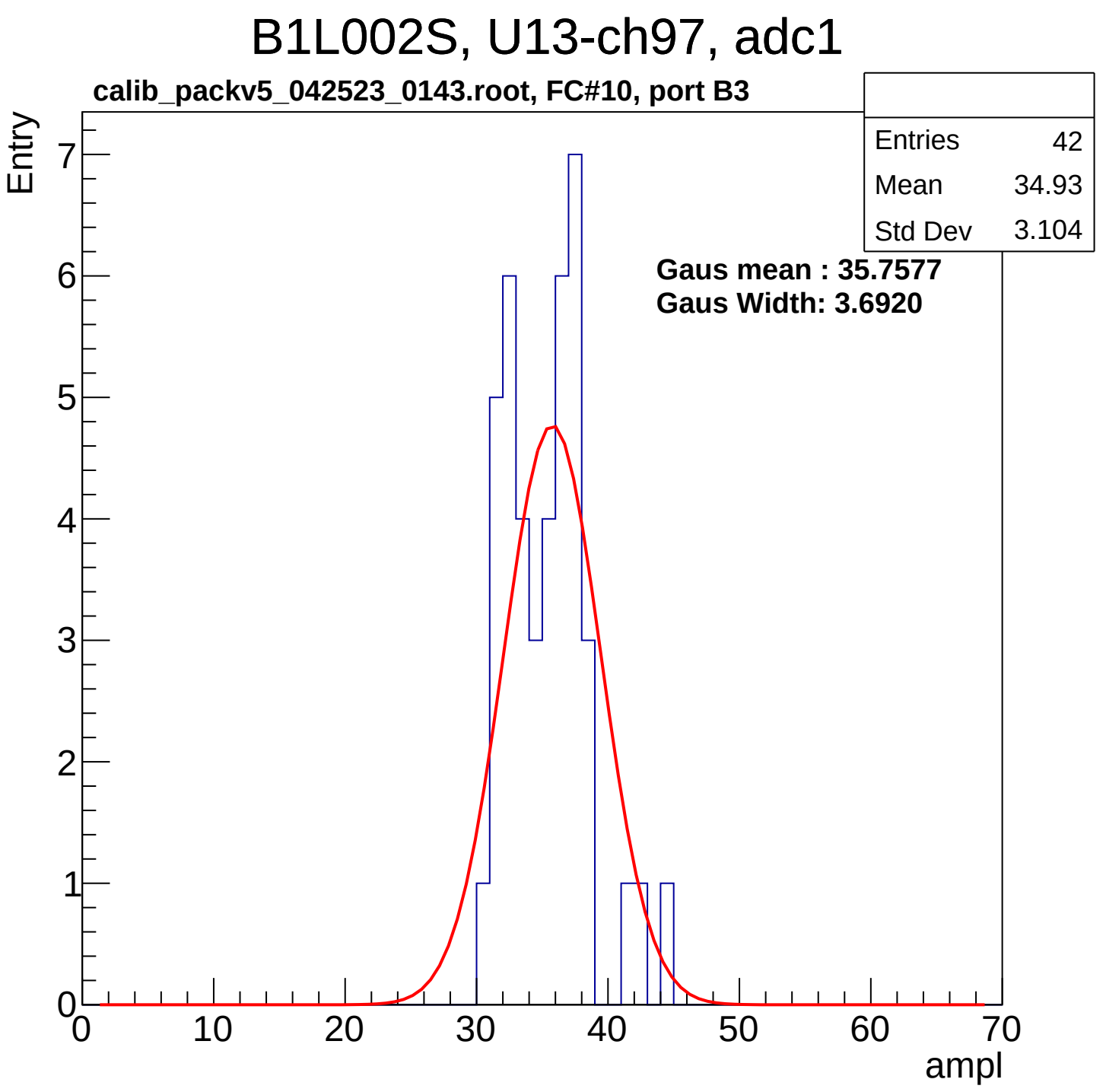

B1L002S, U13-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 61.15
Std Dev 1.603
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.33
Std Dev 29.24
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.04
Std Dev 6.91
Gaus mean : 29.6431
Gaus Width: 3.8837
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 34.93
Std Dev 3.104
Gaus mean : 35.7577
Gaus Width: 3.6920
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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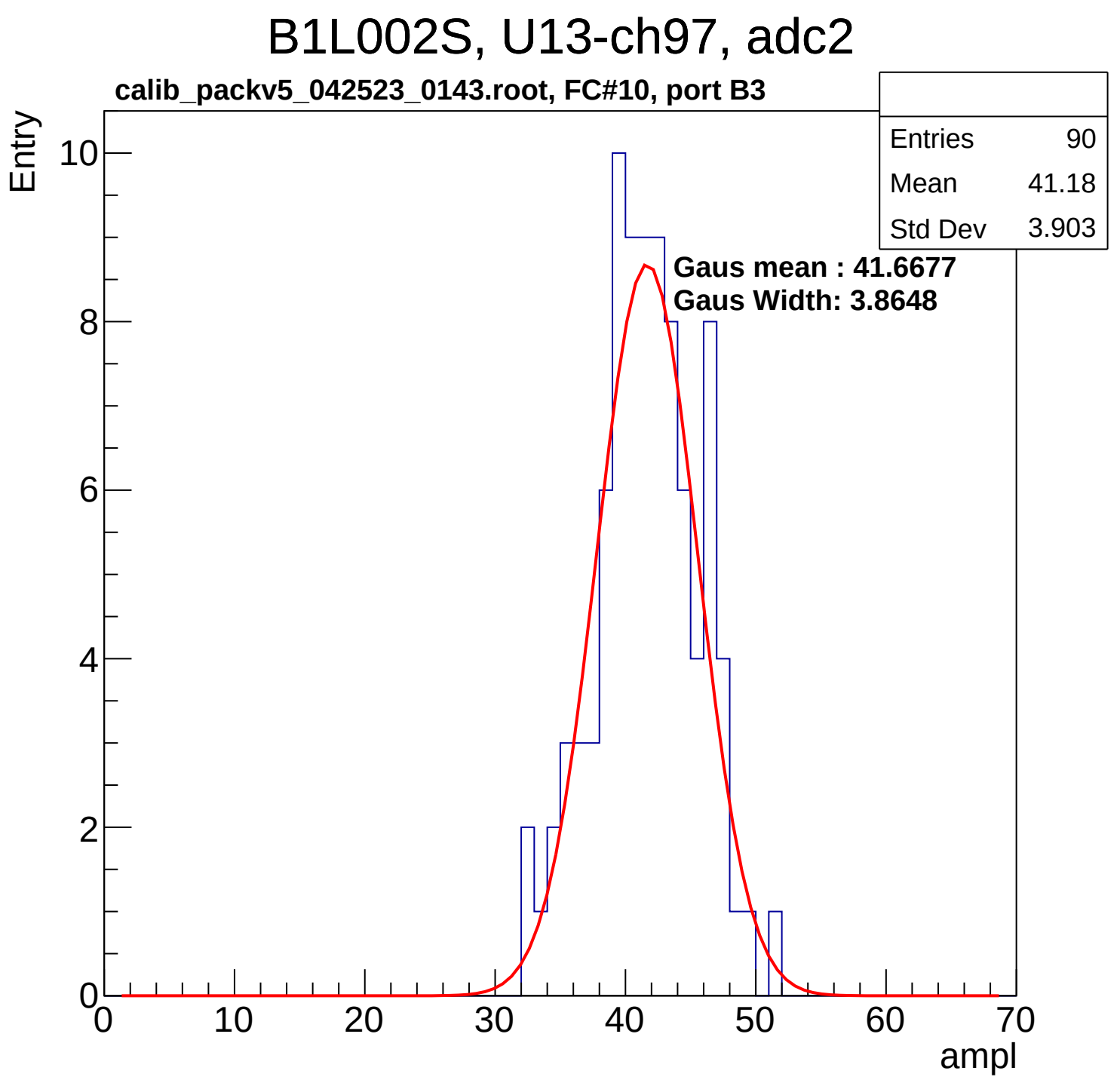
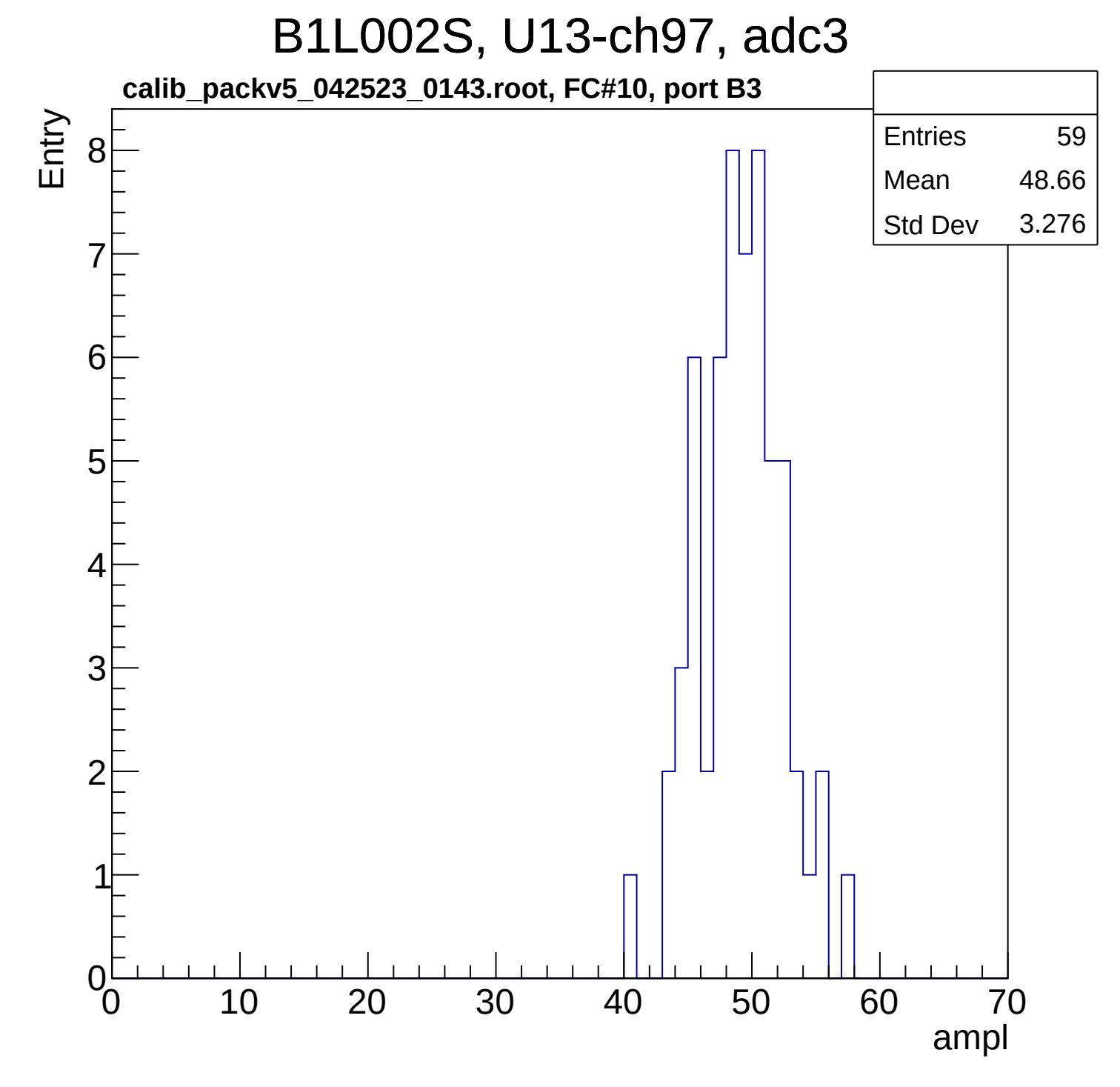
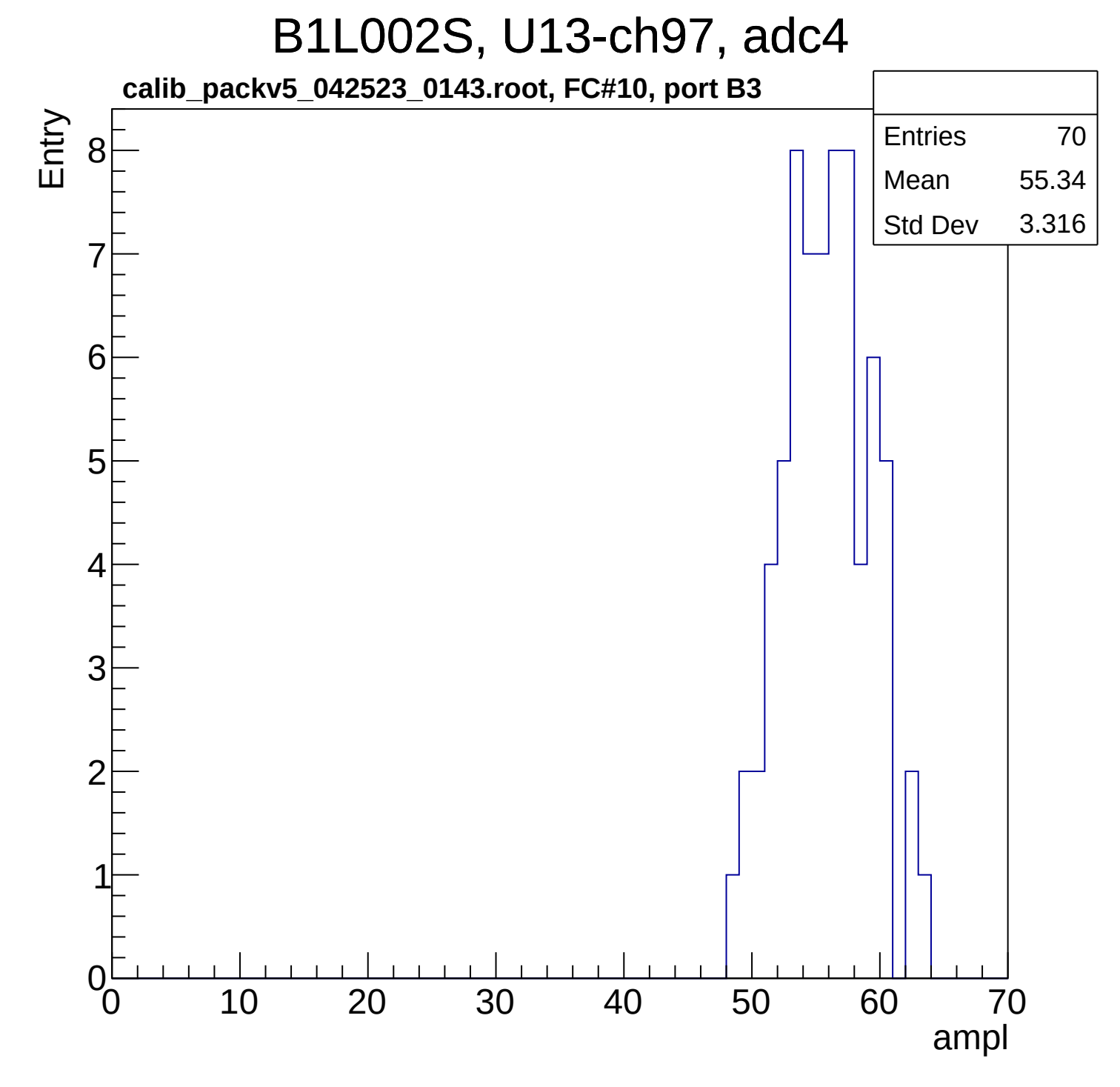
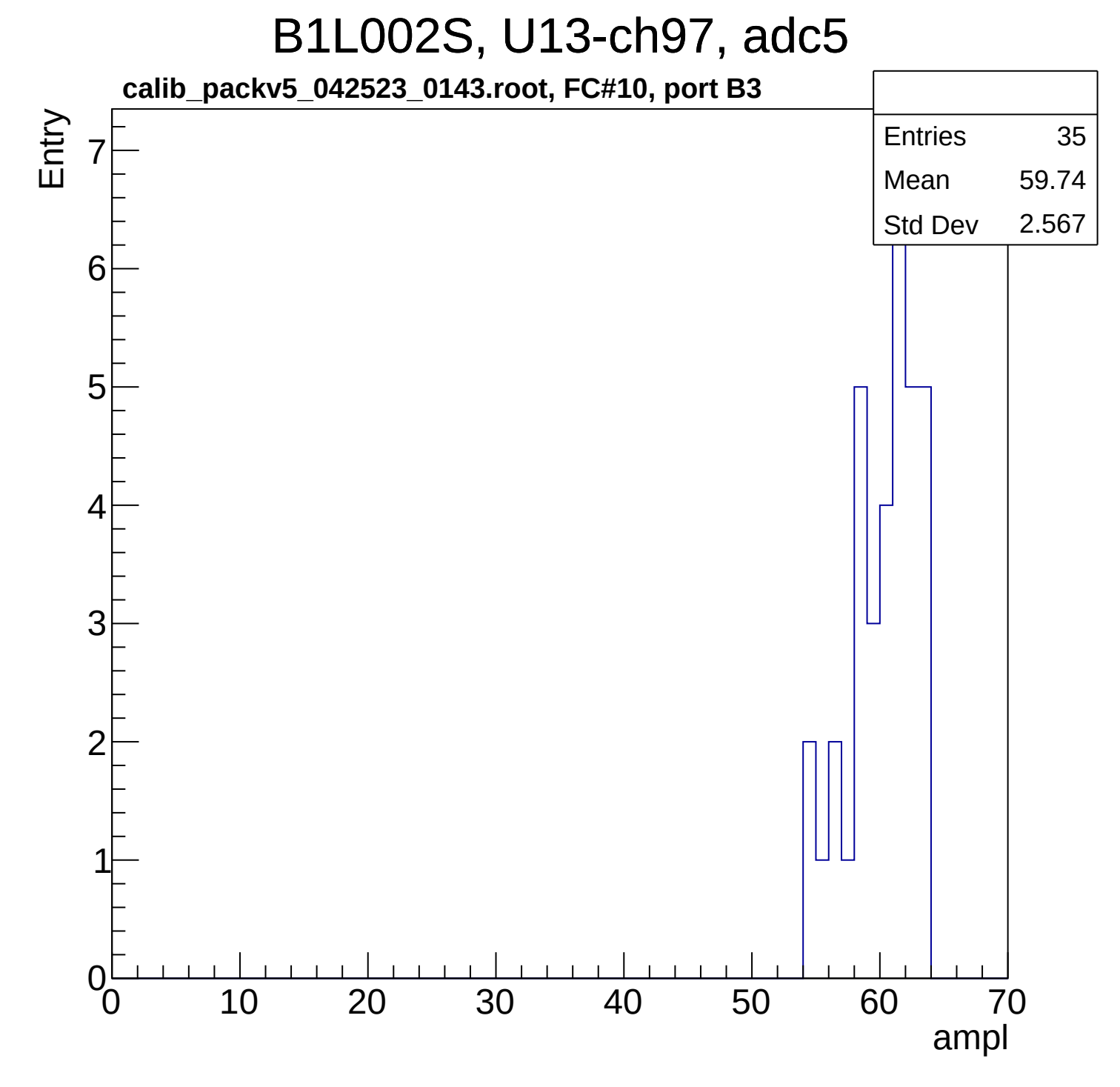
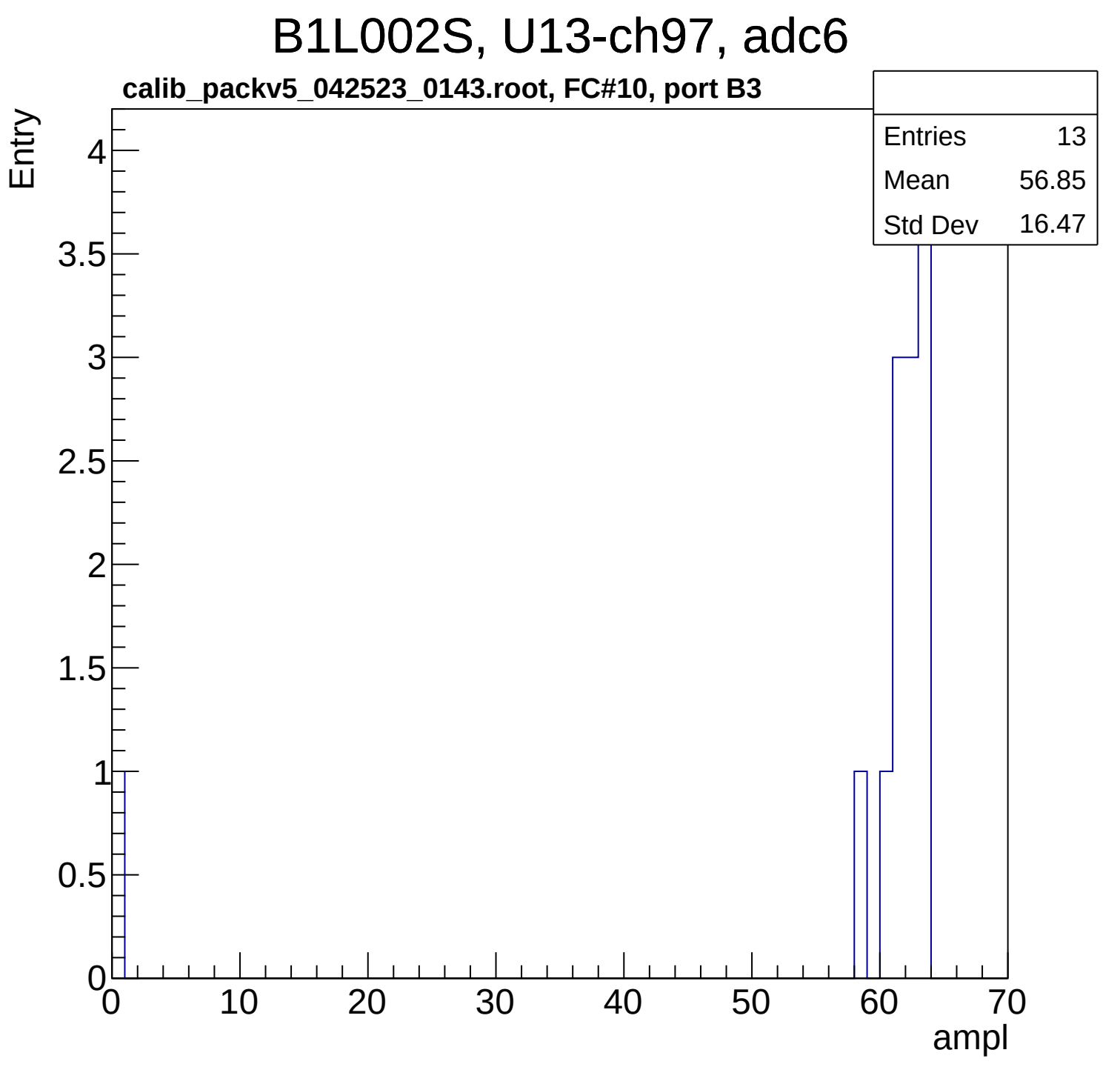
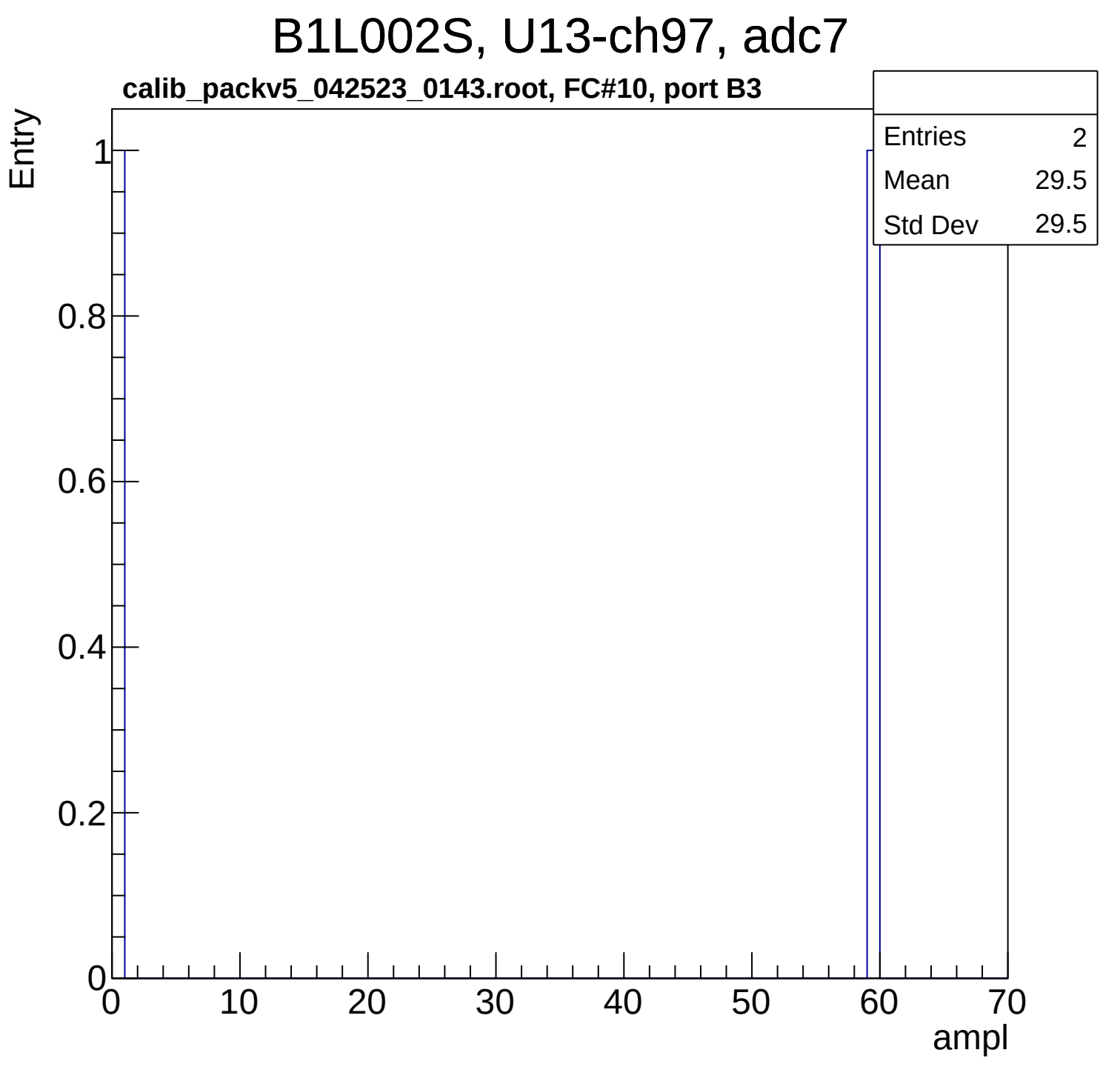

B1L002S, U13-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 41.18
Std Dev 3.903
Gaus mean : 41.6677
Gaus Width: 3.8648
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

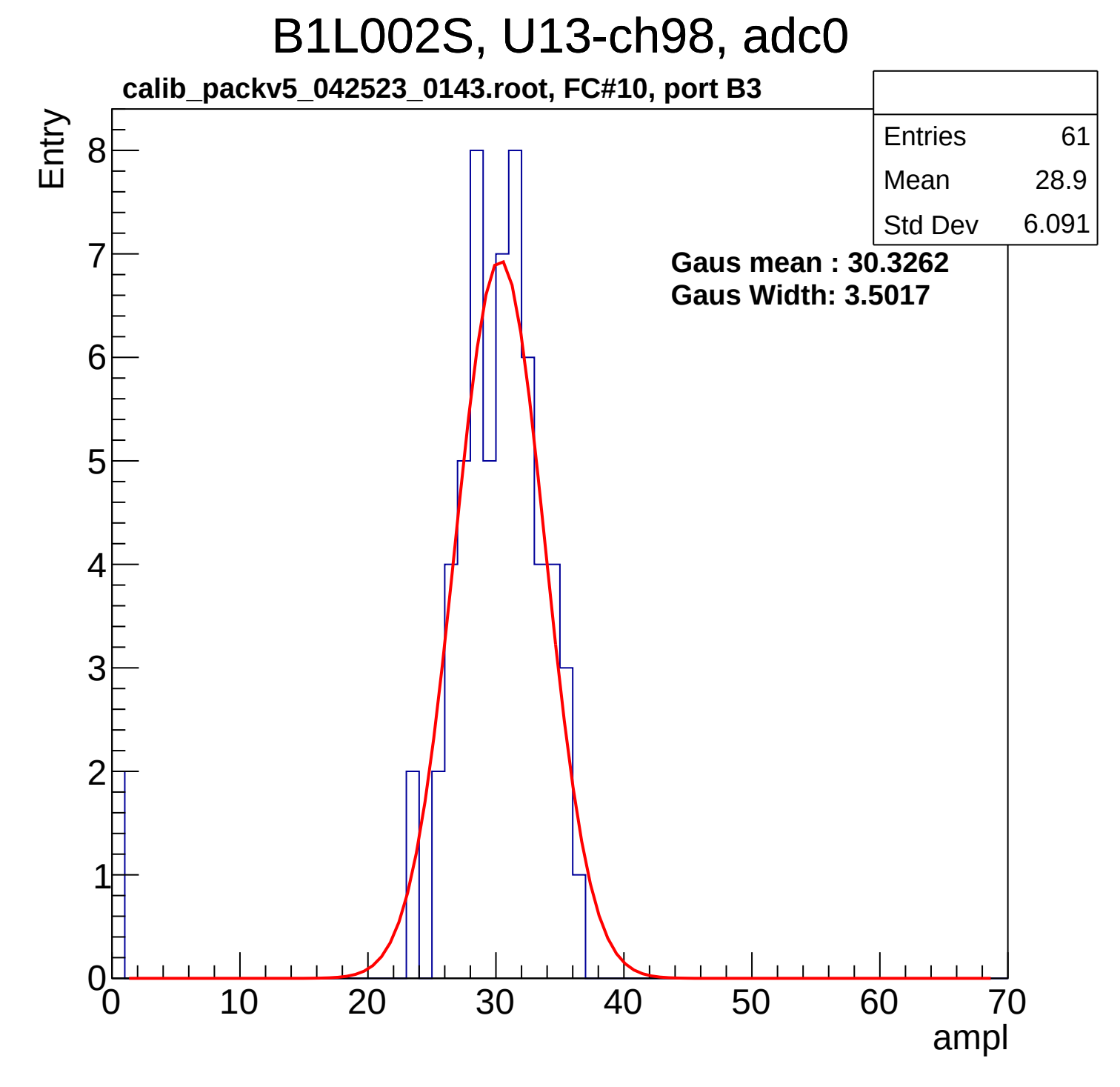

B1L002S, U13-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 48.66
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

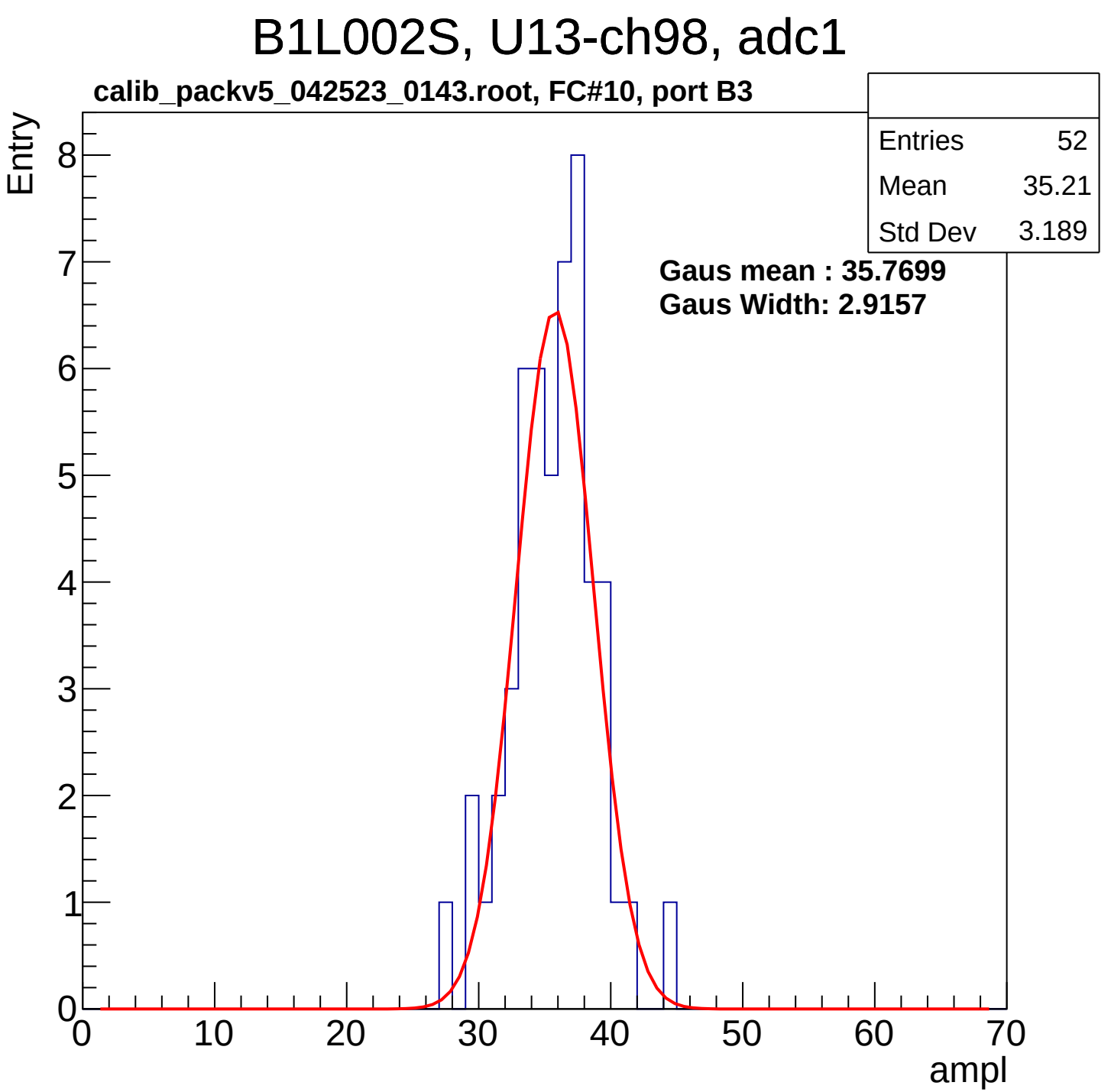

B1L002S, U13-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 55.34
Std Dev 3.316
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

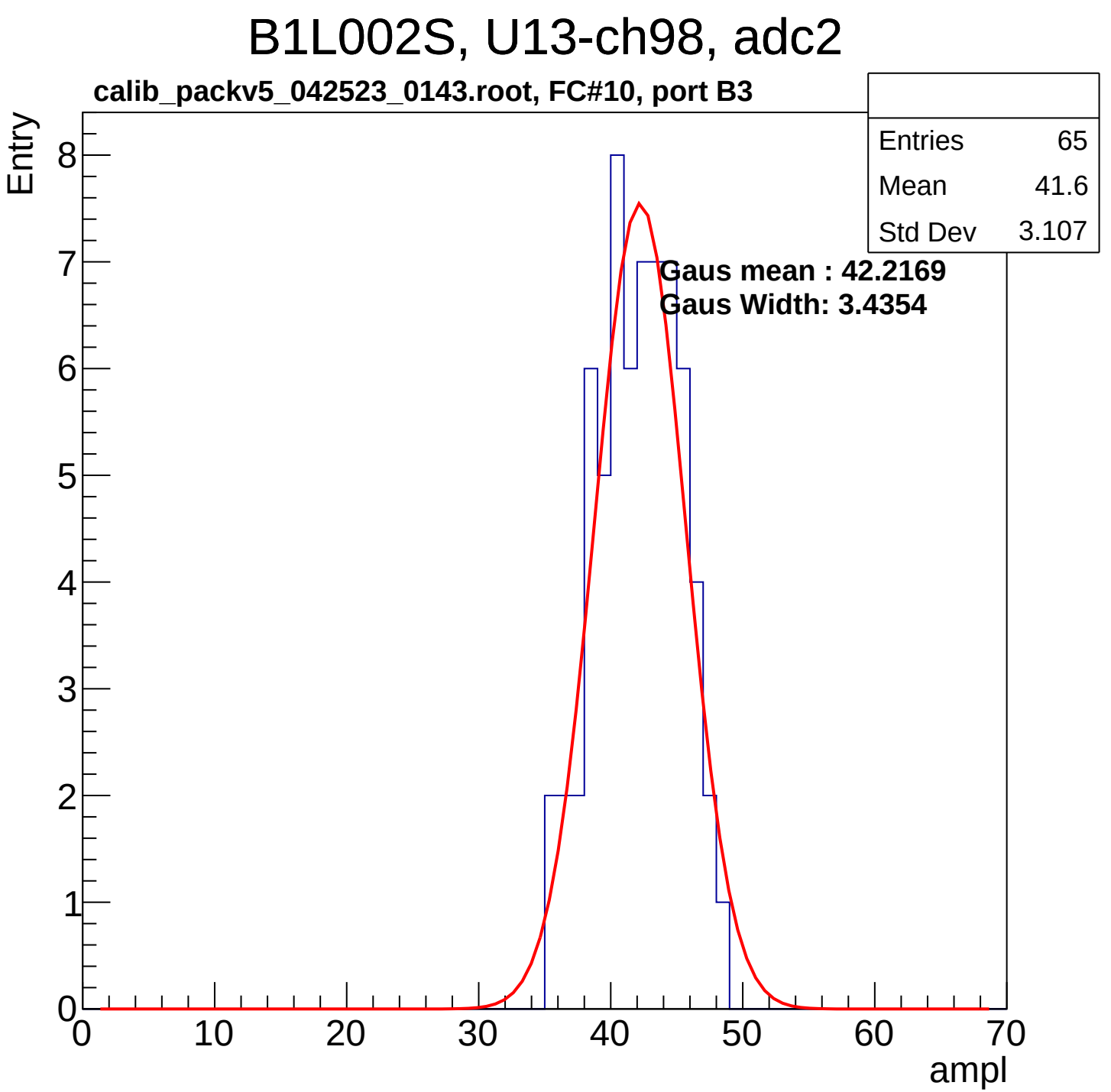

B1L002S, U13-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 59.74
Std Dev 2.567
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

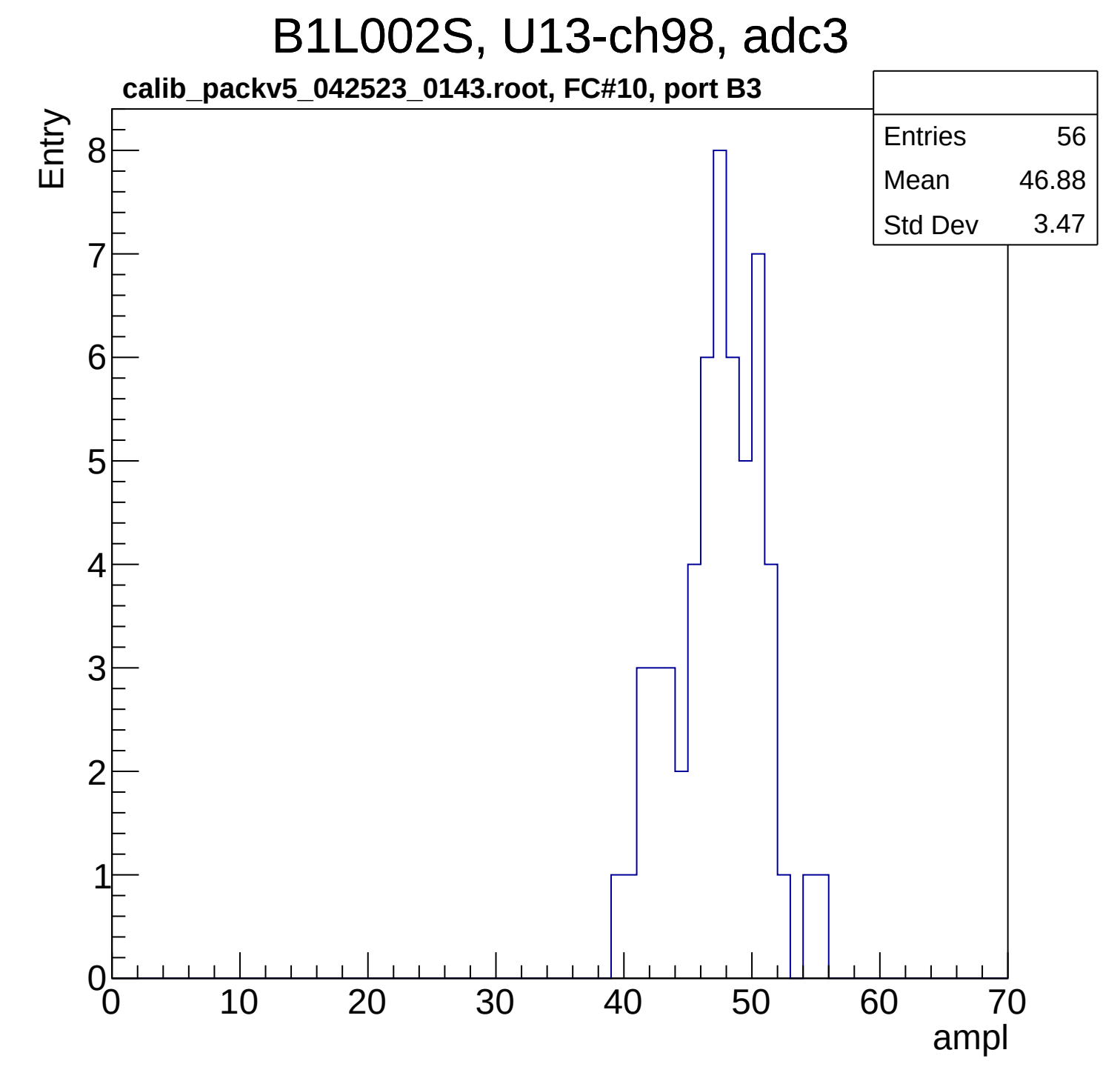

B1L002S, U13-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 56.85
Std Dev 16.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

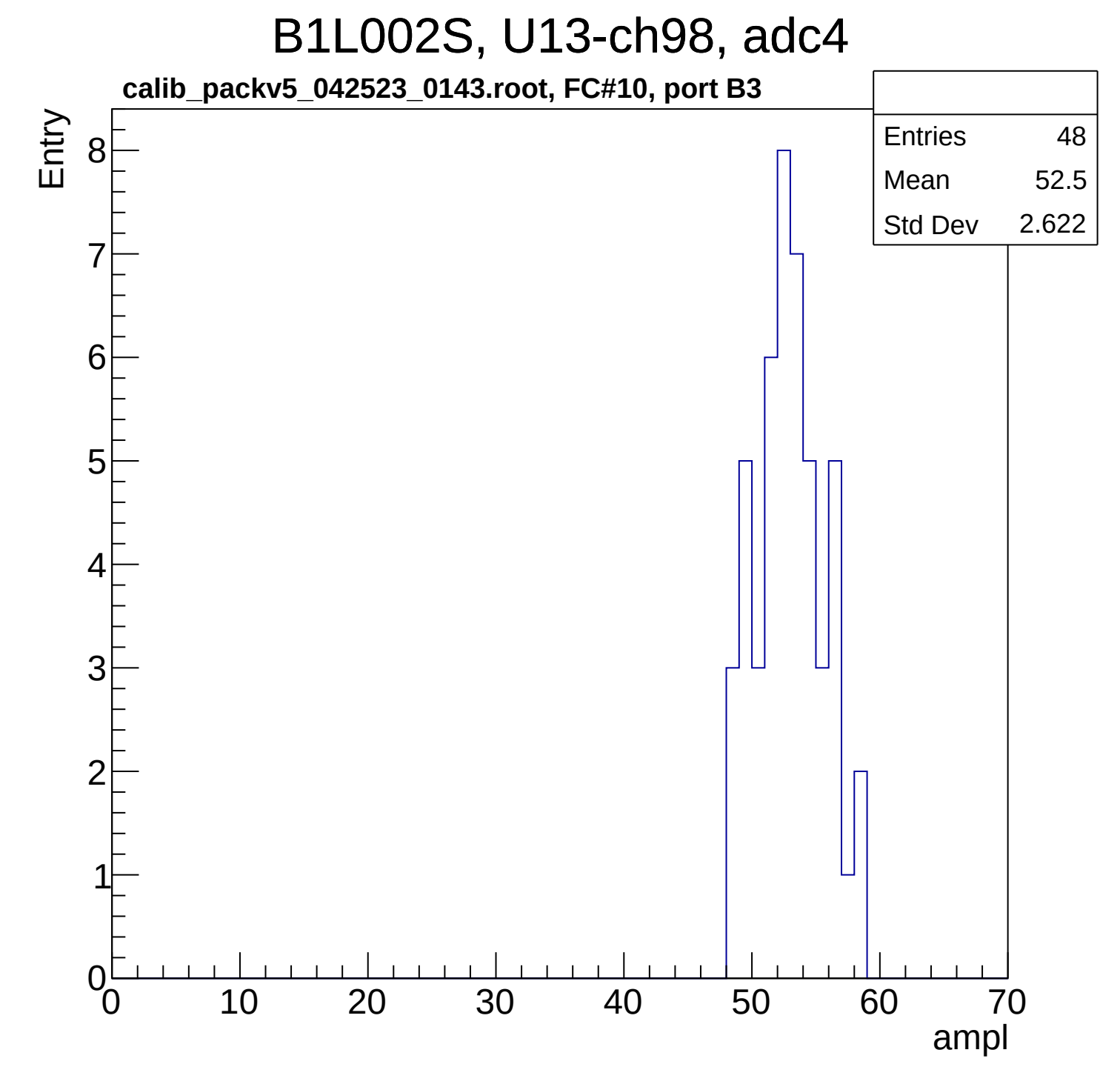

B1L002S, U13-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 29.5
Std Dev 29.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

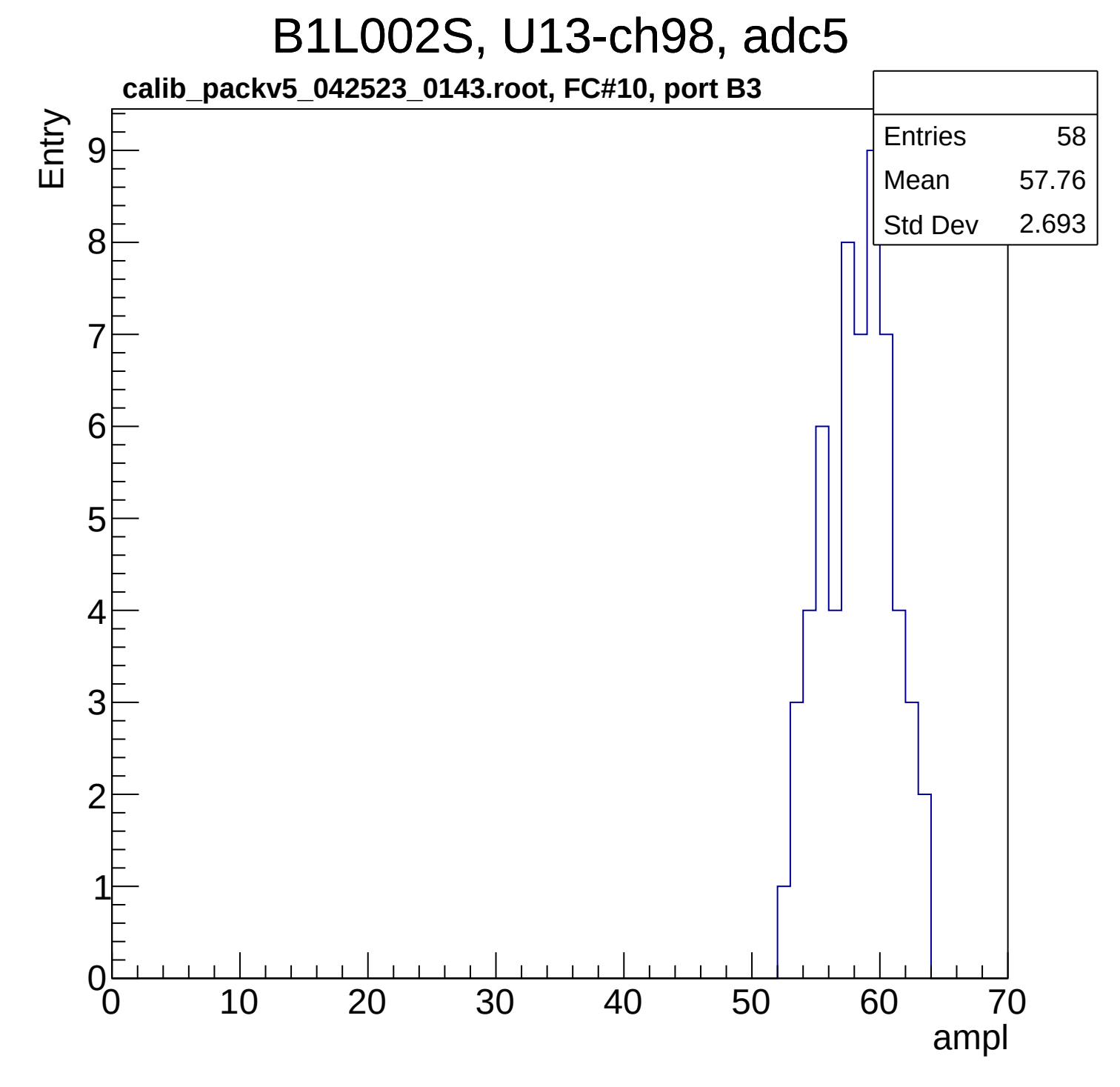

B1L002S, U13-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.9
Std Dev 6.091
Gaus mean : 30.3262
Gaus Width: 3.5017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.21
Std Dev 3.189
Gaus mean : 35.7699
Gaus Width: 2.9157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.6
Std Dev 3.107
Gaus mean : 42.2169
Gaus Width: 3.4354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.88
Std Dev 3.47
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.5
Std Dev 2.622
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.76
Std Dev 2.693
Entry
ampl
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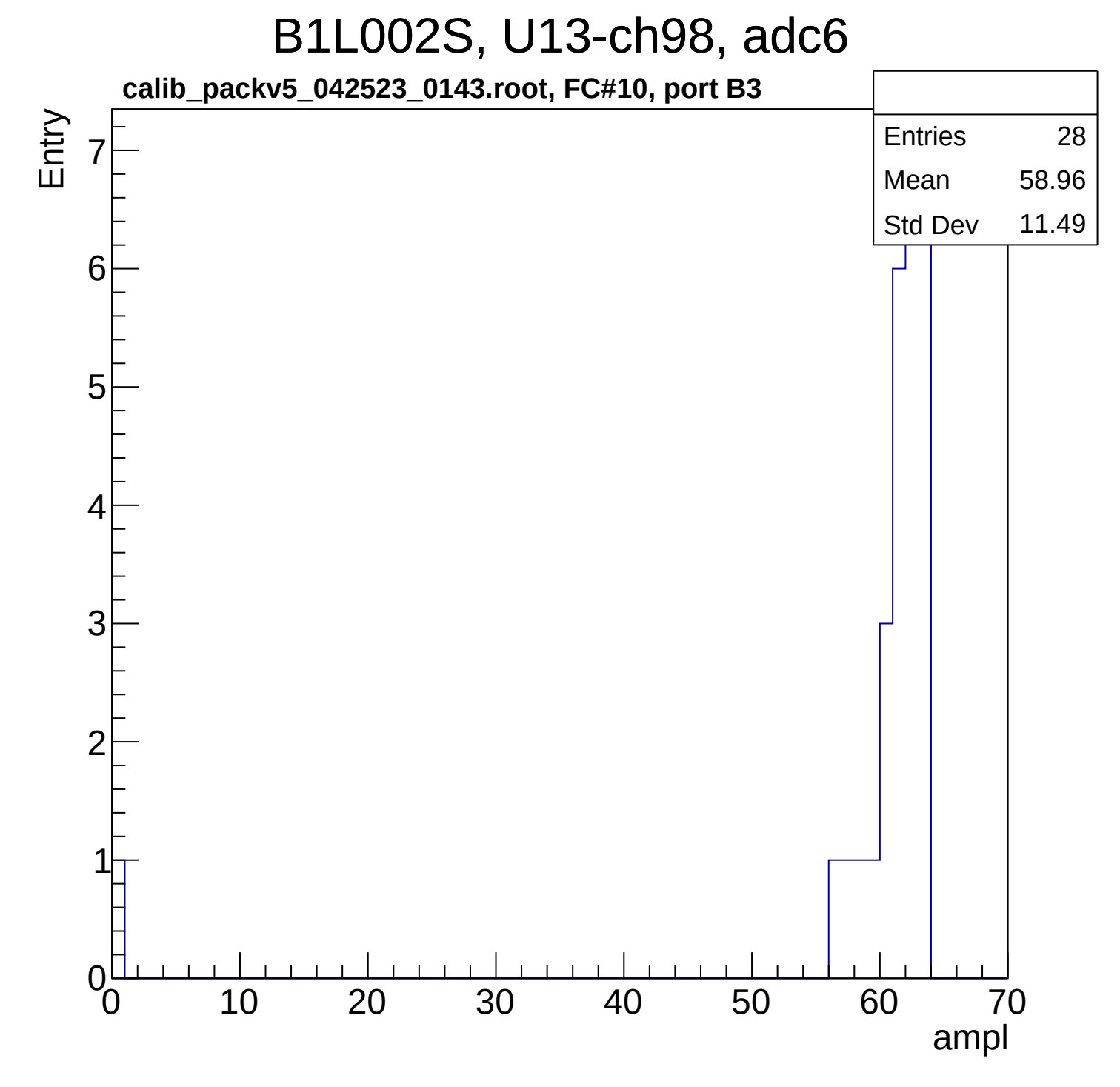
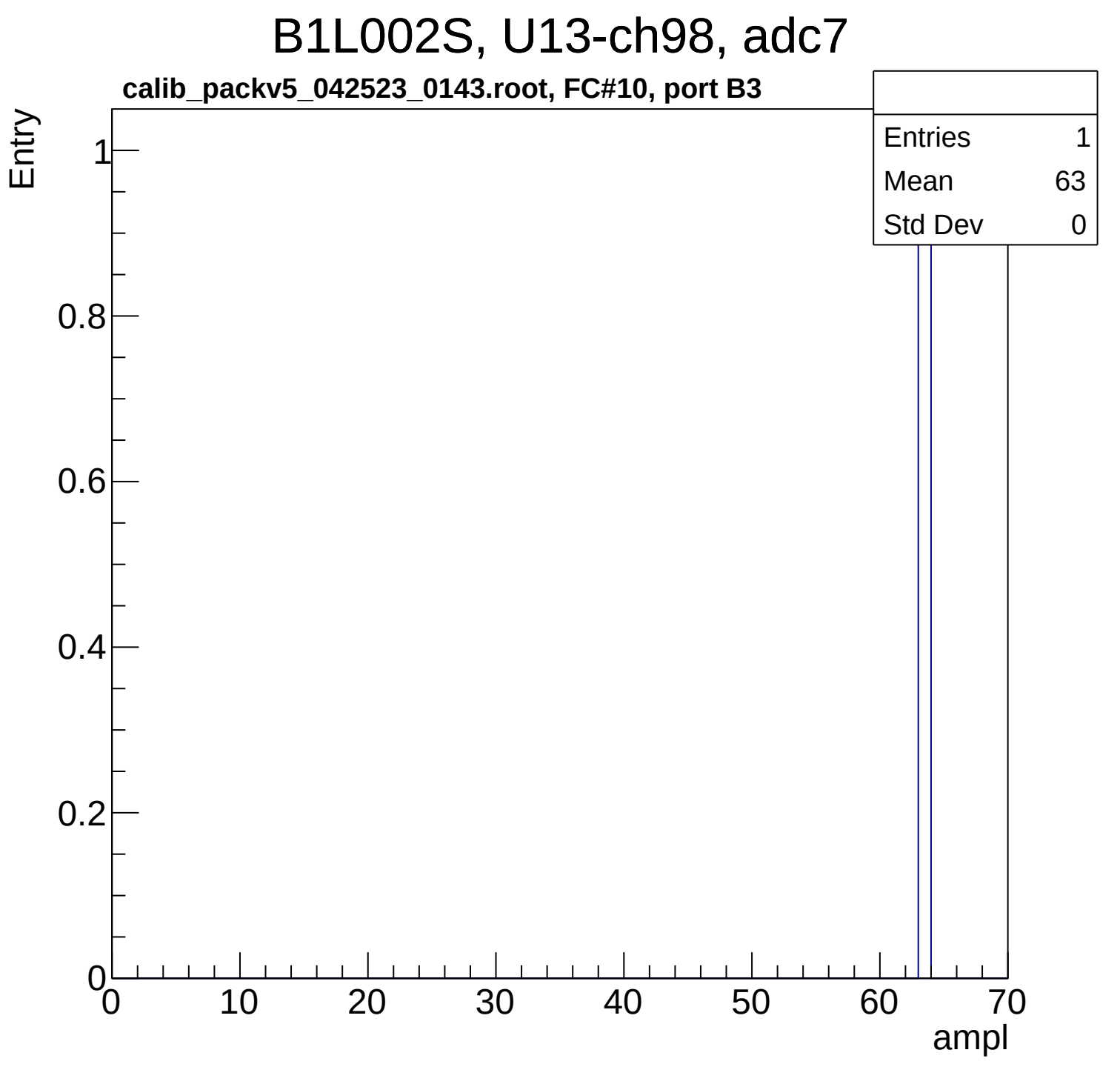
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.96
Std Dev 11.49
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
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0
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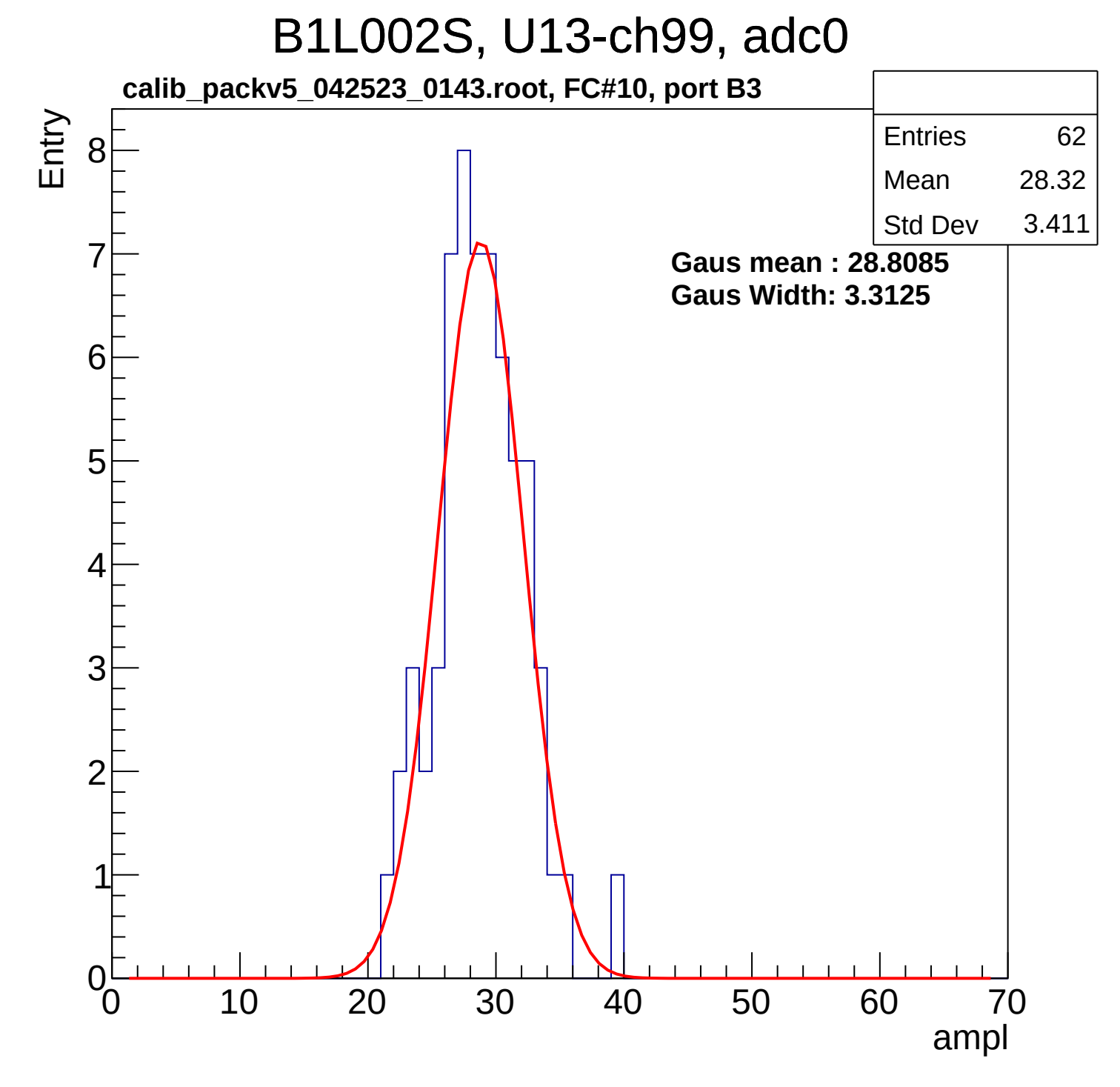
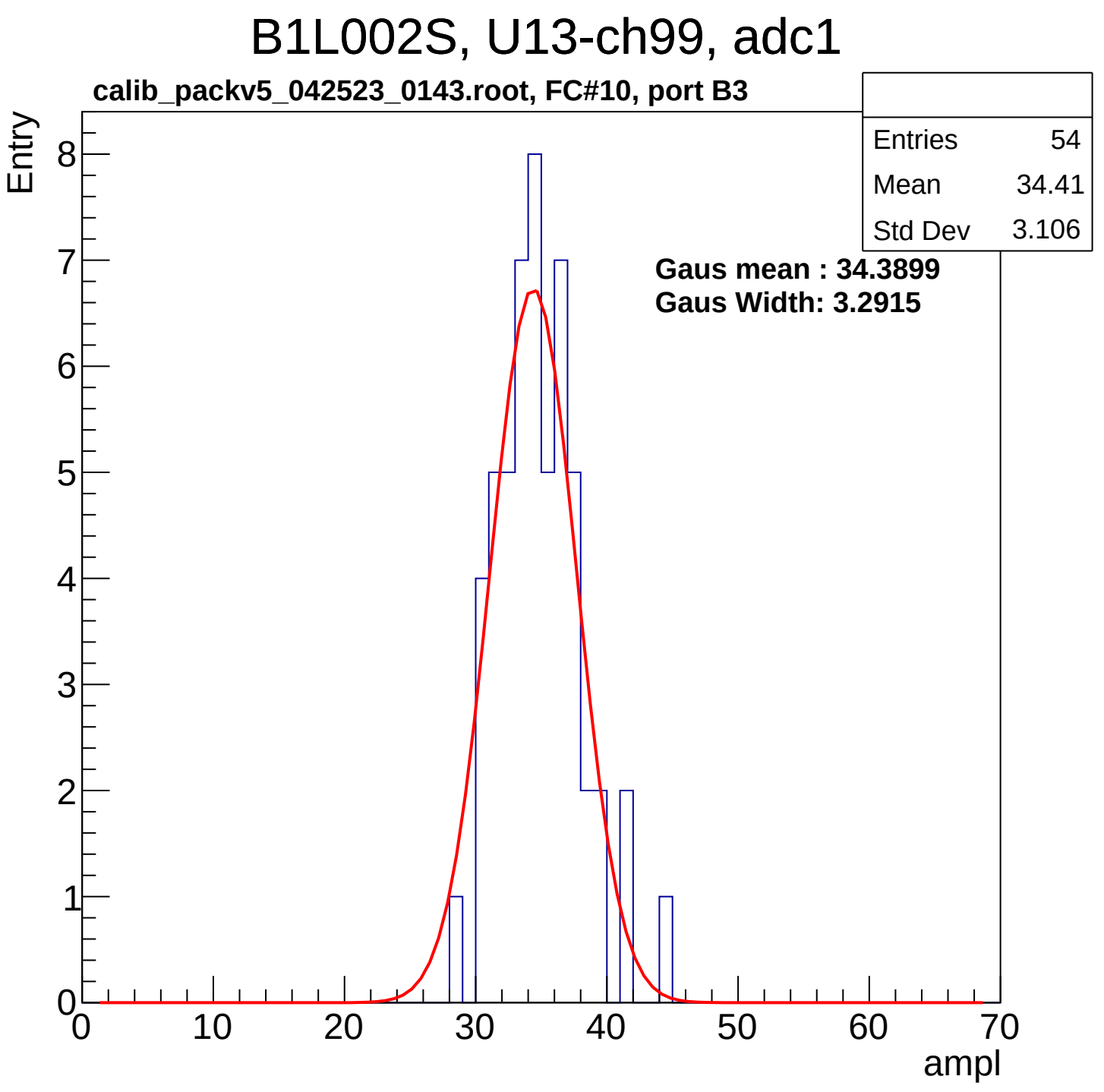
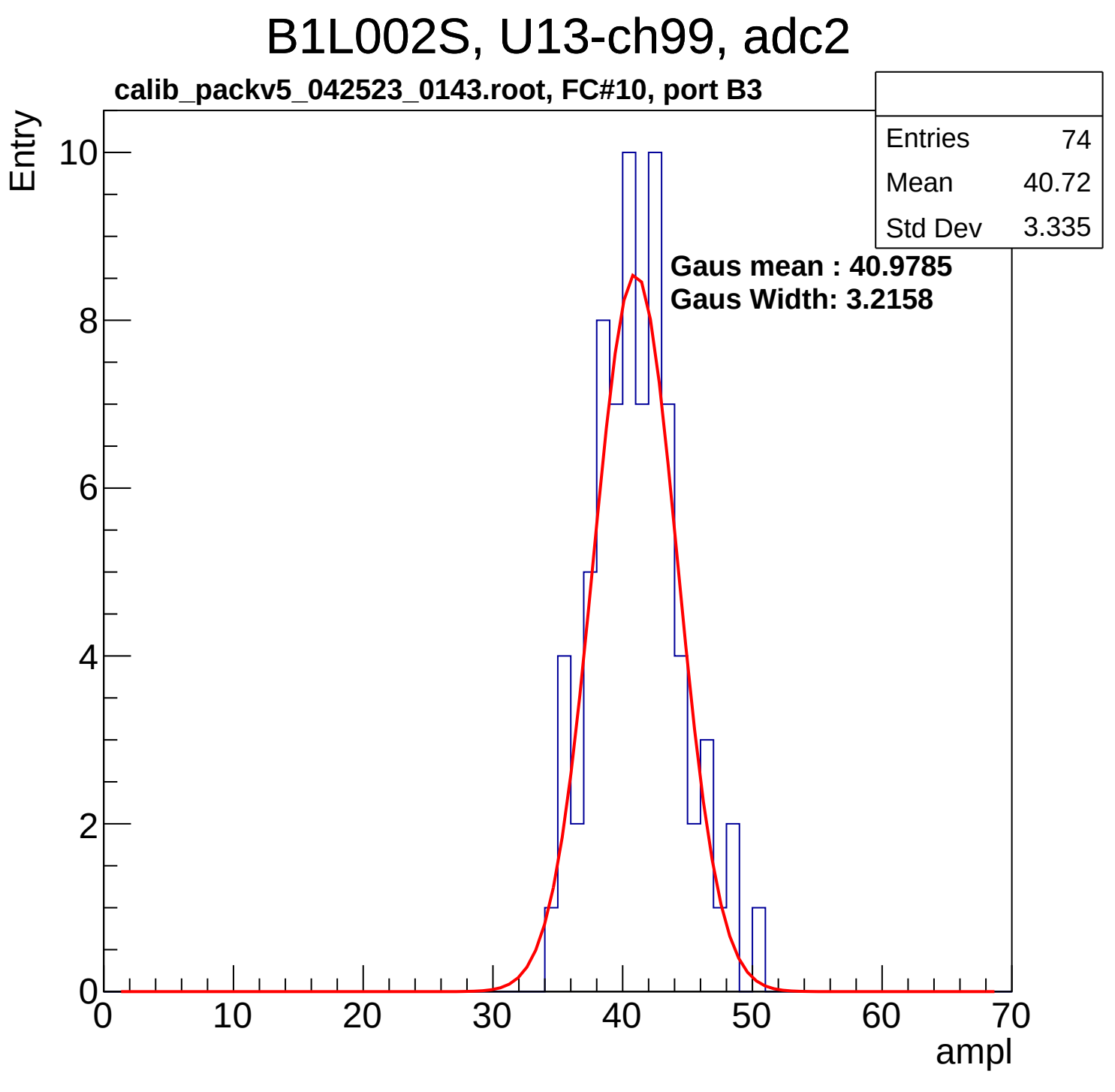
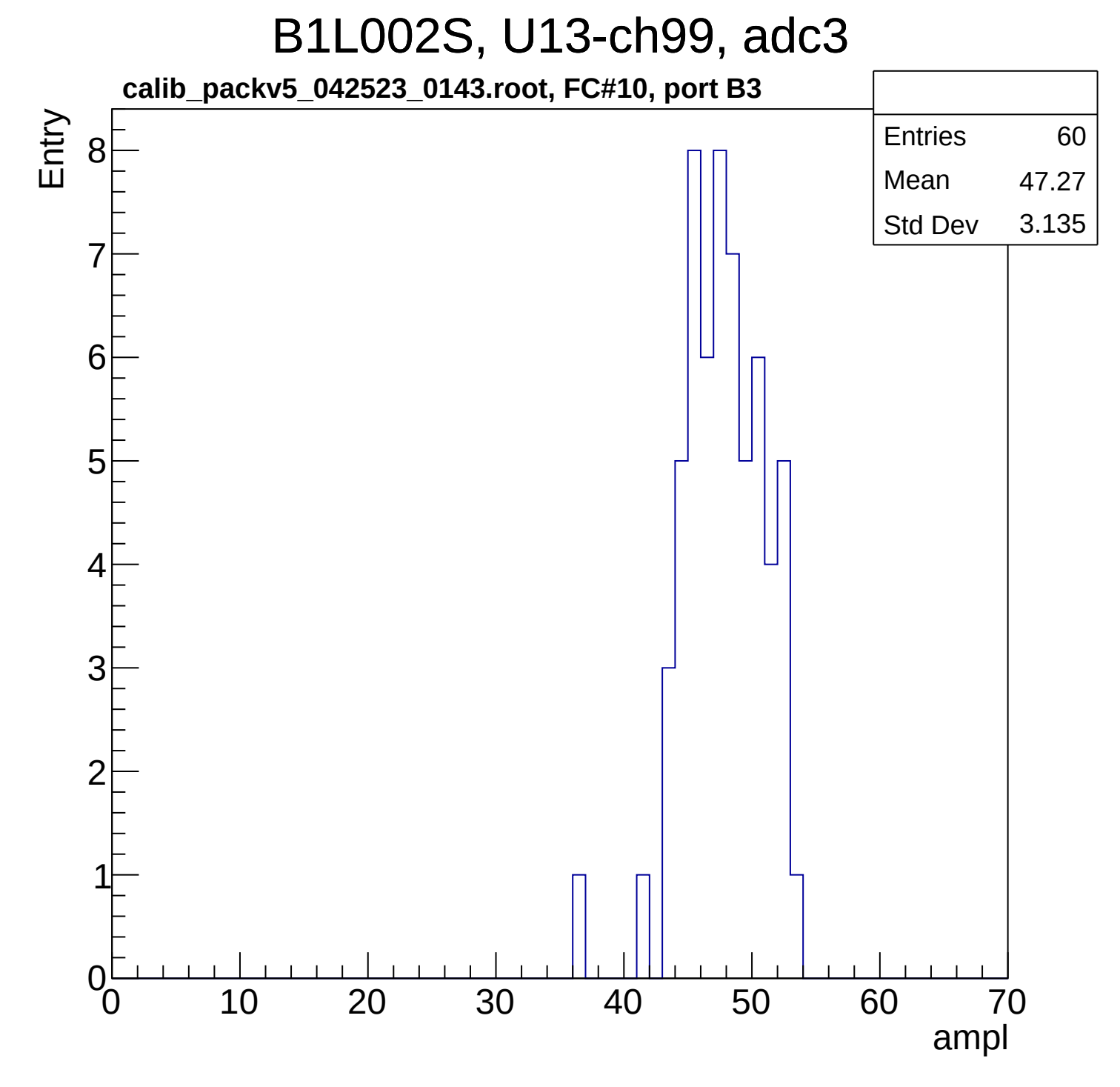
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.32
Std Dev 3.411
Gaus mean : 28.8085
Gaus Width: 3.3125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 34.41
Std Dev 3.106
Gaus mean : 34.3899
Gaus Width: 3.2915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.72
Std Dev 3.335
Gaus mean : 40.9785
Gaus Width: 3.2158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.27
Std Dev 3.135
Entry
ampl
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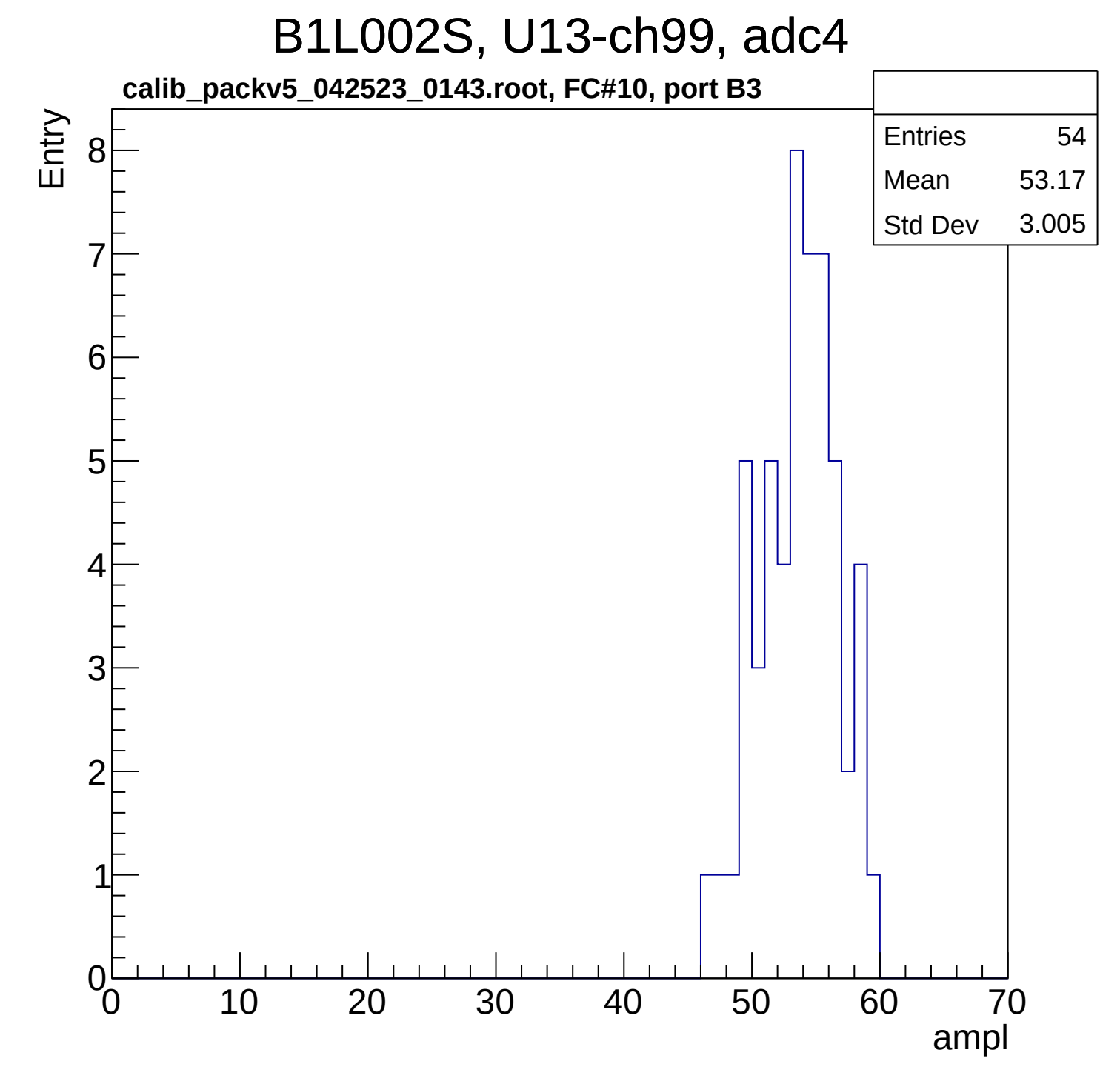
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.17
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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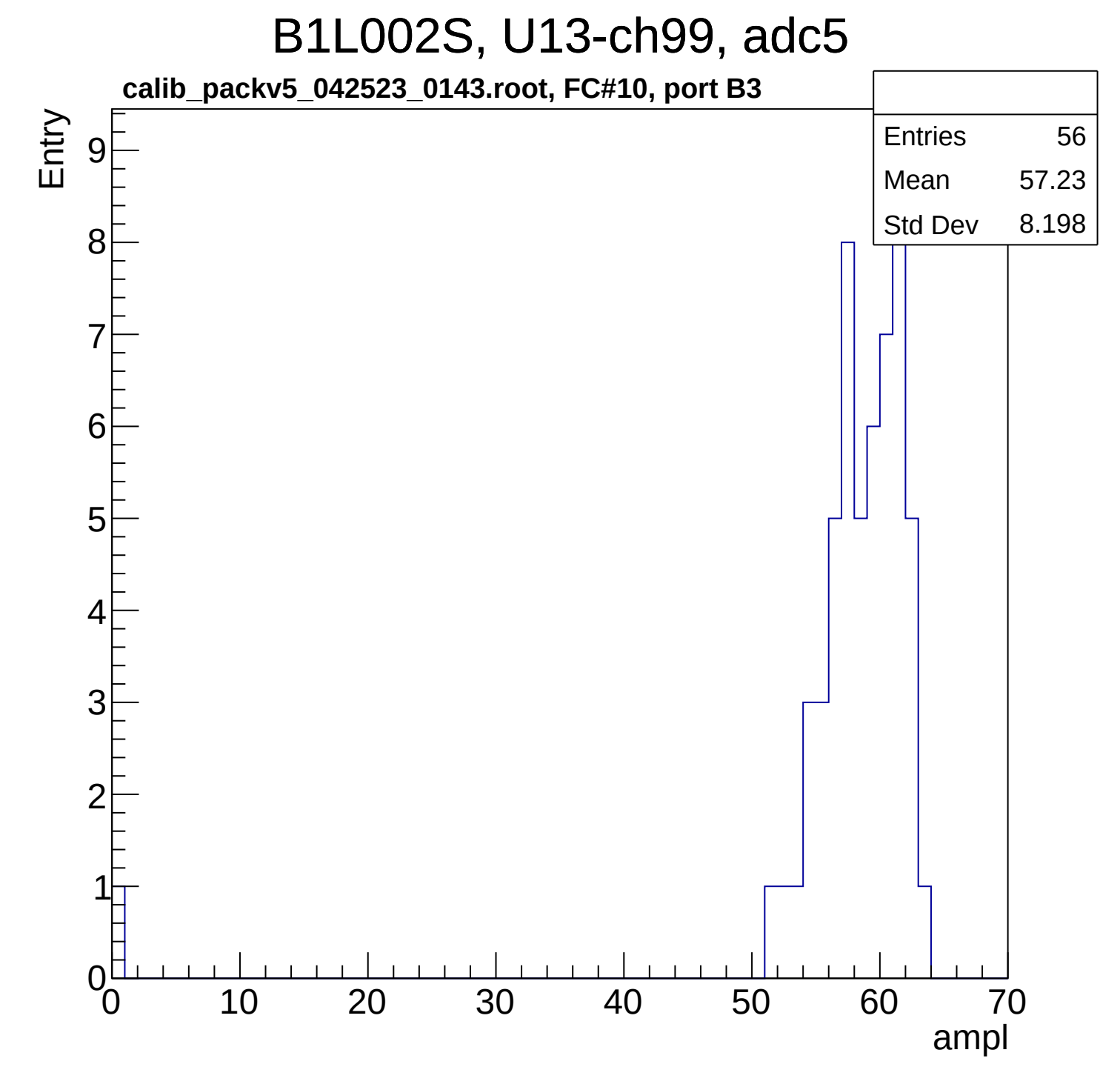
B1L002S, U13-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.23
Std Dev 8.198
Entry
ampl
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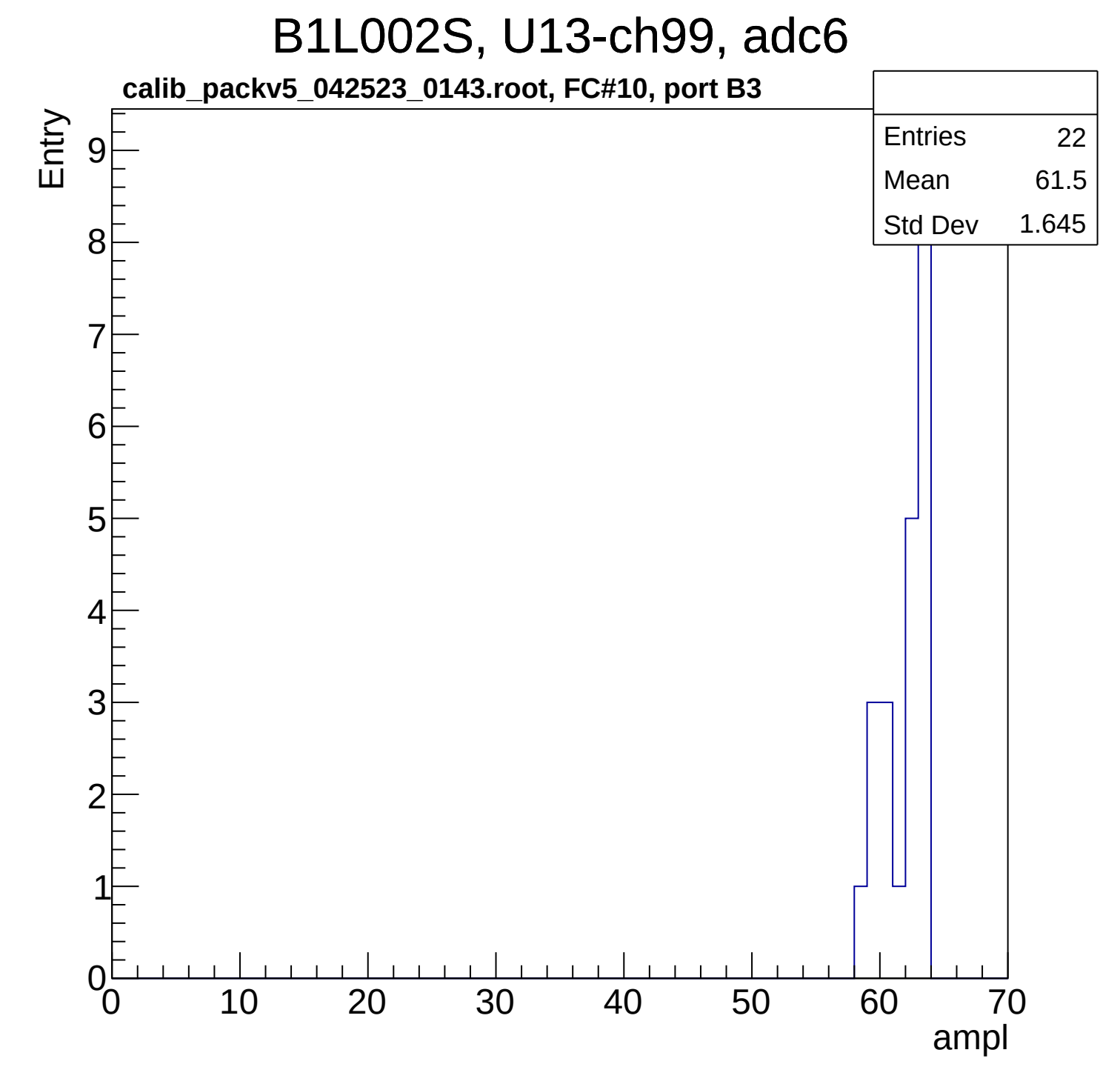
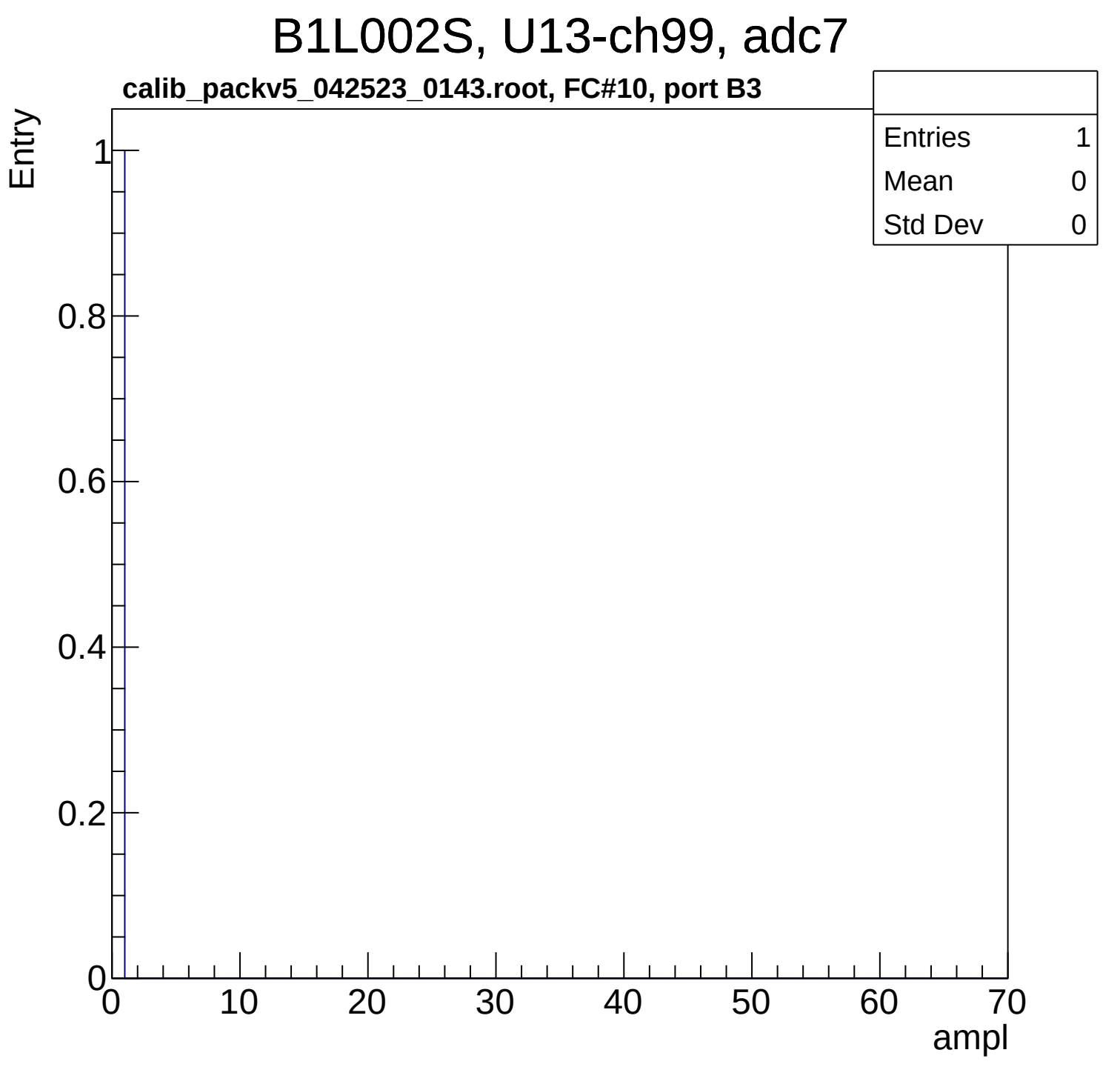
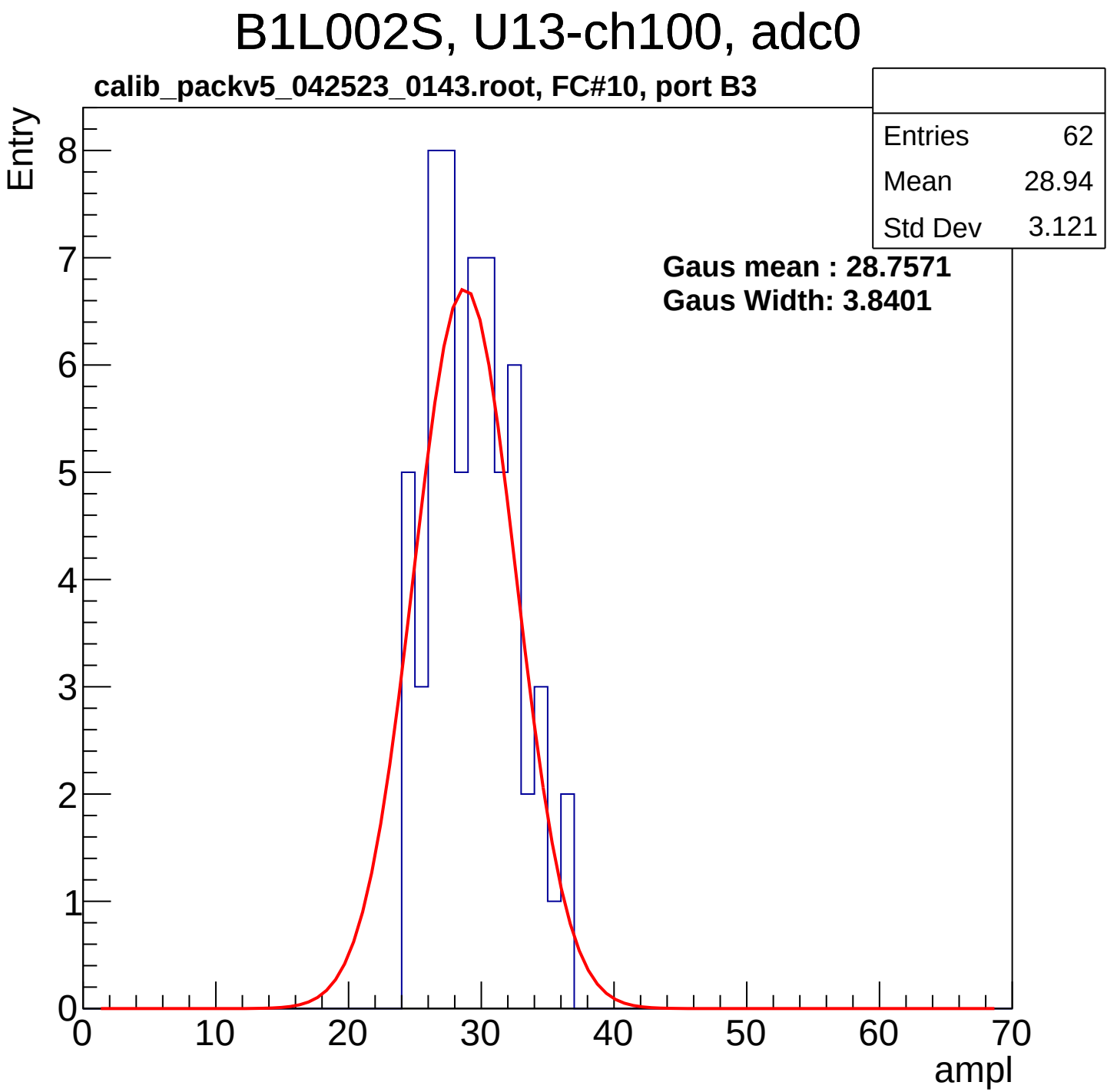
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.5
Std Dev 1.645
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.94
Std Dev 3.121
Gaus mean : 28.7571
Gaus Width: 3.8401
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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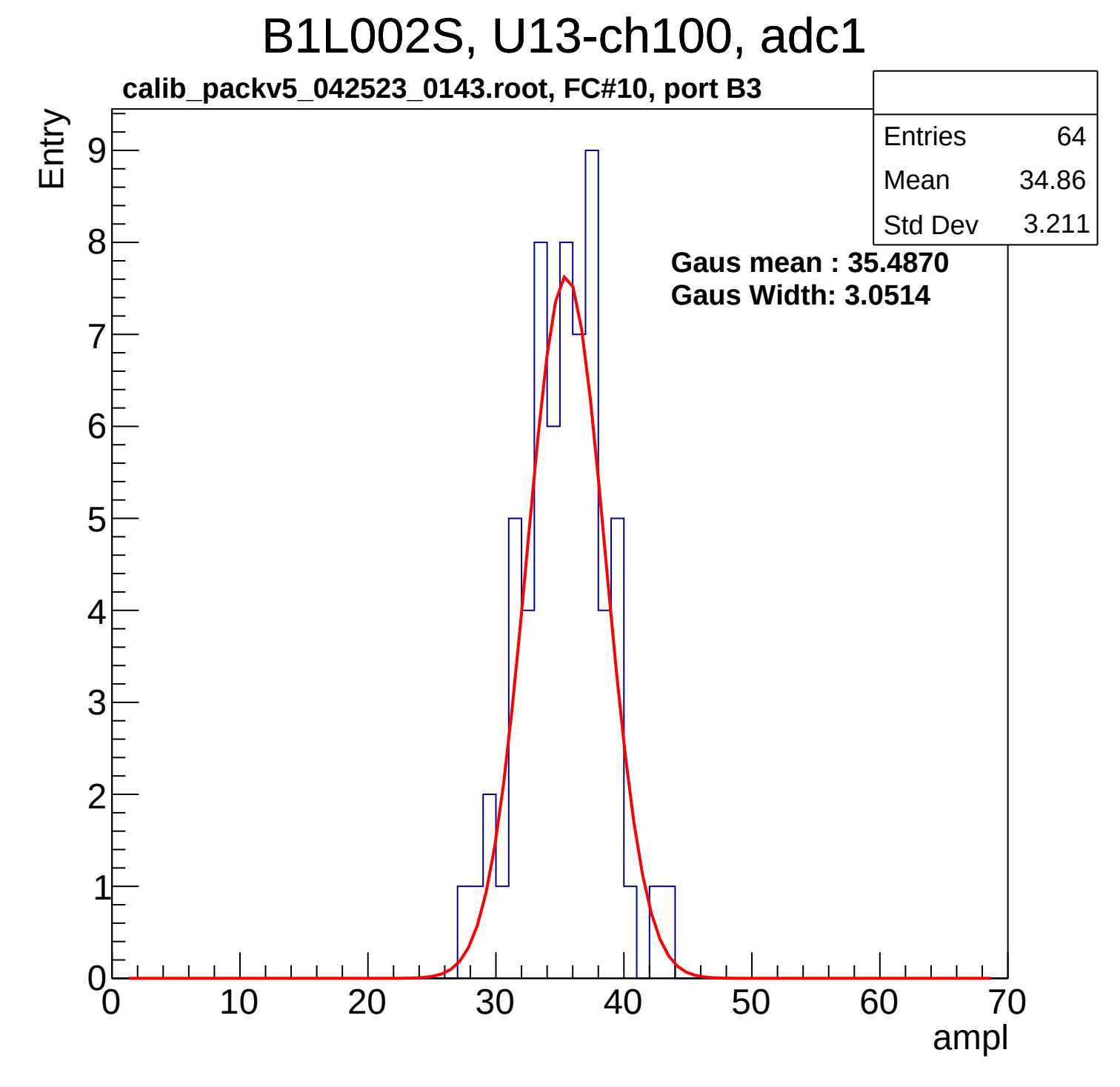
B1L002S, U13-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.86
Std Dev 3.211
Gaus mean : 35.4870
Gaus Width: 3.0514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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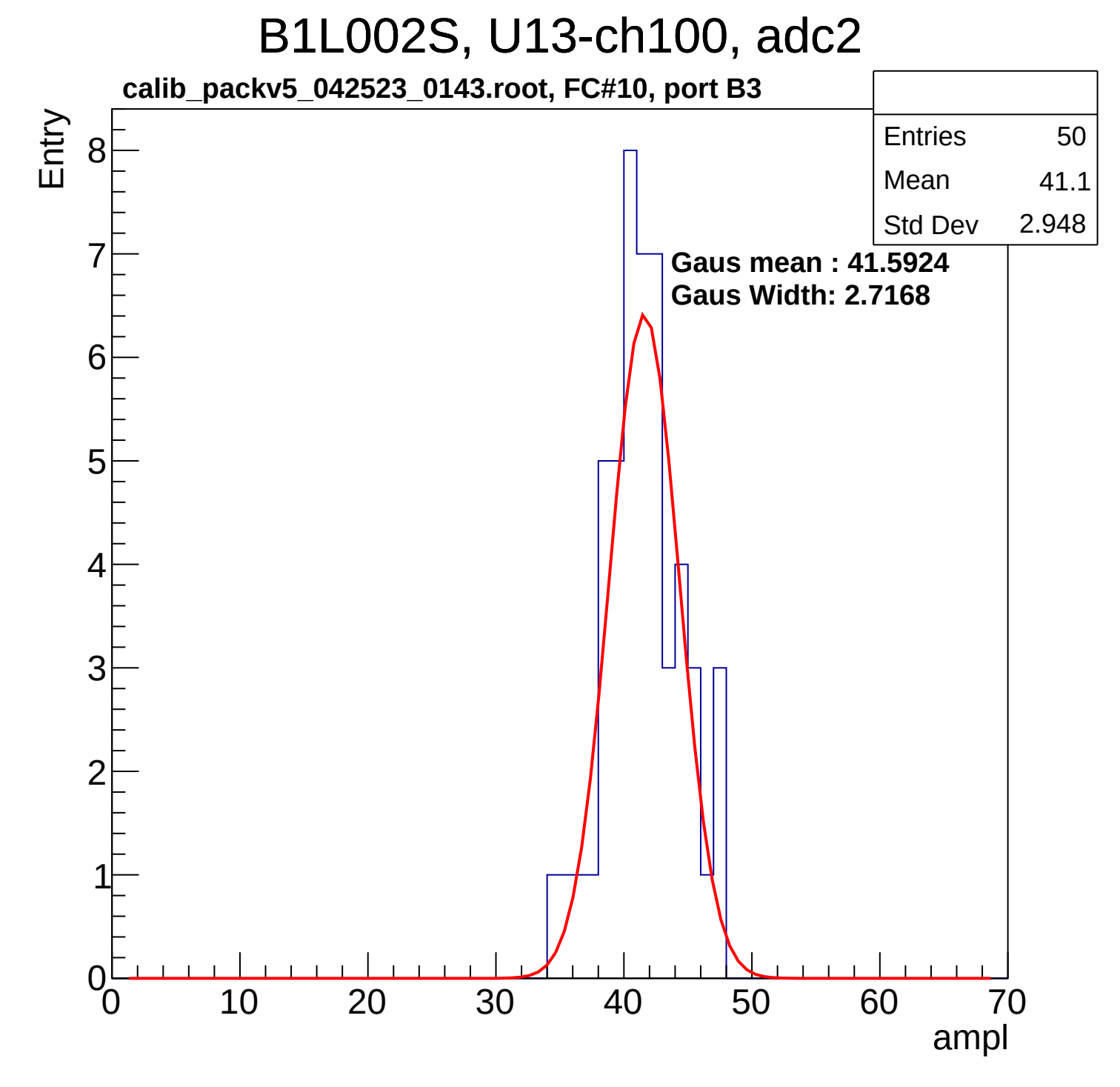
B1L002S, U13-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.1
Std Dev 2.948
Gaus mean : 41.5924
Gaus Width: 2.7168
Entry
ampl
0
1
2
3
4
5
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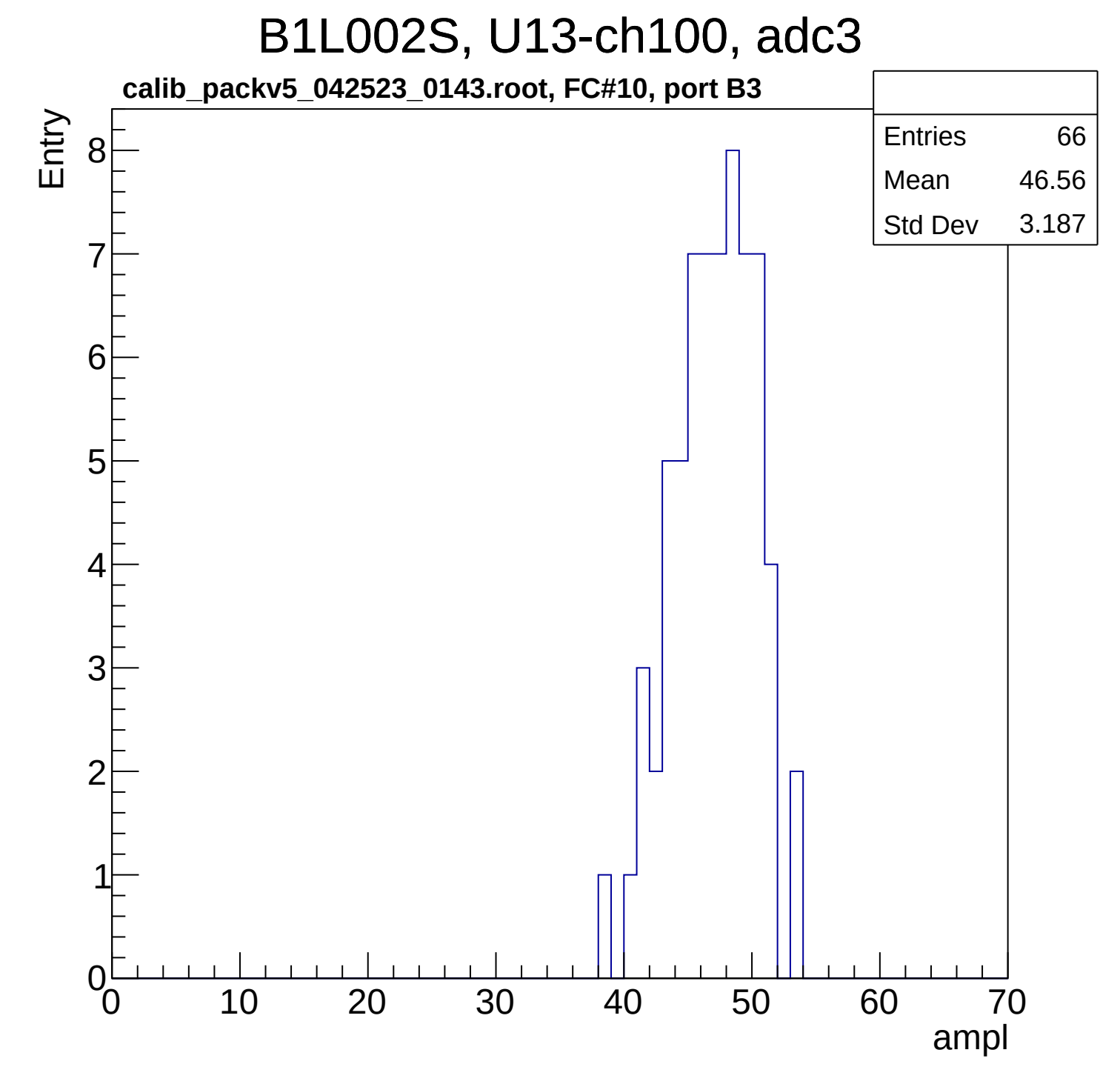
B1L002S, U13-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.56
Std Dev 3.187
Entry
ampl
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2
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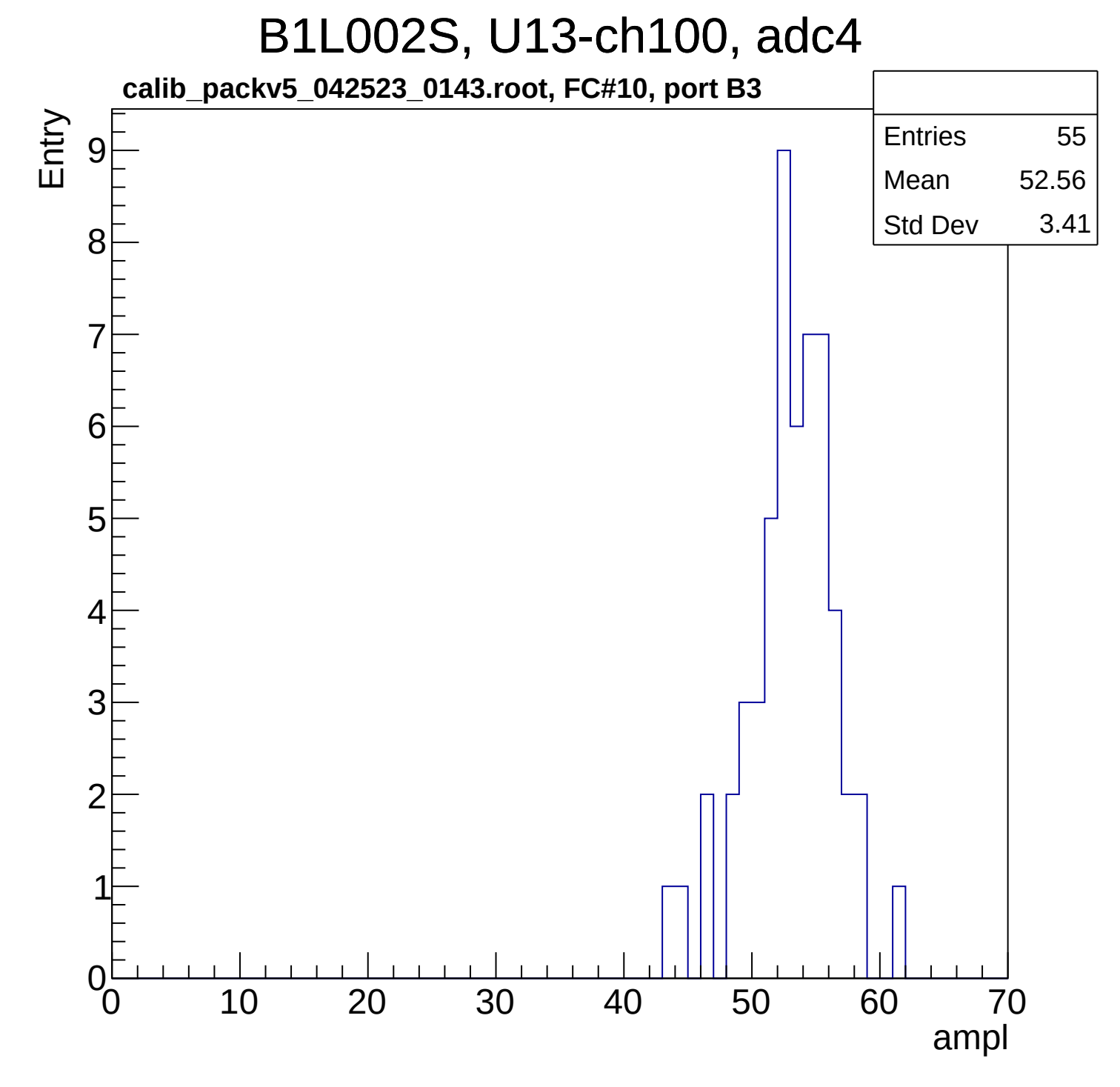
B1L002S, U13-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.56
Std Dev 3.41
Entry
ampl
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2
3
4
5
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0
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60
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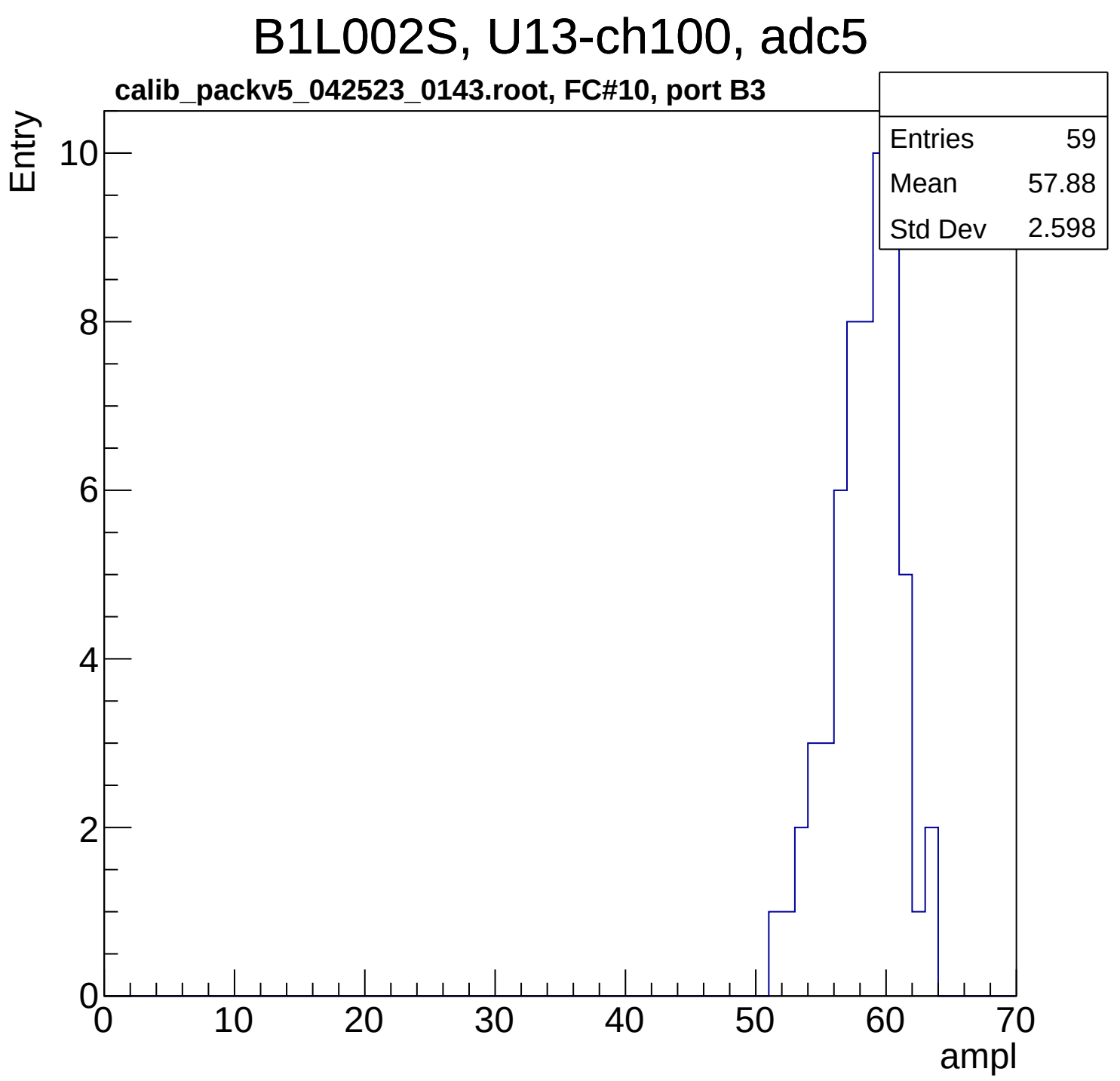
B1L002S, U13-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.88
Std Dev 2.598
Entry
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6
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10
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10
20
30
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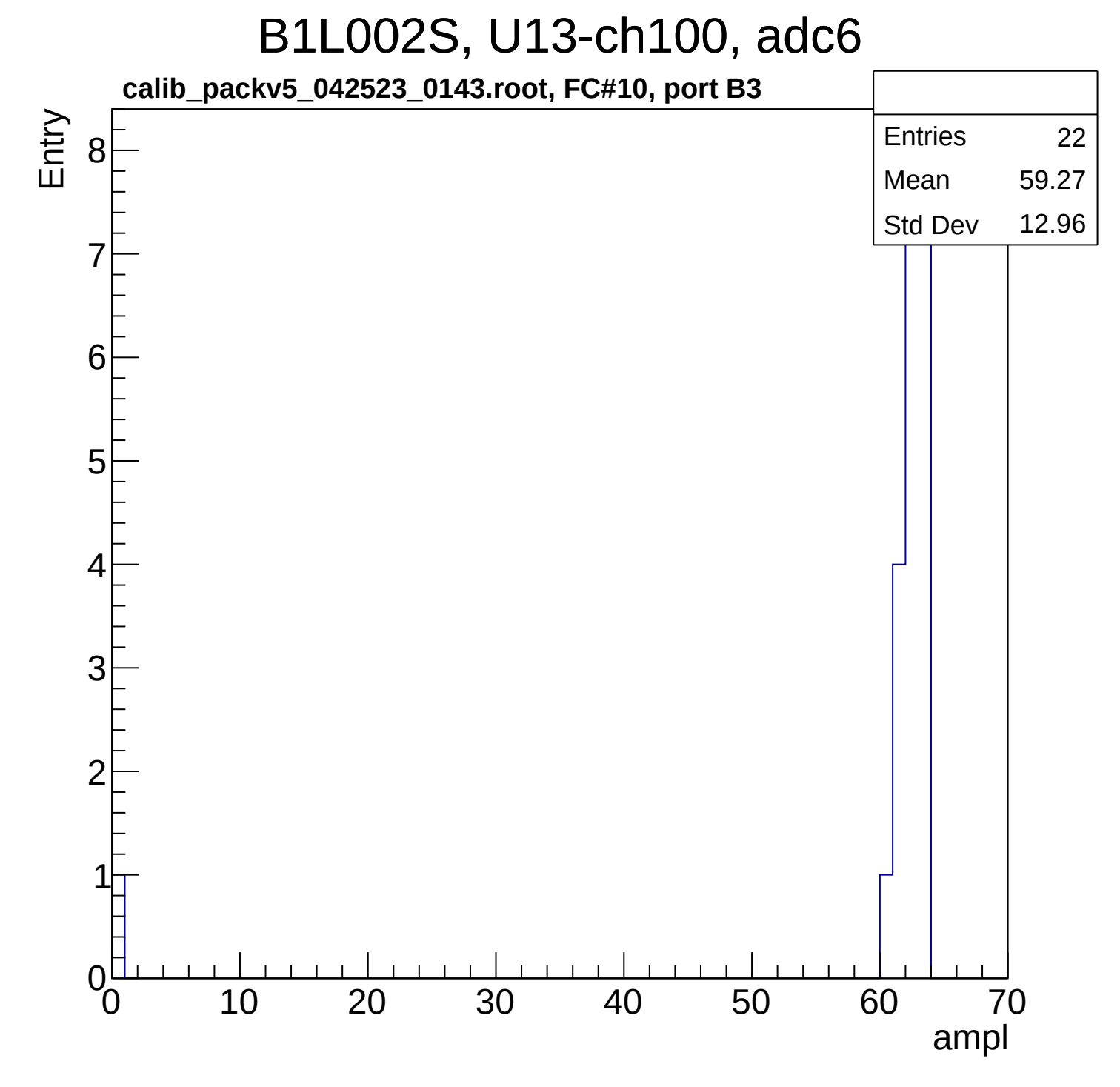
50
60
70
ampl


B1L002S, U13-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 59.27
Std Dev 12.96
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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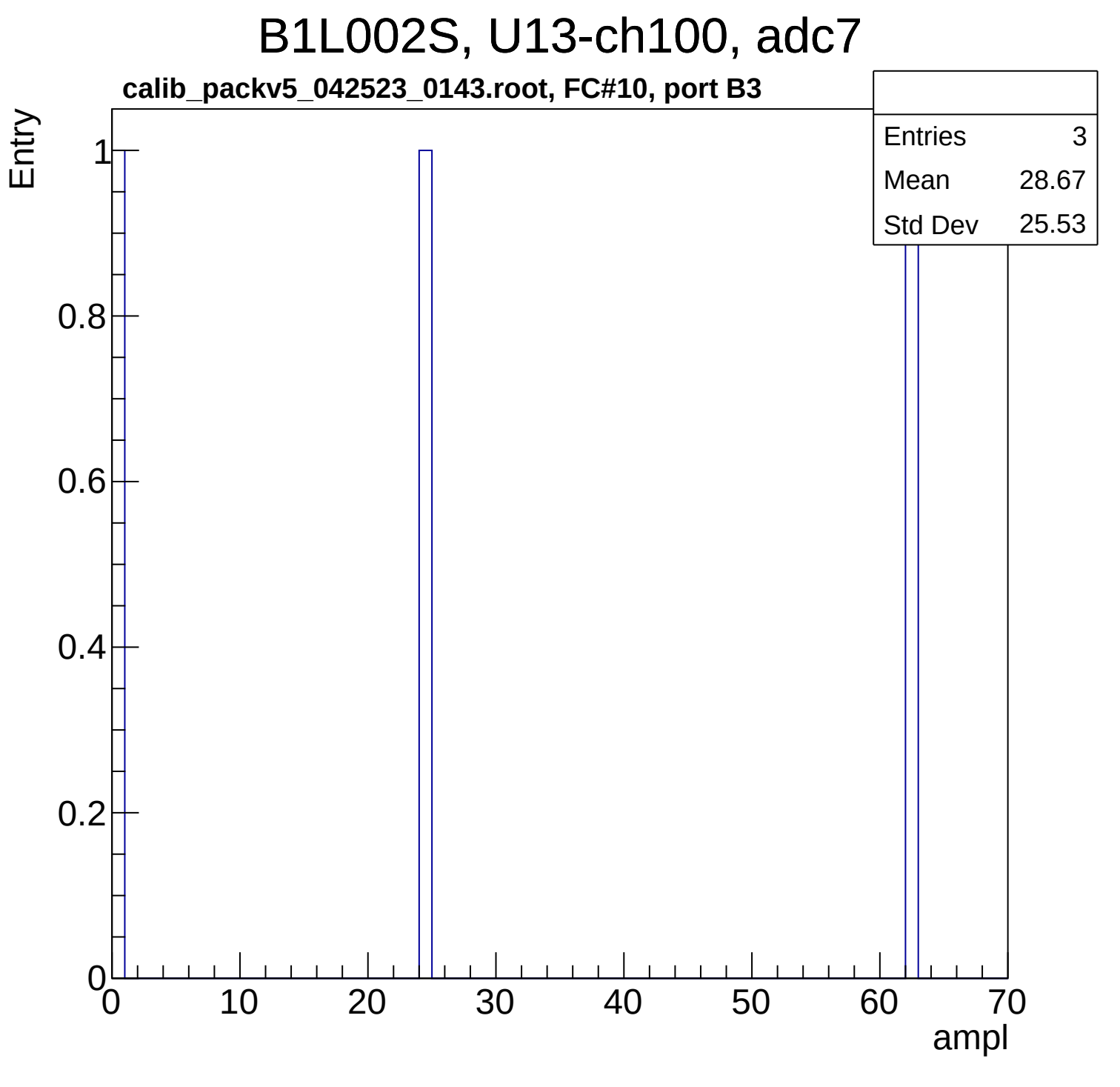
B1L002S, U13-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 25.53
Entry
ampl
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0.2
0.4
0.6
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0
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20
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40
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60
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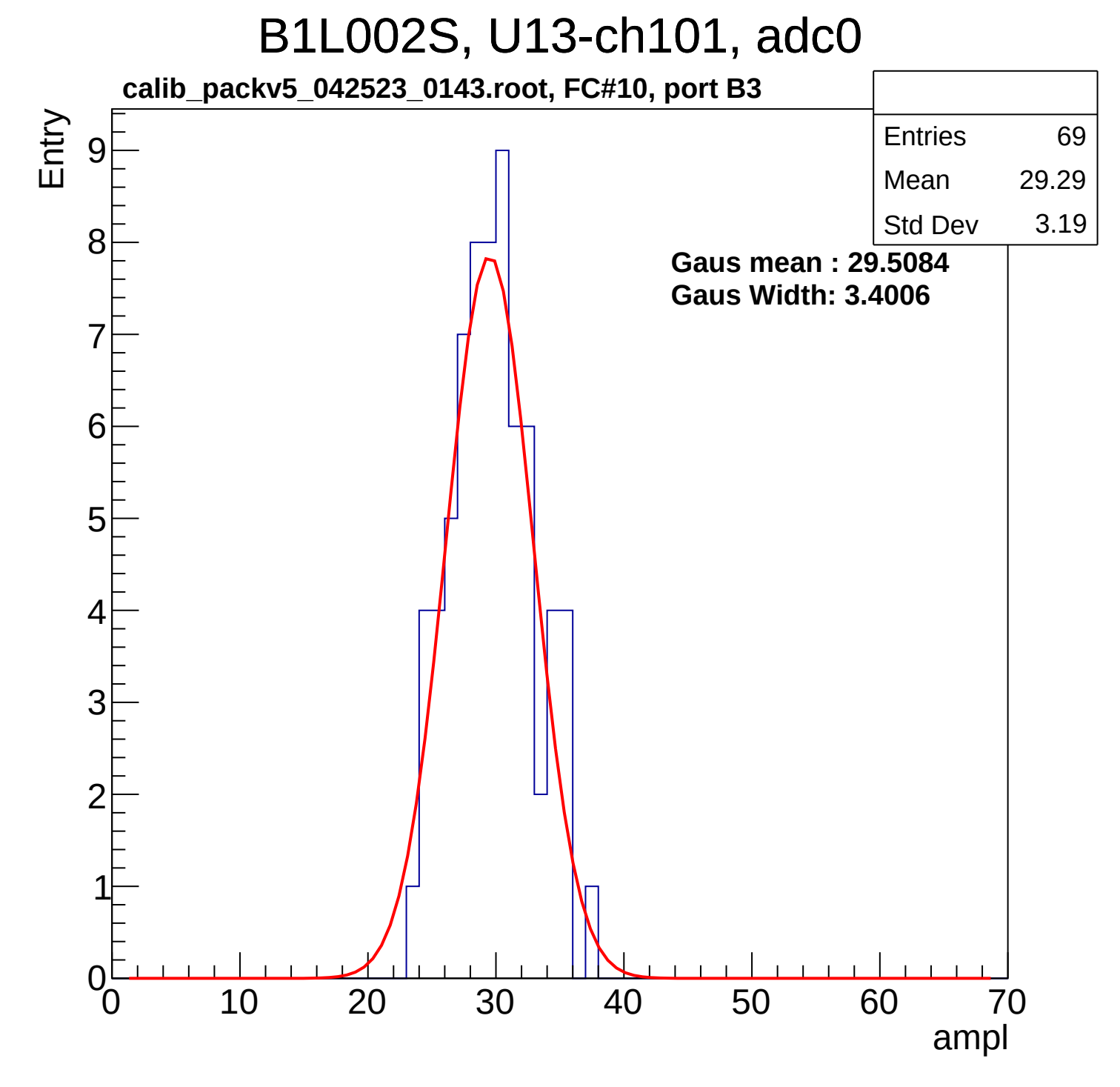
B1L002S, U13-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 29.29
Std Dev 3.19
Gaus mean : 29.5084
Gaus Width: 3.4006
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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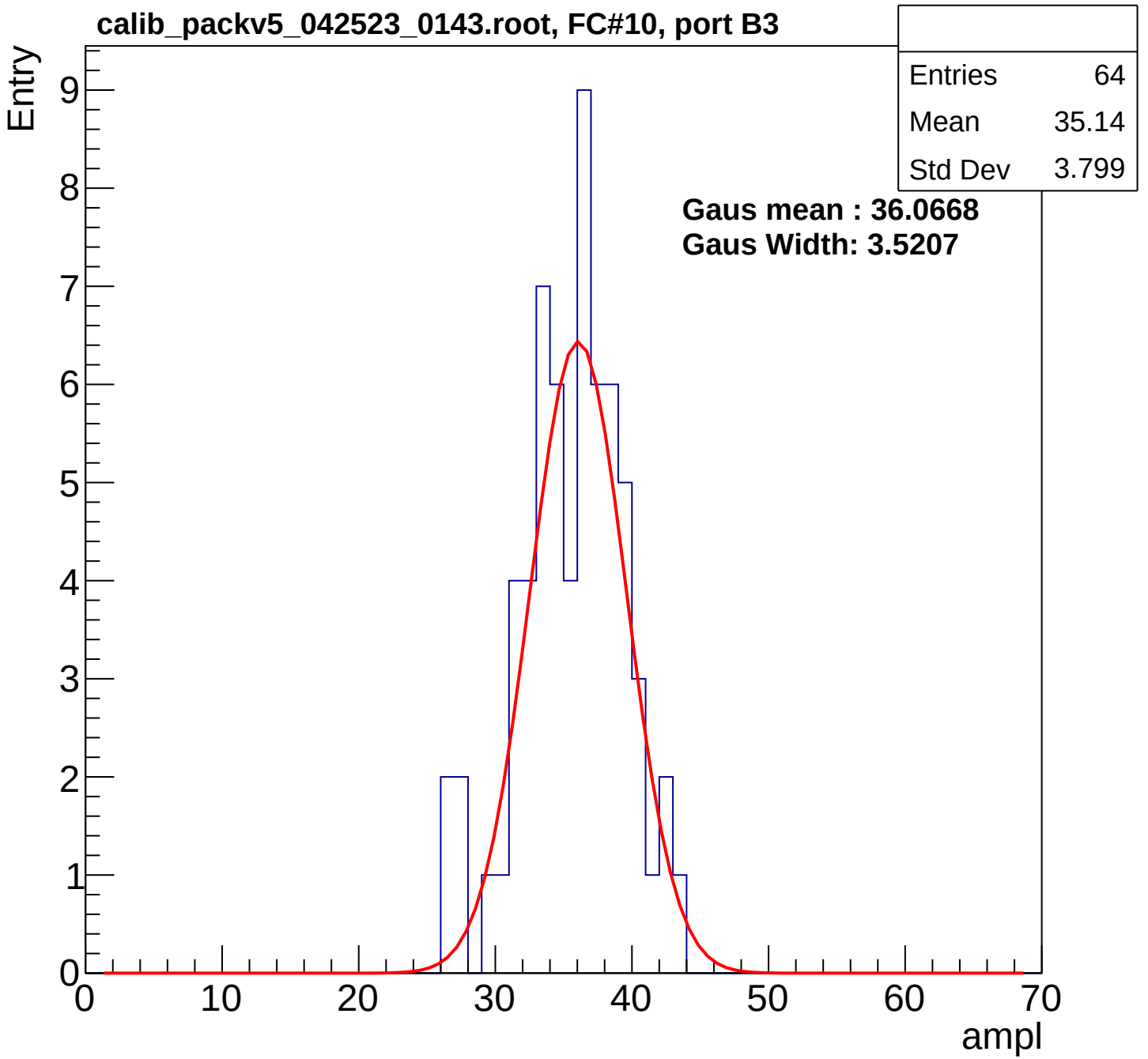
B1L002S, U13-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.14
Std Dev 3.799
Gaus mean : 36.0668
Gaus Width: 3.5207
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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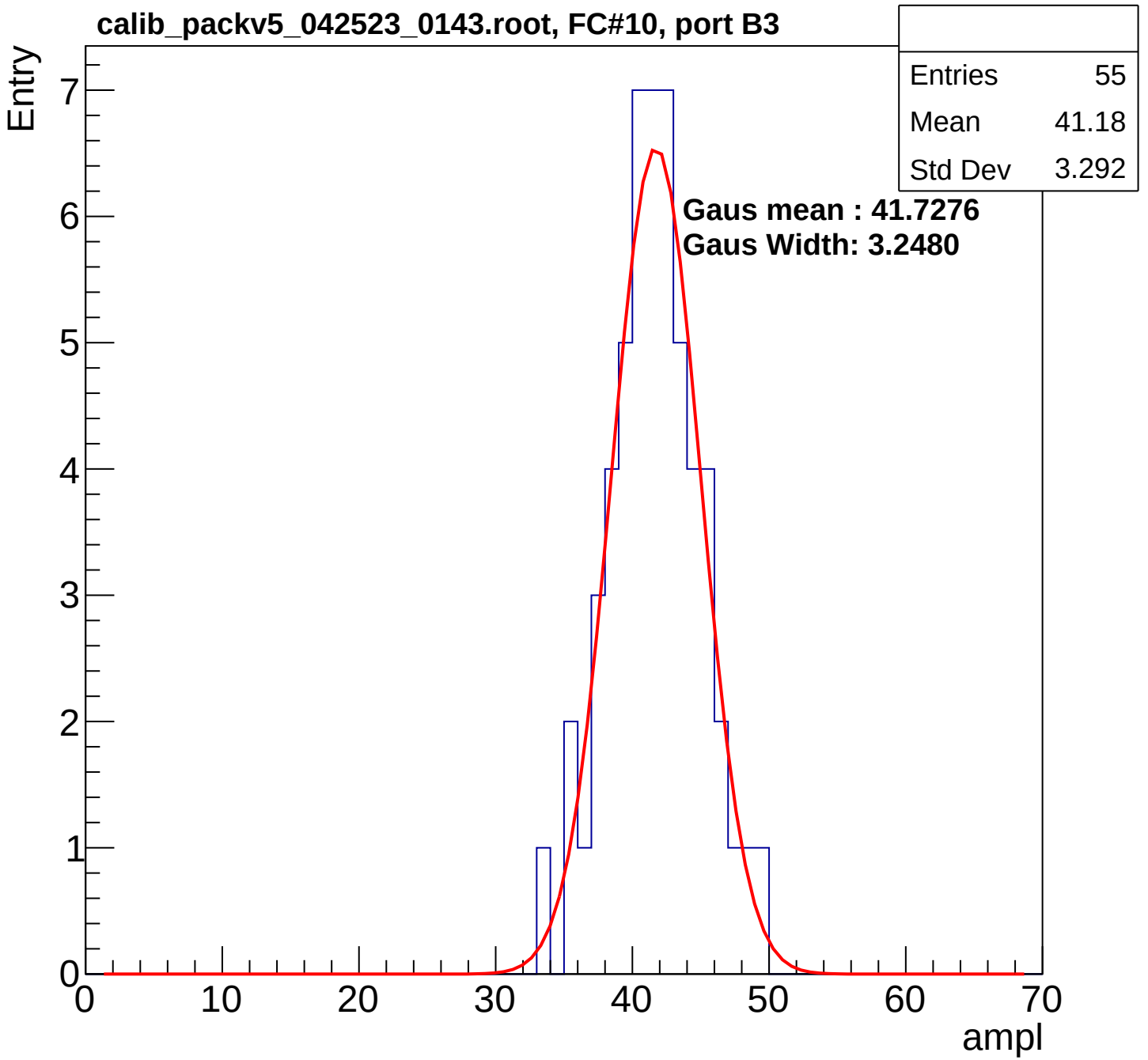
B1L002S, U13-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.18
Std Dev 3.292
Gaus mean : 41.7276
Gaus Width: 3.2480
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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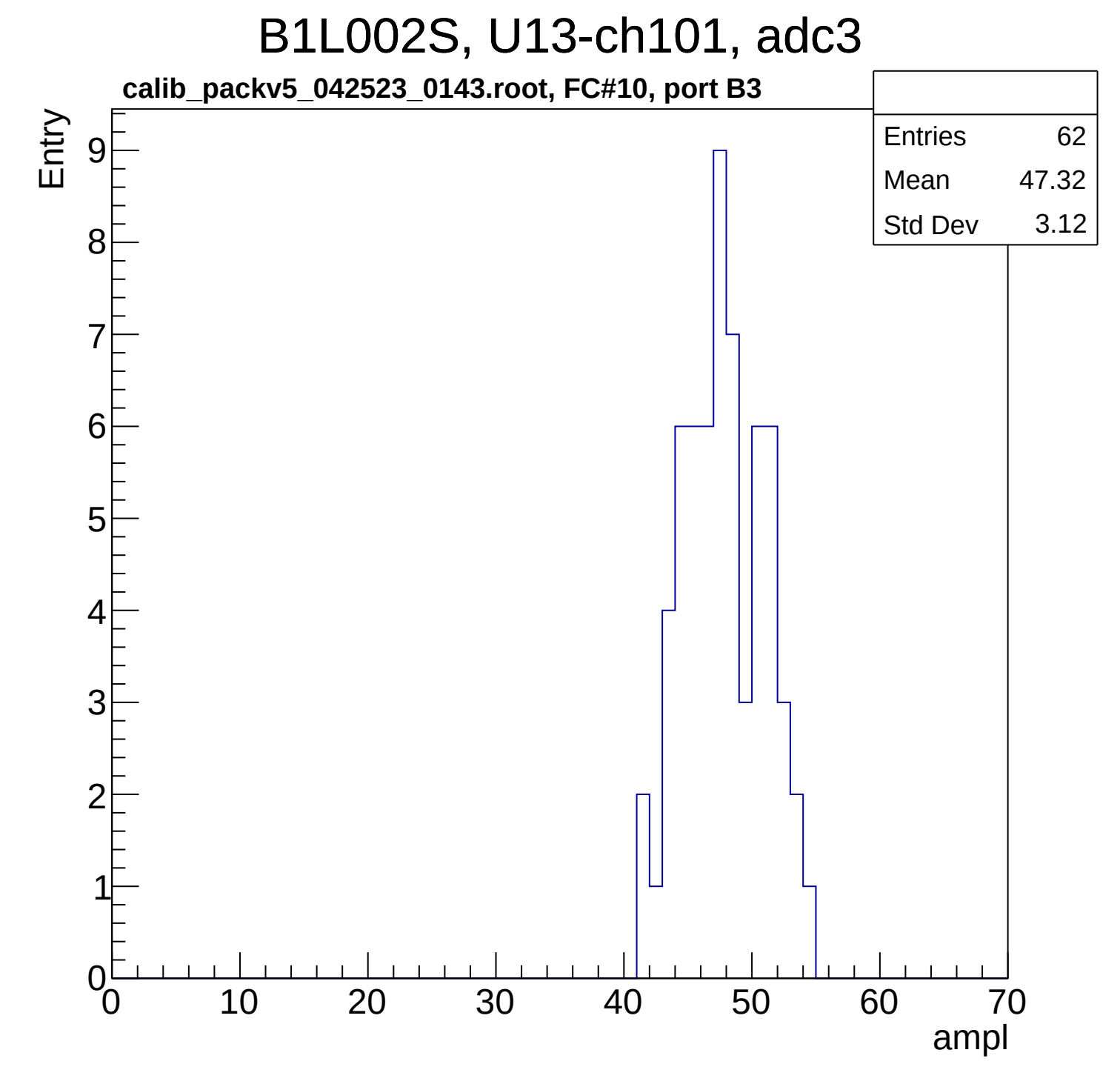
B1L002S, U13-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.32
Std Dev 3.12
Entry
ampl
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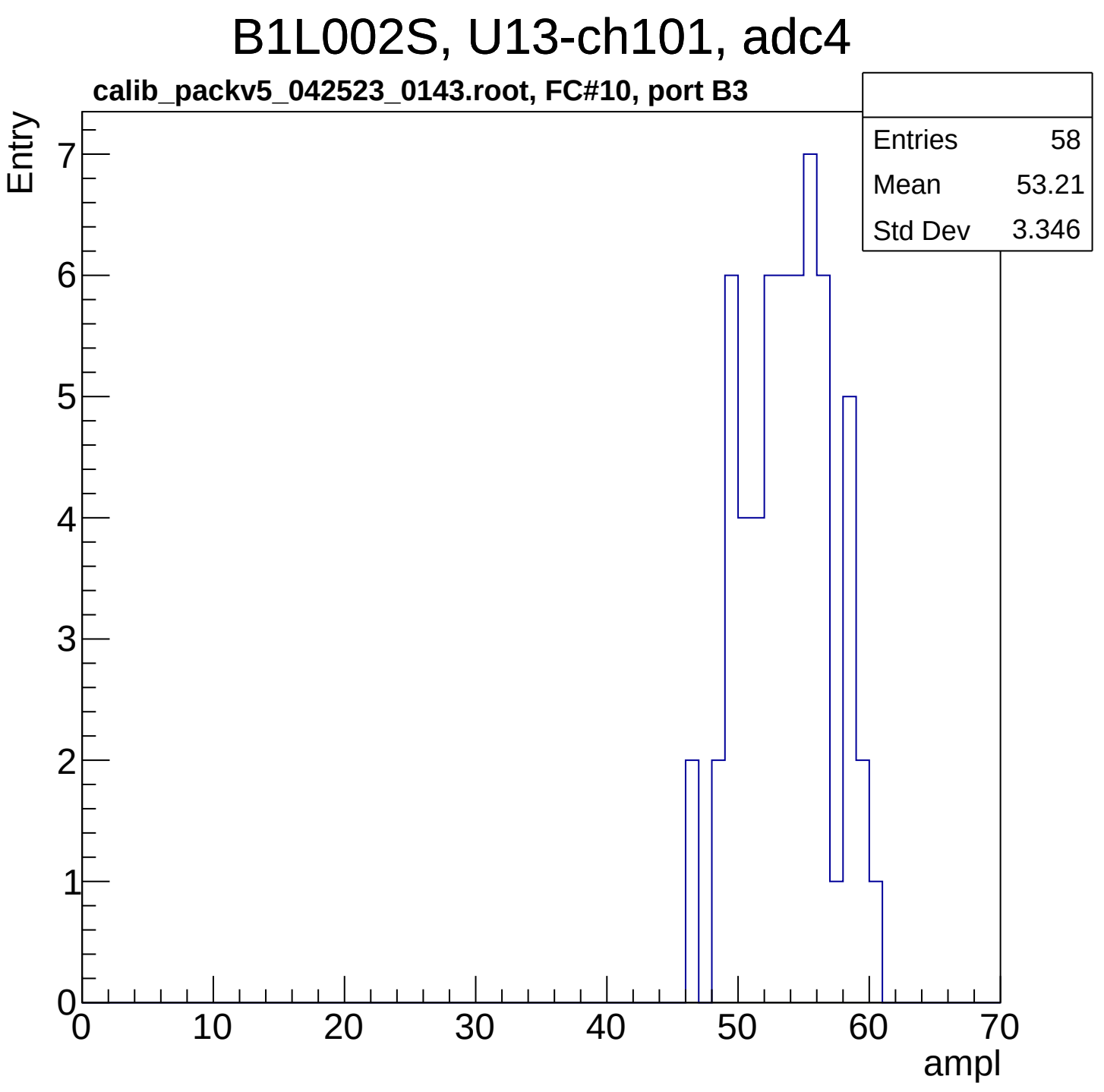
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.21
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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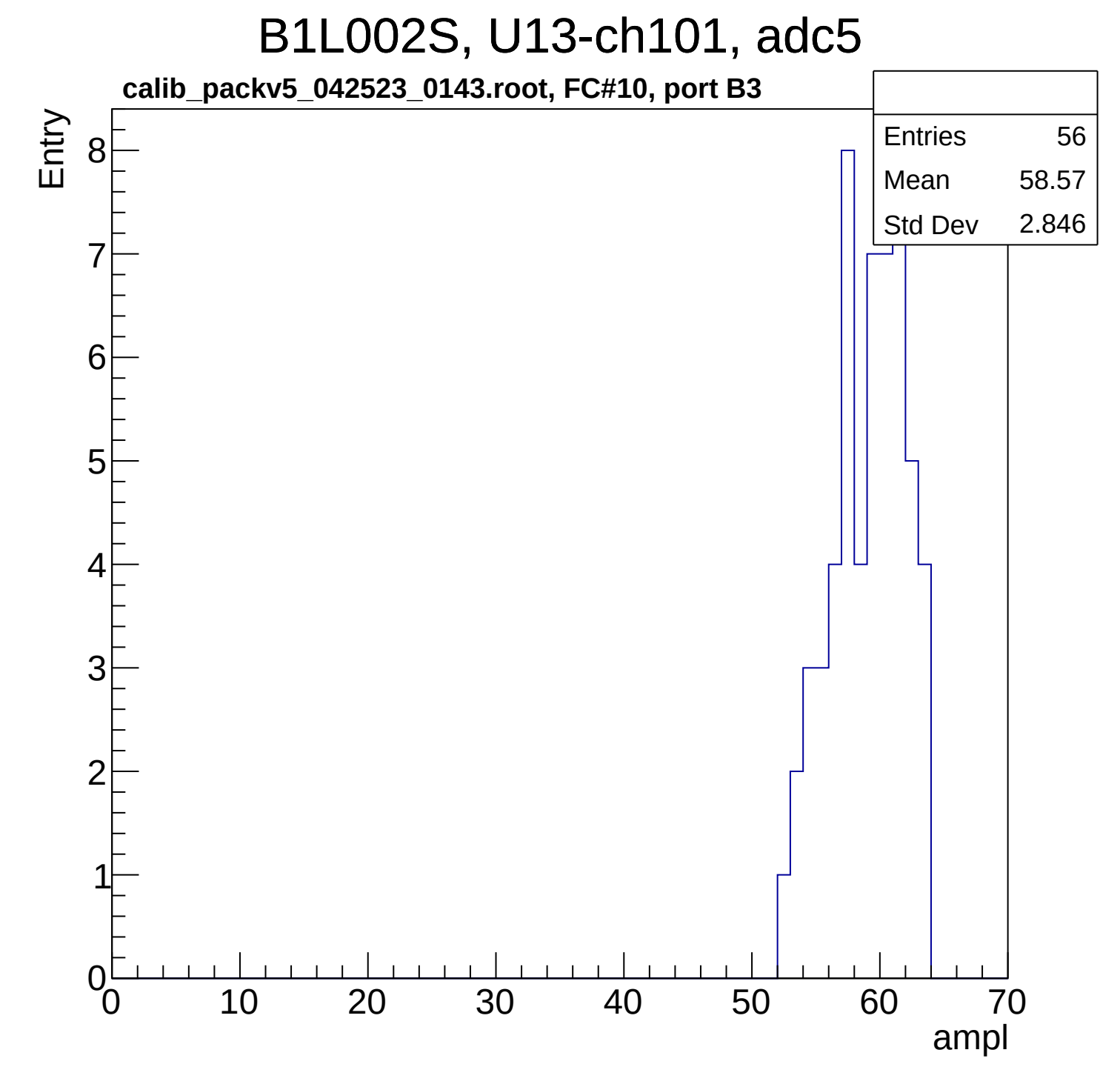
B1L002S, U13-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.57
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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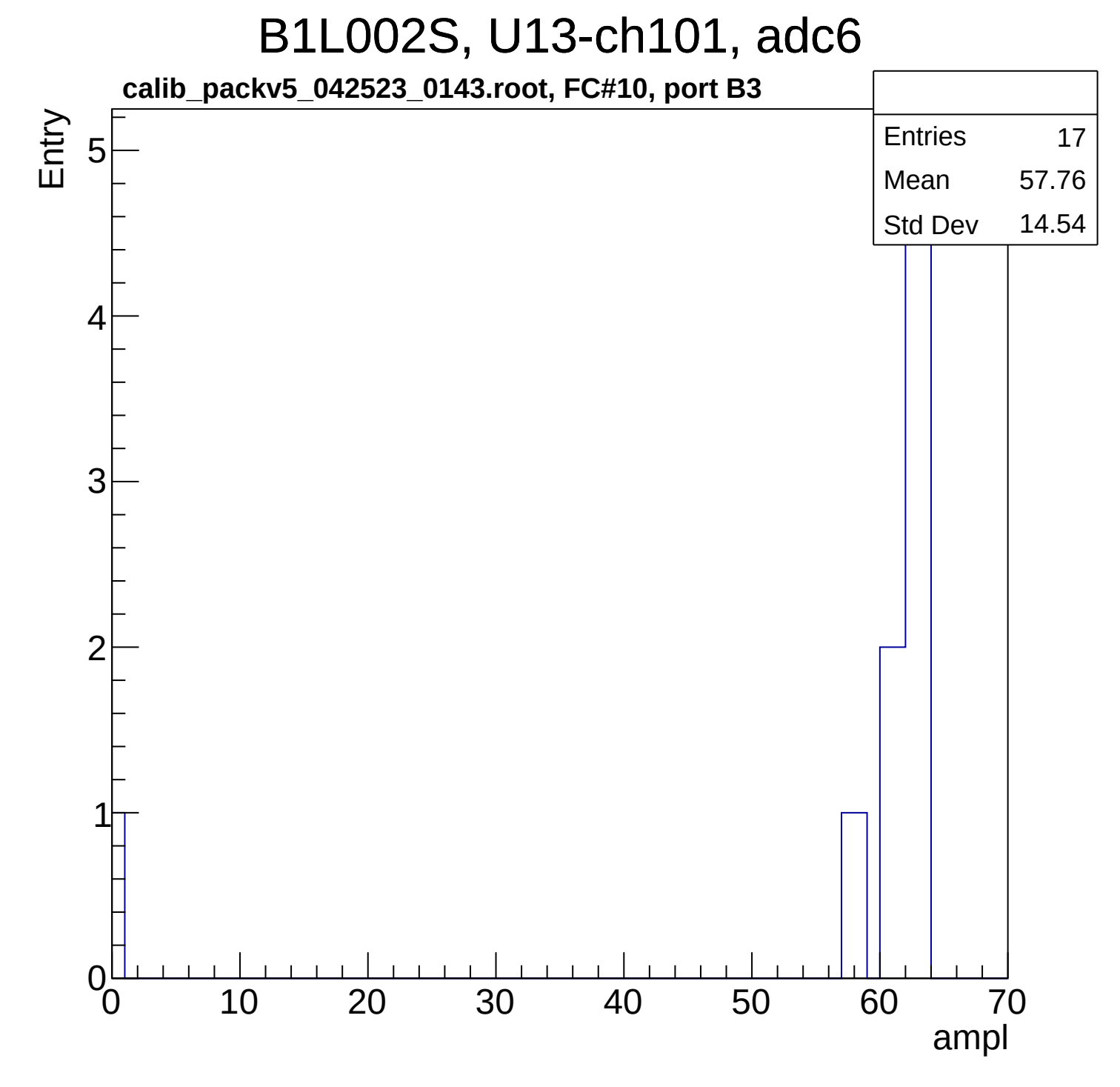
B1L002S, U13-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.76
Std Dev 14.54
Entry
ampl
0
1
2
3
4
5
0
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20
30
40
50
60
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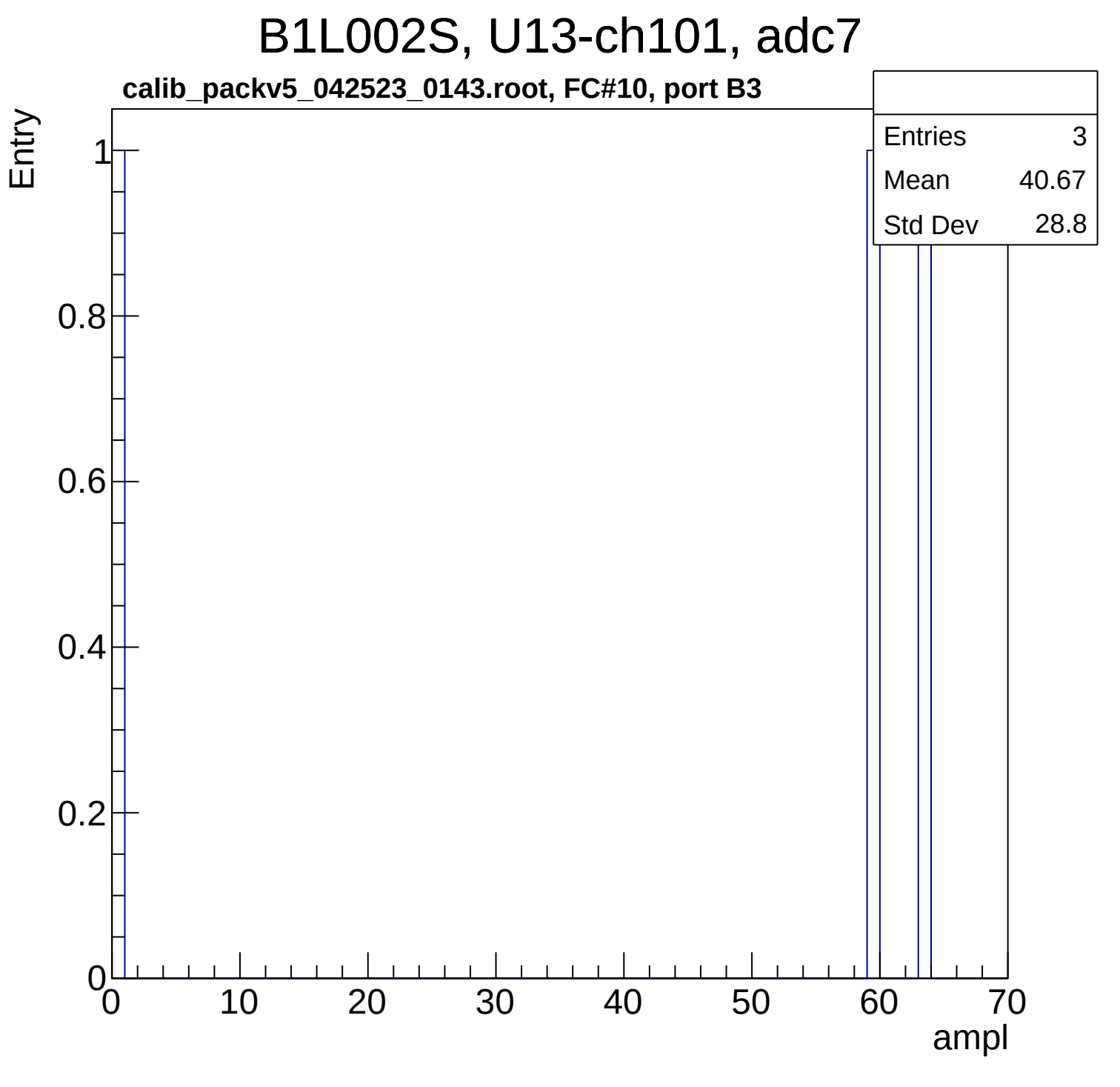
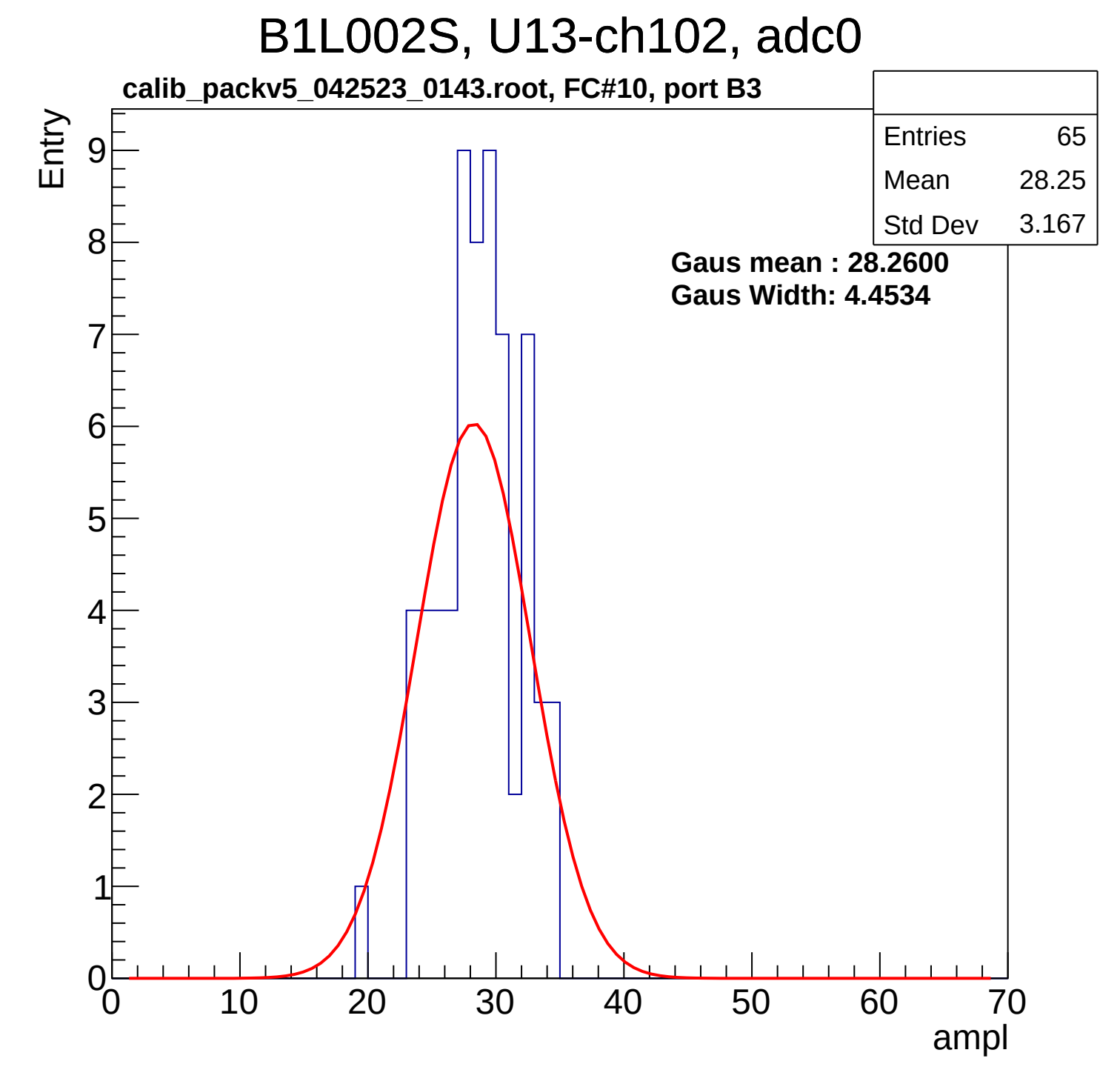

B1L002S, U13-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 40.67
Std Dev 28.8
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.25
Std Dev 3.167
Gaus mean : 28.2600
Gaus Width: 4.4534
Entry
ampl
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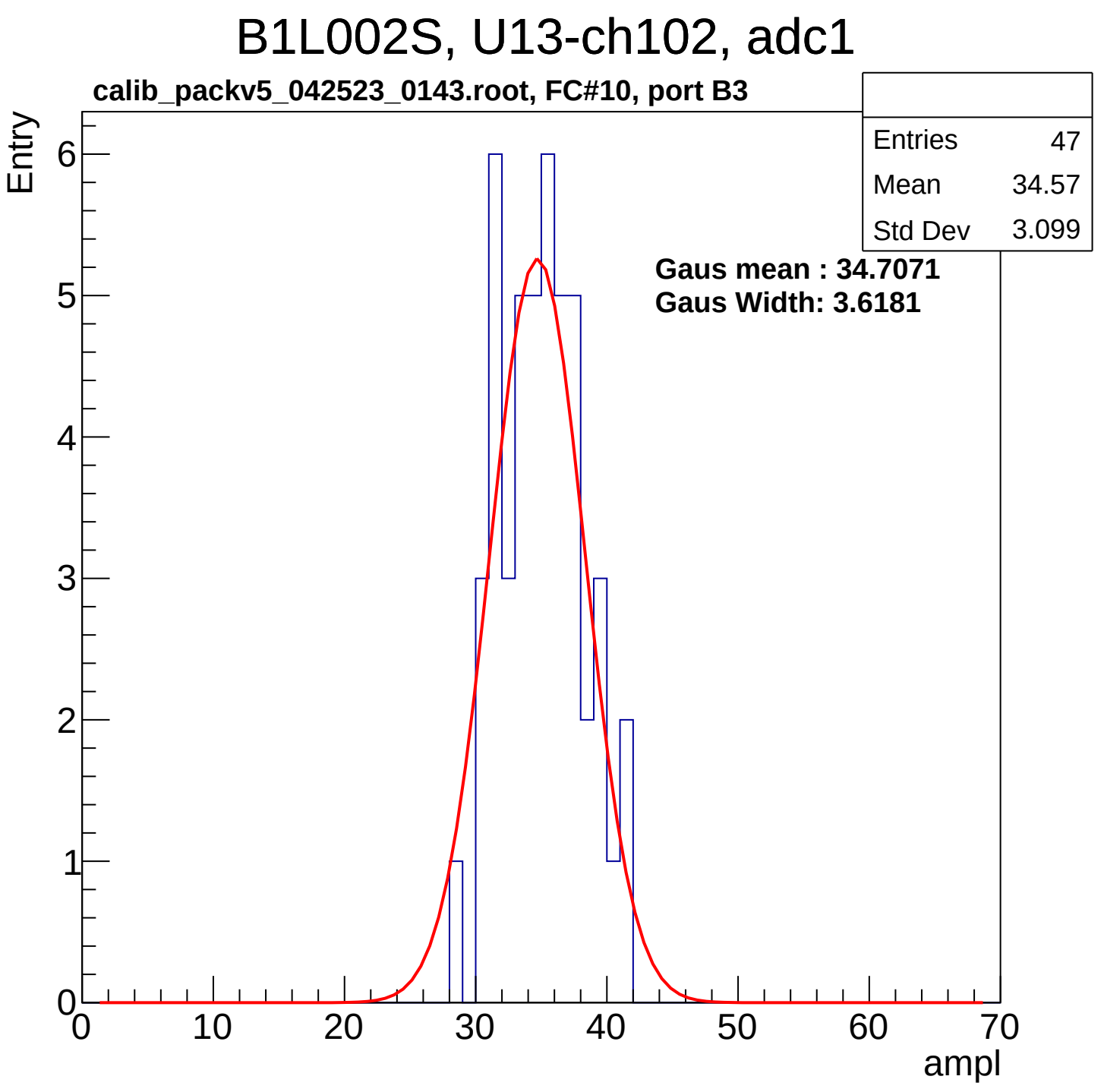
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 34.57
Std Dev 3.099
Gaus mean : 34.7071
Gaus Width: 3.6181
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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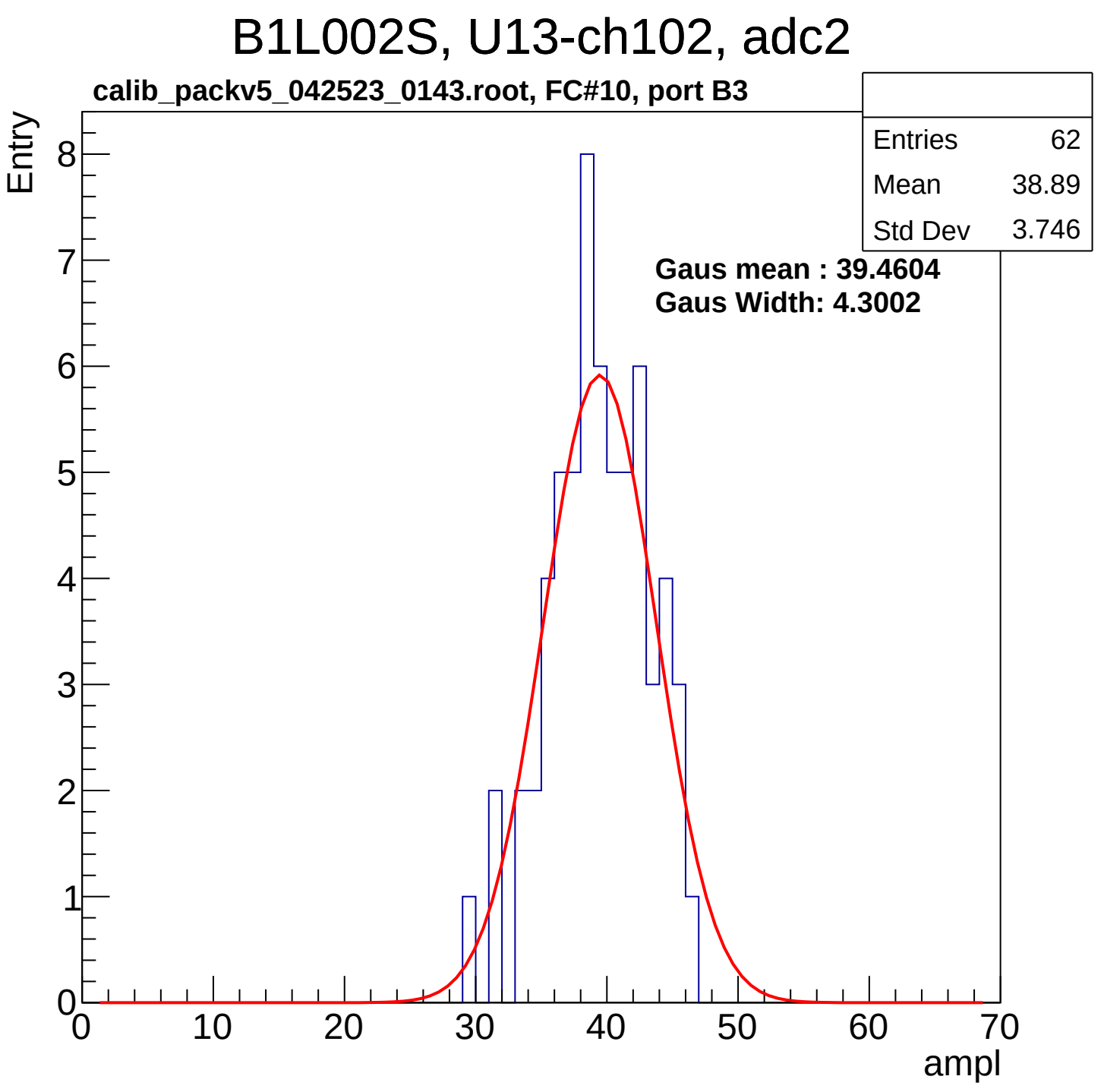
B1L002S, U13-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 38.89
Std Dev 3.746
Gaus mean : 39.4604
Gaus Width: 4.3002
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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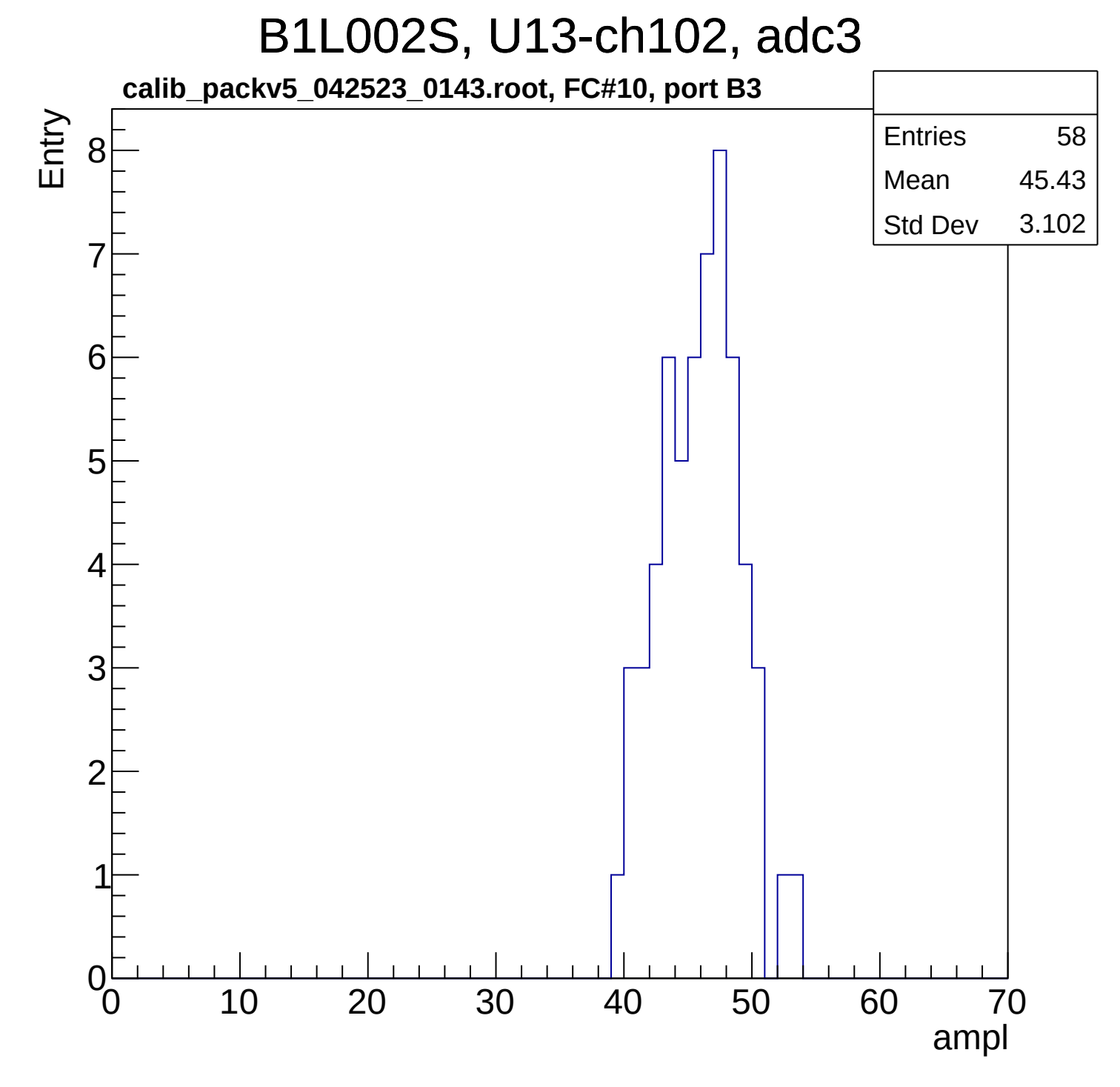
B1L002S, U13-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 45.43
Std Dev 3.102
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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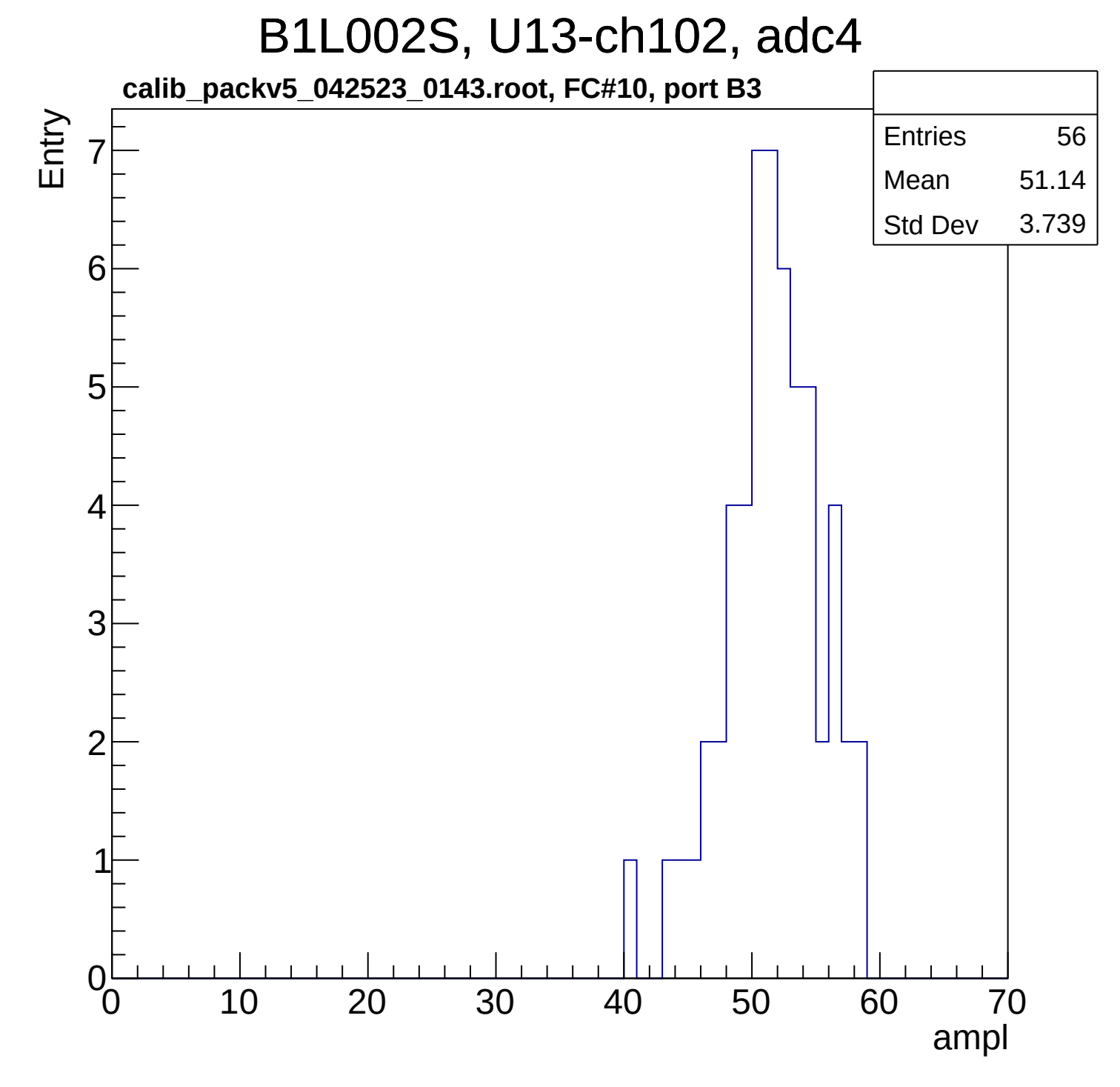
B1L002S, U13-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.14
Std Dev 3.739
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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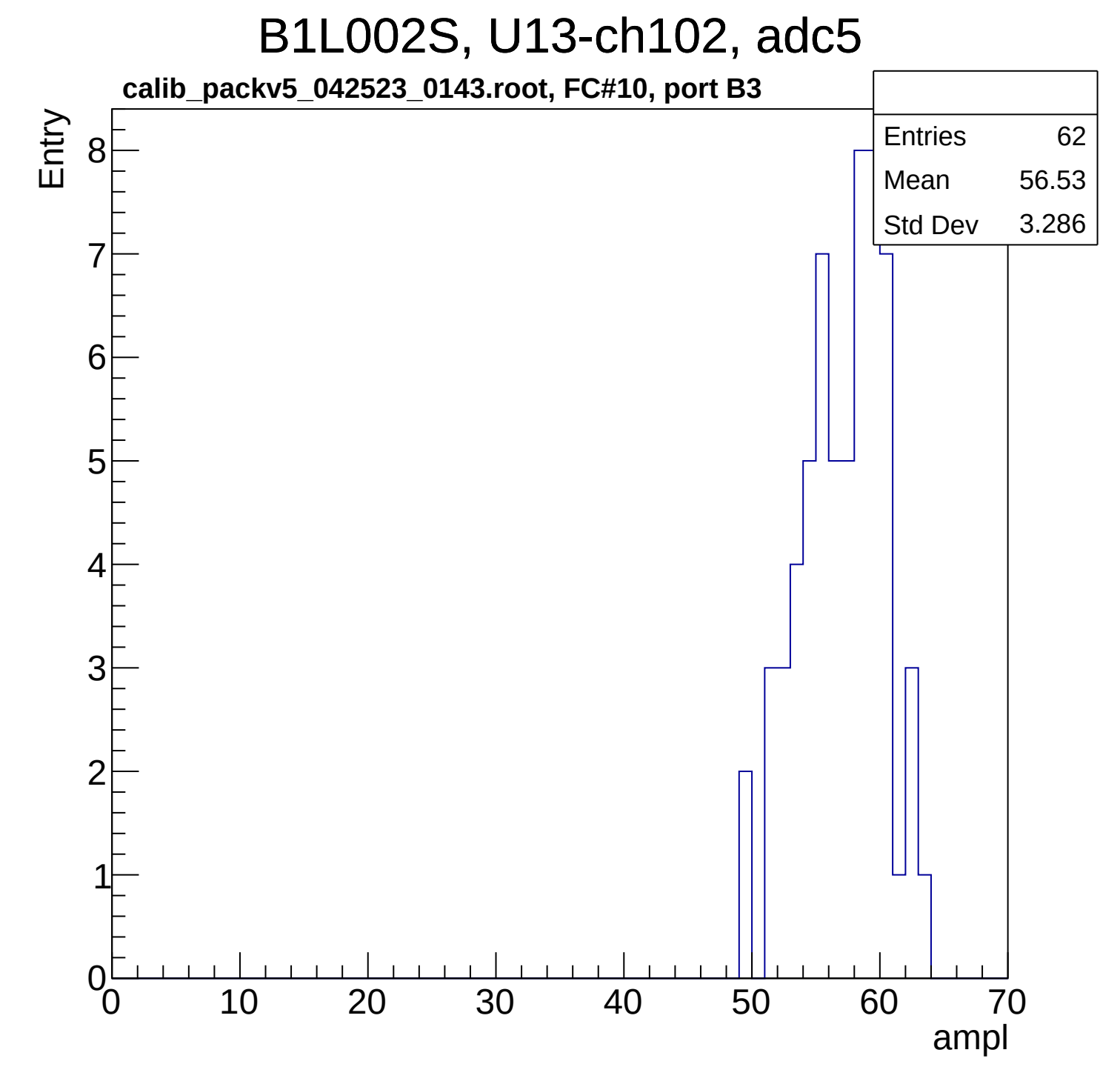
B1L002S, U13-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.53
Std Dev 3.286
Entry
ampl
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3
4
5
6
7
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0
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20
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40
50
60
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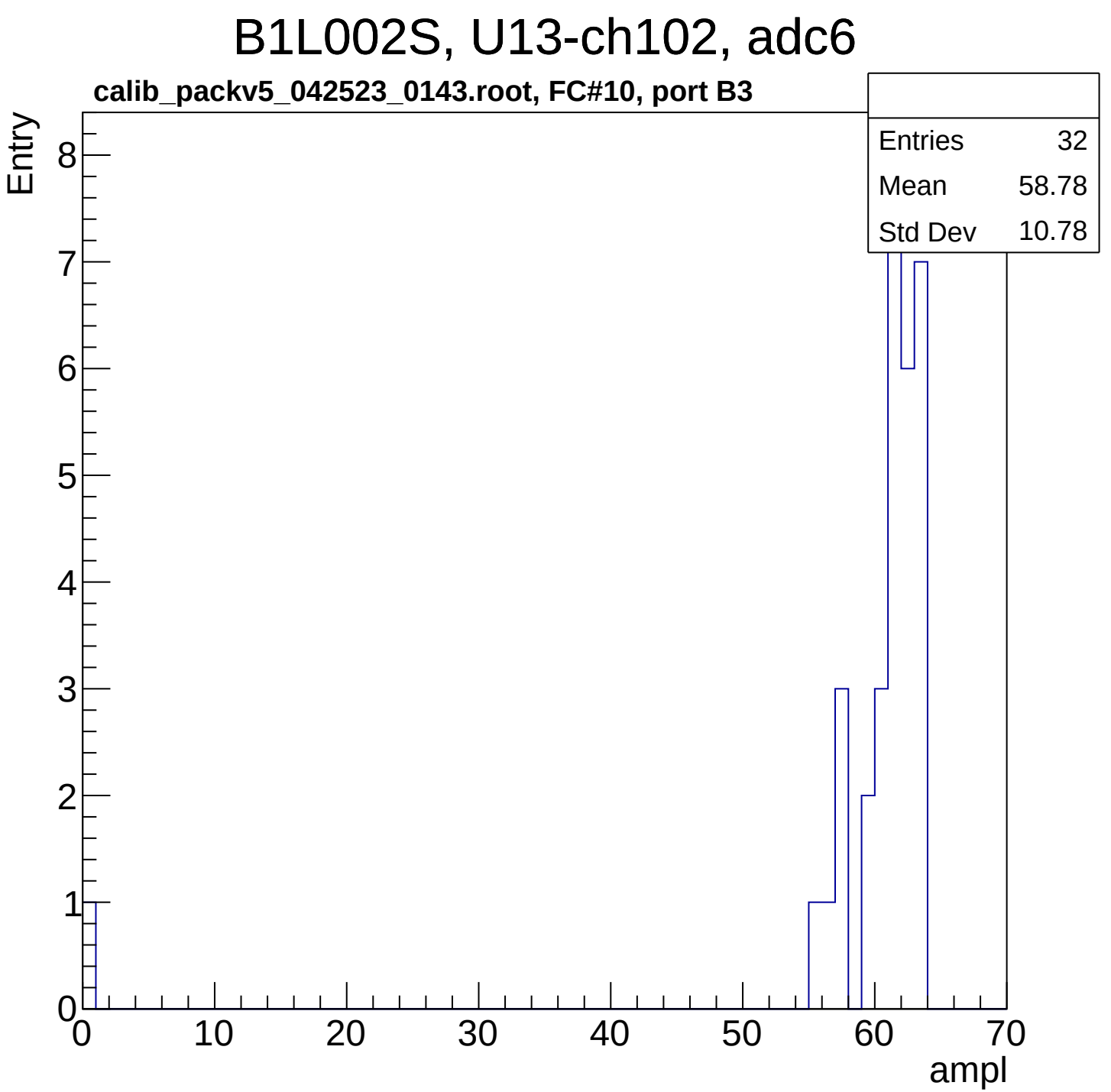
B1L002S, U13-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.78
Std Dev 10.78
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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60
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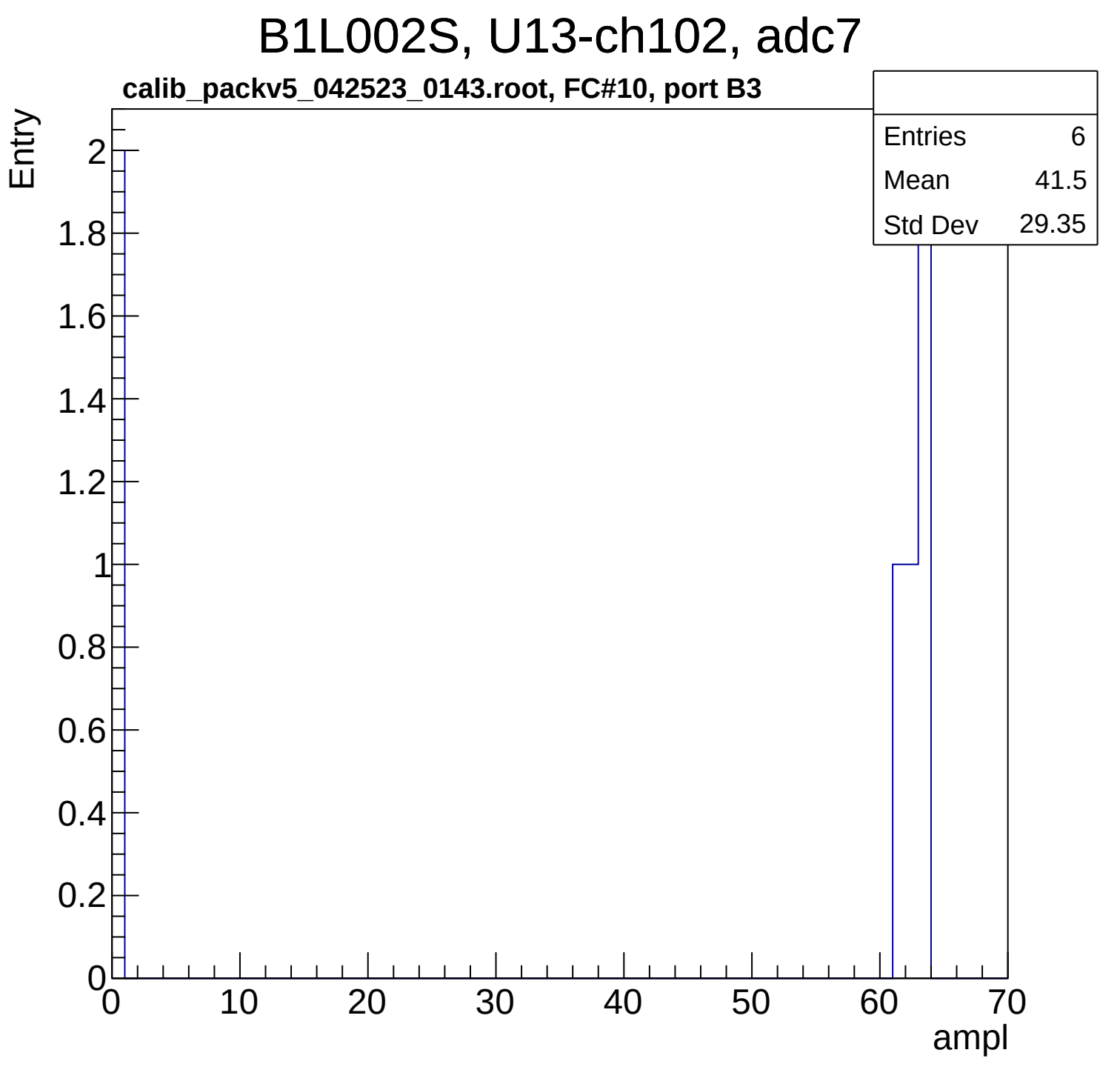
B1L002S, U13-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.5
Std Dev 29.35
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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60
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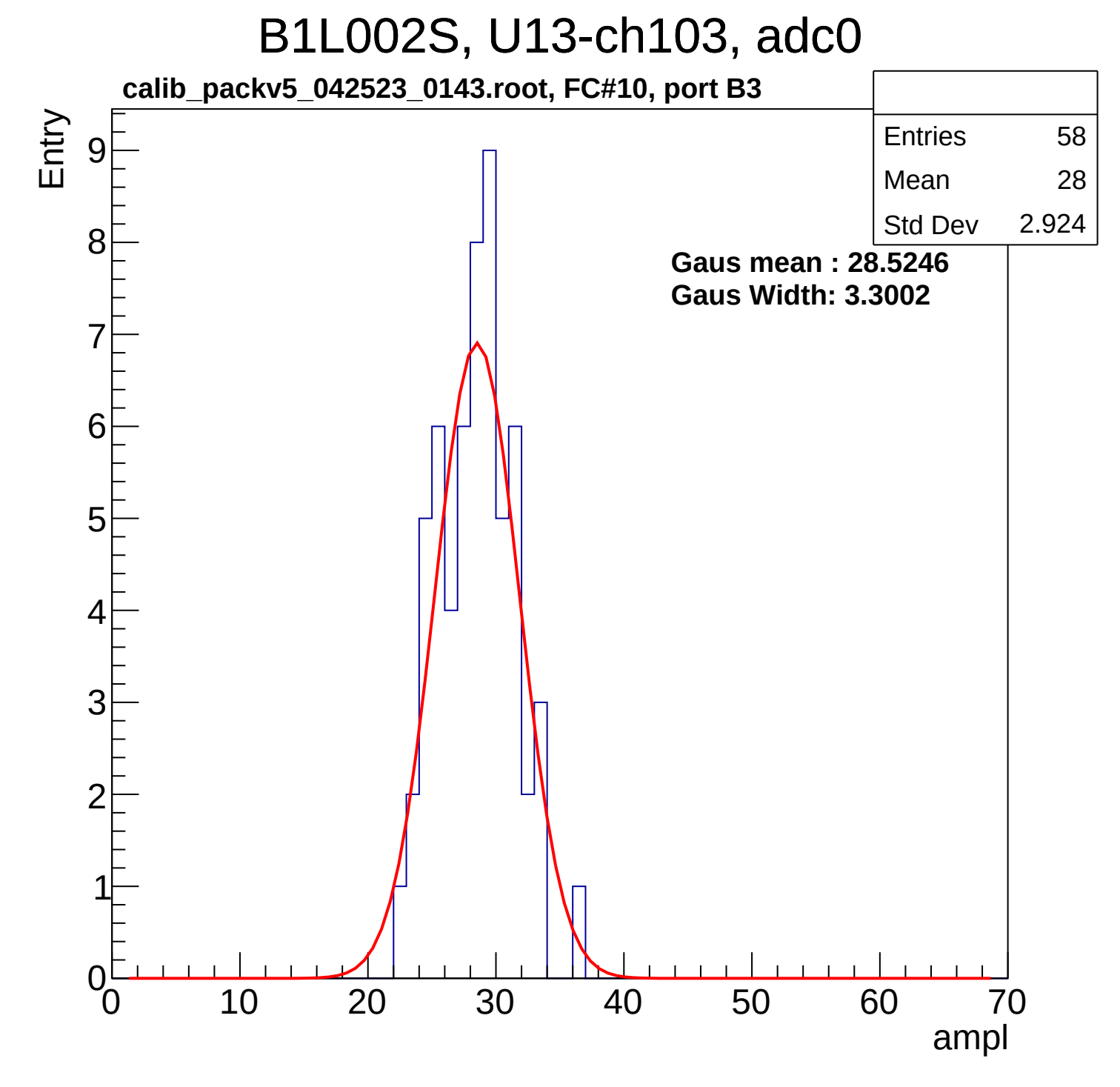
B1L002S, U13-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28
Std Dev 2.924
Gaus mean : 28.5246
Gaus Width: 3.3002
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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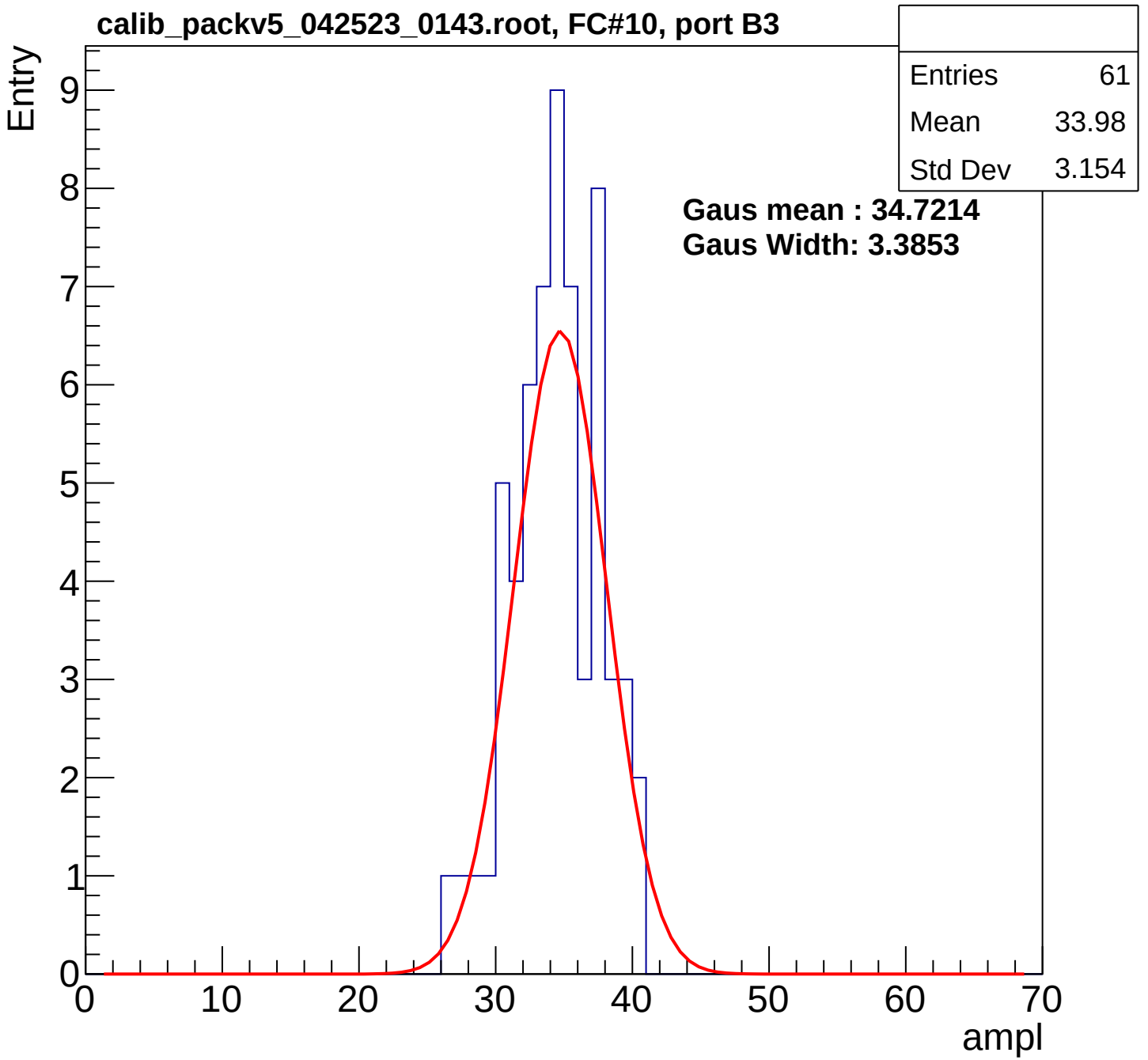
B1L002S, U13-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 33.98
Std Dev 3.154
Gaus mean : 34.7214
Gaus Width: 3.3853
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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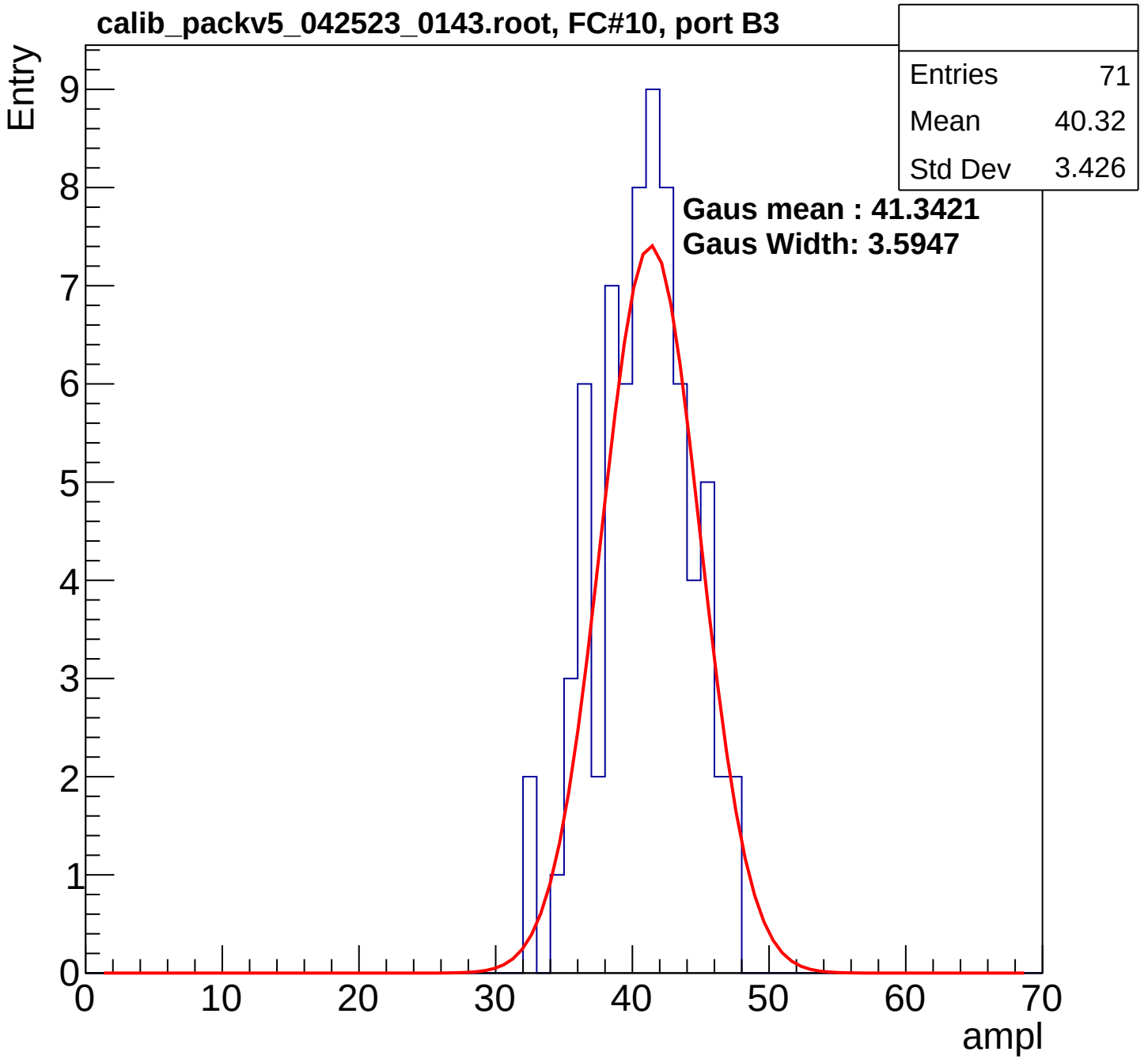
B1L002S, U13-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.32
Std Dev 3.426
Gaus mean : 41.3421
Gaus Width: 3.5947
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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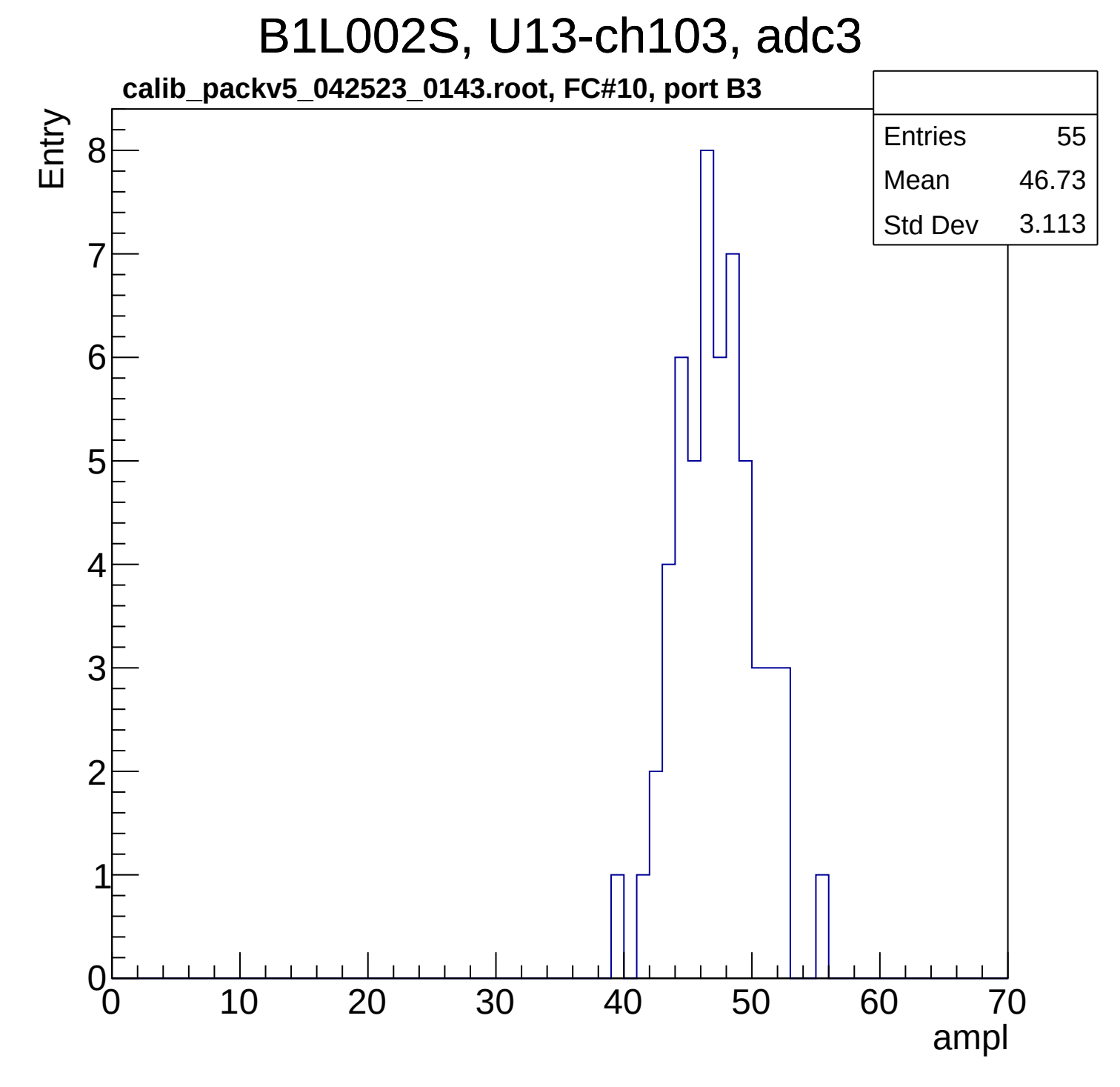
B1L002S, U13-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.73
Std Dev 3.113
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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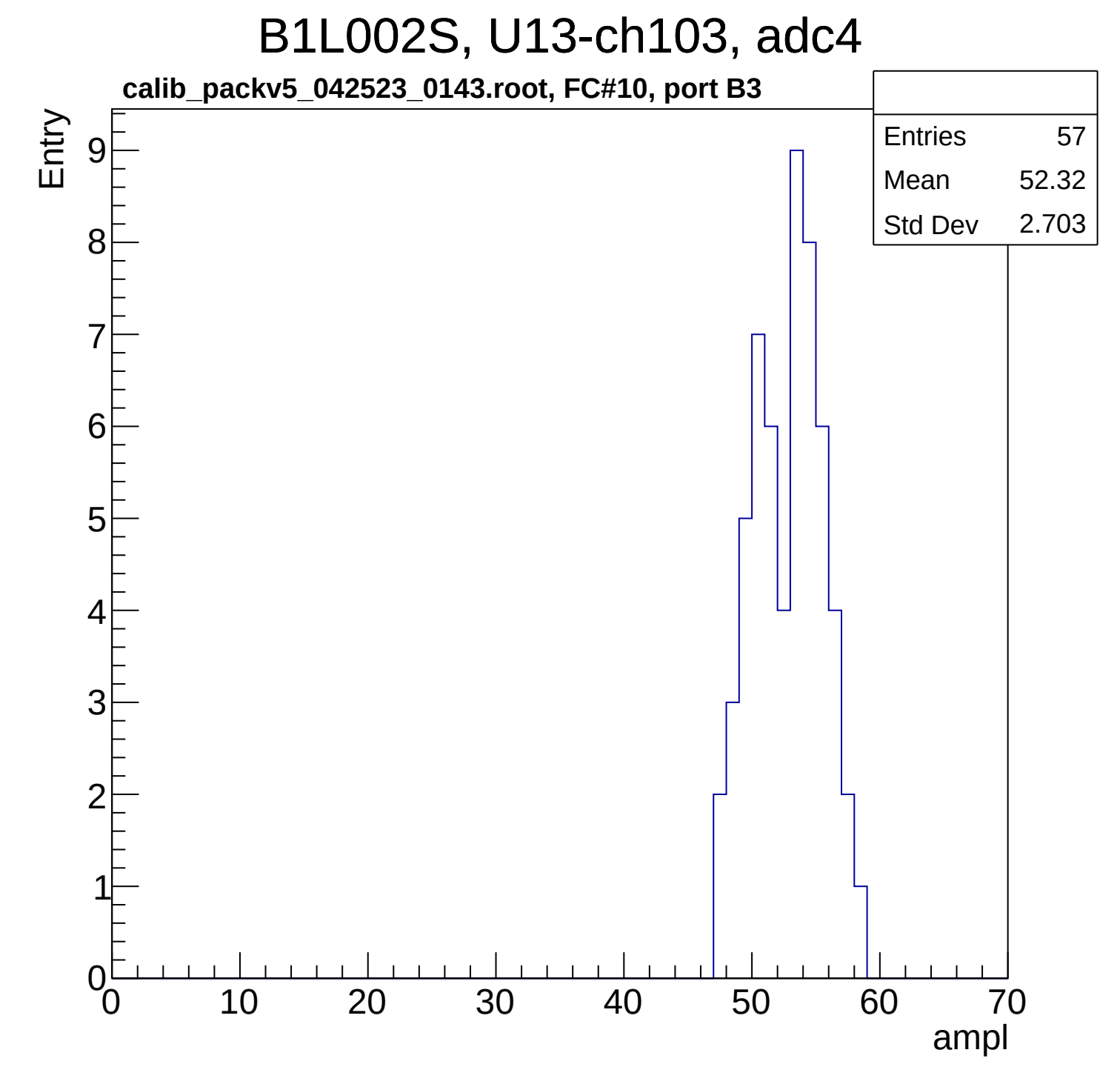
B1L002S, U13-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.32
Std Dev 2.703
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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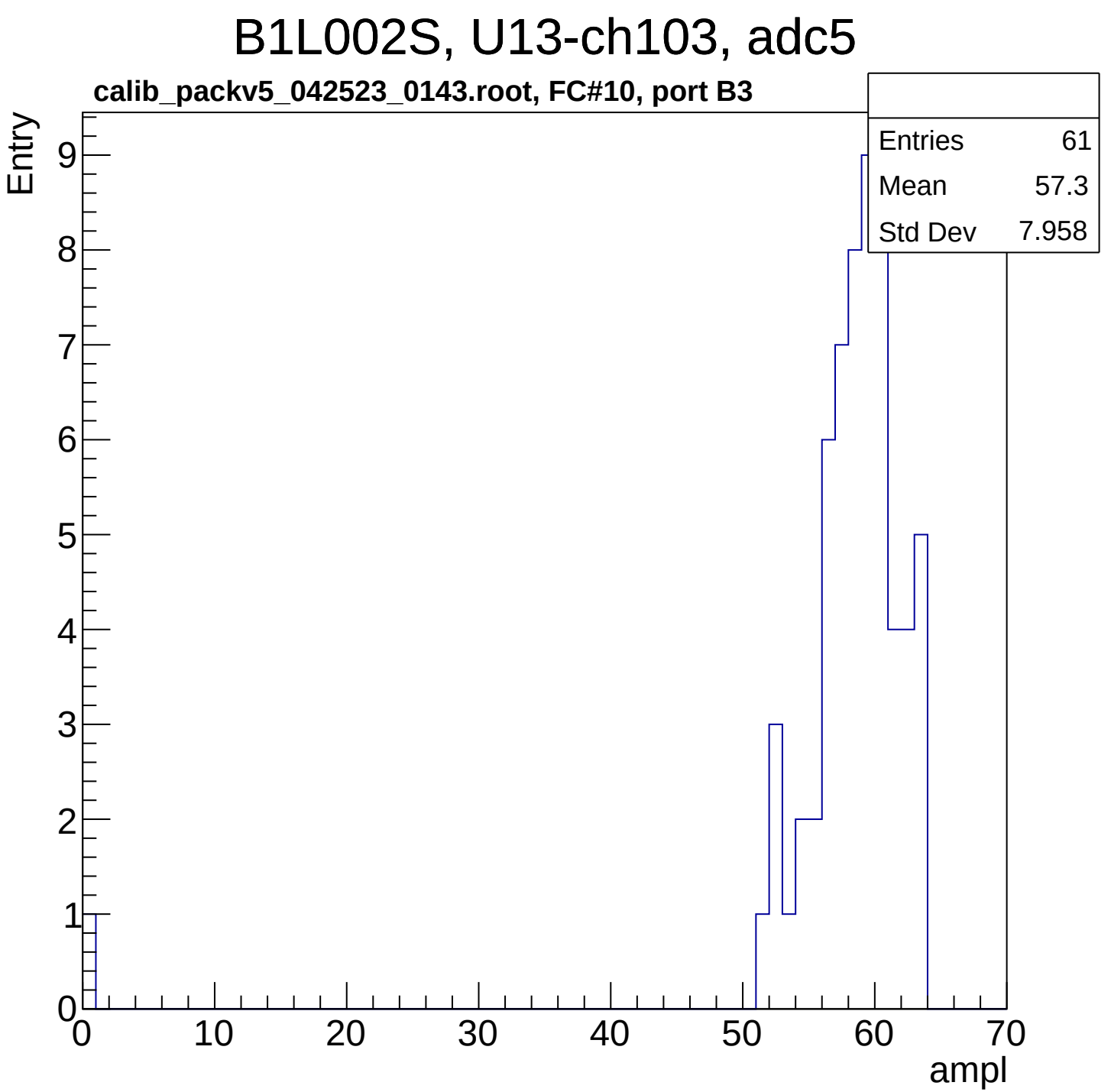
40
50
60
70


B1L002S, U13-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.3
Std Dev 7.958
Entry
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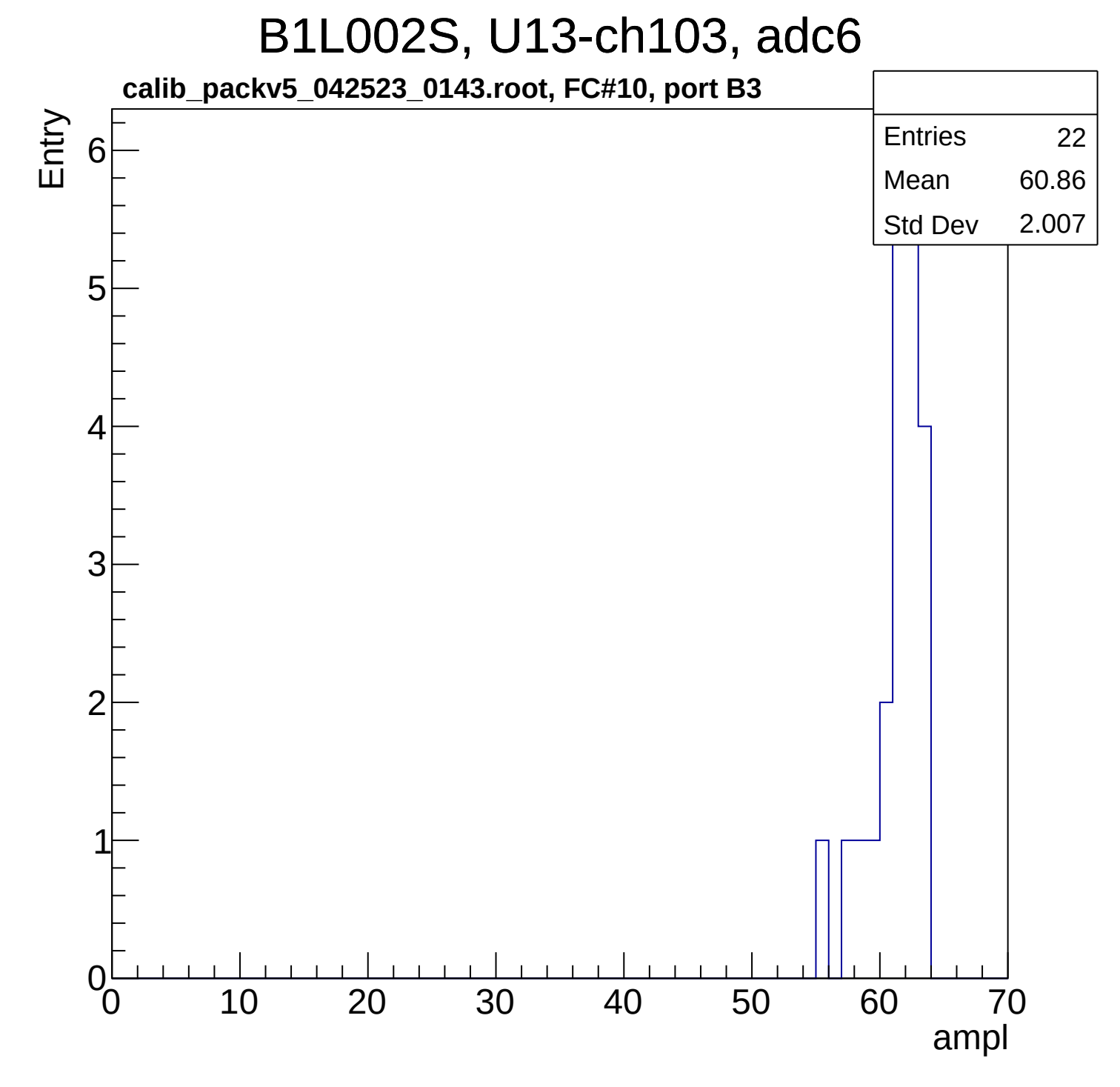
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 60.86
Std Dev 2.007
Entry
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2
3
4
5
6
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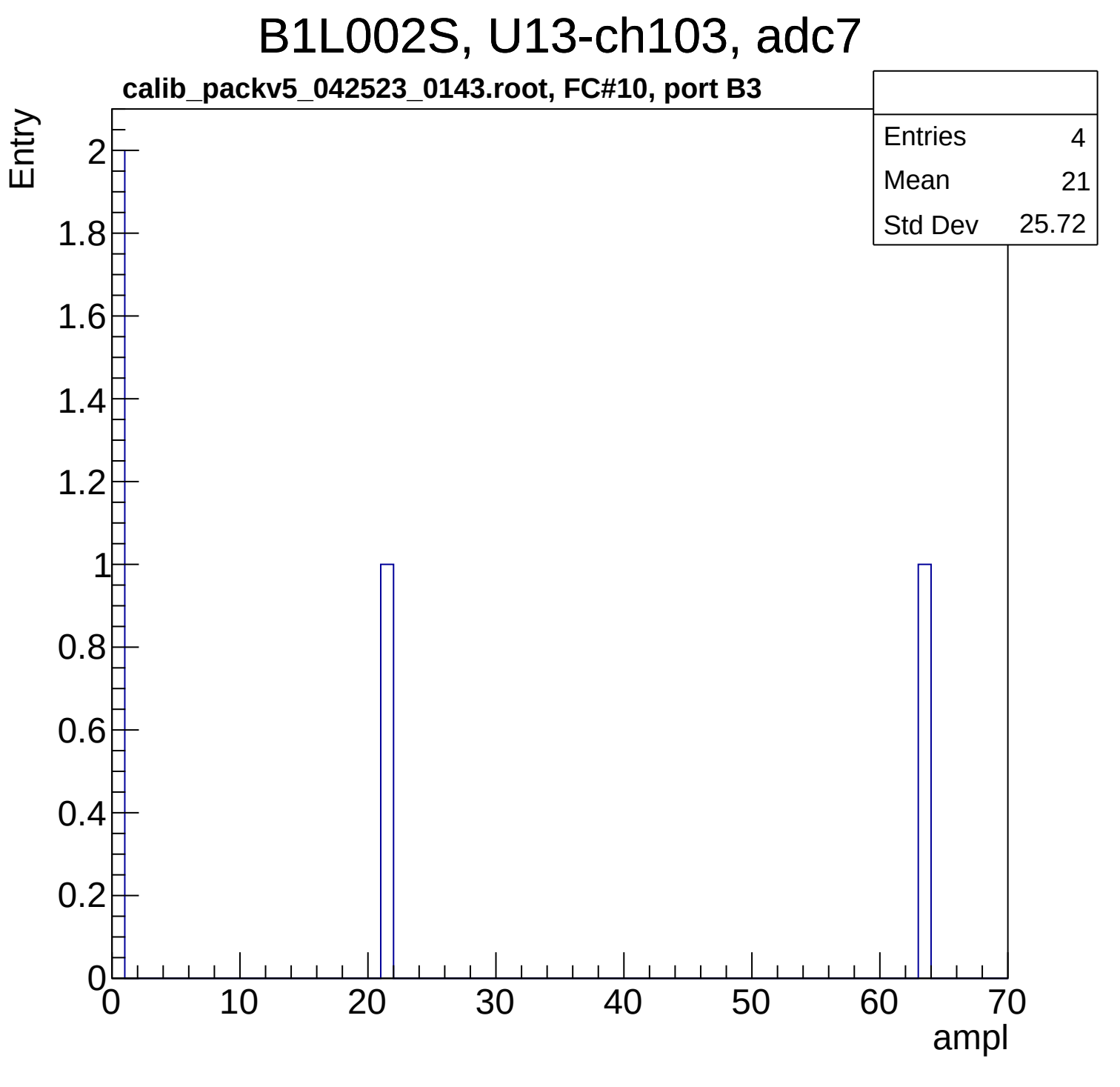
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
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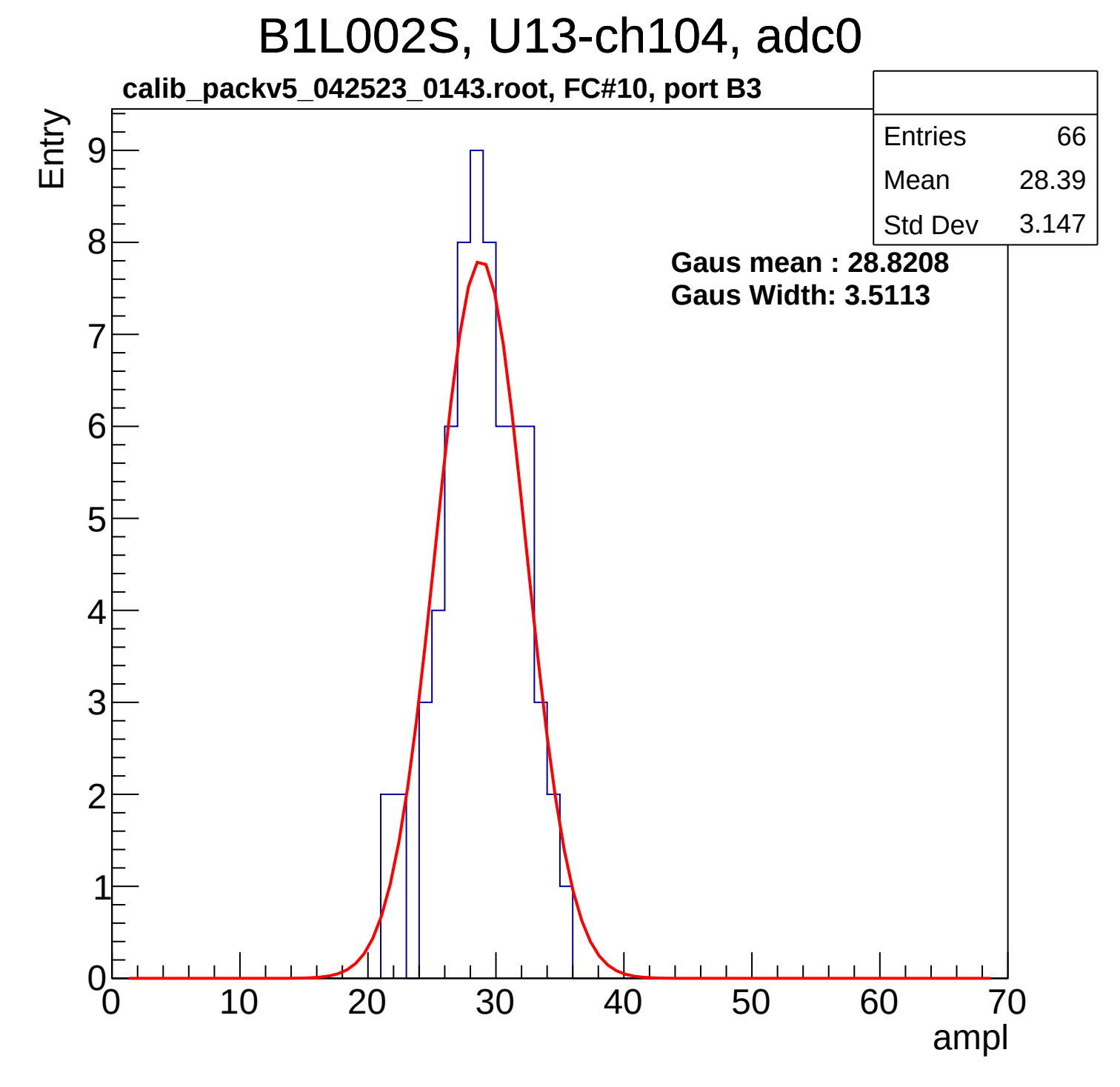
B1L002S, U13-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.39
Std Dev 3.147
Gaus mean : 28.8208
Gaus Width: 3.5113
Entry
ampl
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2
3
4
5
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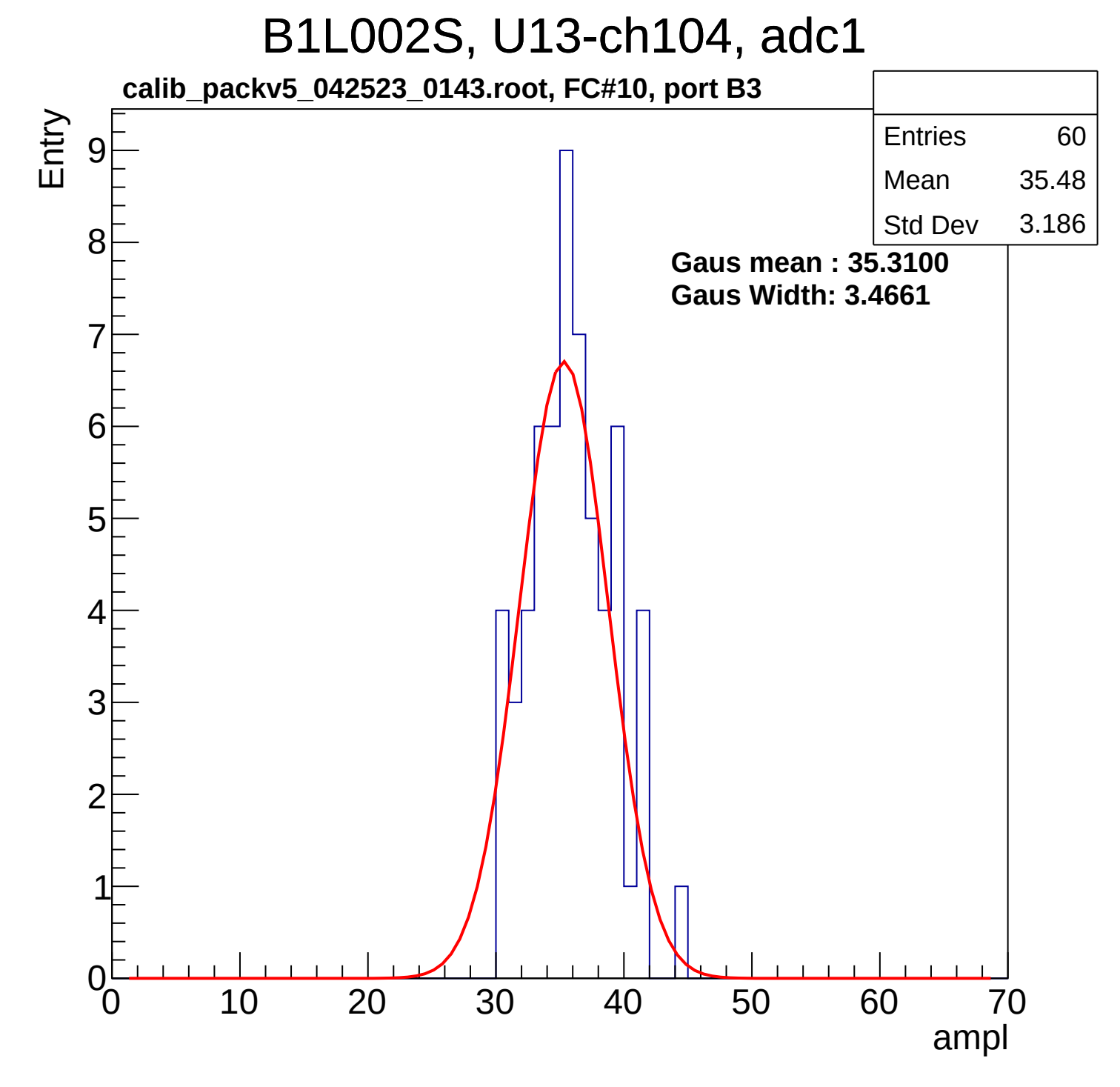
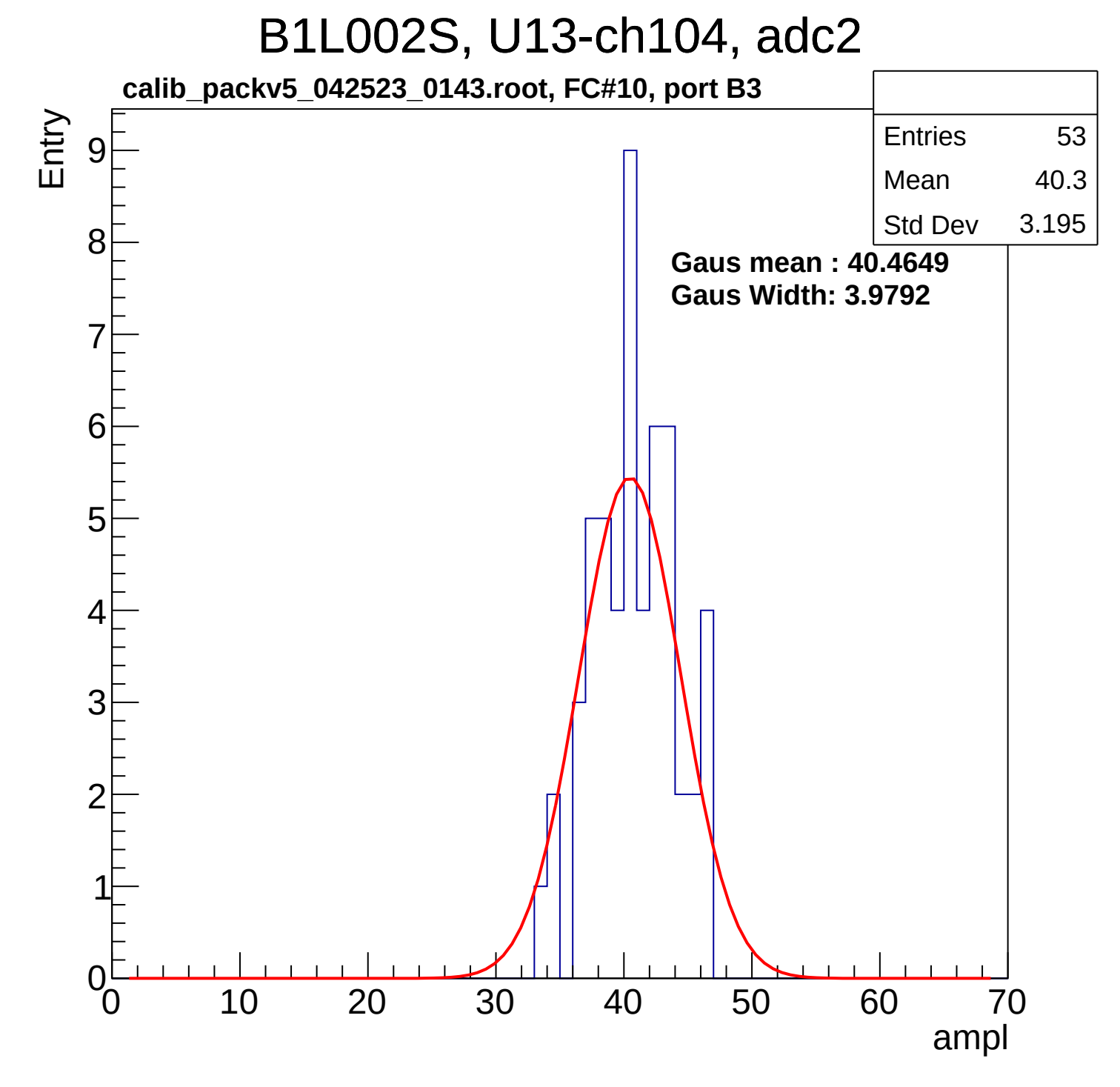

B1L002S, U13-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.48
Std Dev 3.186
Gaus mean : 35.3100
Gaus Width: 3.4661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 40.3
Std Dev 3.195
Gaus mean : 40.4649
Gaus Width: 3.9792
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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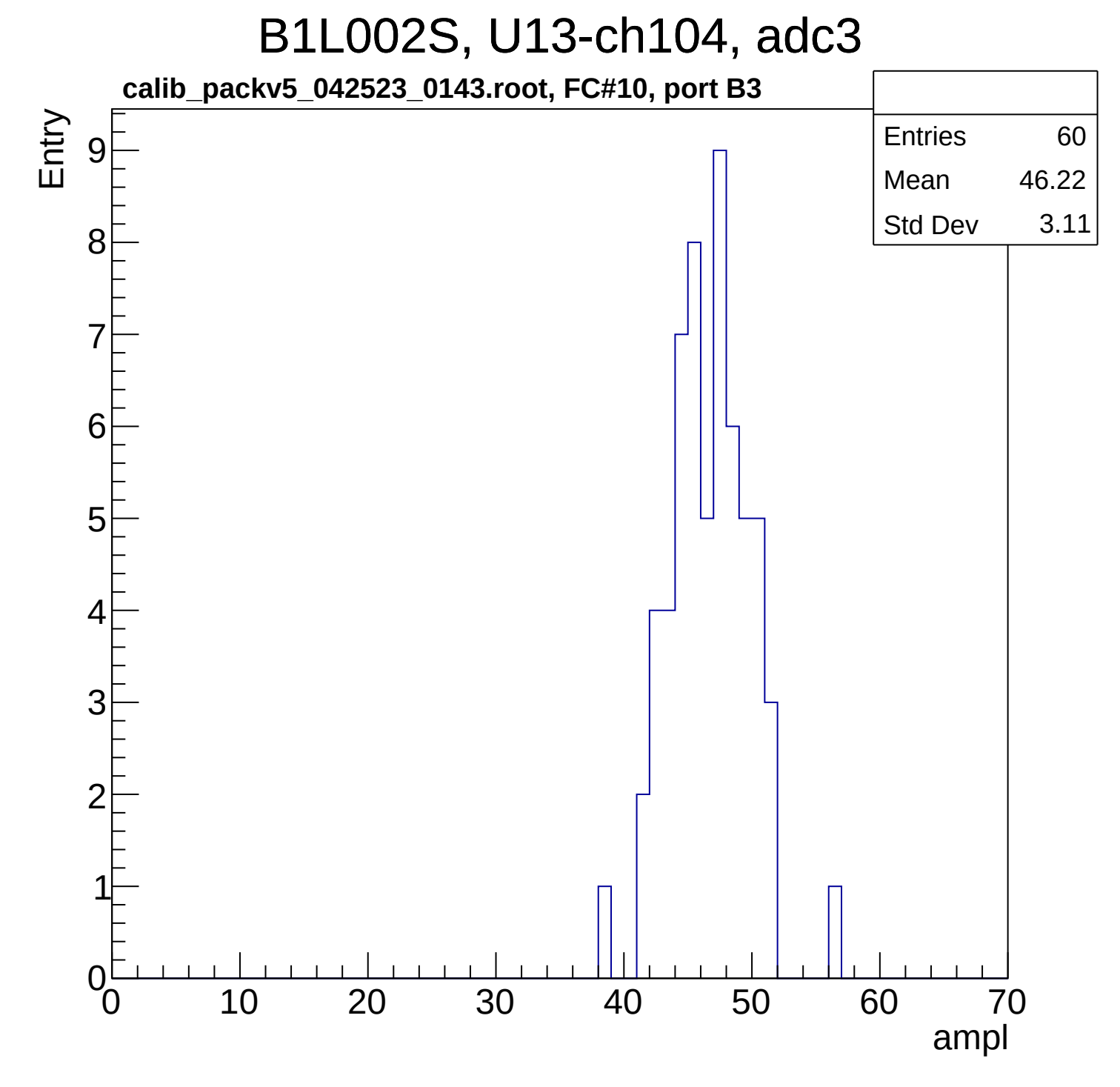
B1L002S, U13-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.22
Std Dev 3.11
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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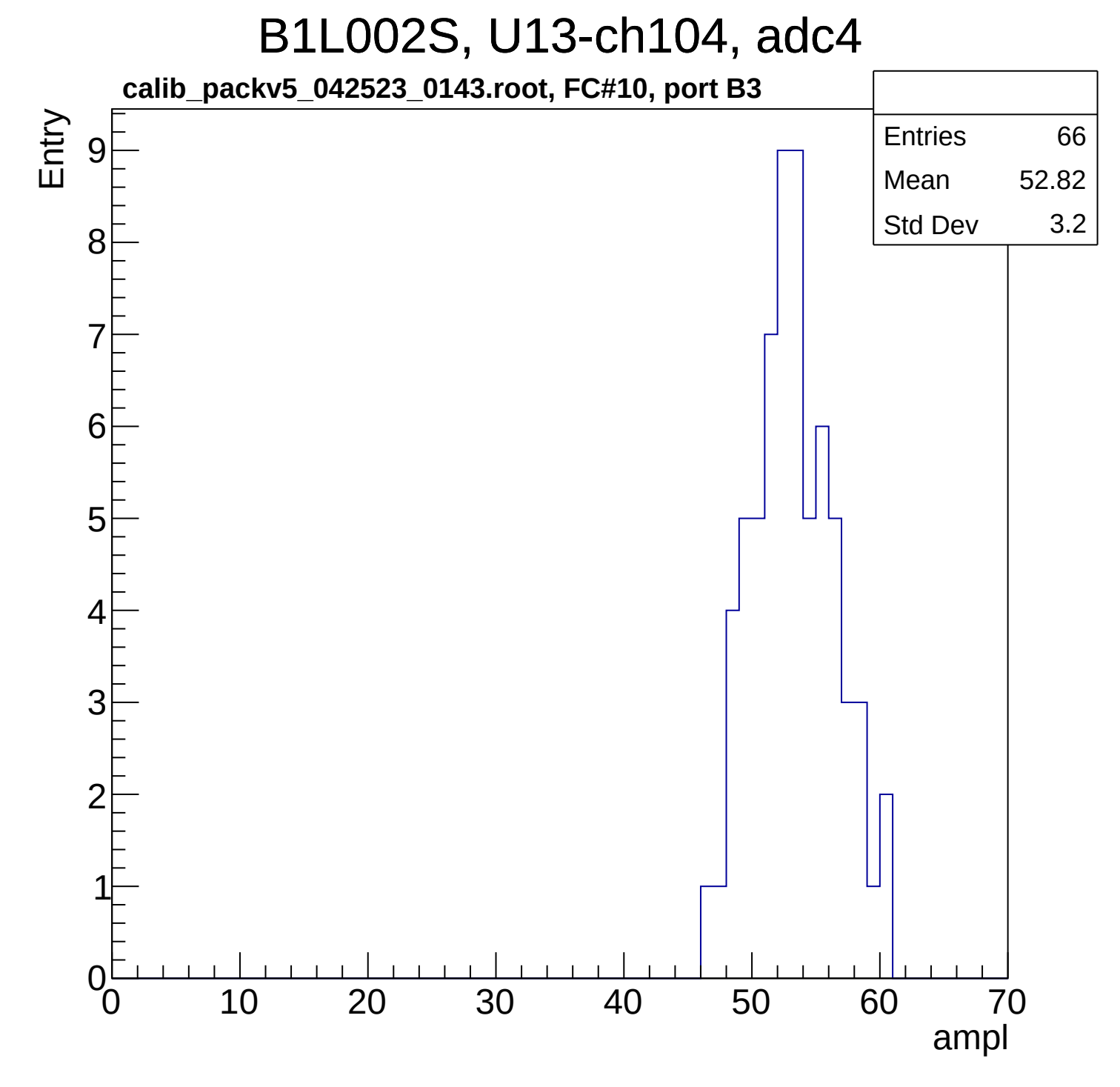
B1L002S, U13-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.82
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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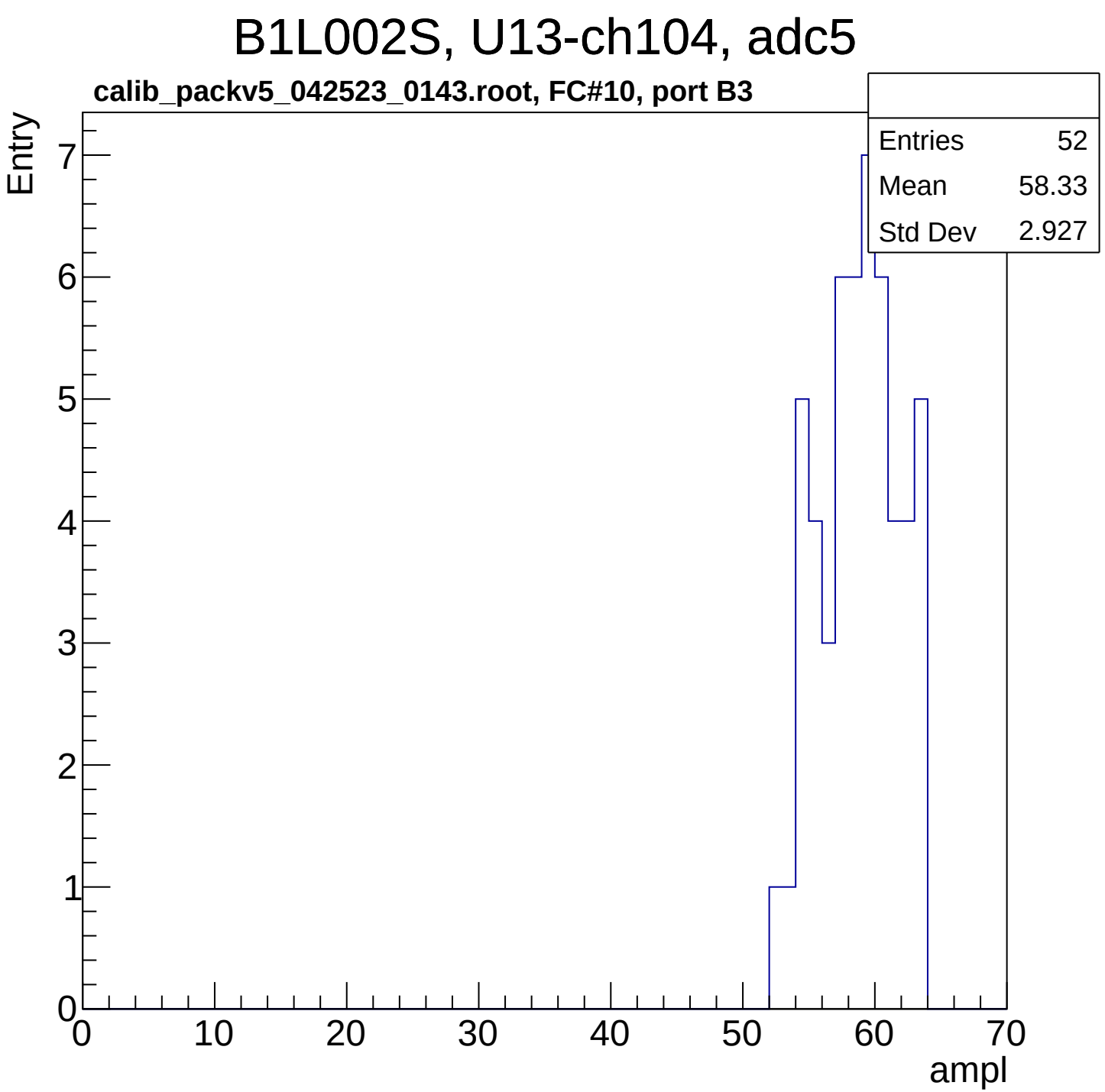
B1L002S, U13-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.33
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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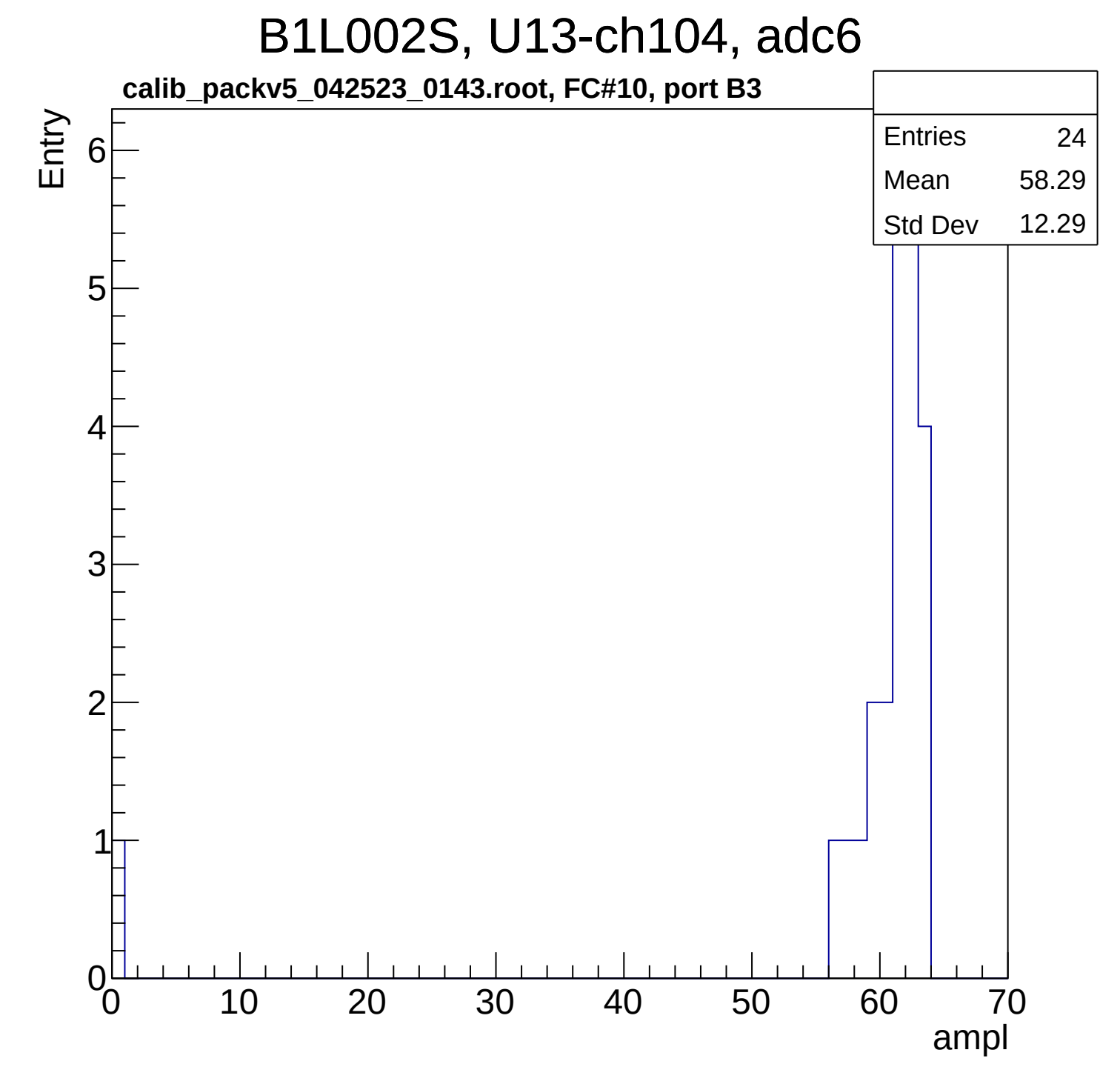
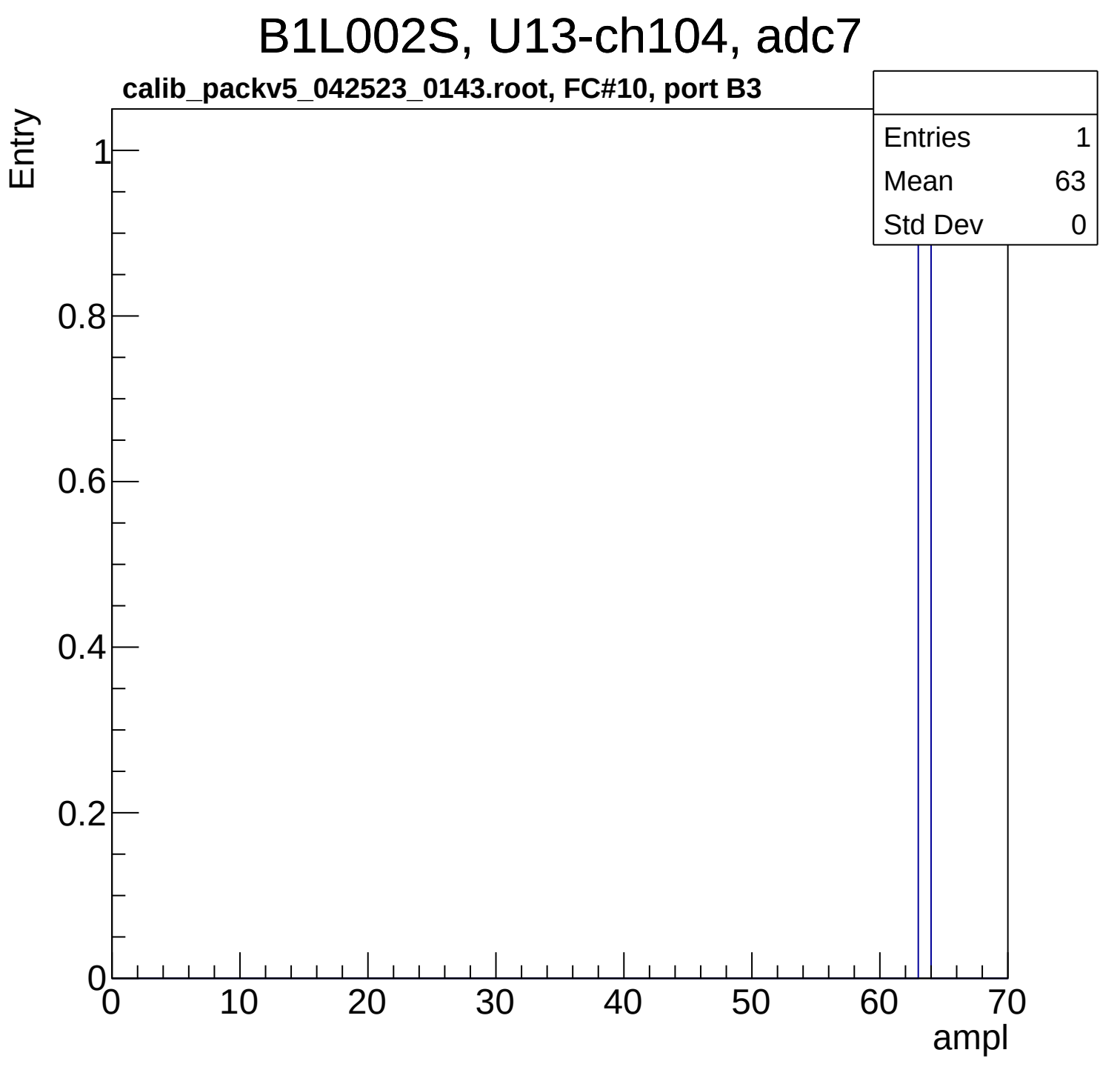
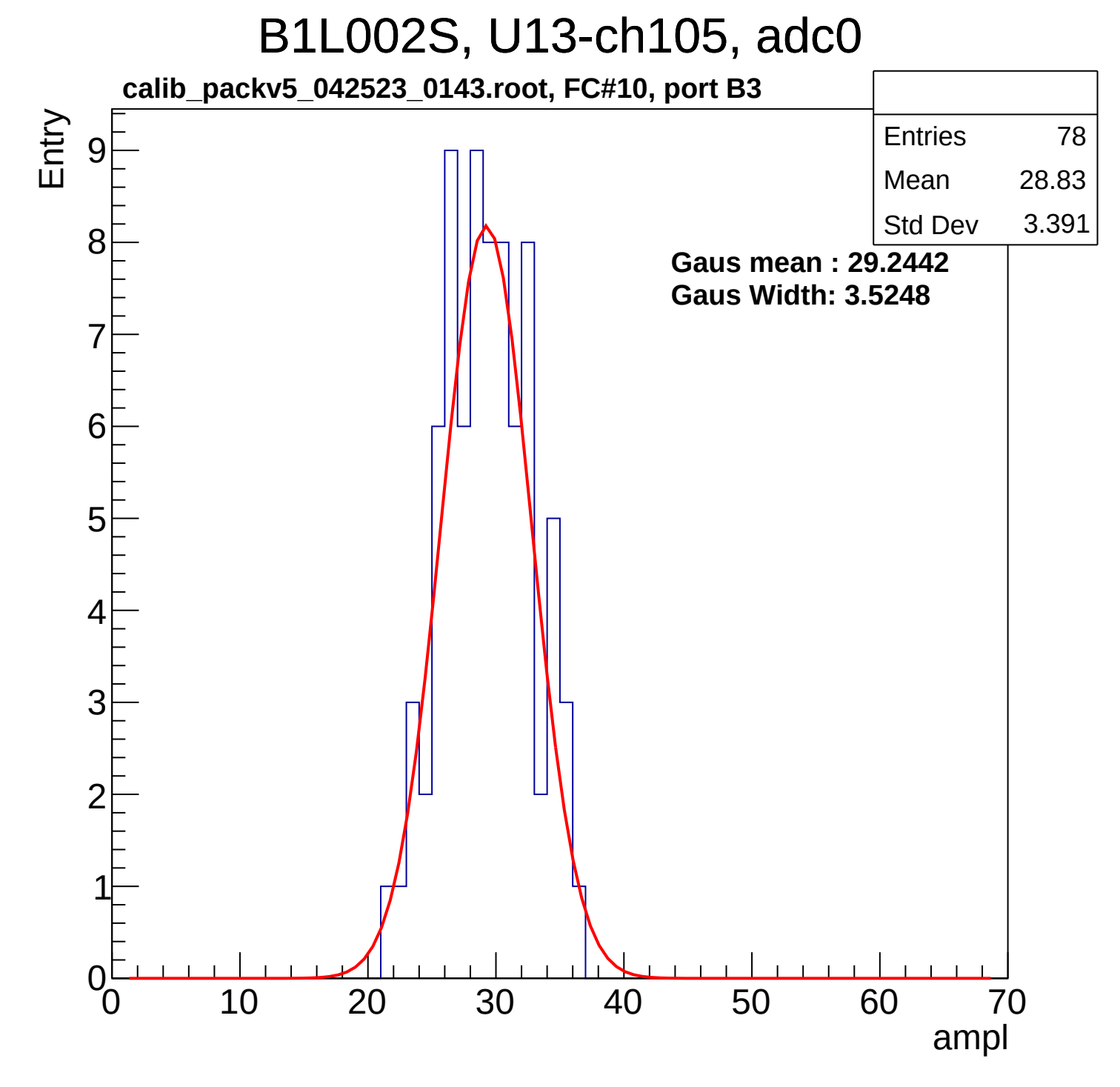
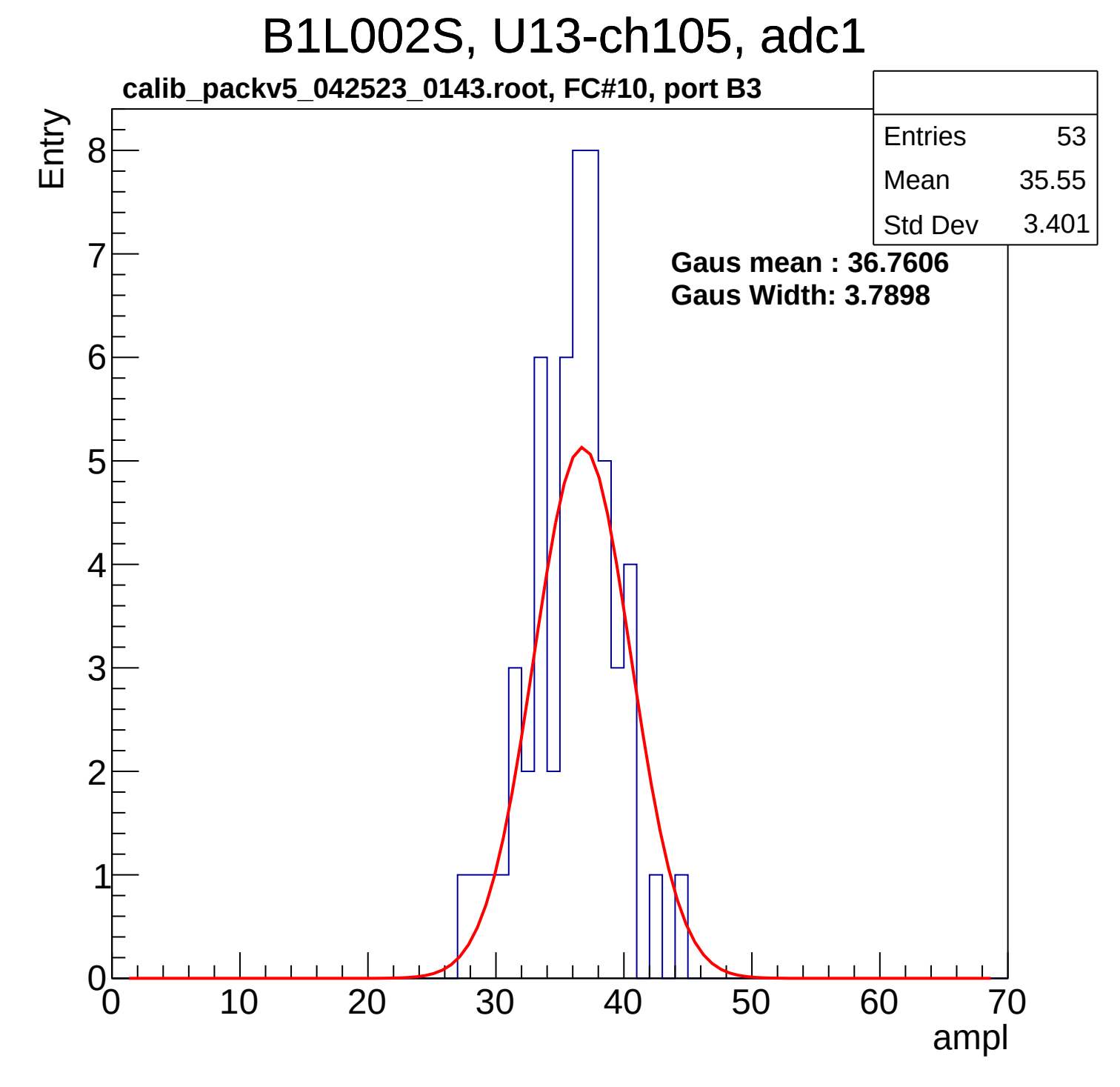

B1L002S, U13-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.29
Std Dev 12.29
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.83
Std Dev 3.391
Gaus mean : 29.2442
Gaus Width: 3.5248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 35.55
Std Dev 3.401
Gaus mean : 36.7606
Gaus Width: 3.7898
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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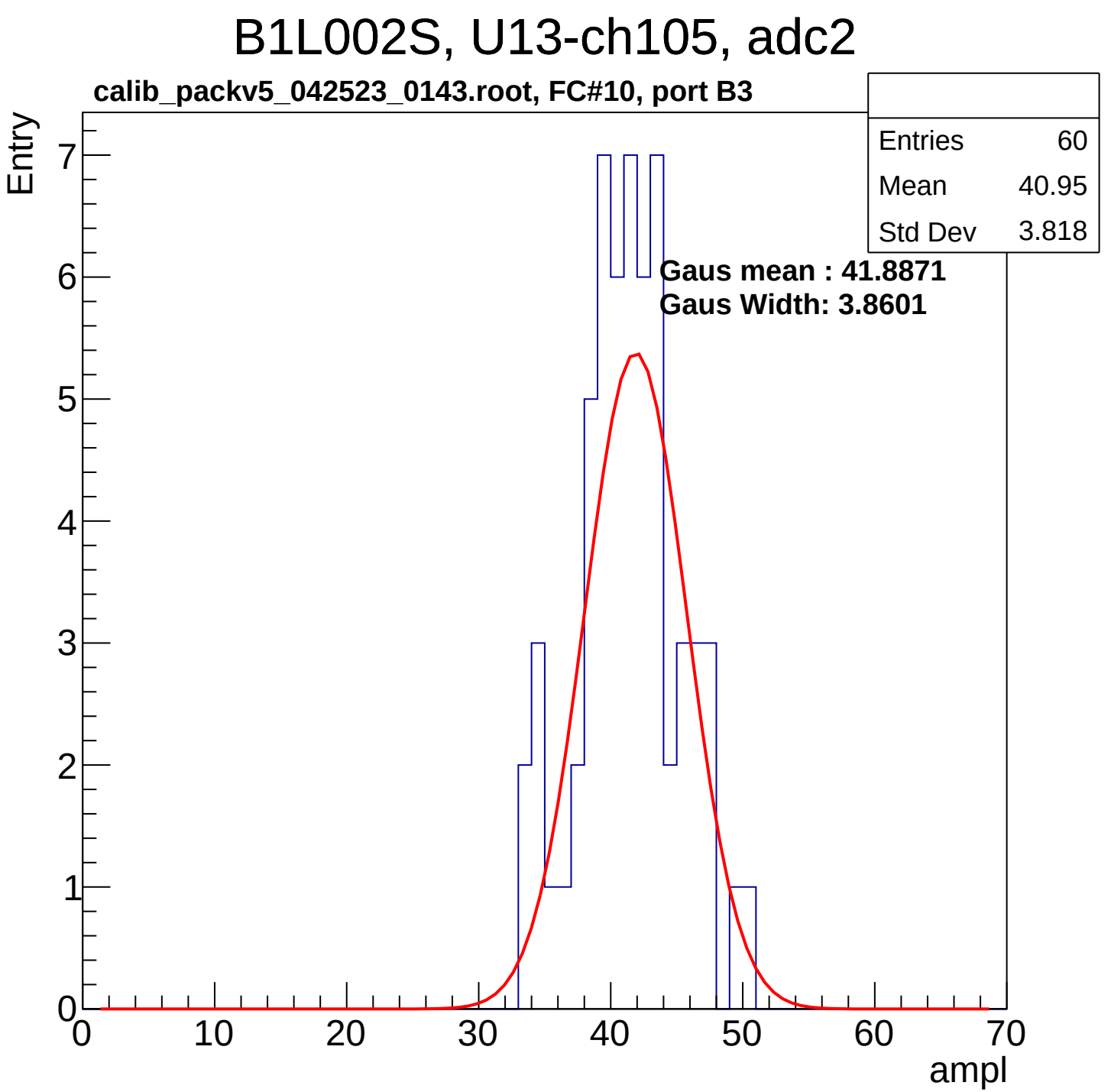
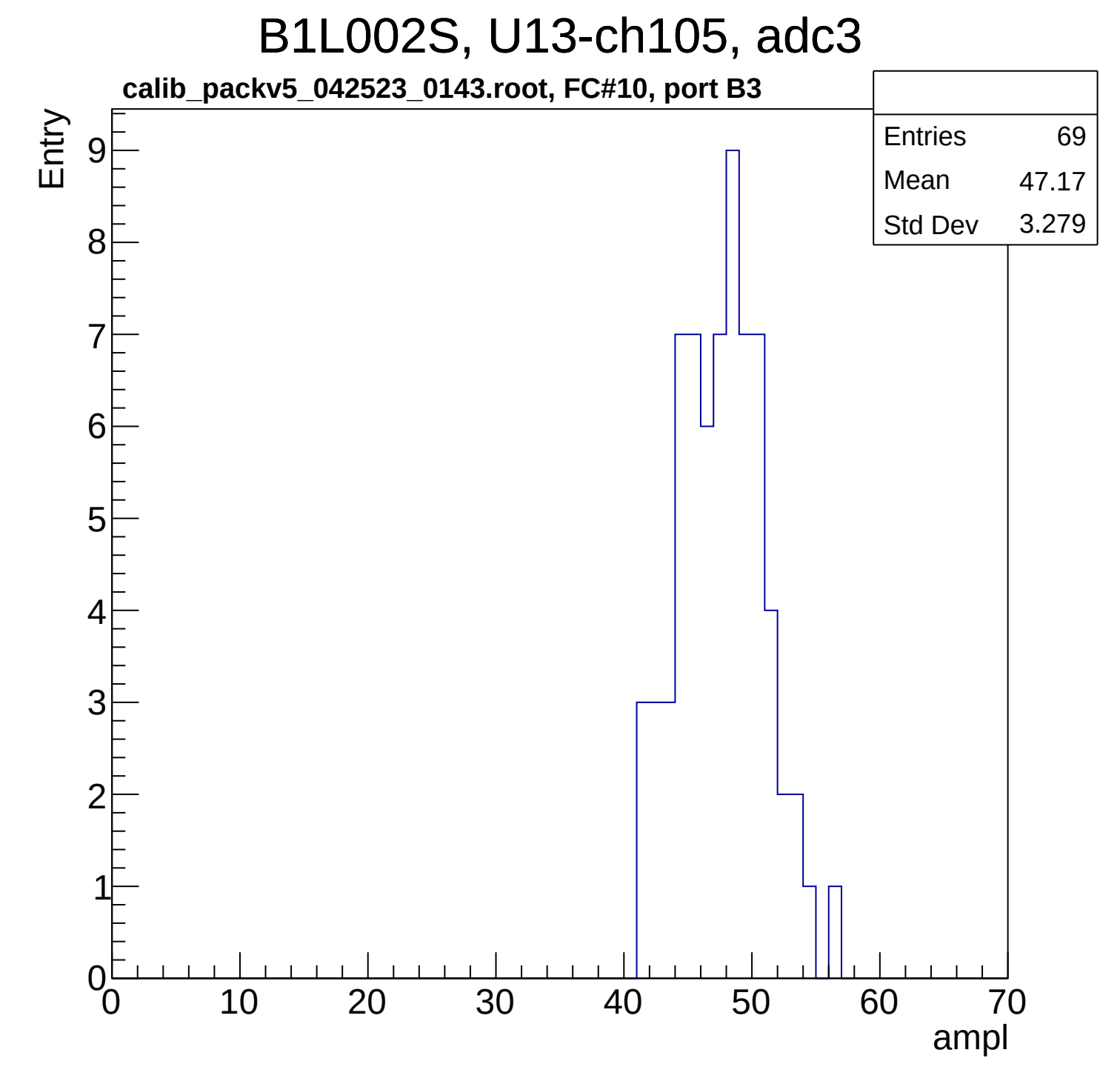
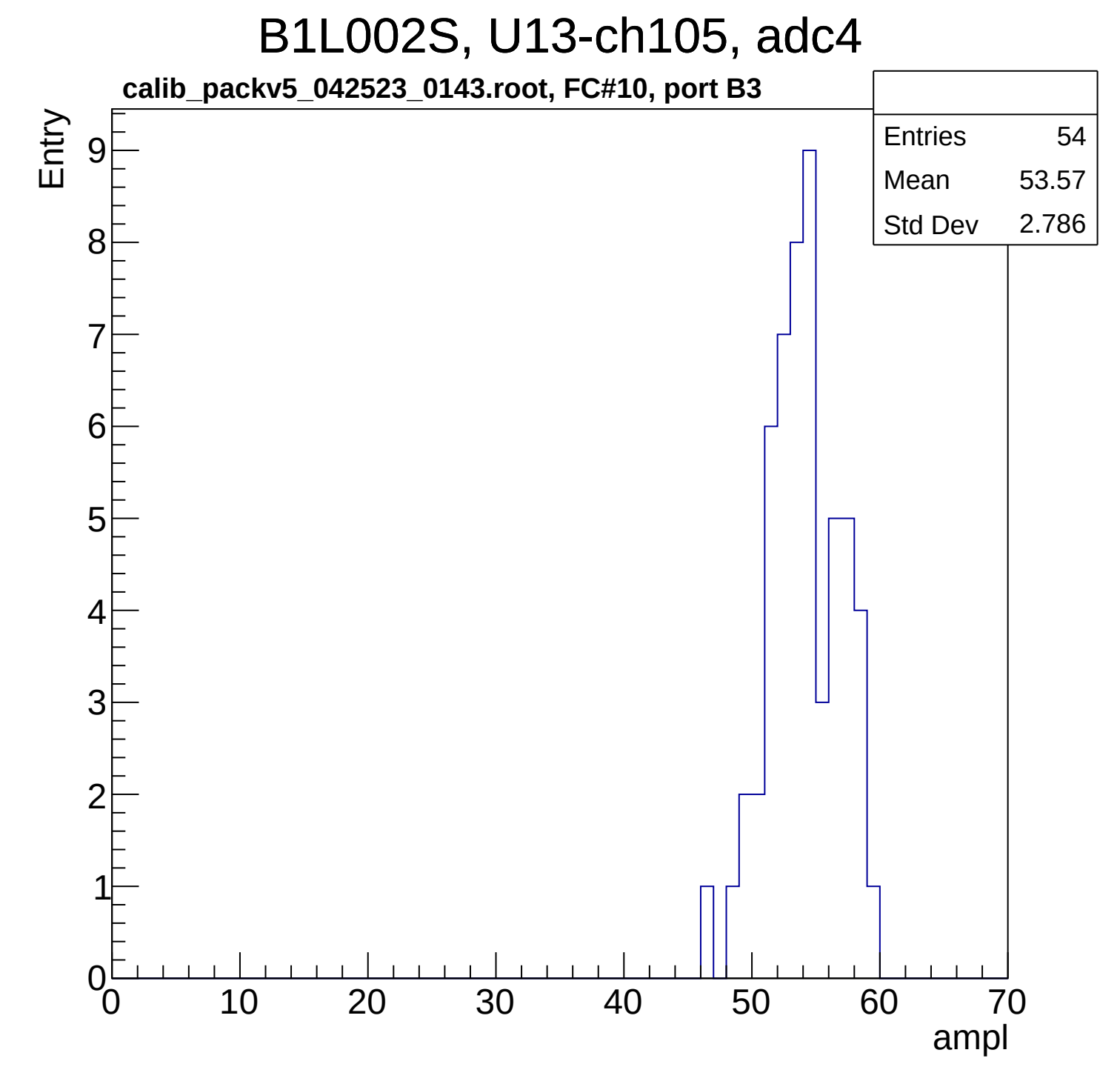
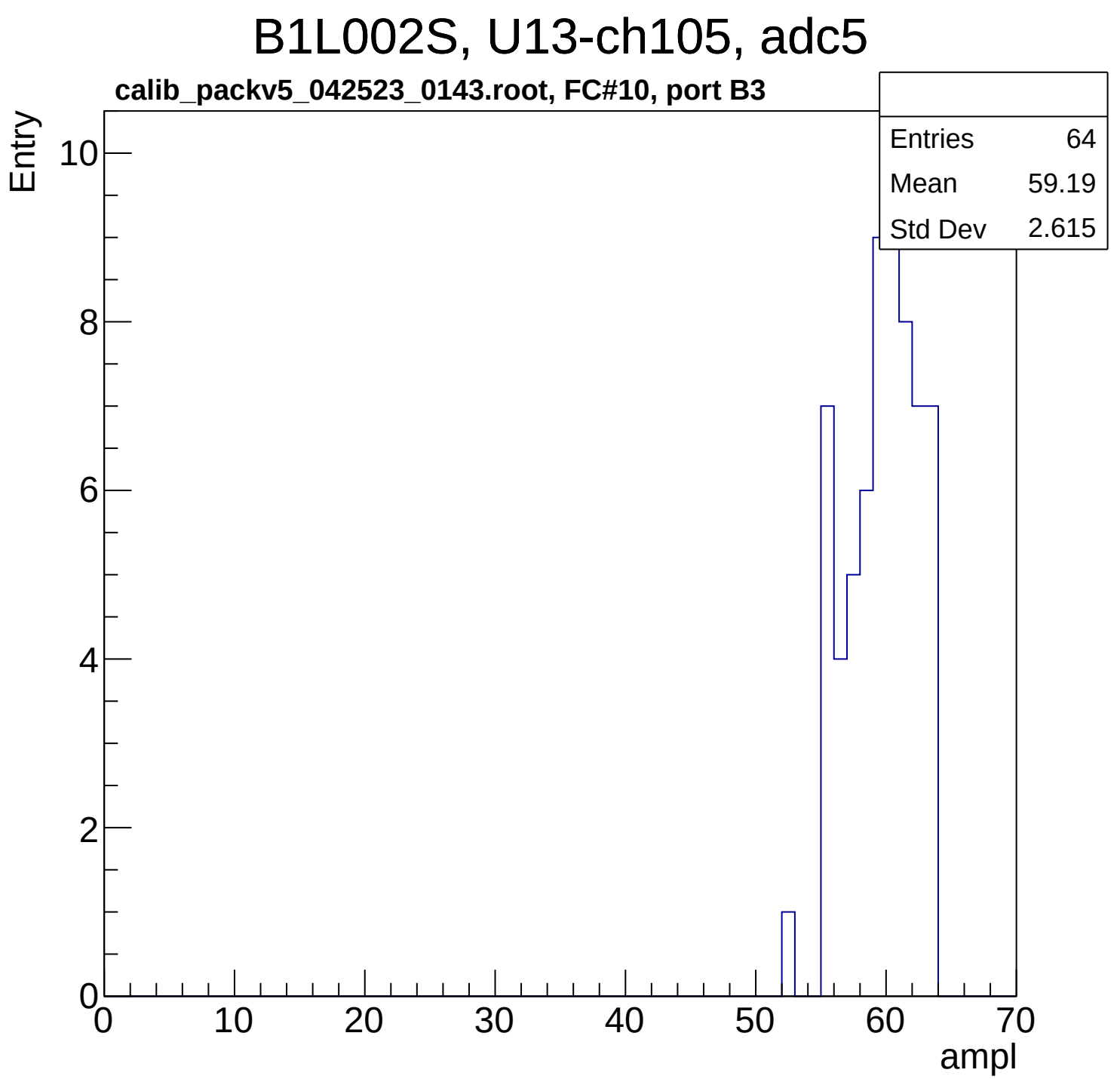
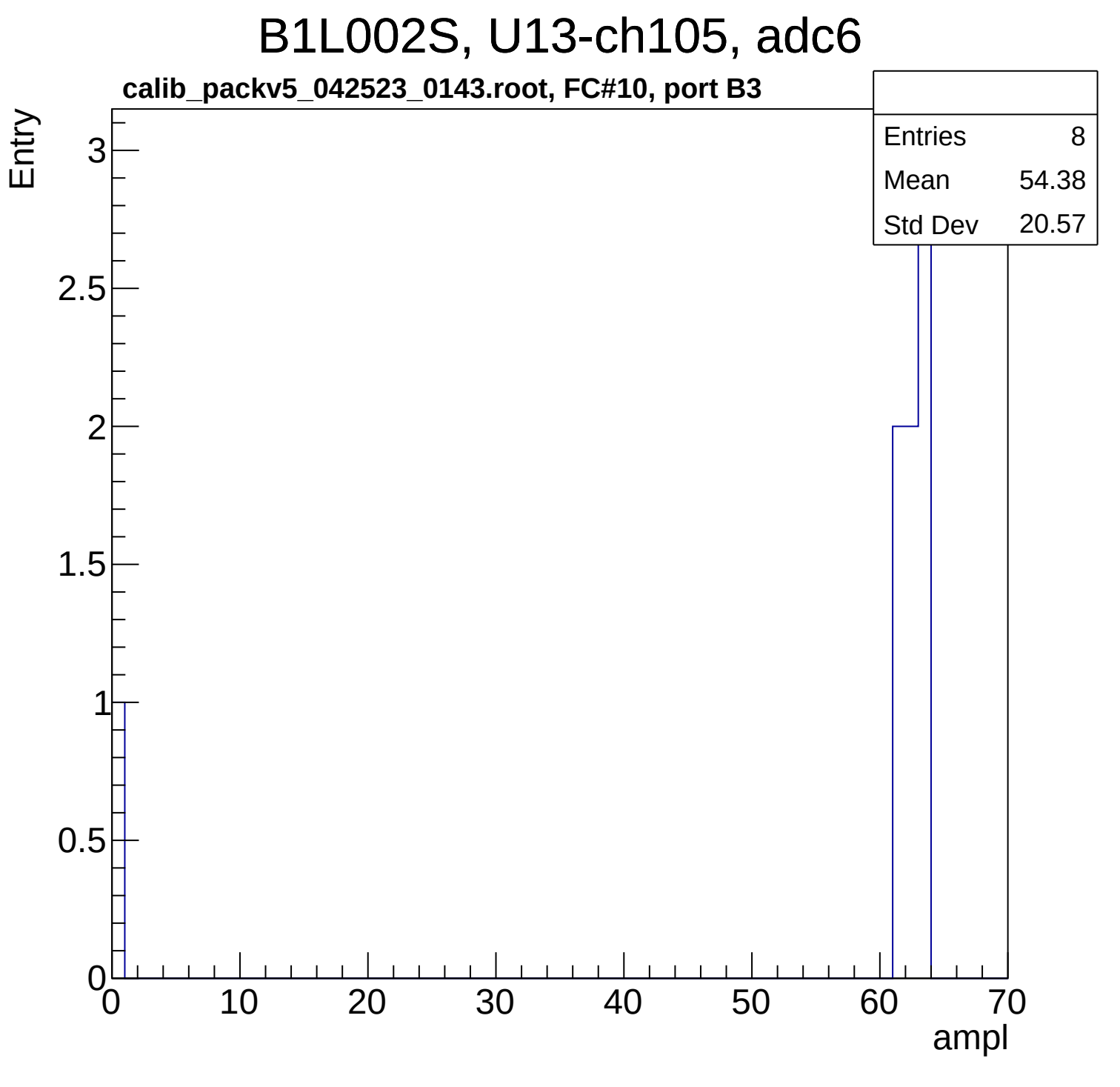
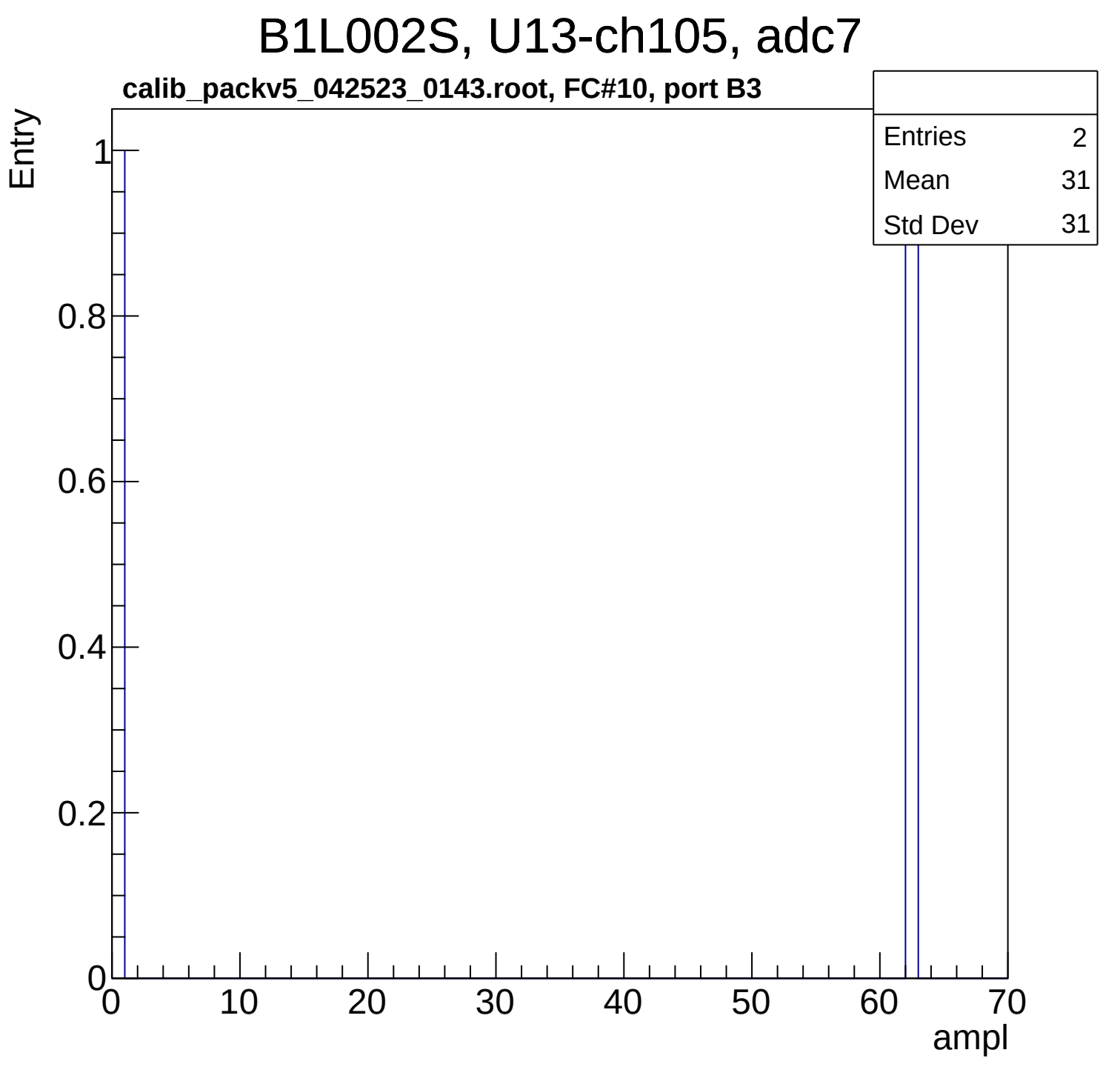

B1L002S, U13-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.95
Std Dev 3.818
Gaus mean : 41.8871
Gaus Width: 3.8601
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.17
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.57
Std Dev 2.786
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 59.19
Std Dev 2.615
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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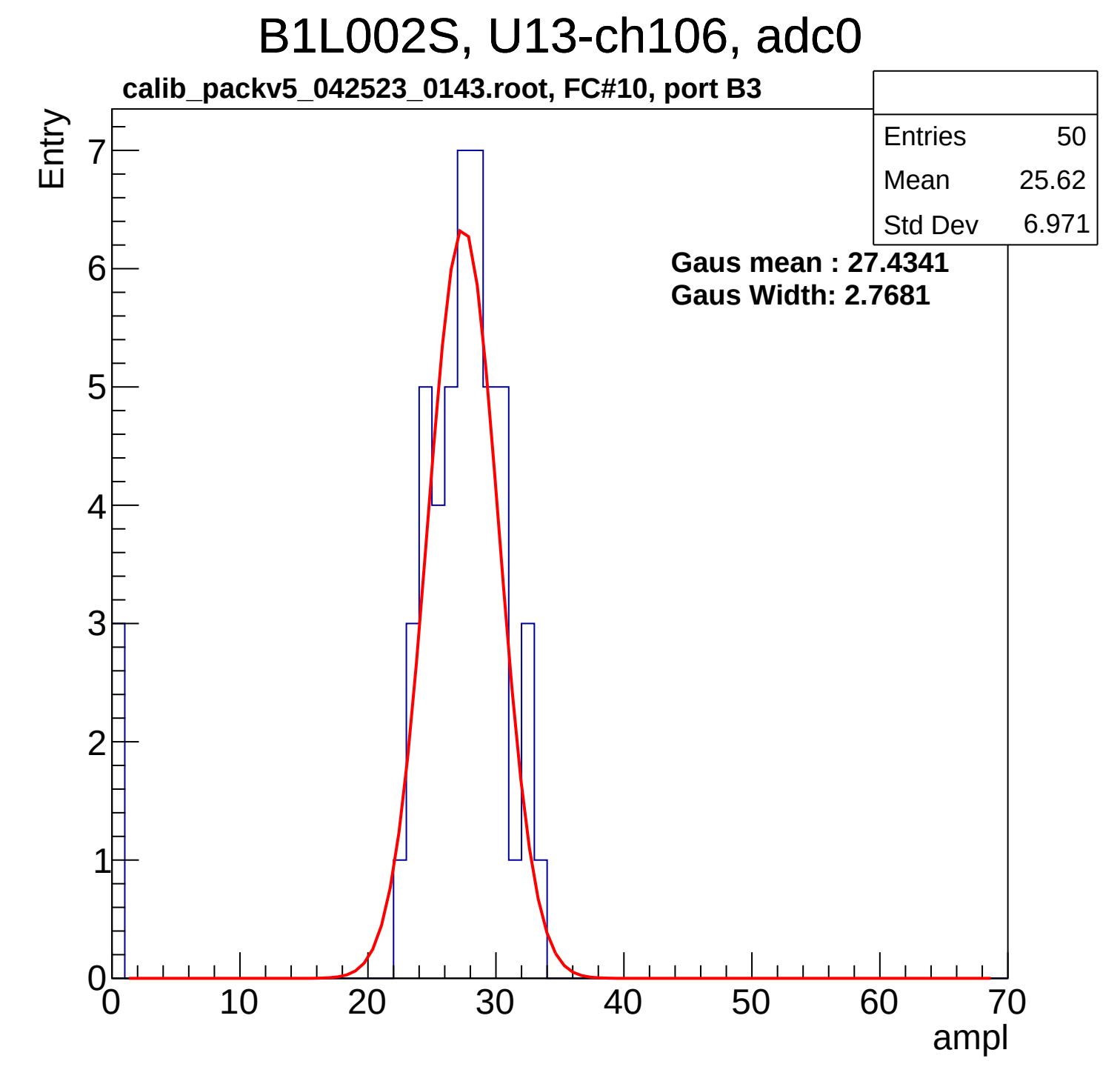
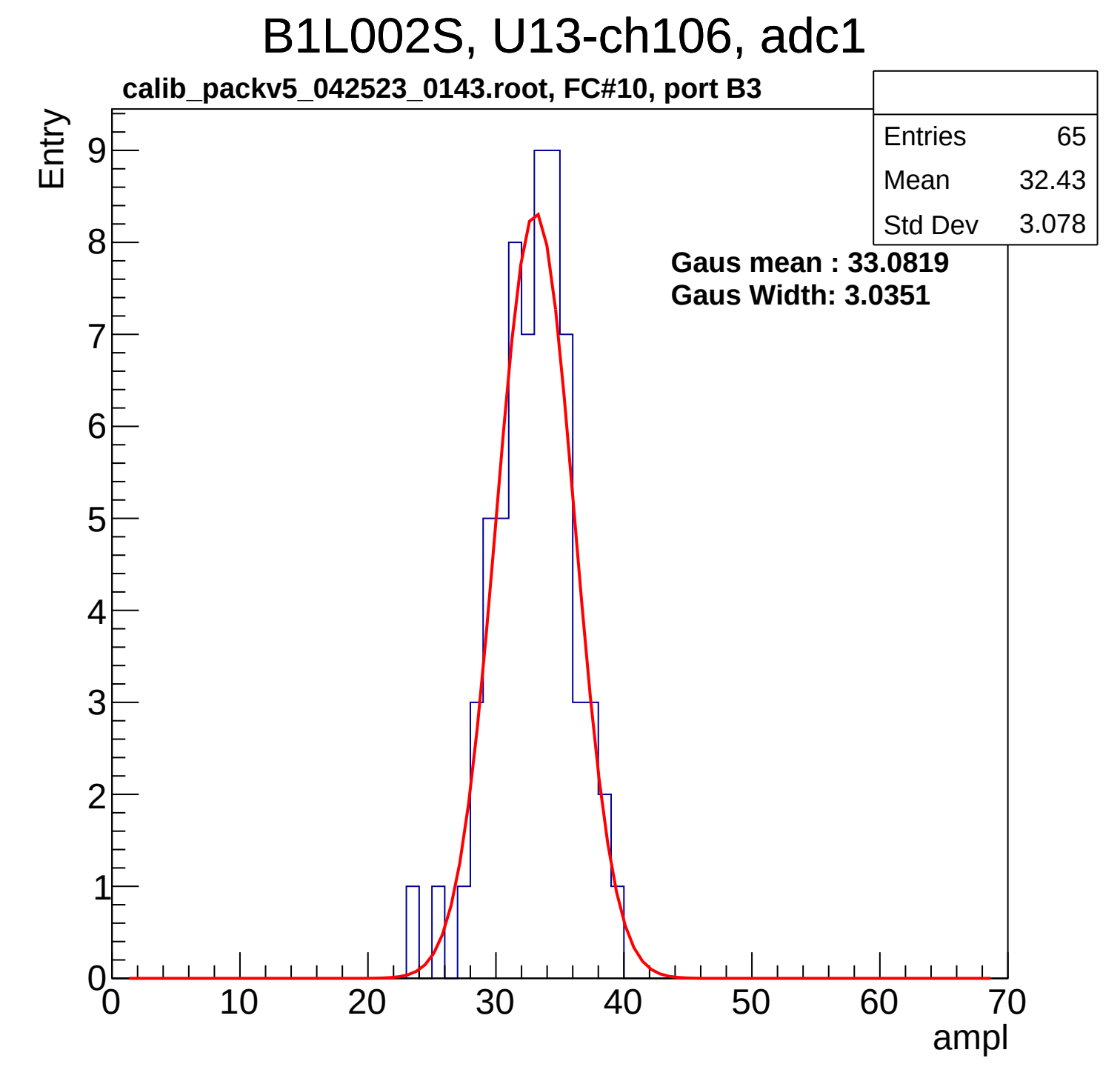
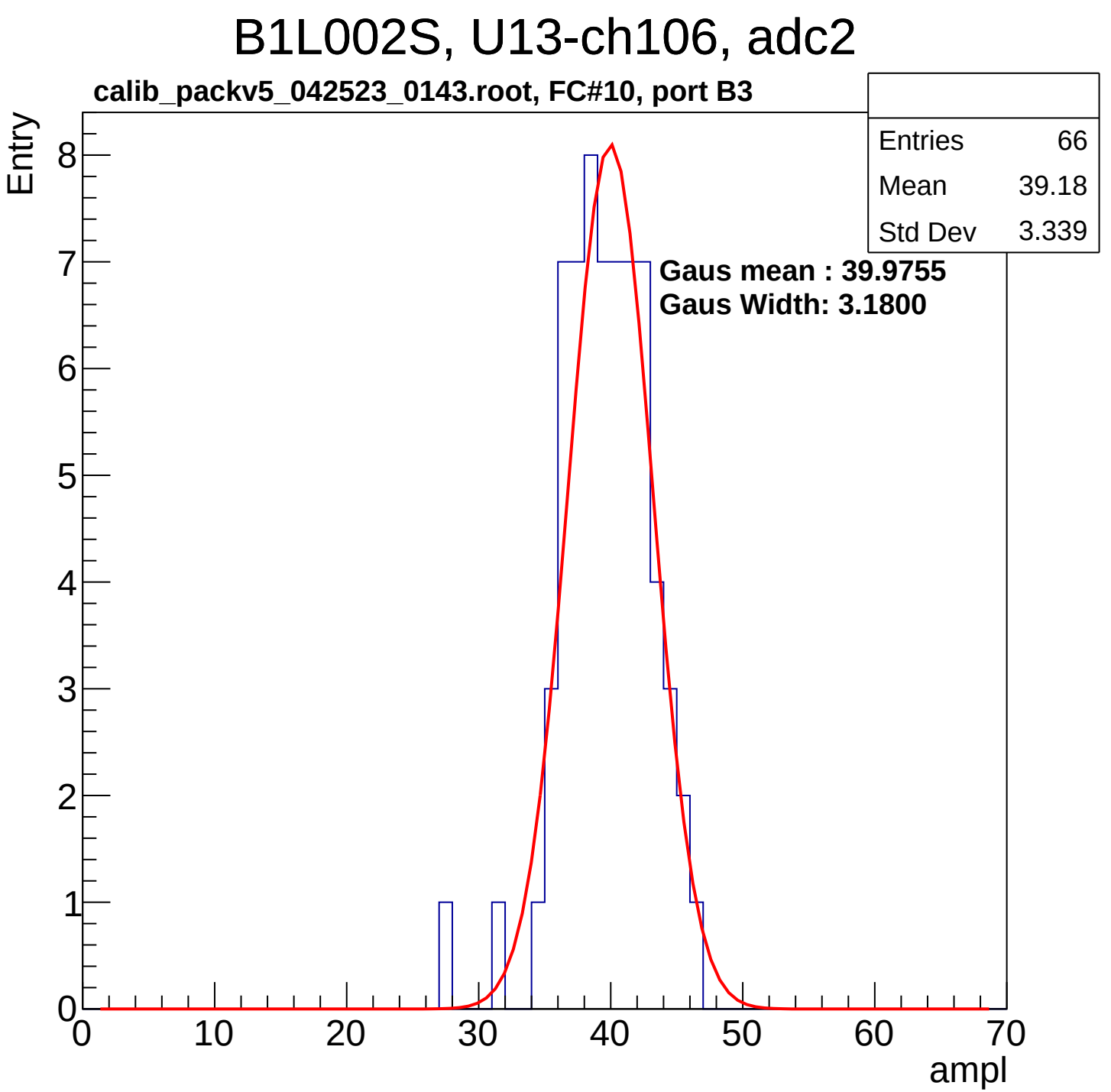
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 25.62
Std Dev 6.971
Gaus mean : 27.4341
Gaus Width: 2.7681
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 32.43
Std Dev 3.078
Gaus mean : 33.0819
Gaus Width: 3.0351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 39.18
Std Dev 3.339
Gaus mean : 39.9755
Gaus Width: 3.1800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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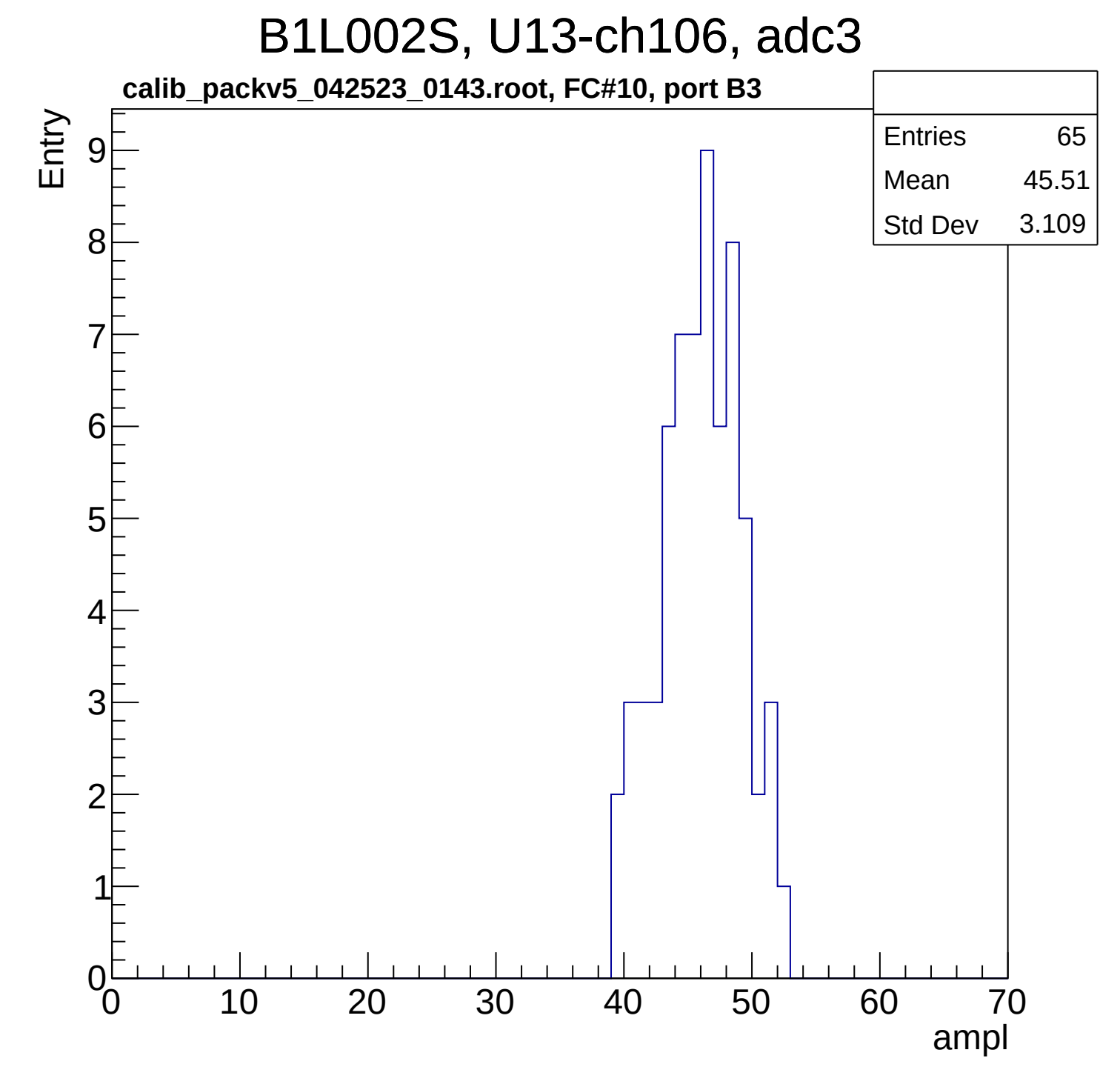
B1L002S, U13-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.51
Std Dev 3.109
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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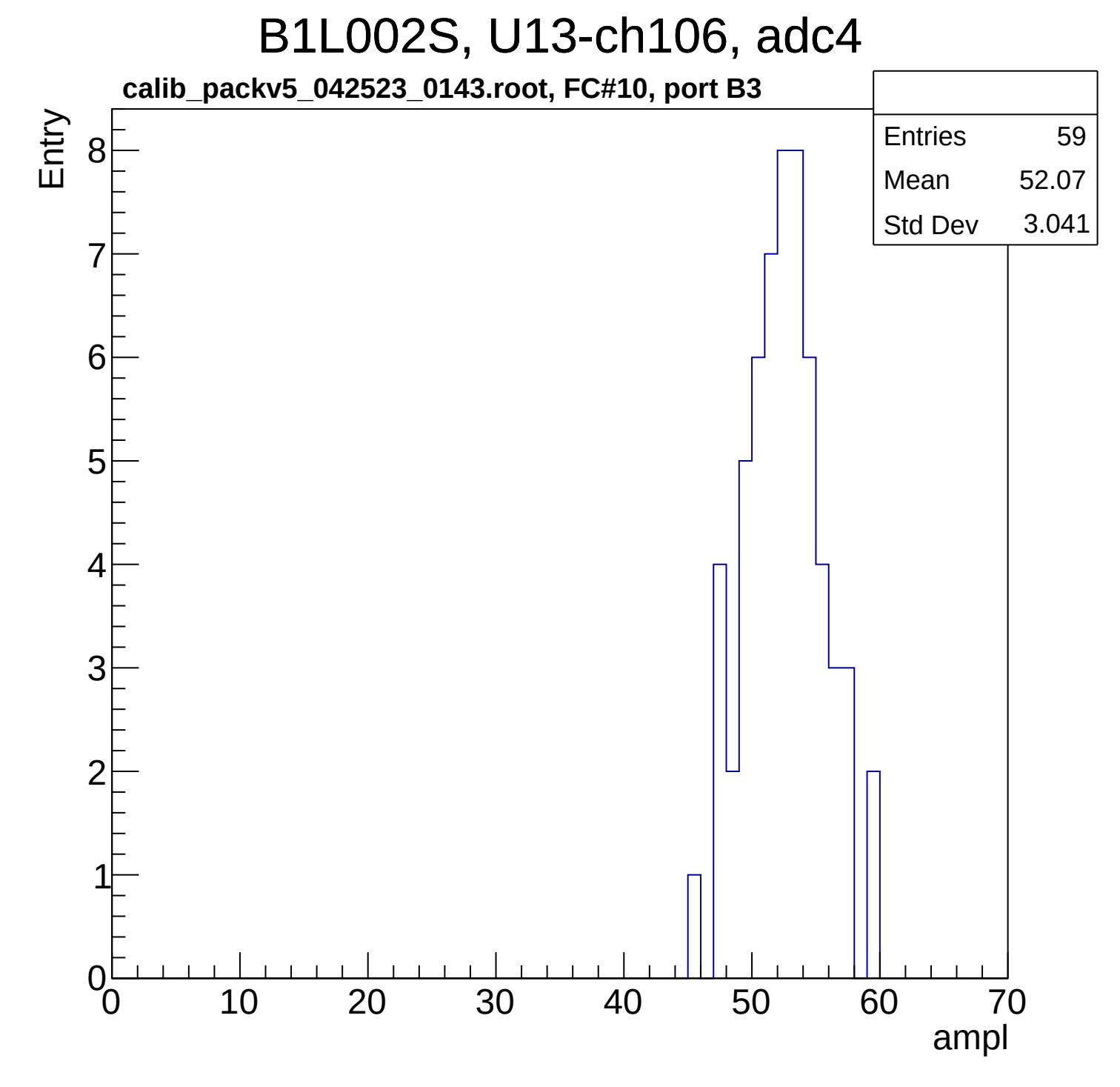
B1L002S, U13-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.07
Std Dev 3.041
Entry
ampl
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4
5
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7
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0
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40
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60
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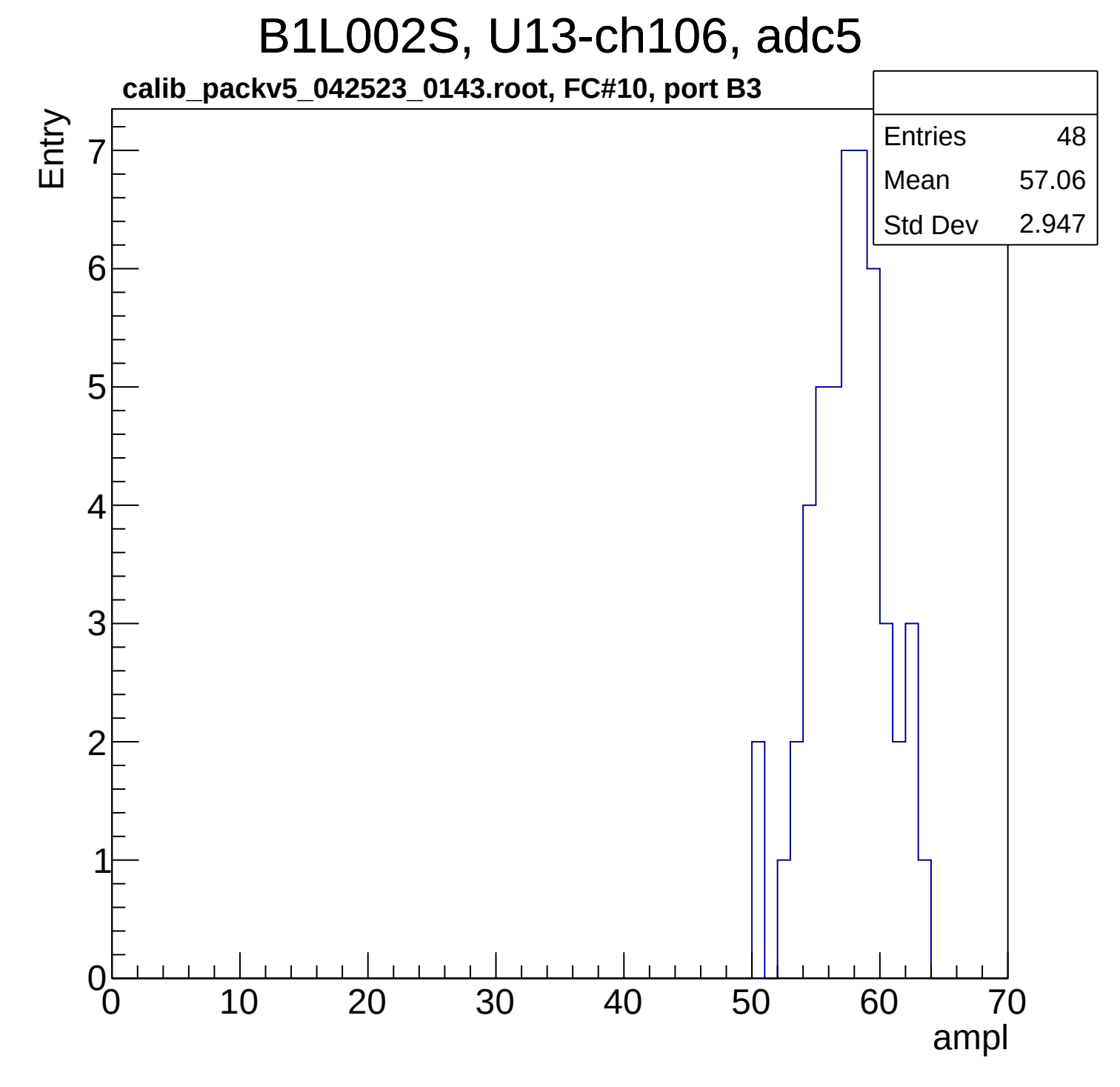
B1L002S, U13-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.06
Std Dev 2.947
Entry
ampl
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1
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3
4
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20
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40
50
60
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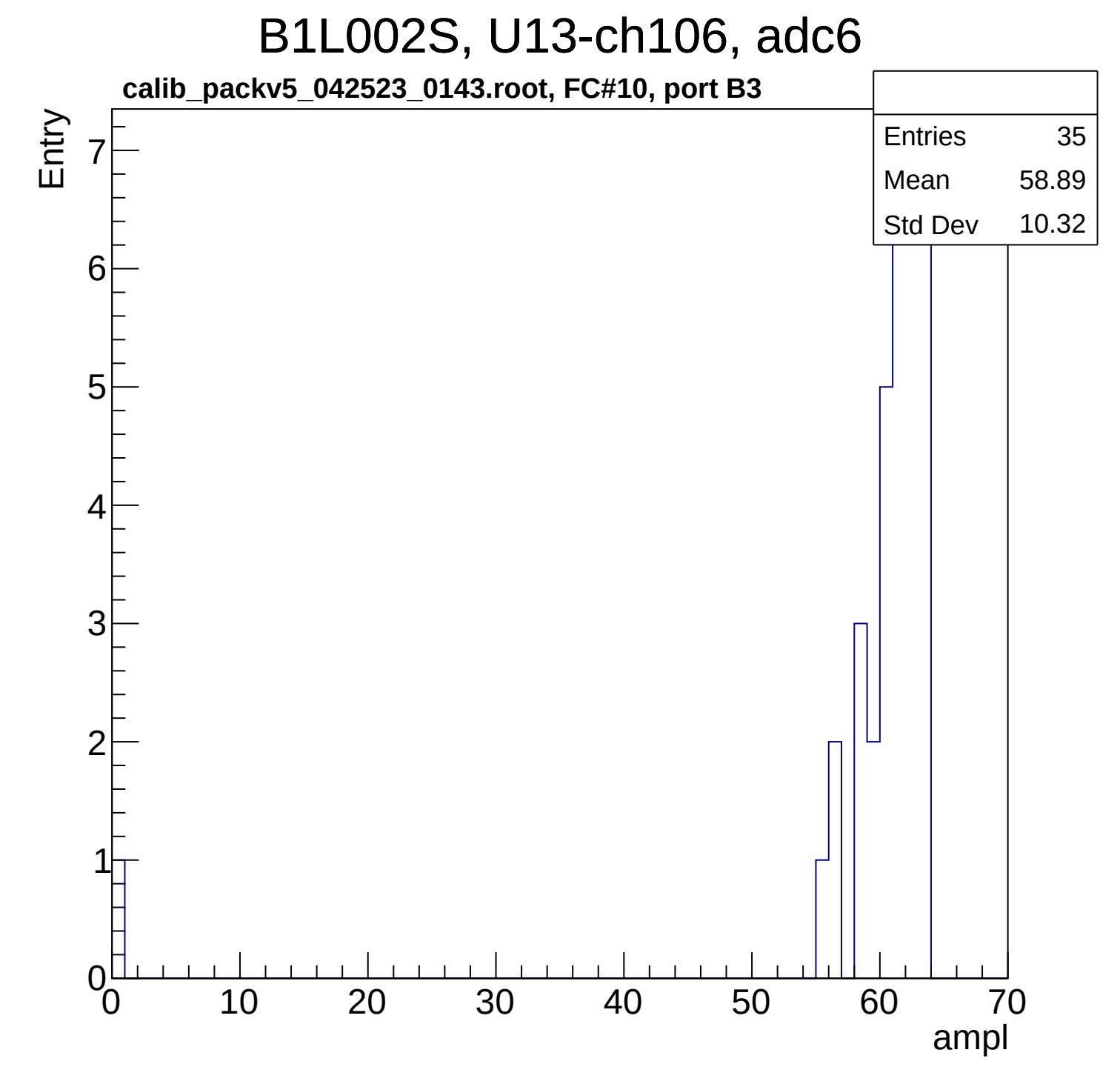
B1L002S, U13-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.89
Std Dev 10.32
Entry
ampl
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3
4
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40
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60
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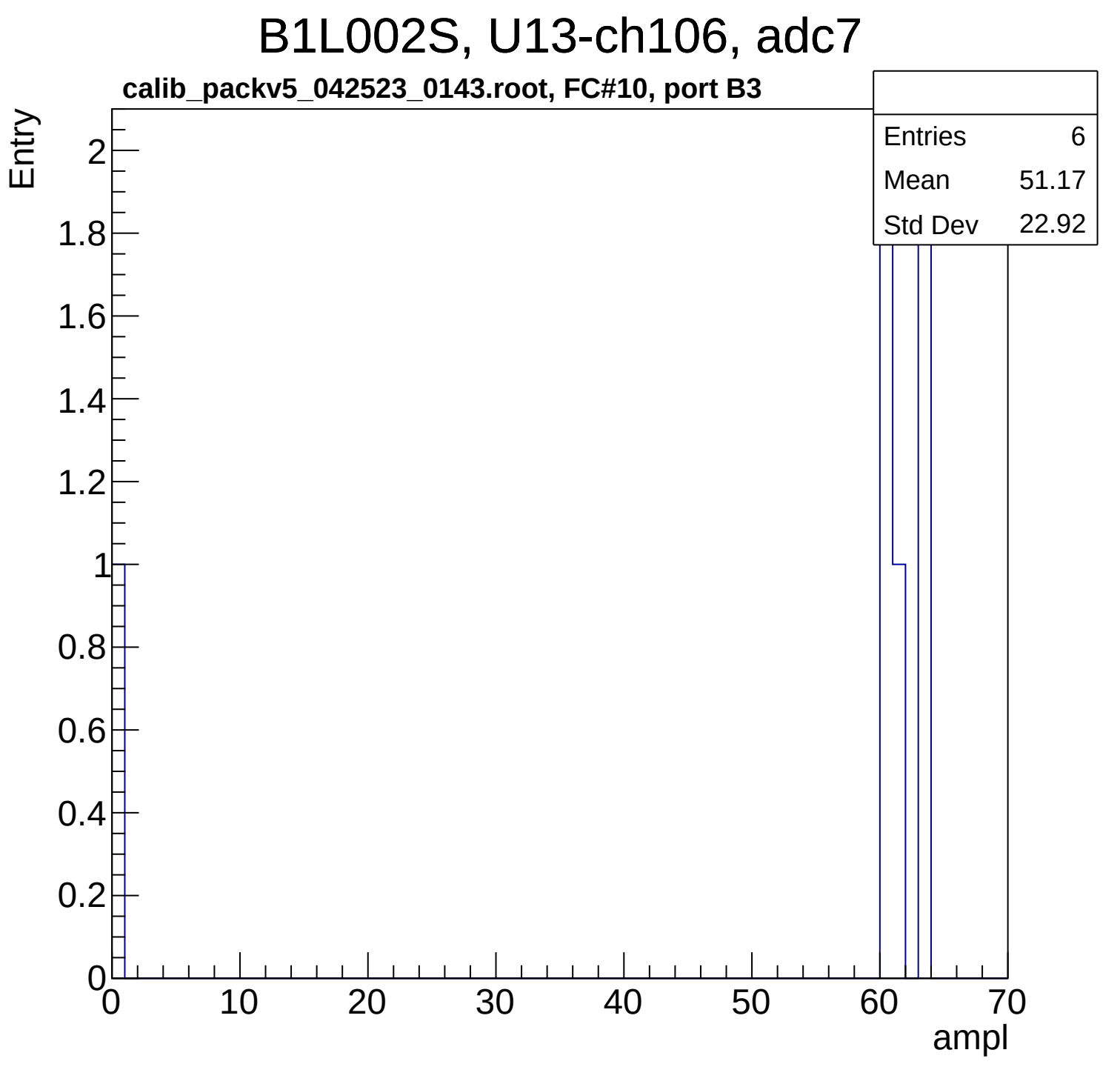
B1L002S, U13-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51.17
Std Dev 22.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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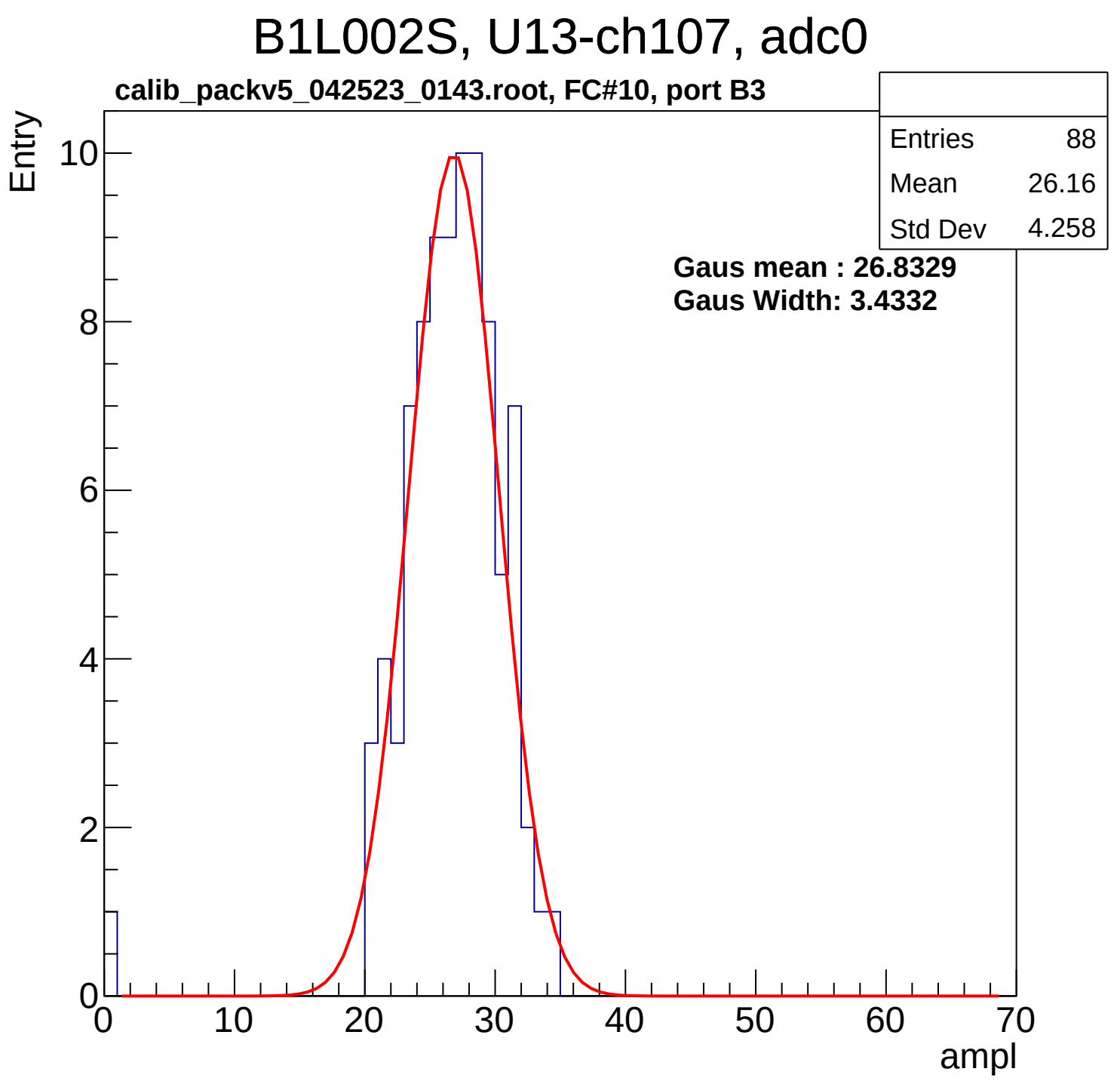
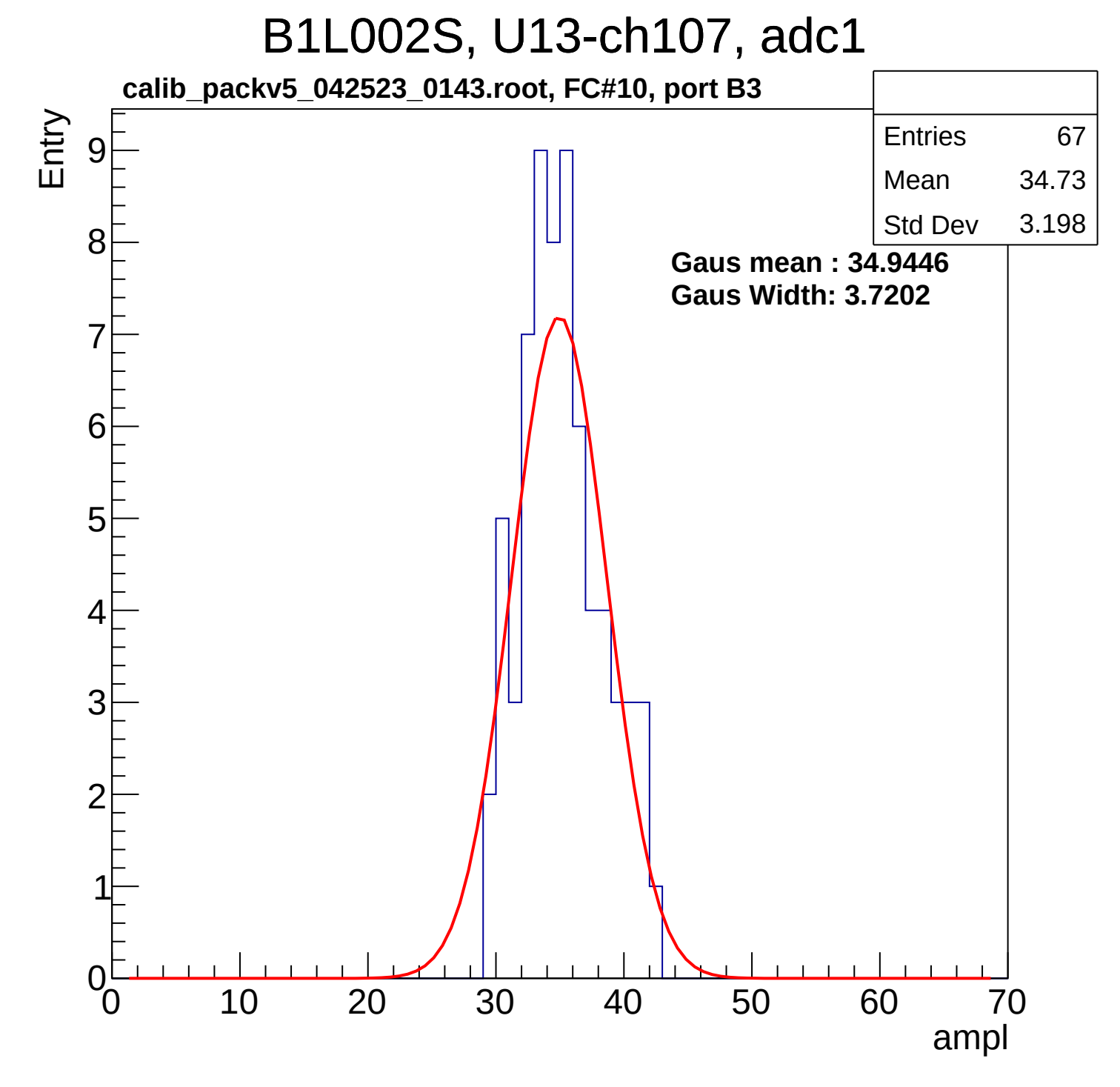
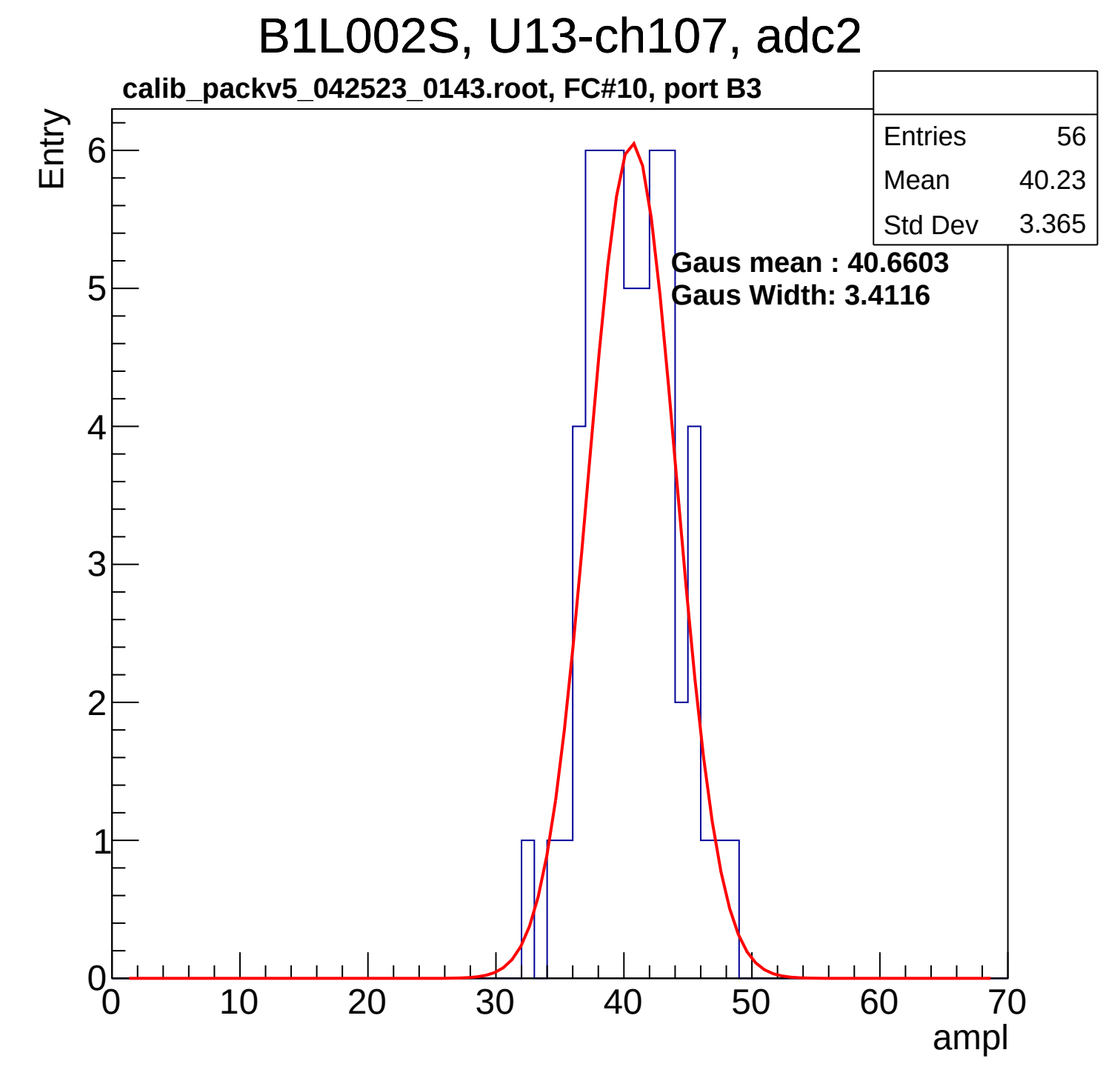
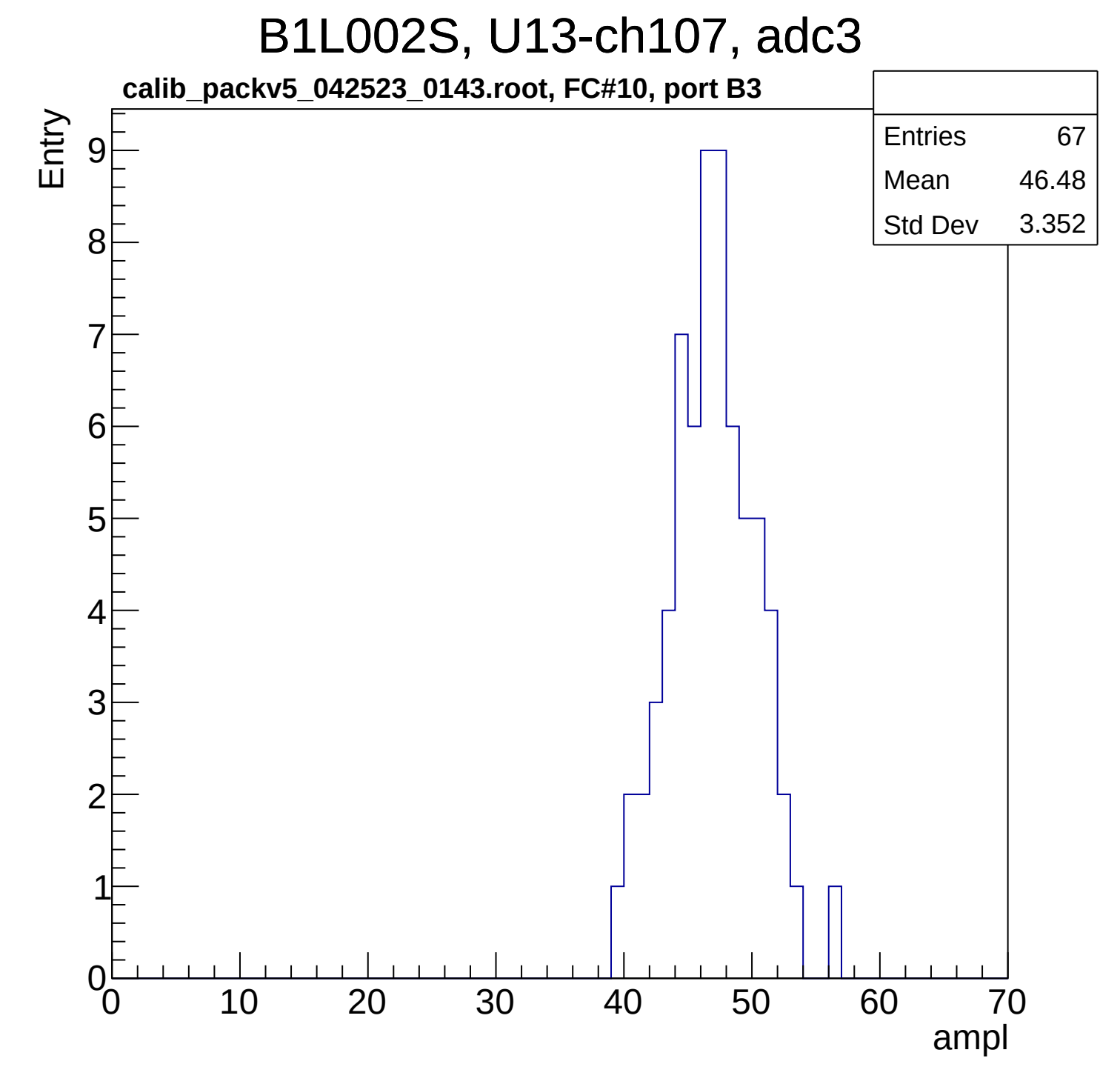

B1L002S, U13-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 88
Mean 26.16
Std Dev 4.258
Gaus mean : 26.8329
Gaus Width: 3.4332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.73
Std Dev 3.198
Gaus mean : 34.9446
Gaus Width: 3.7202
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U13-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.23
Std Dev 3.365
Gaus mean : 40.6603
Gaus Width: 3.4116
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.48
Std Dev 3.352
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
30
40
50
60
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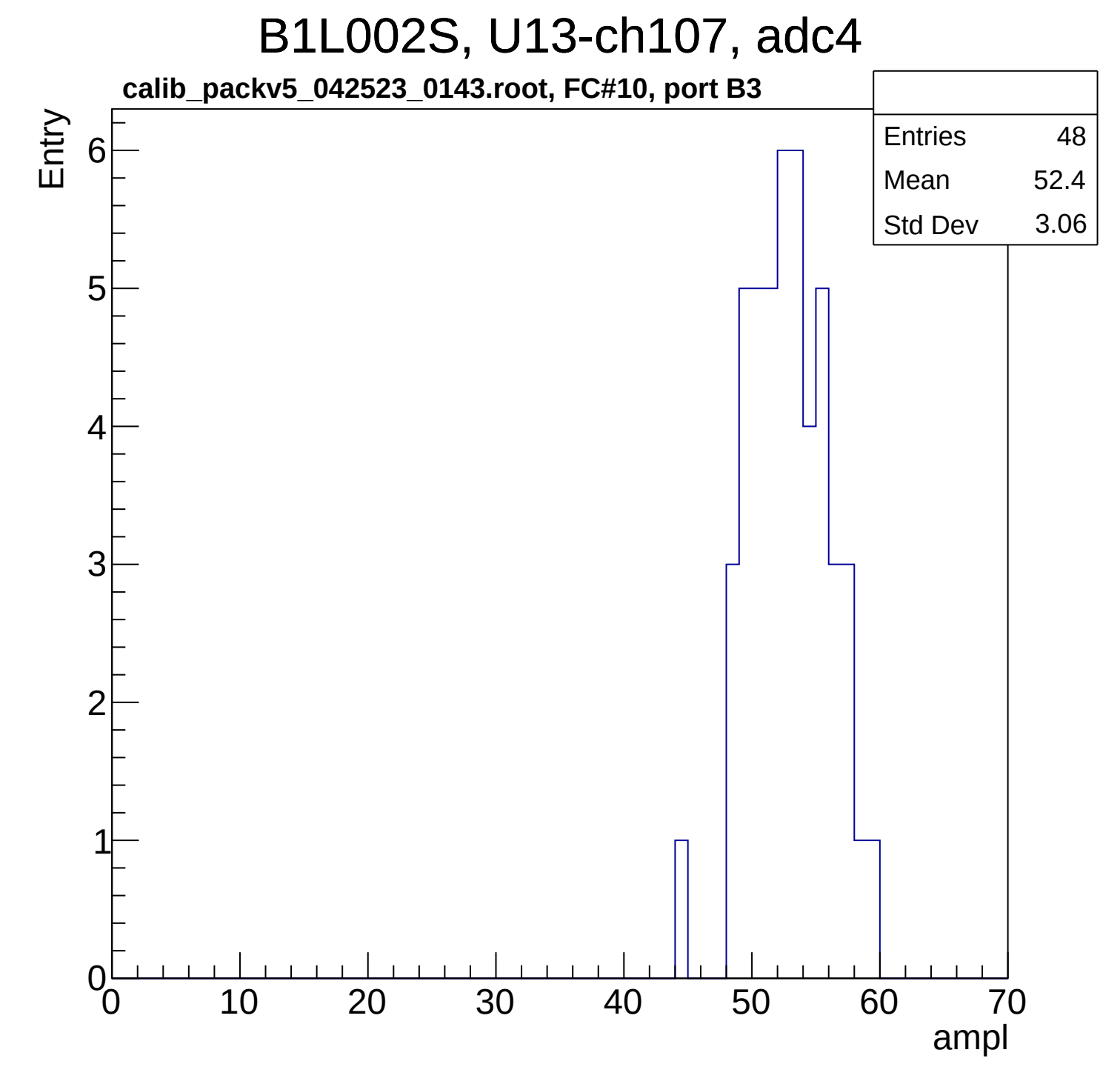
B1L002S, U13-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.4
Std Dev 3.06
Entry
0
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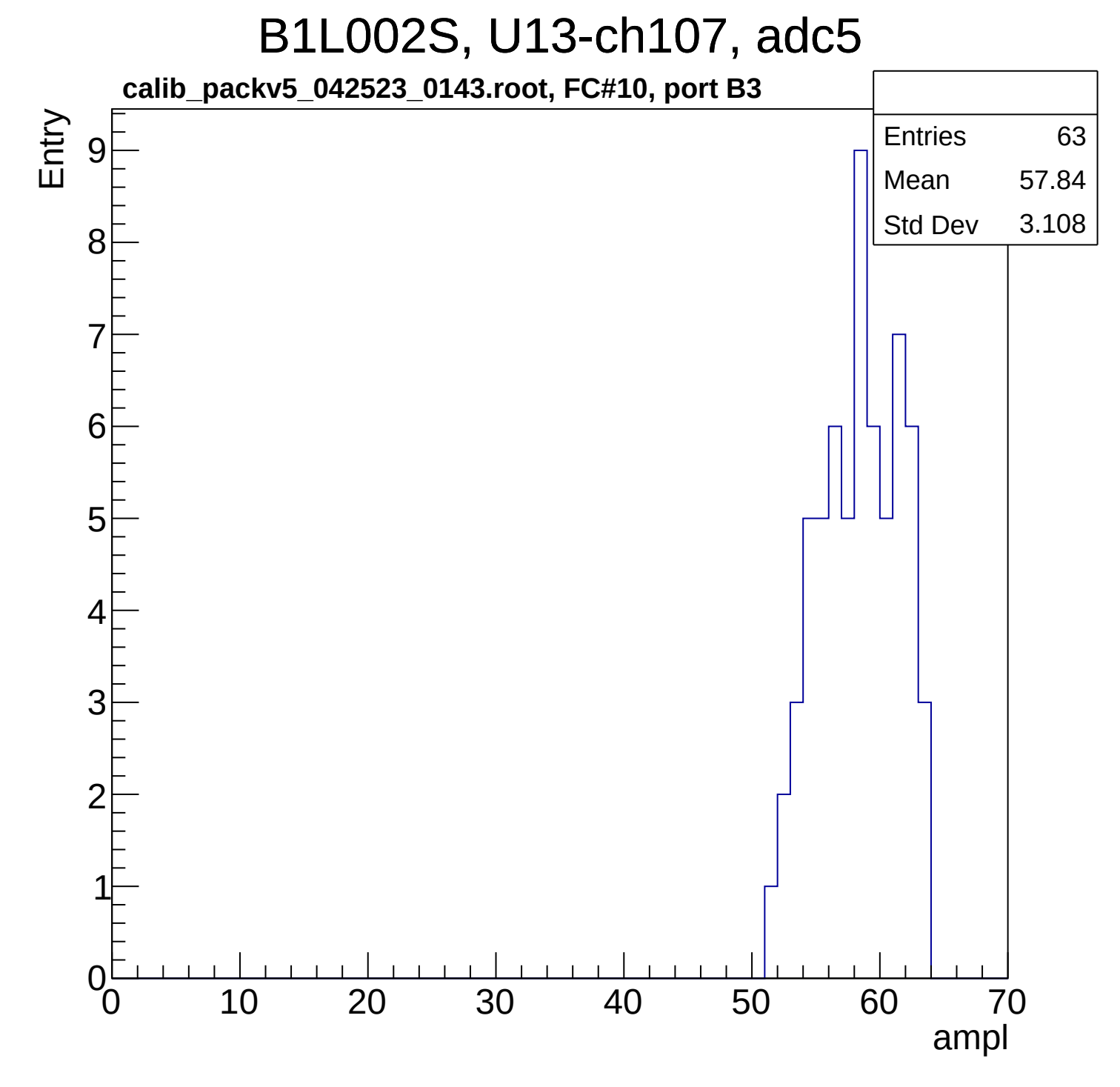
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.84
Std Dev 3.108
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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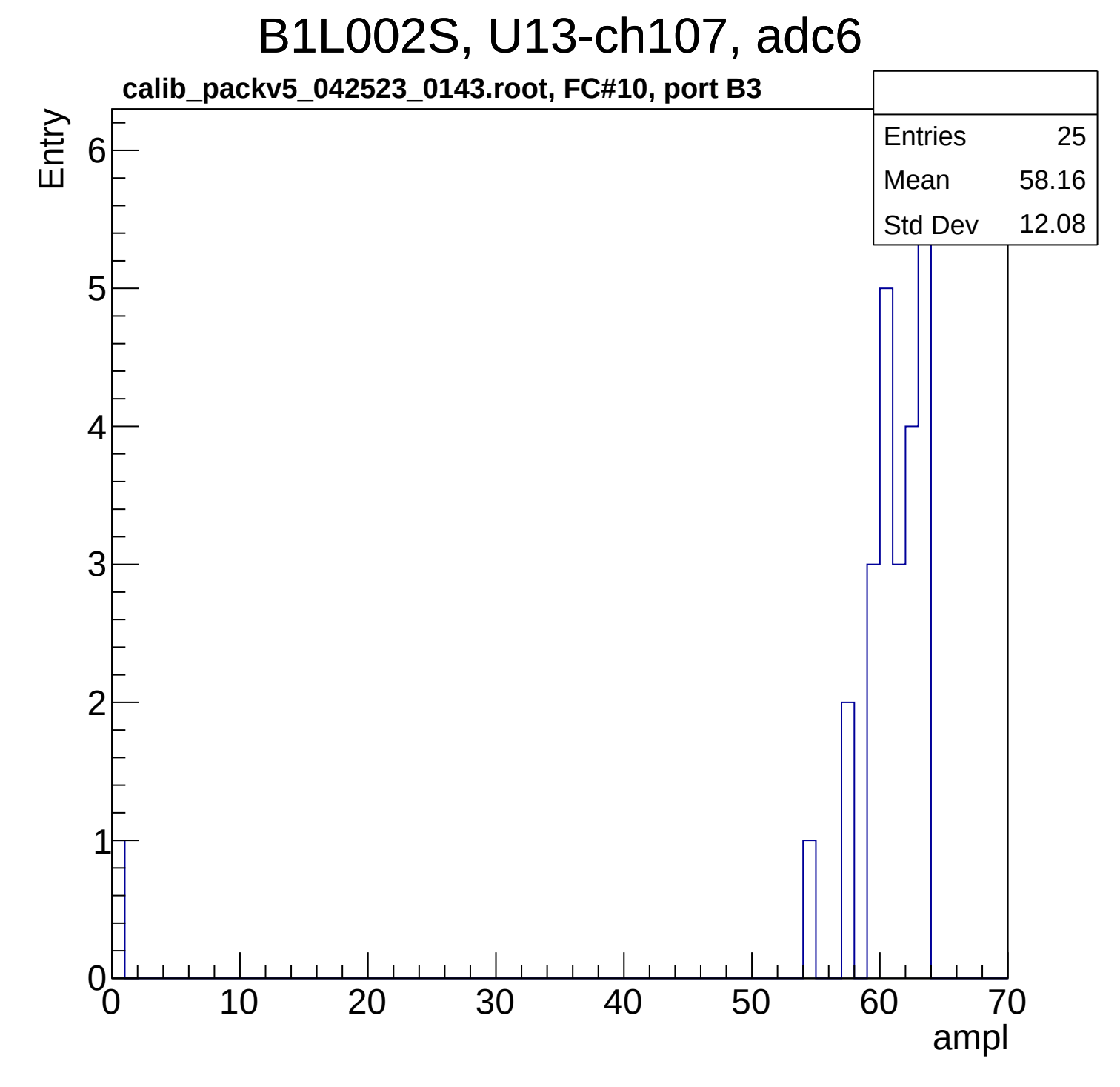
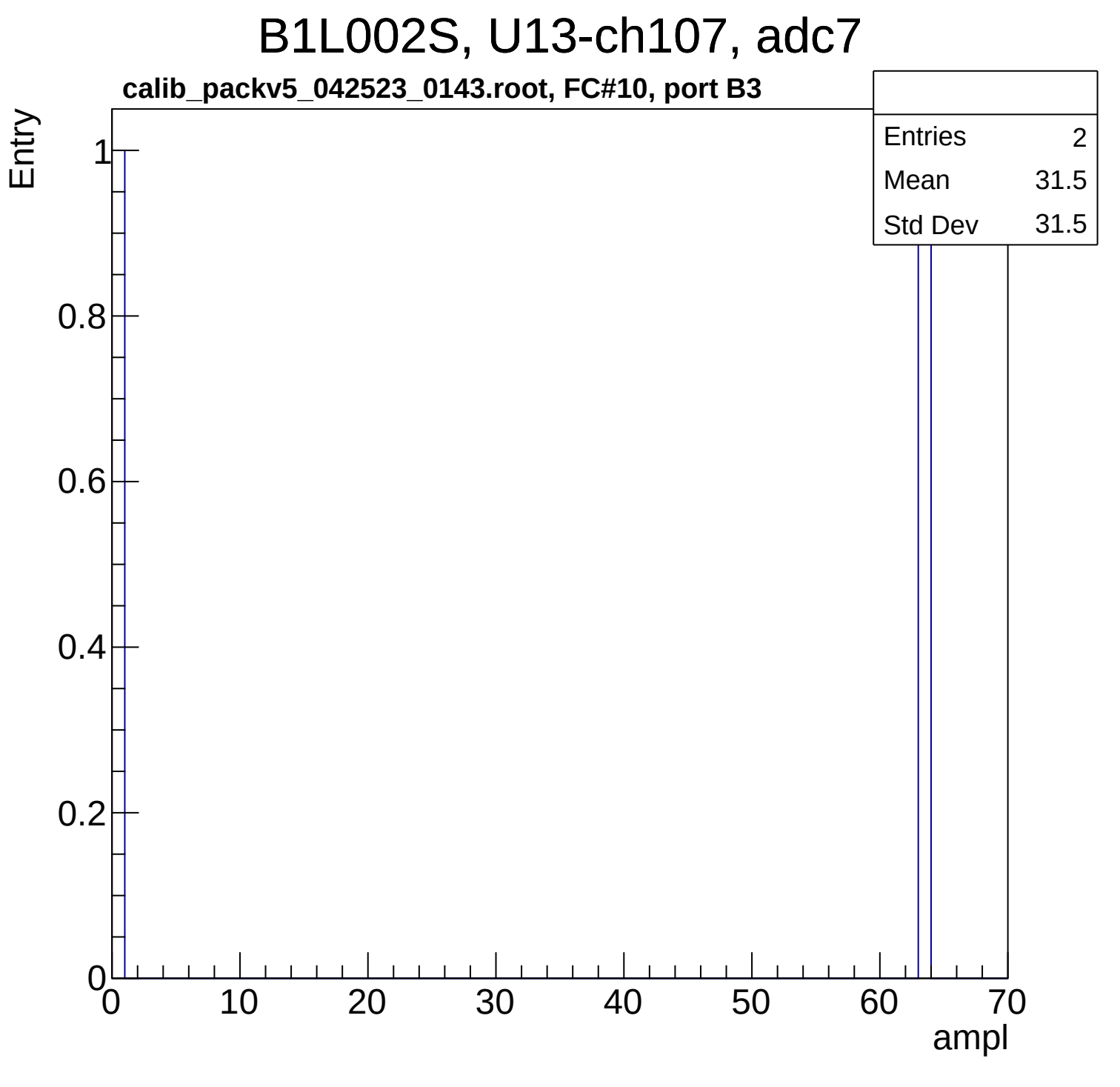

B1L002S, U13-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.16
Std Dev 12.08
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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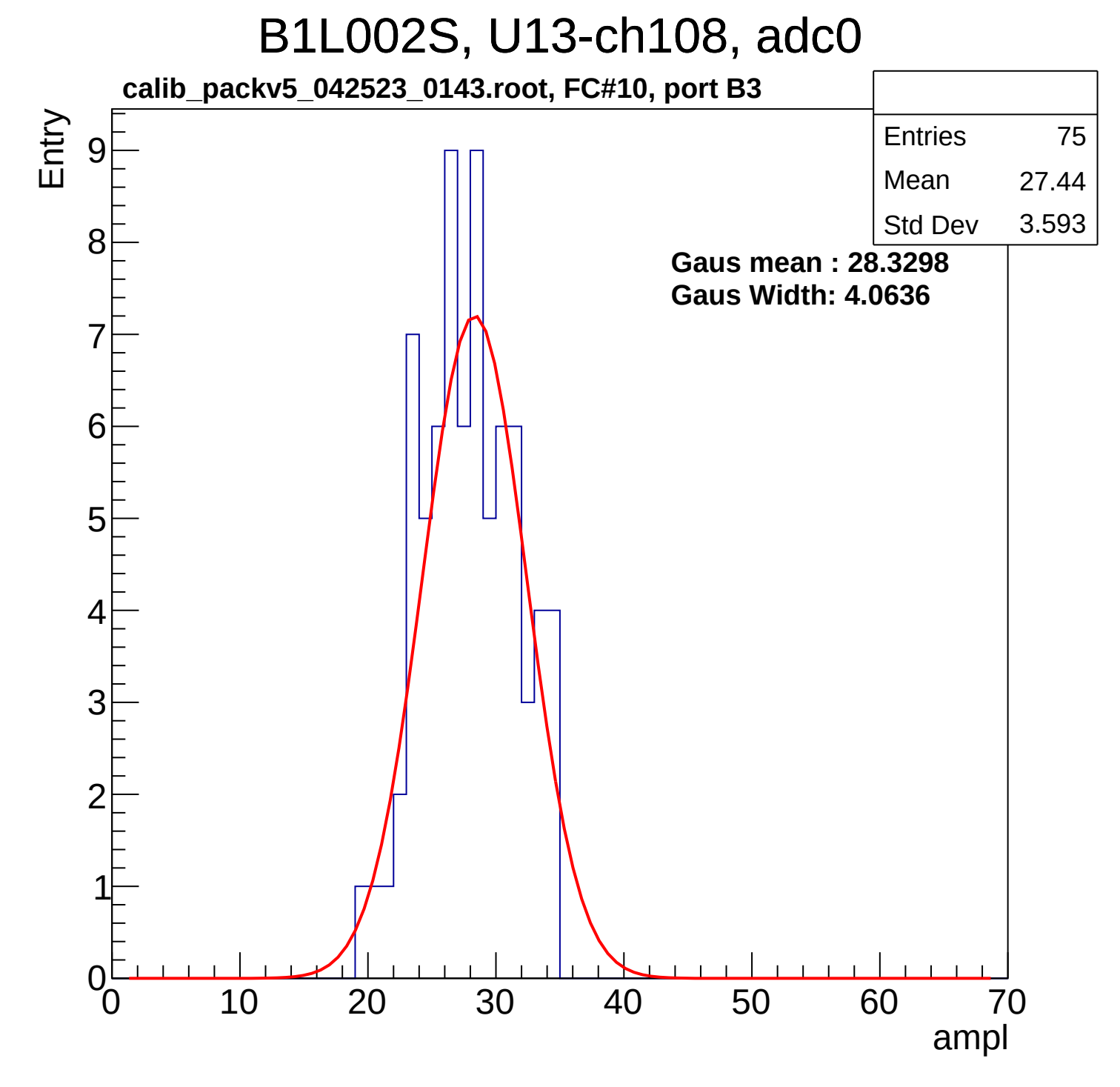
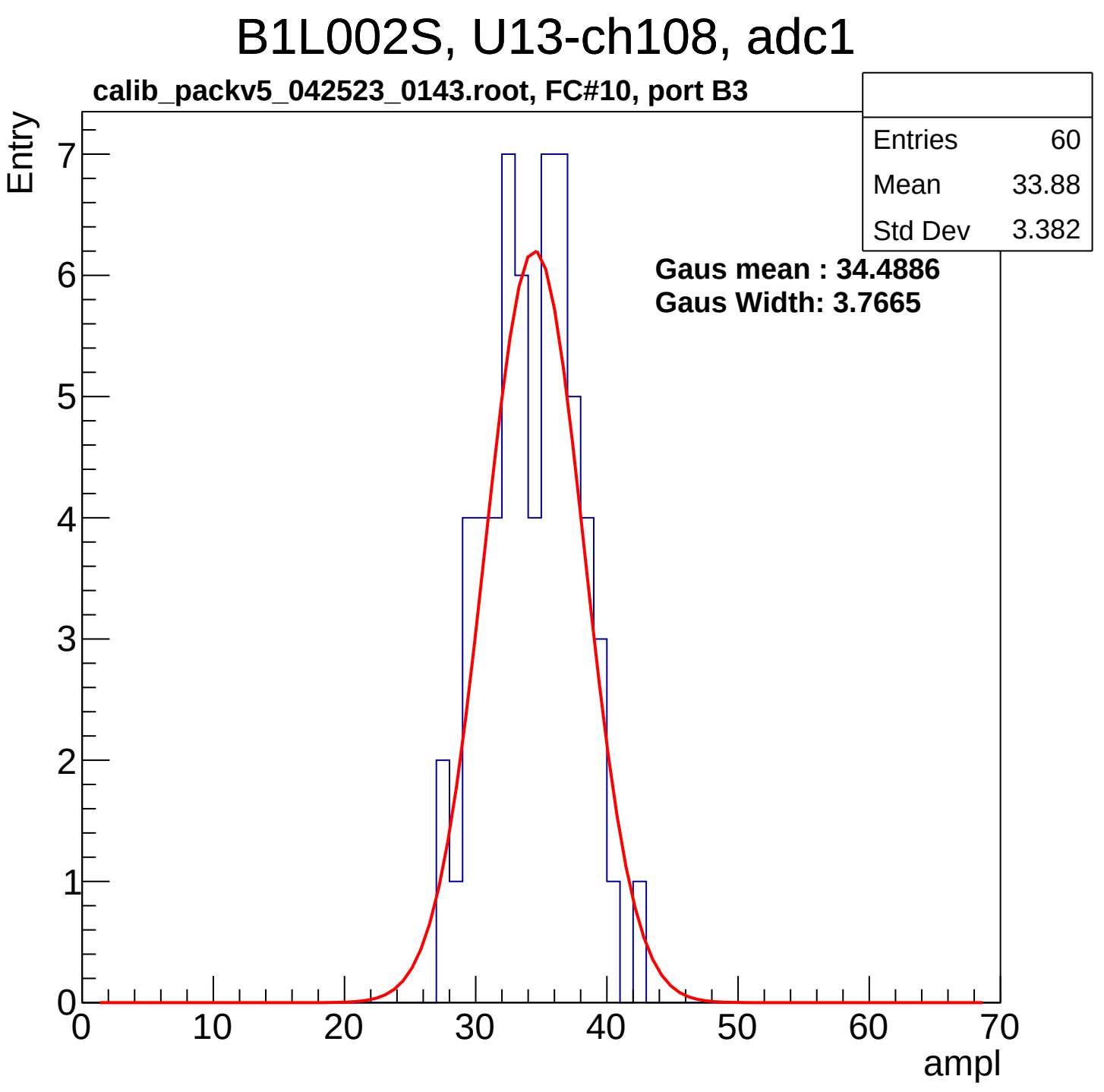
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.44
Std Dev 3.593
Gaus mean : 28.3298
Gaus Width: 4.0636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 33.88
Std Dev 3.382
Gaus mean : 34.4886
Gaus Width: 3.7665
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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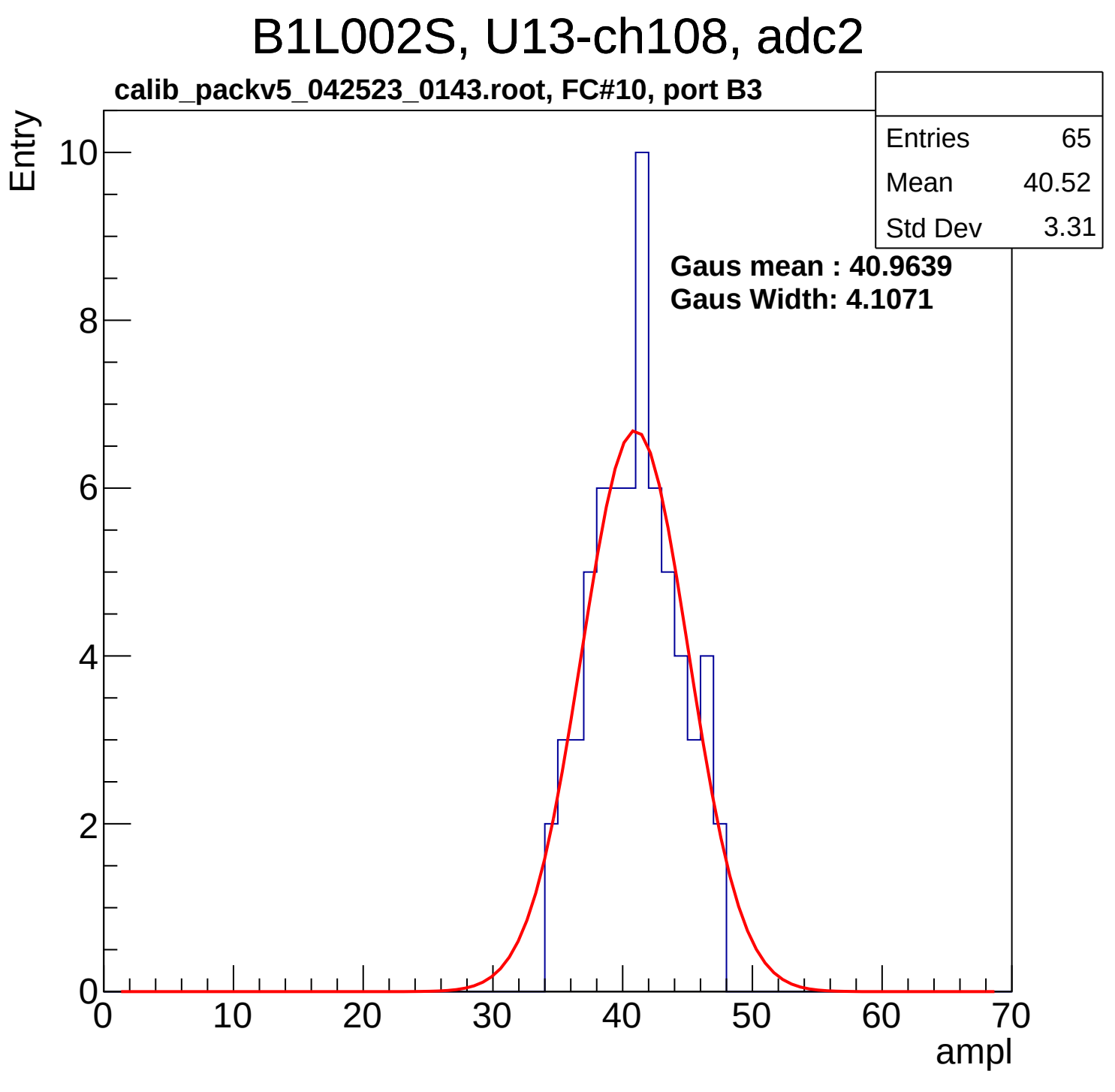
B1L002S, U13-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.52
Std Dev 3.31
Gaus mean : 40.9639
Gaus Width: 4.1071
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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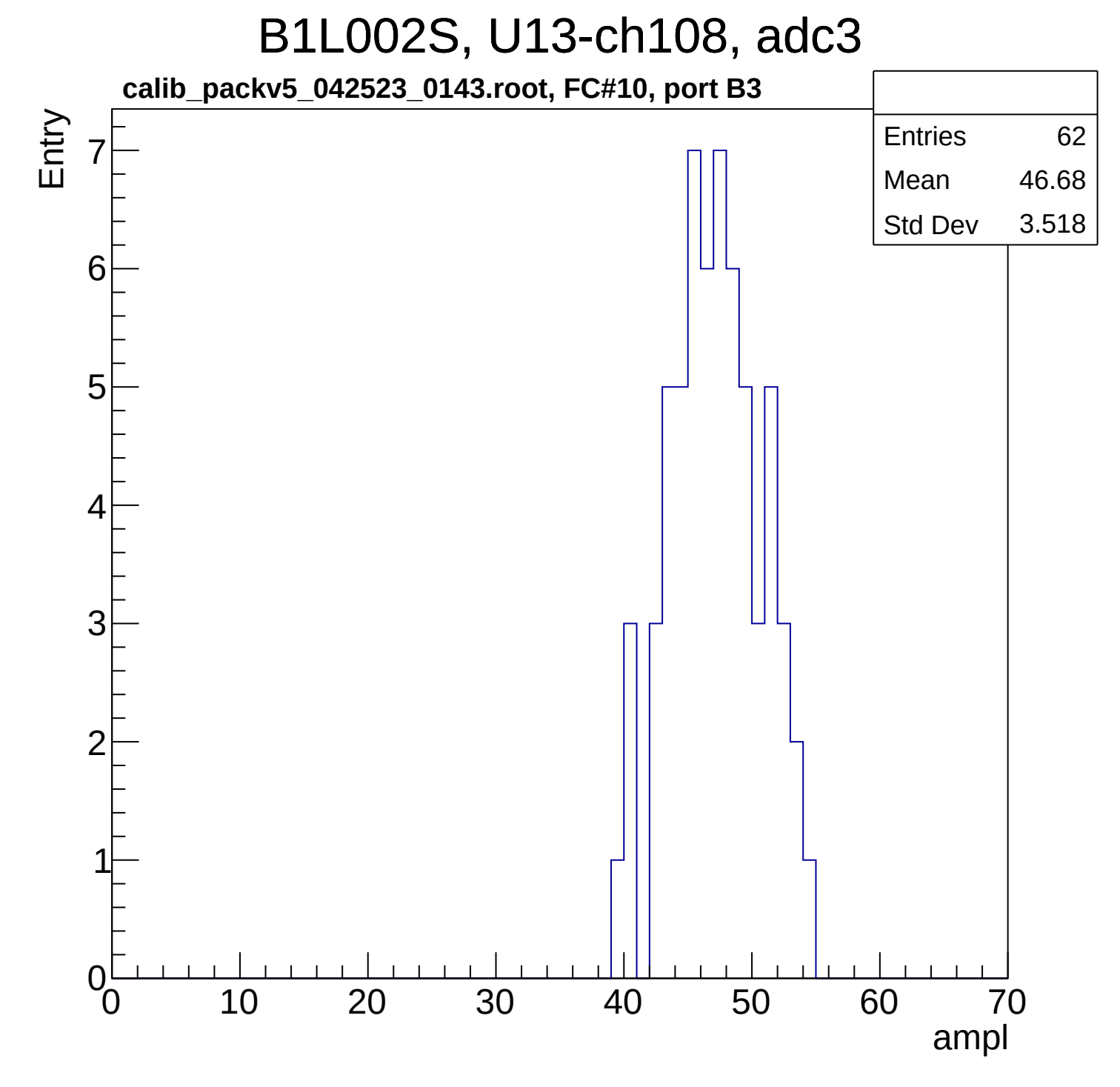
B1L002S, U13-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.68
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
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7
0
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20
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60
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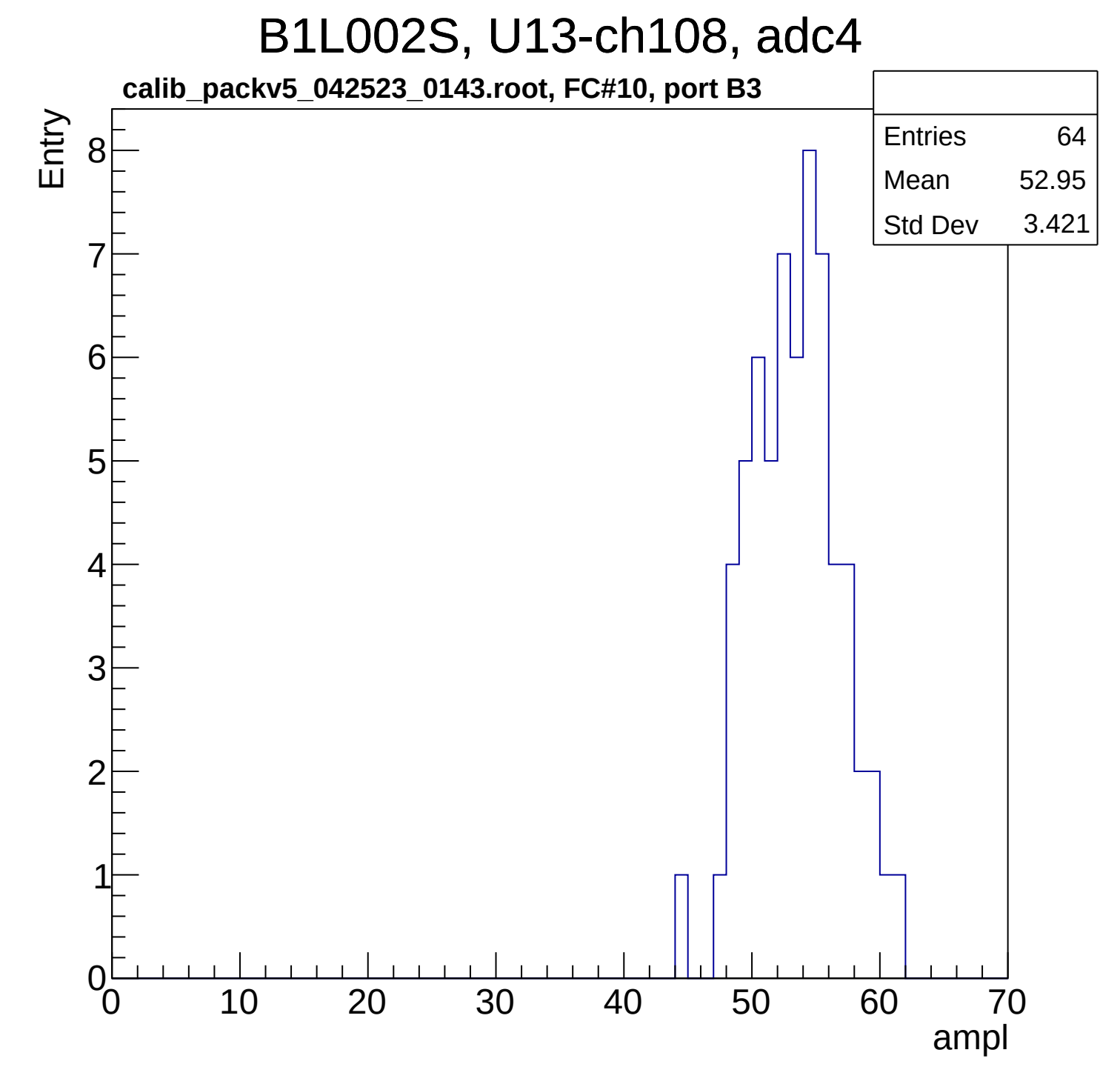
B1L002S, U13-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.95
Std Dev 3.421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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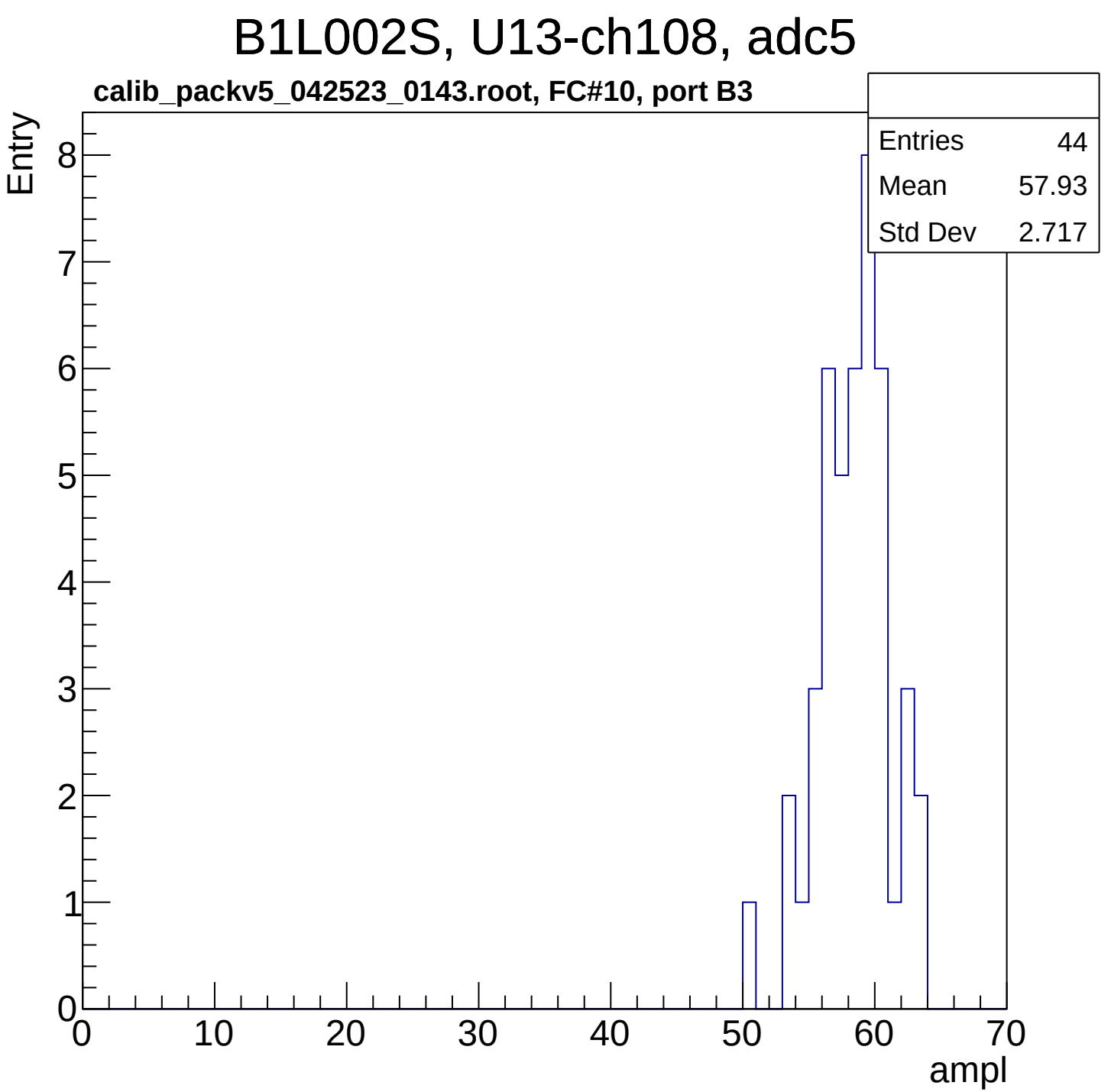
B1L002S, U13-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.93
Std Dev 2.717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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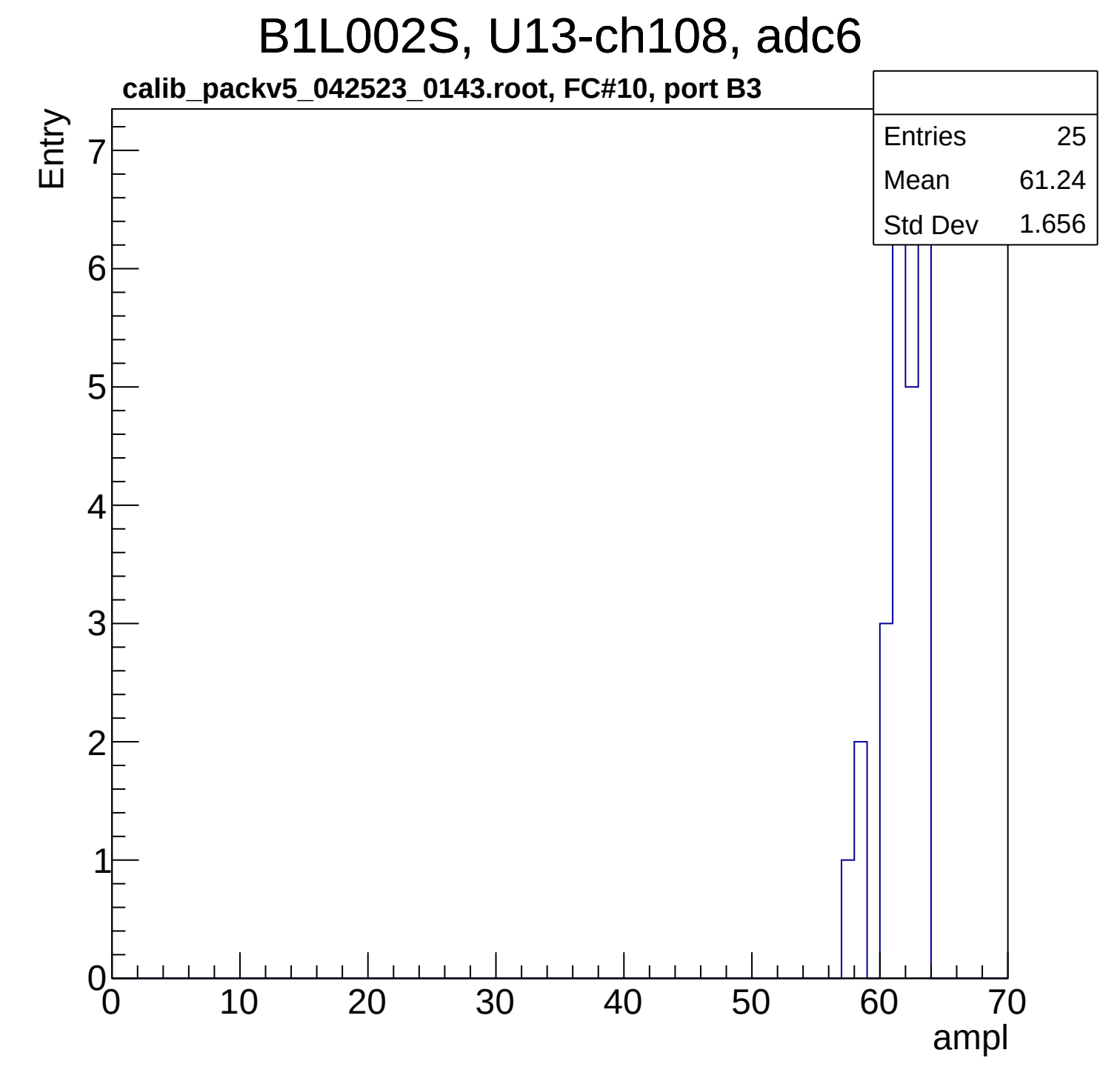
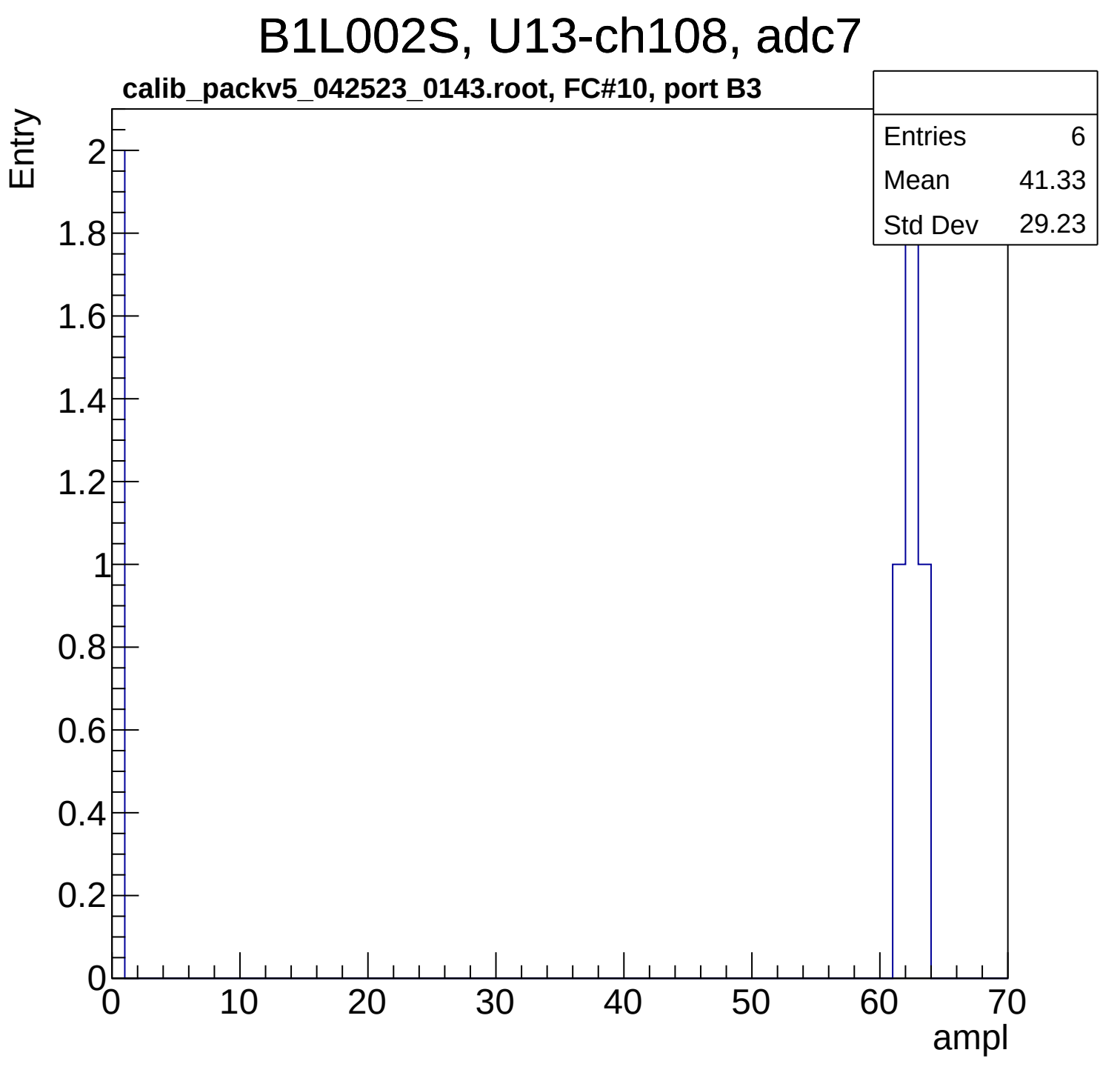

B1L002S, U13-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.24
Std Dev 1.656
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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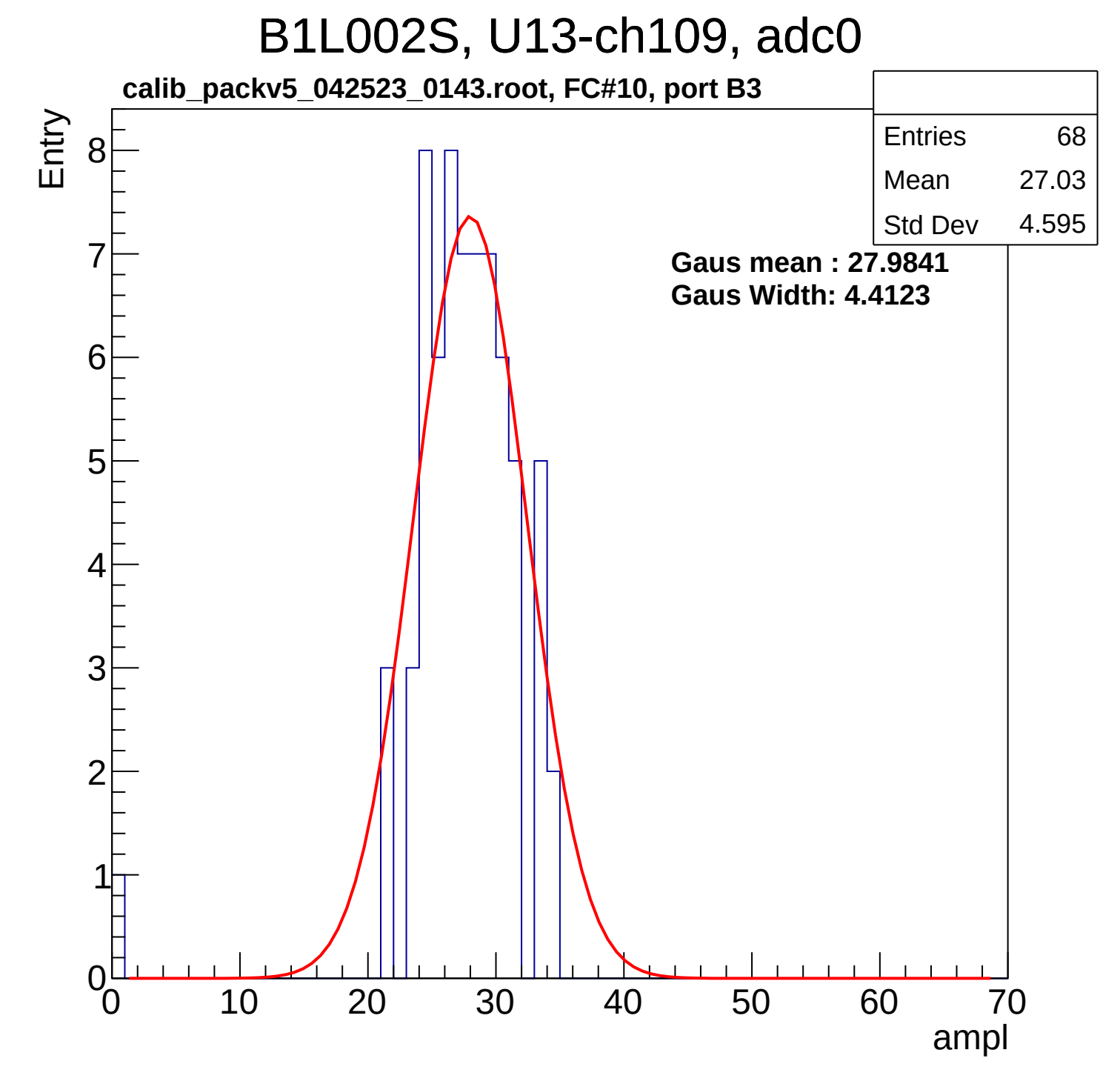
B1L002S, U13-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.03
Std Dev 4.595
Gaus mean : 27.9841
Gaus Width: 4.4123
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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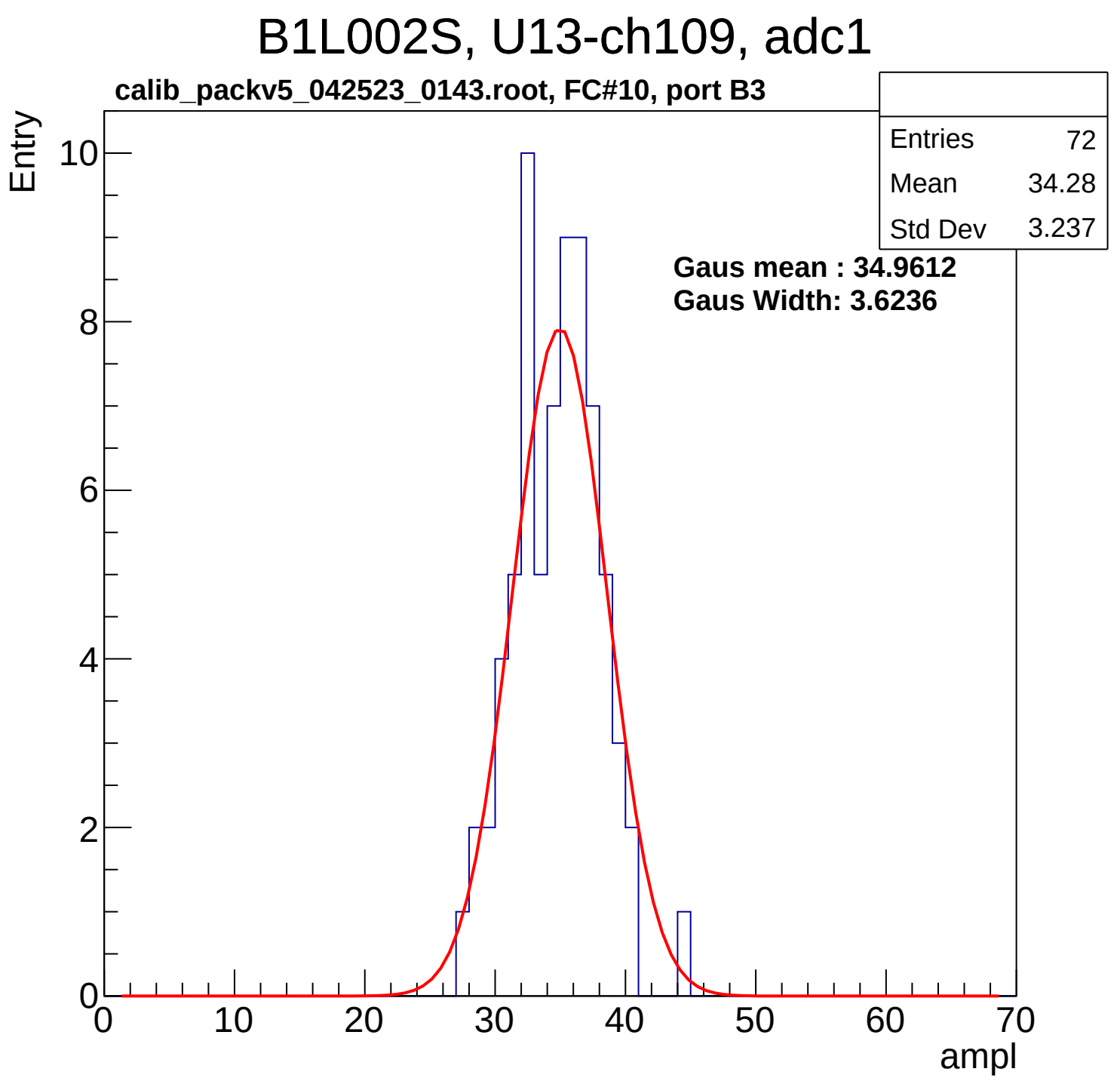
B1L002S, U13-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.28
Std Dev 3.237
Gaus mean : 34.9612
Gaus Width: 3.6236
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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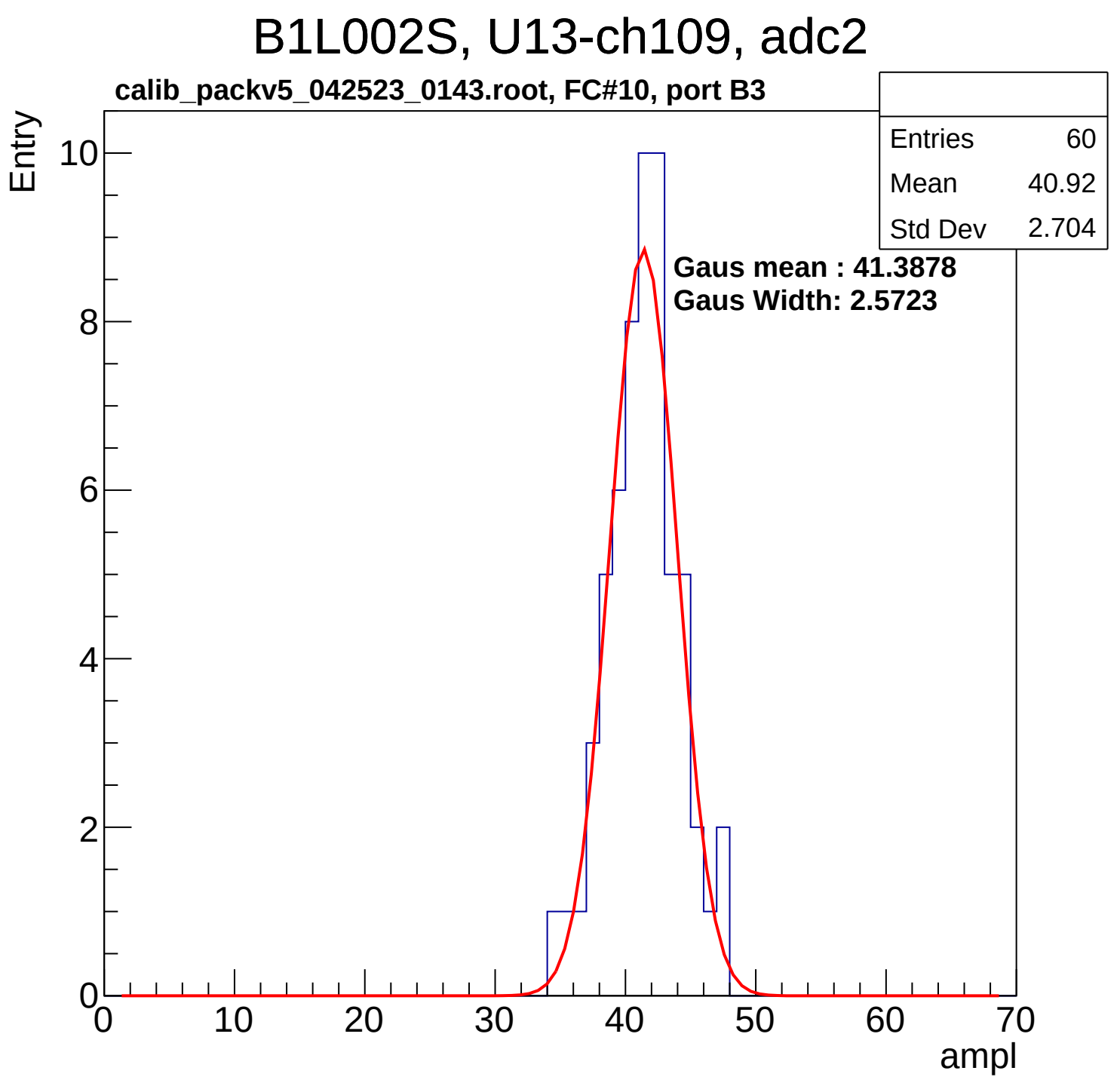
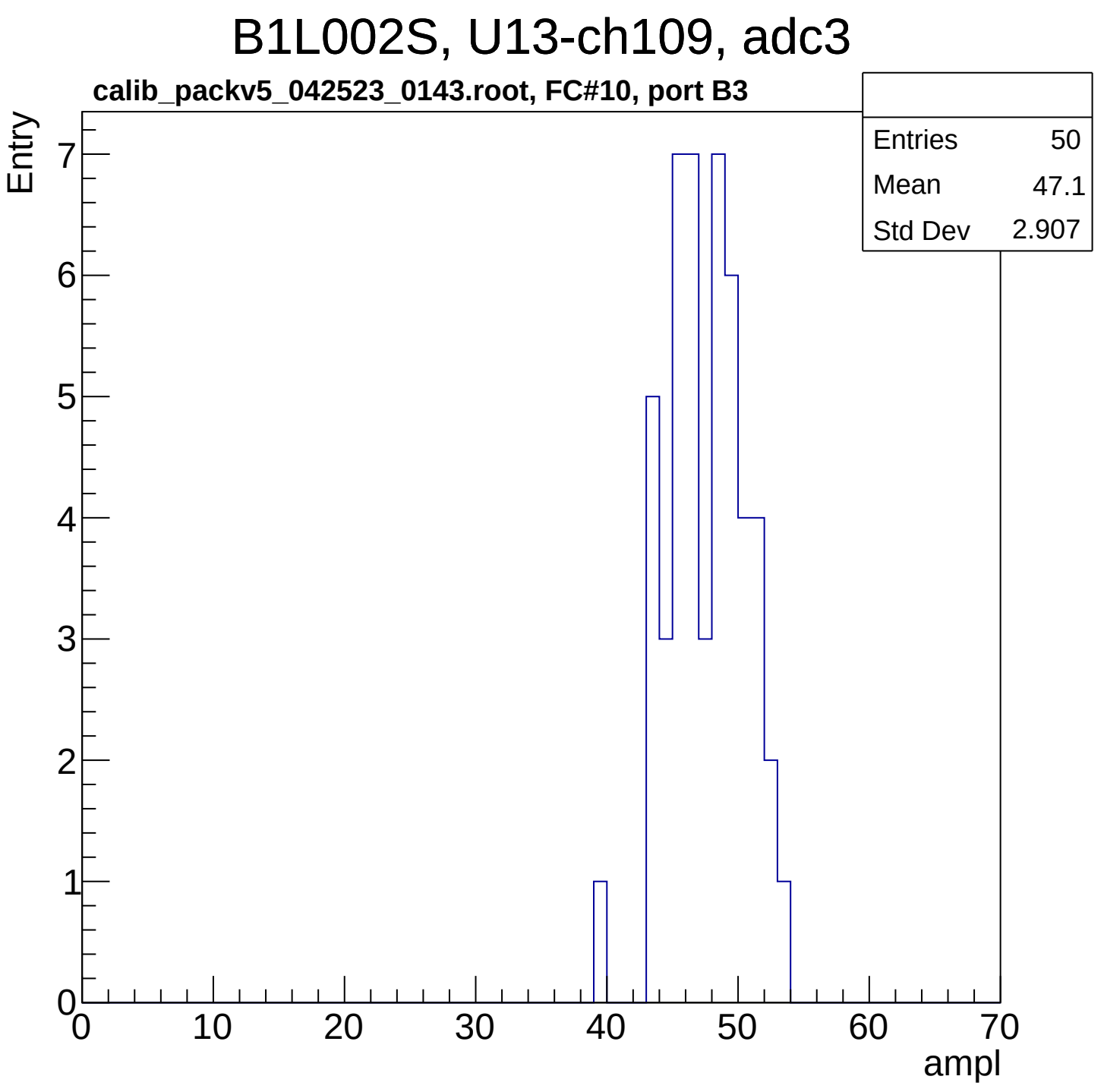

B1L002S, U13-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.92
Std Dev 2.704
Gaus mean : 41.3878
Gaus Width: 2.5723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.1
Std Dev 2.907
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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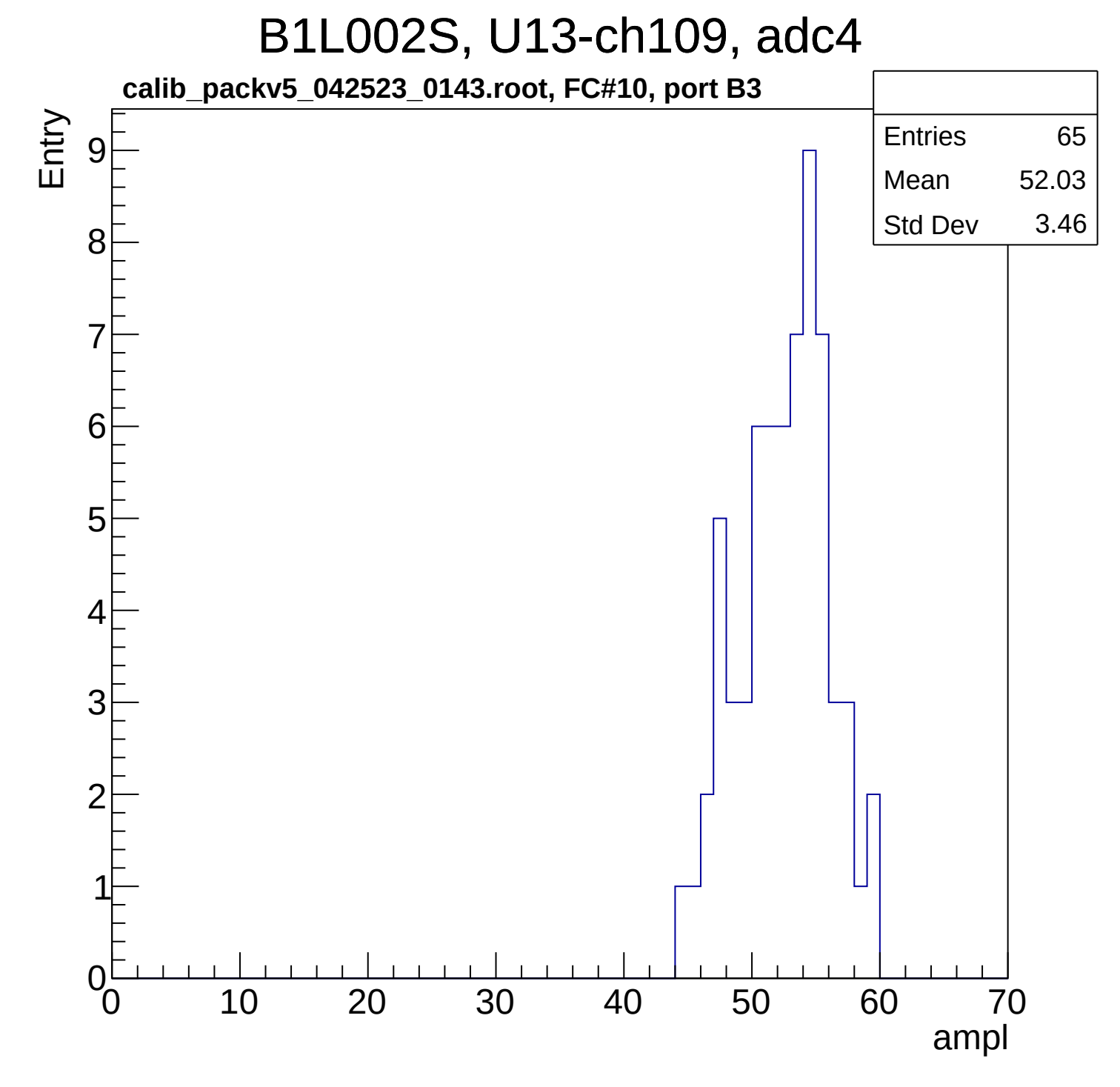
B1L002S, U13-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.03
Std Dev 3.46
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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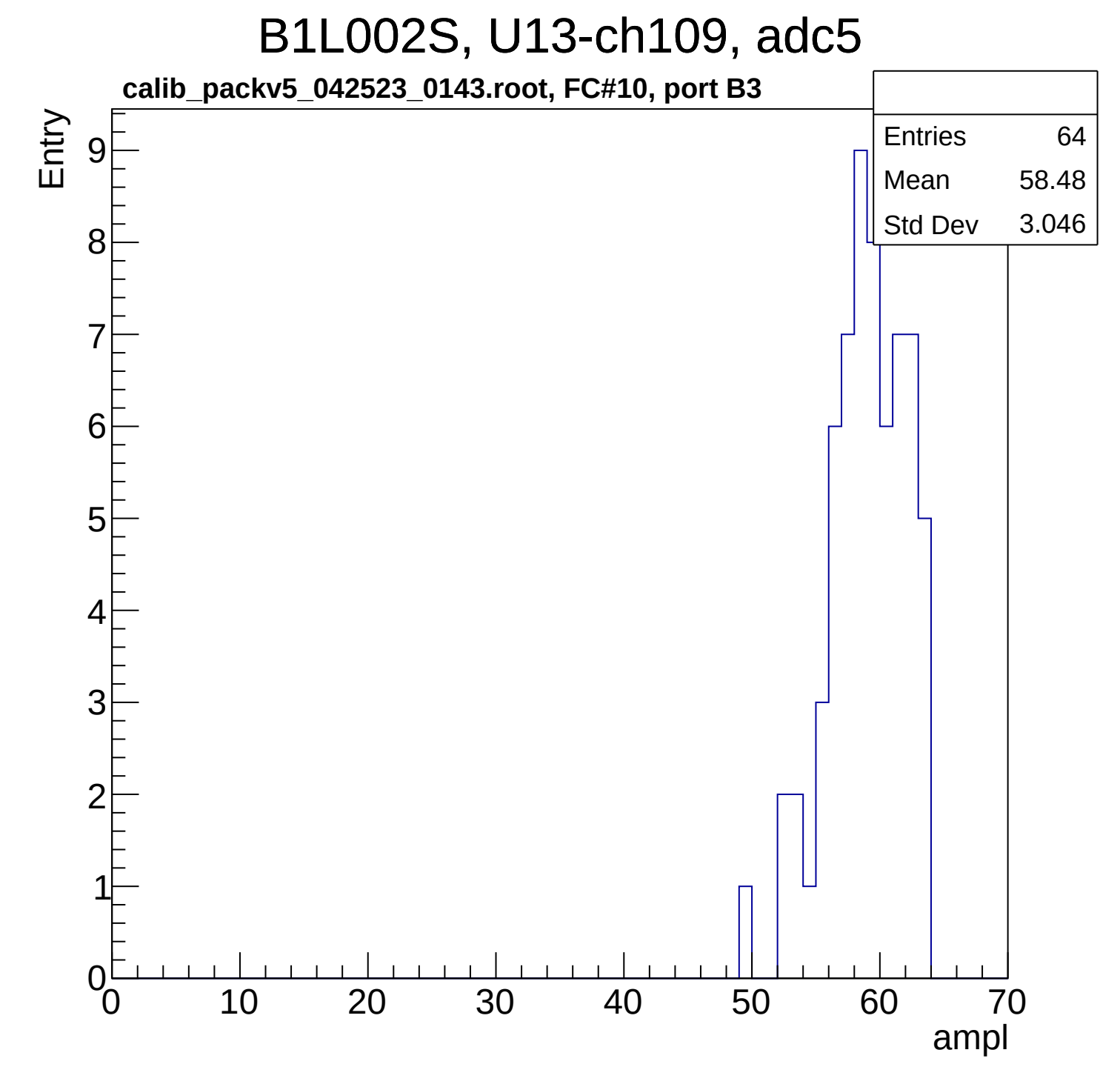
B1L002S, U13-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.48
Std Dev 3.046
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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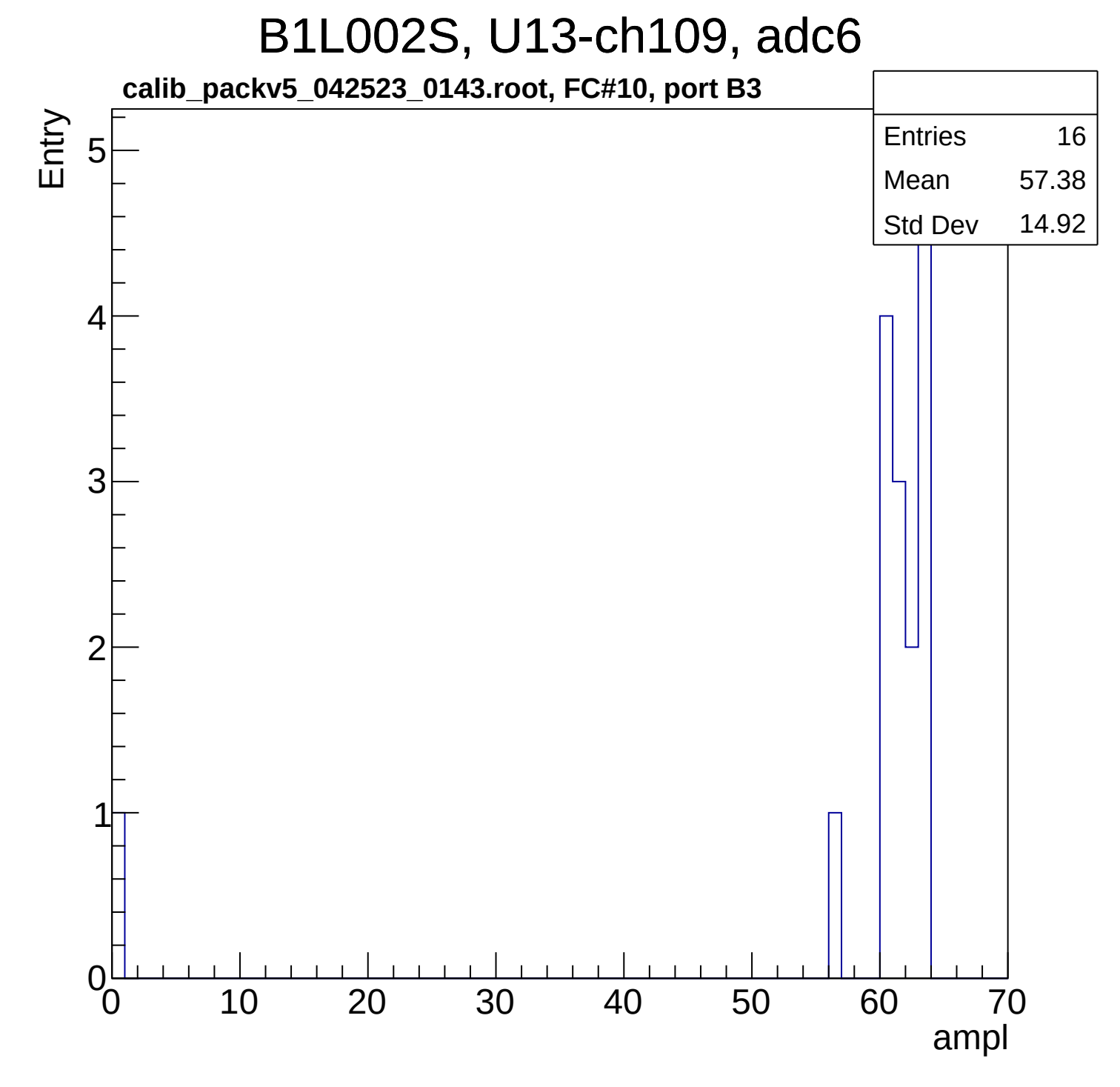
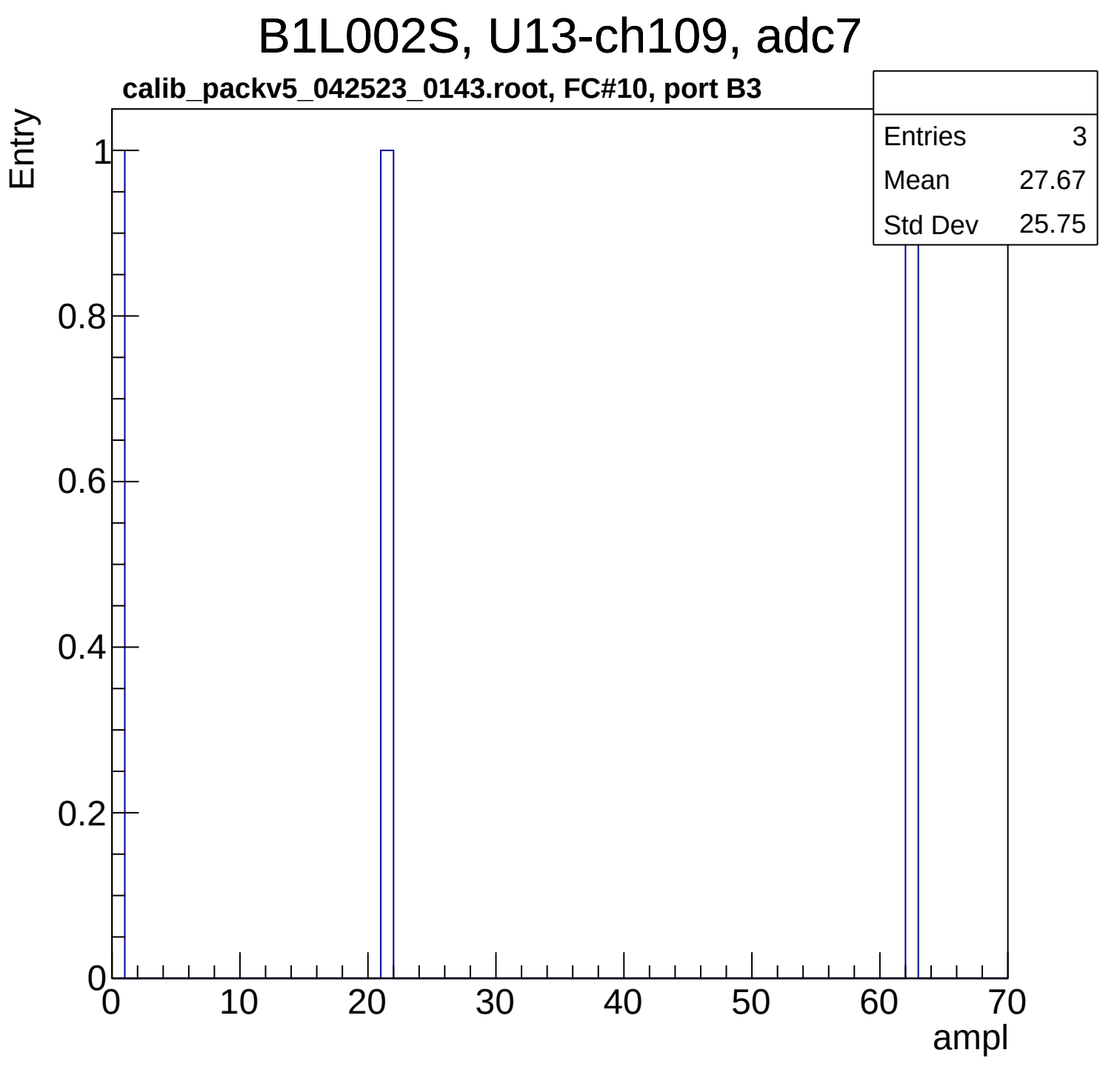

B1L002S, U13-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.38
Std Dev 14.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.67
Std Dev 25.75
Entry
ampl
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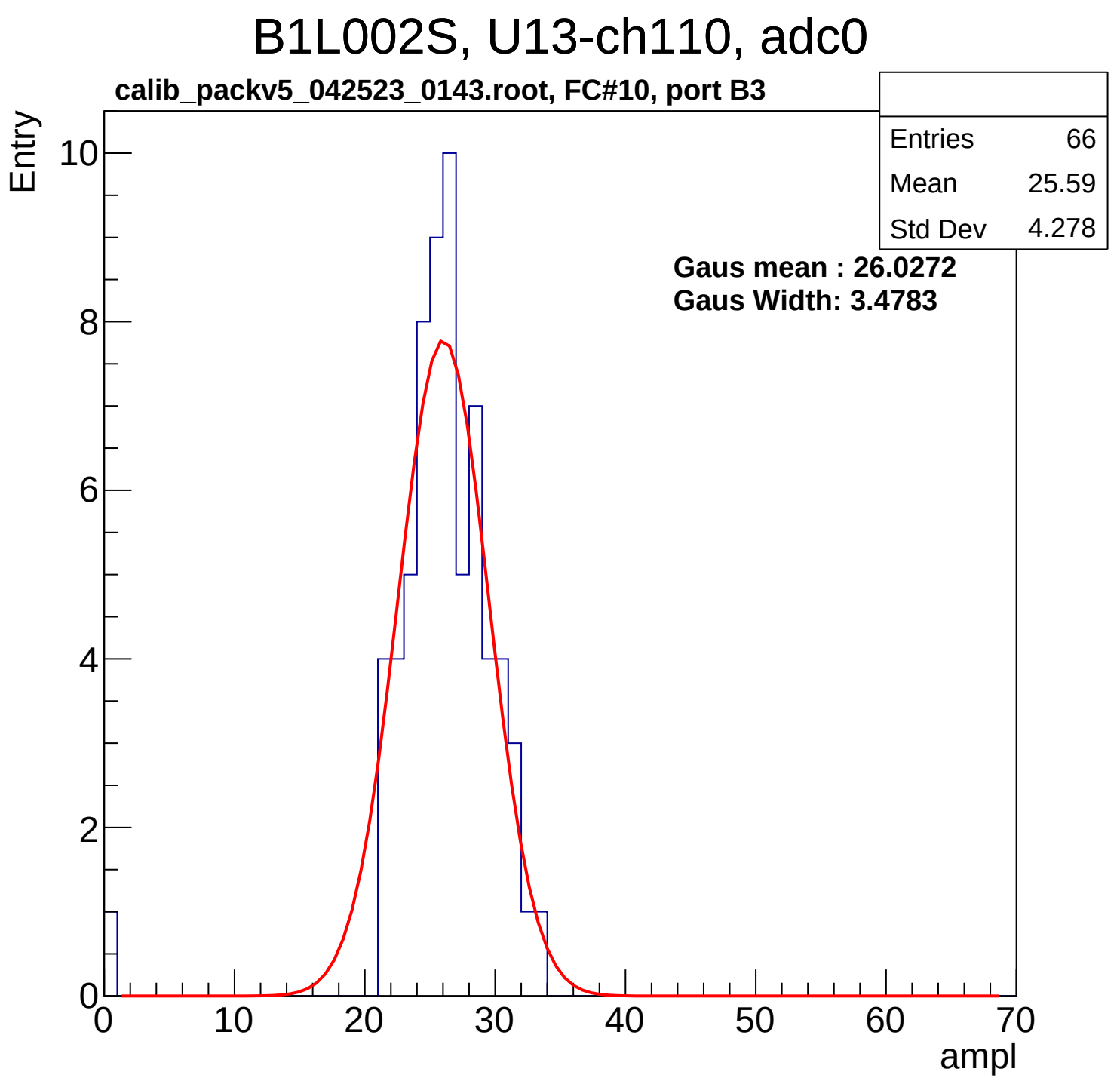
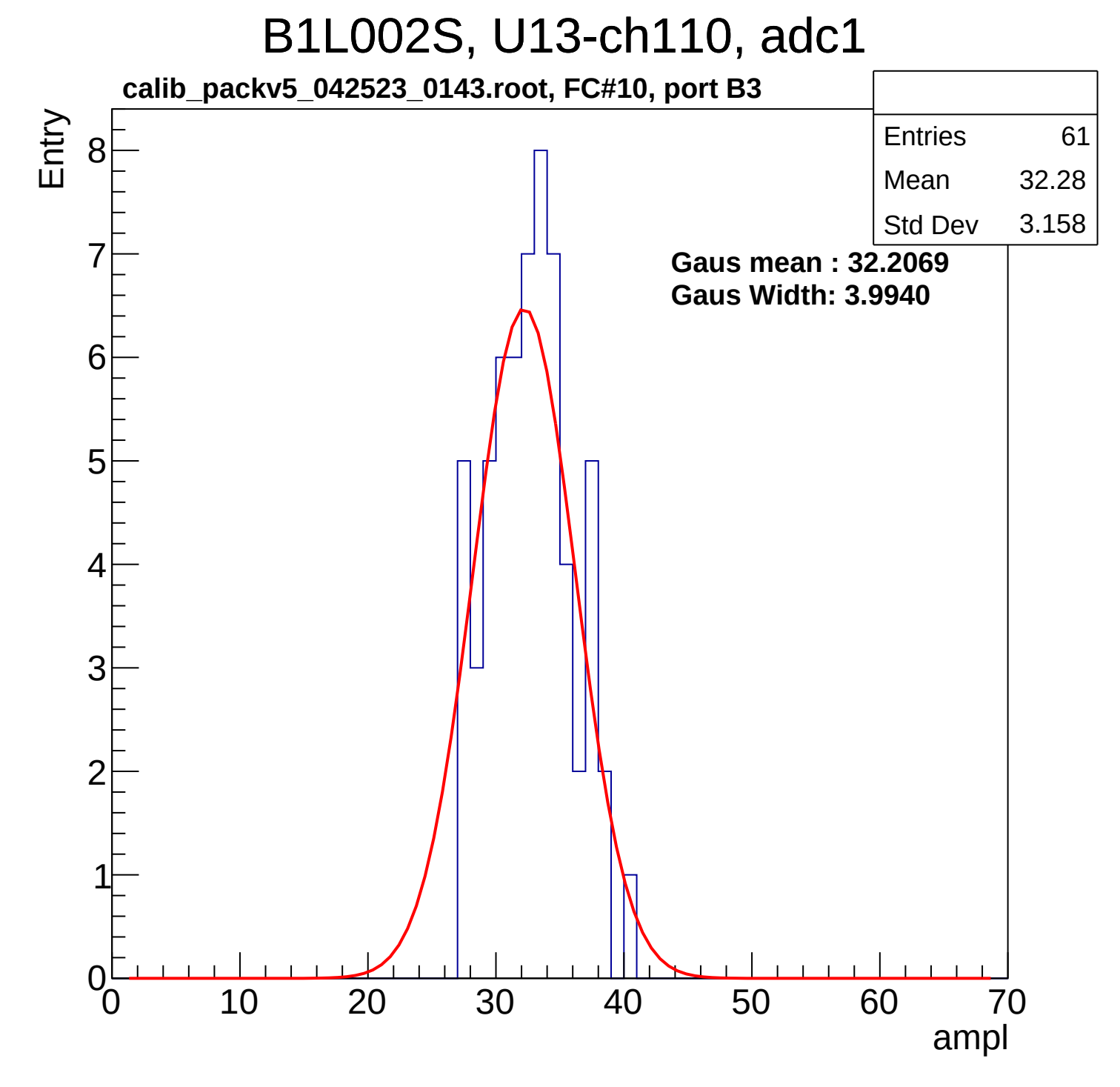
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U13-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 25.59
Std Dev 4.278
Gaus mean : 26.0272
Gaus Width: 3.4783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 32.28
Std Dev 3.158
Gaus mean : 32.2069
Gaus Width: 3.9940
Entry
ampl
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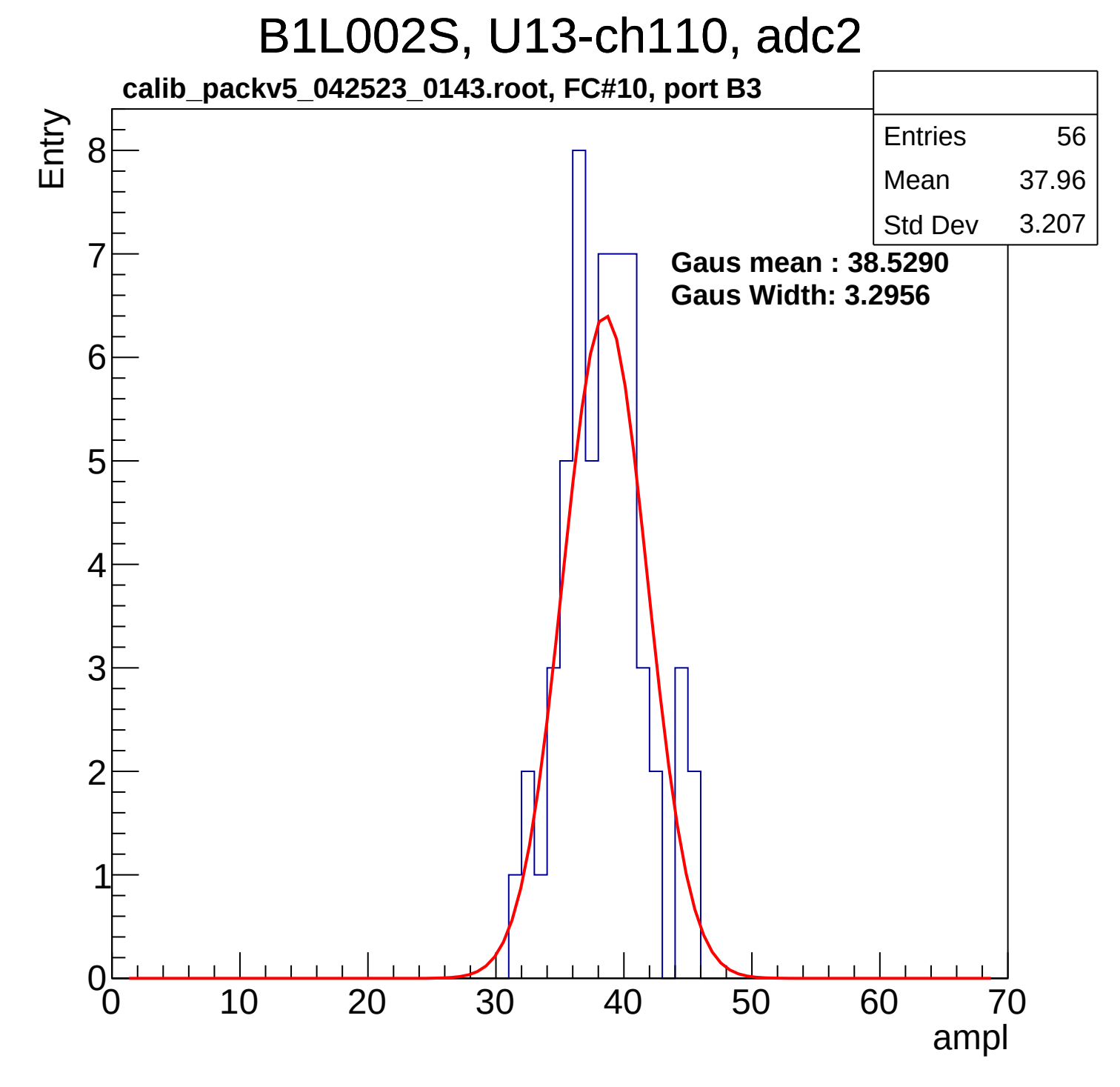
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 37.96
Std Dev 3.207
Gaus mean : 38.5290
Gaus Width: 3.2956
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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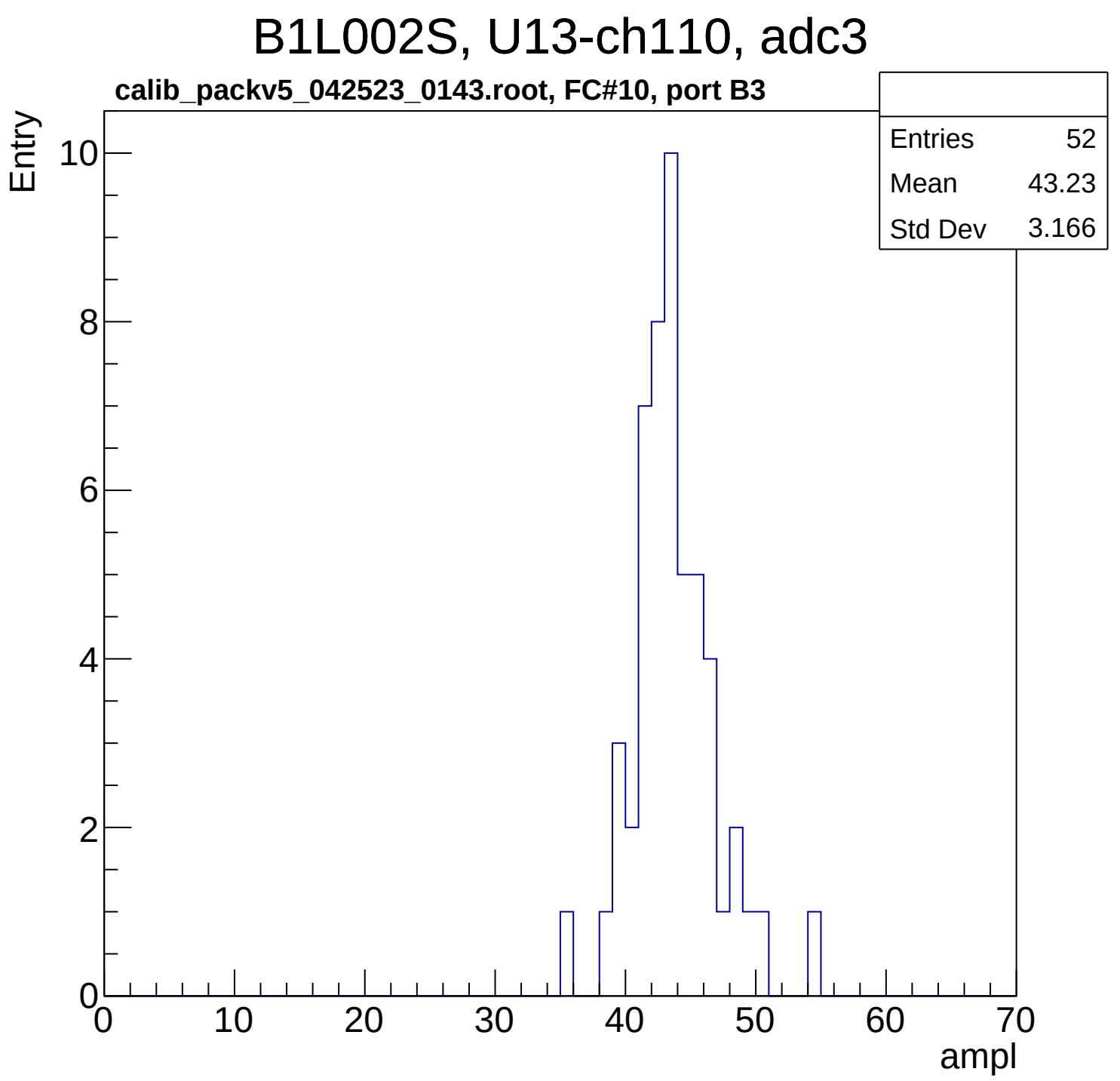
B1L002S, U13-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 43.23
Std Dev 3.166
Entry
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2
4
6
8
10
0
10
20
30
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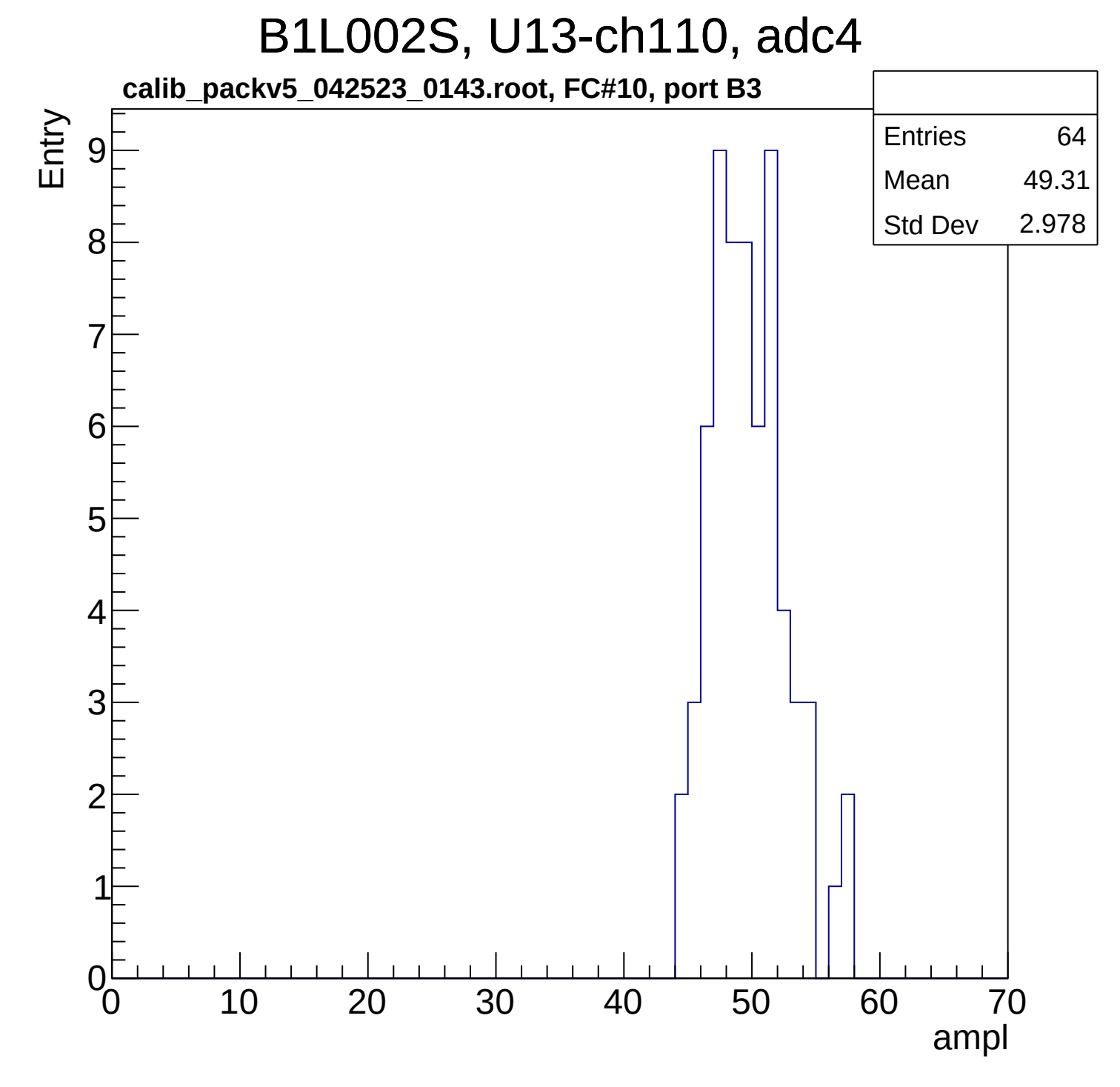
50
60
70
ampl


B1L002S, U13-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.31
Std Dev 2.978
Entry
ampl
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3
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5
6
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9
0
10
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60
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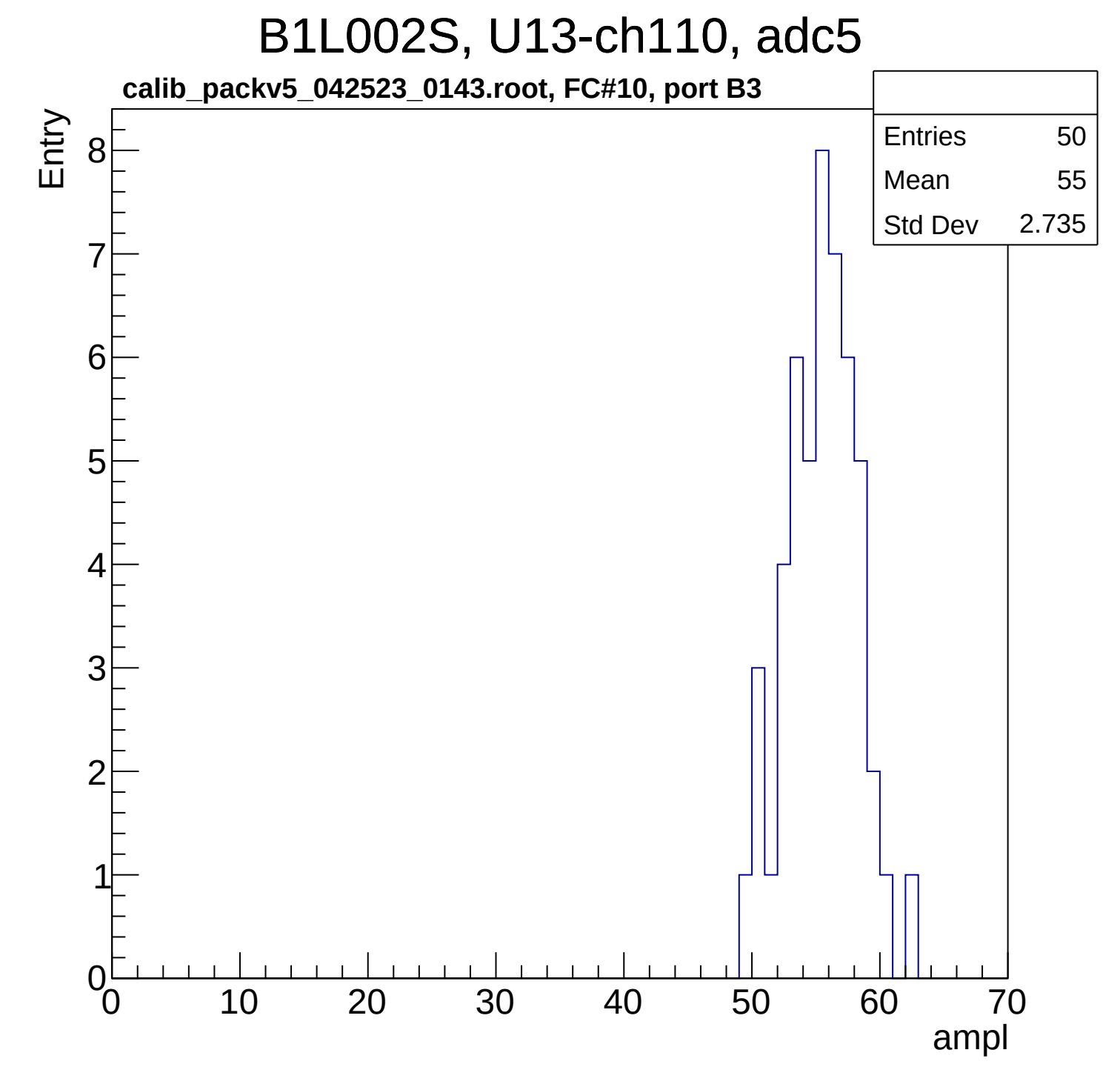
B1L002S, U13-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 55
Std Dev 2.735
Entry
ampl
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60
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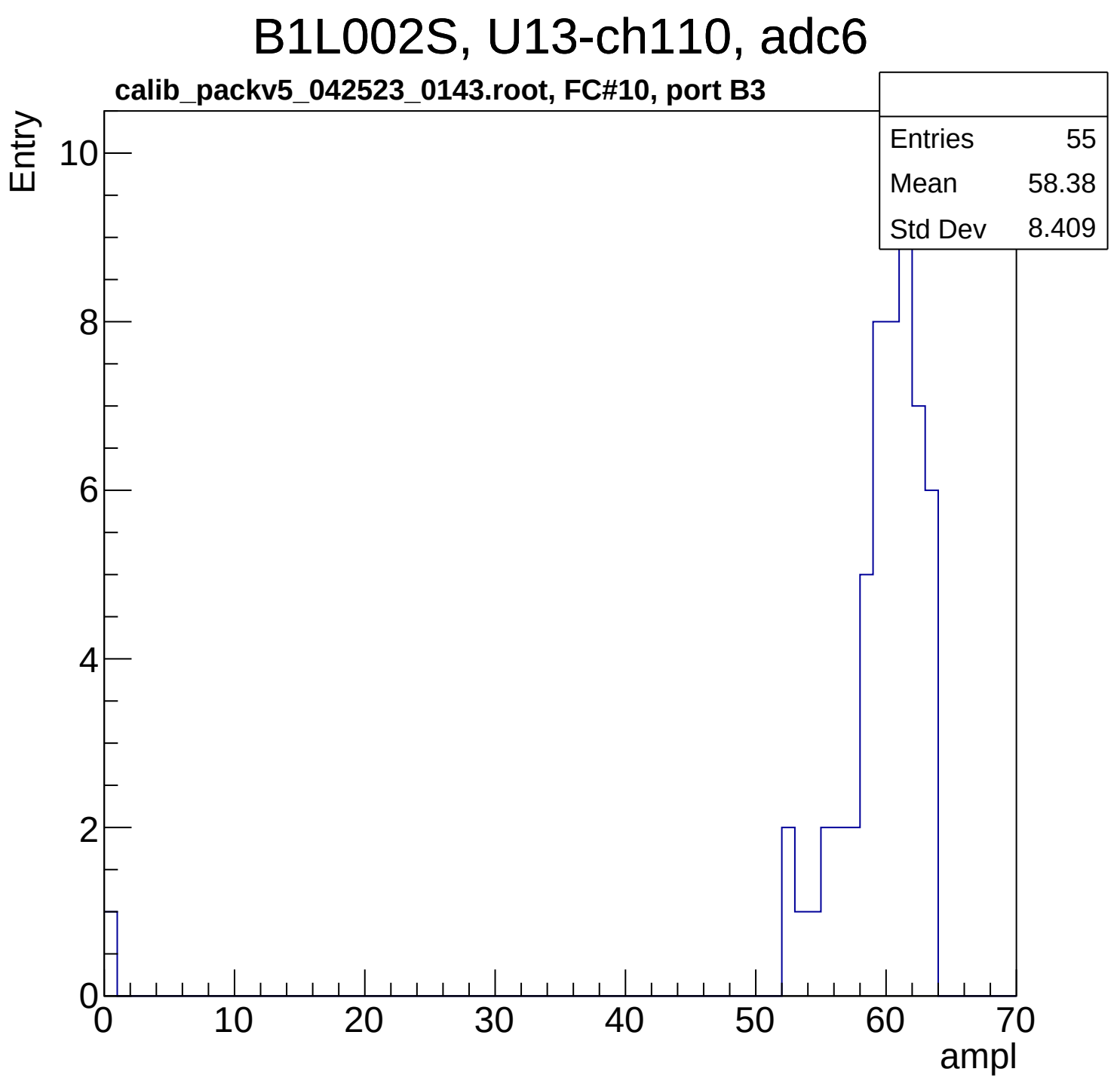
B1L002S, U13-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.38
Std Dev 8.409
Entry
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4
6
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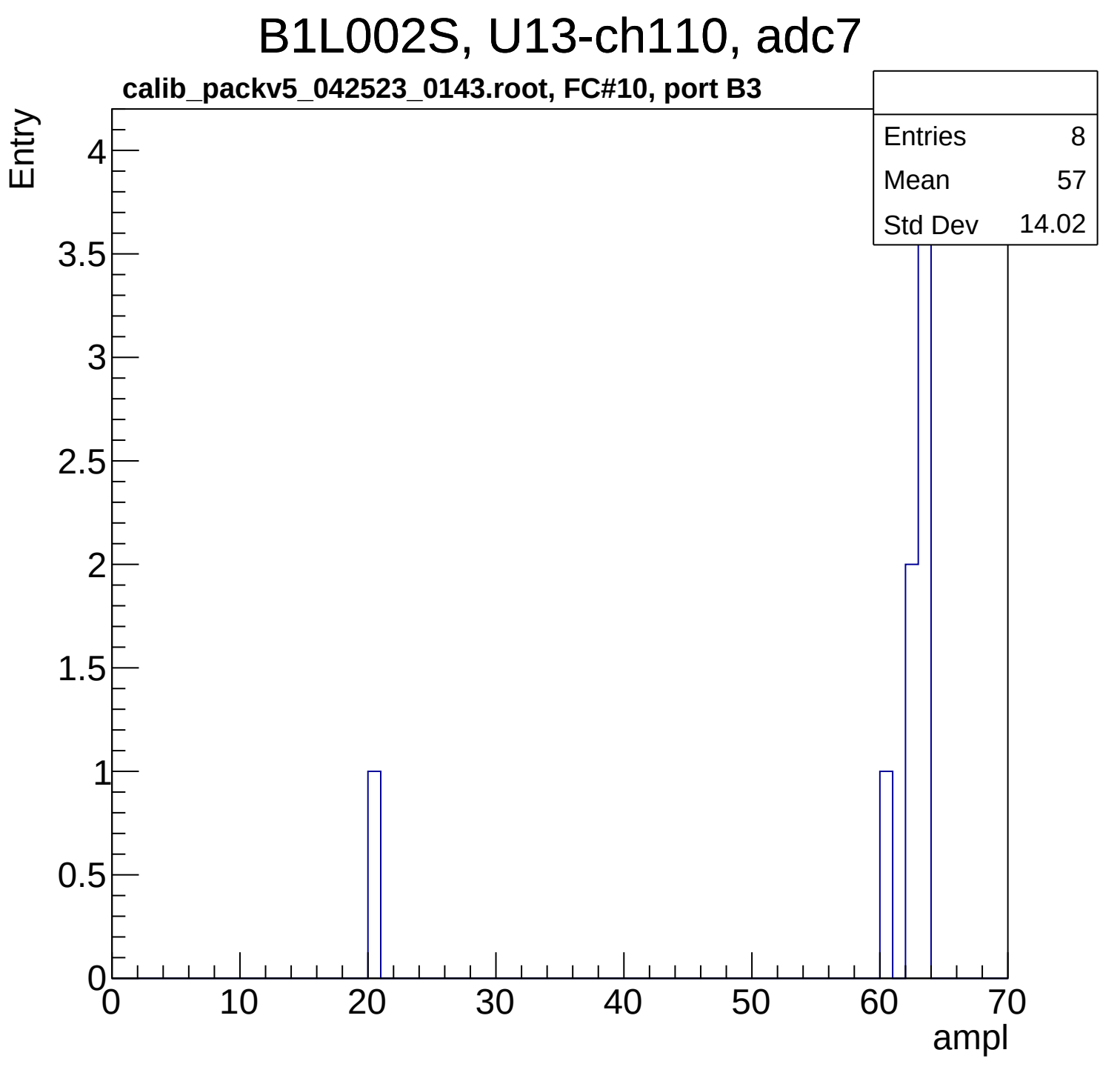
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 57
Std Dev 14.02
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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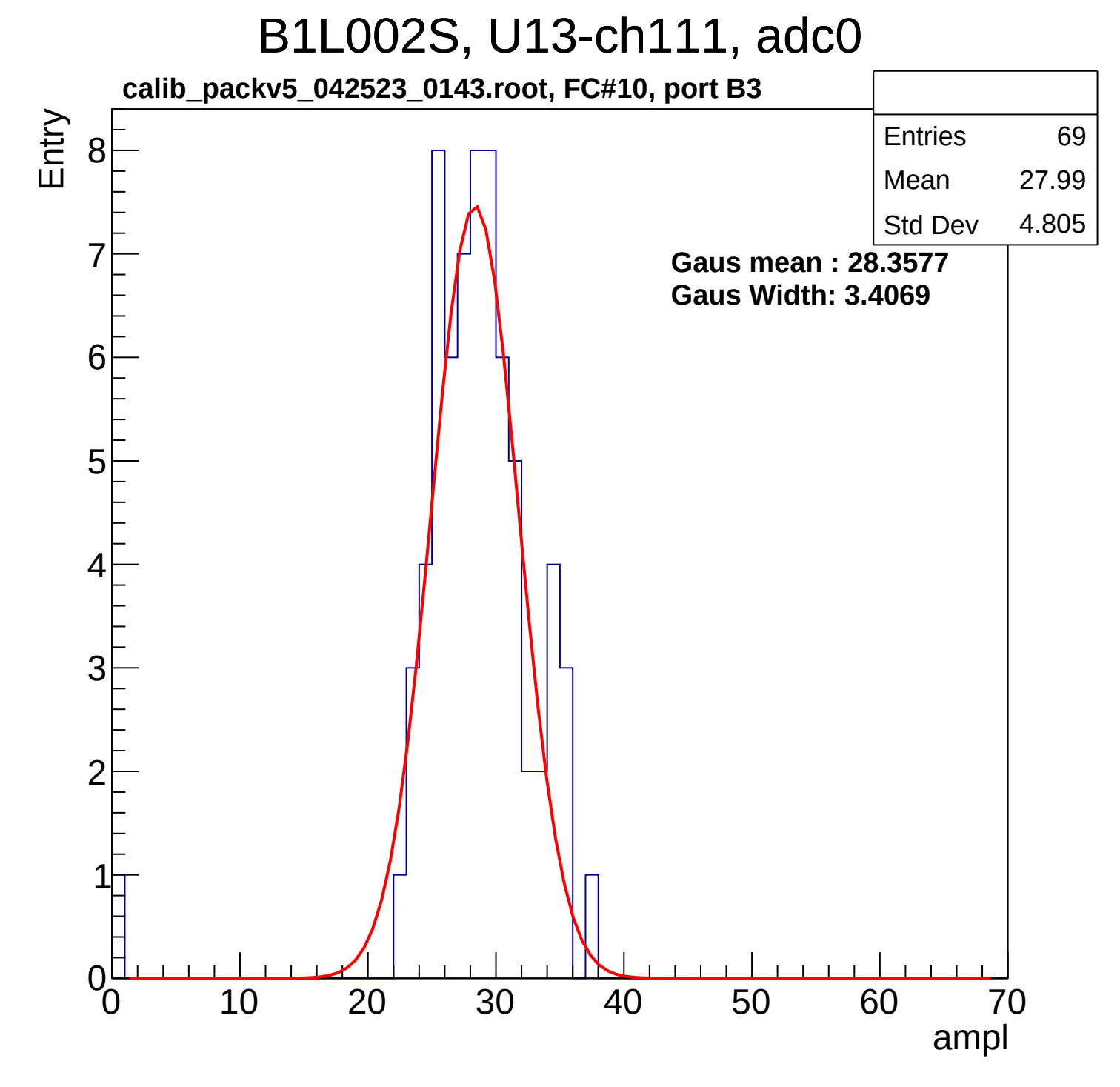
B1L002S, U13-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.99
Std Dev 4.805
Gaus mean : 28.3577
Gaus Width: 3.4069
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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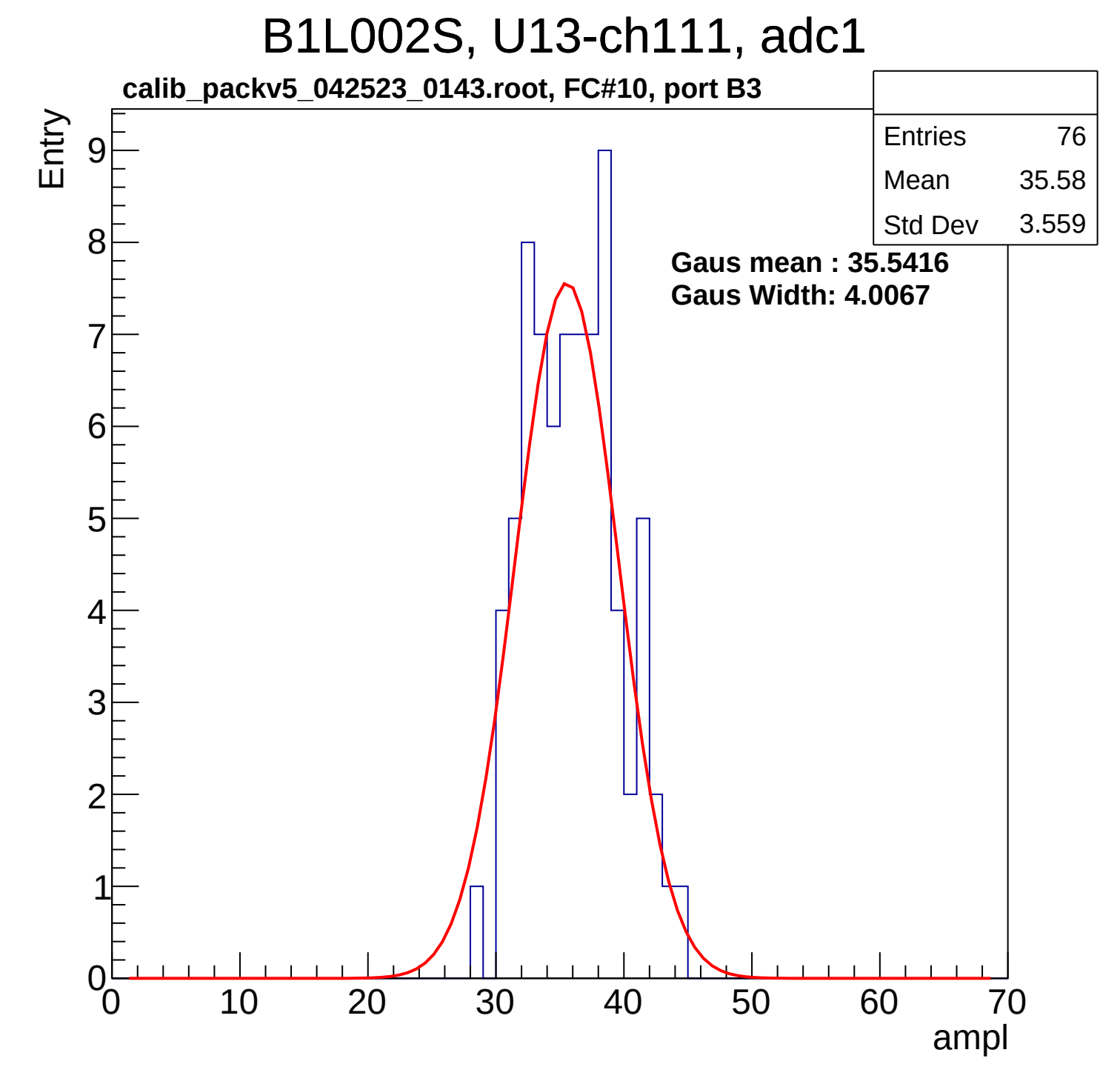
B1L002S, U13-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35.58
Std Dev 3.559
Gaus mean : 35.5416
Gaus Width: 4.0067
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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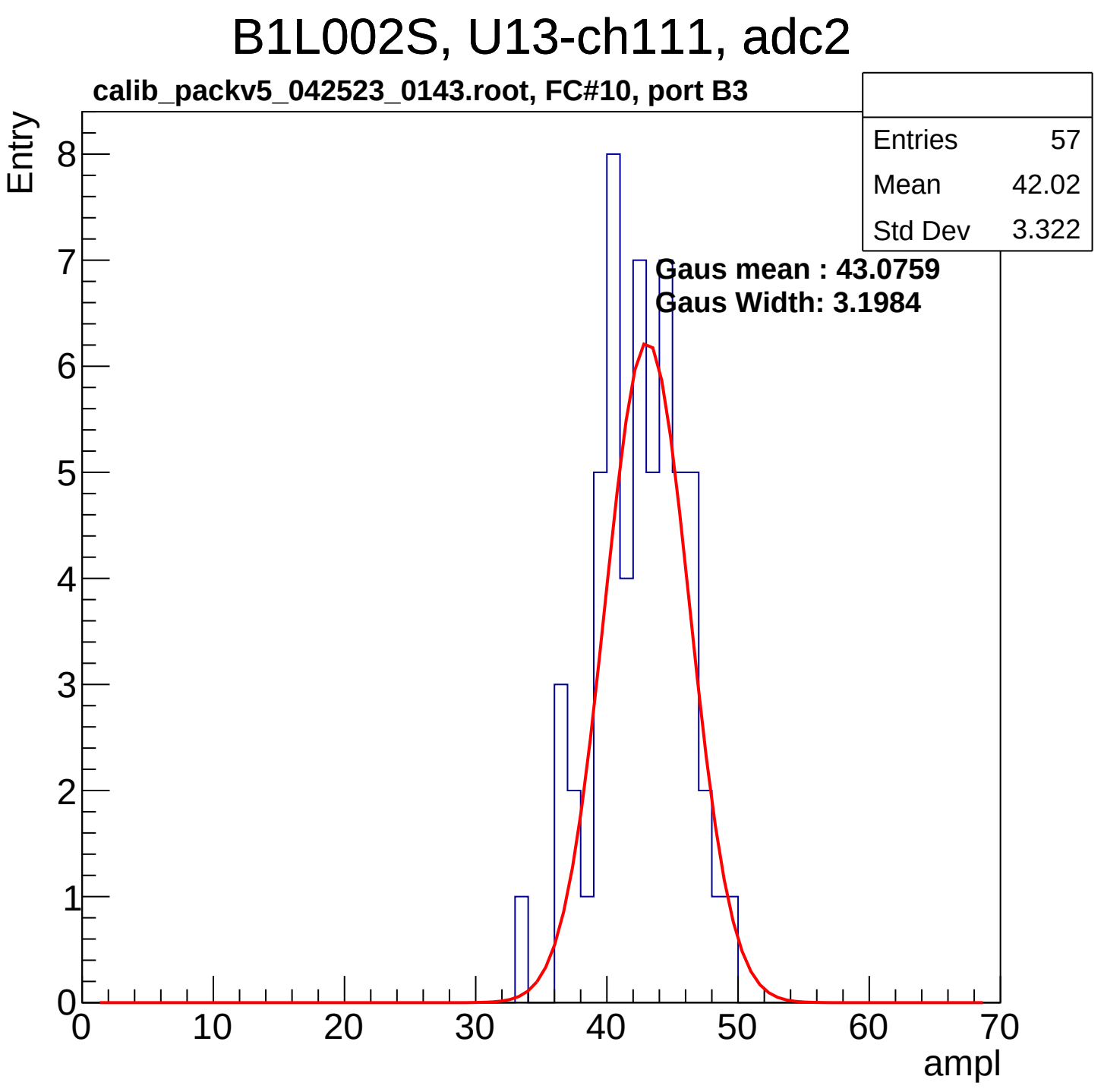
B1L002S, U13-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.02
Std Dev 3.322
Gaus mean : 43.0759
Gaus Width: 3.1984
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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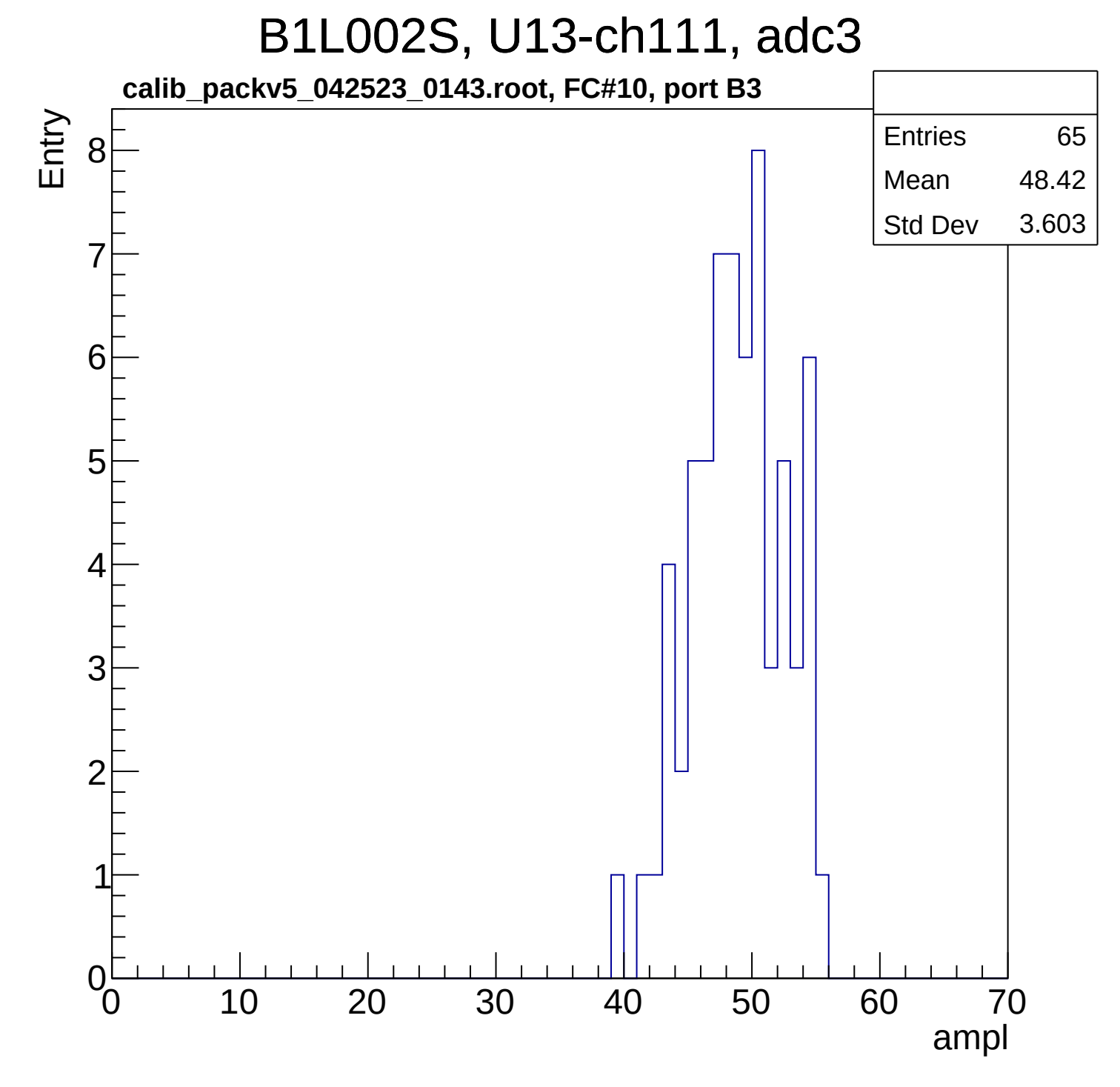
B1L002S, U13-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.42
Std Dev 3.603
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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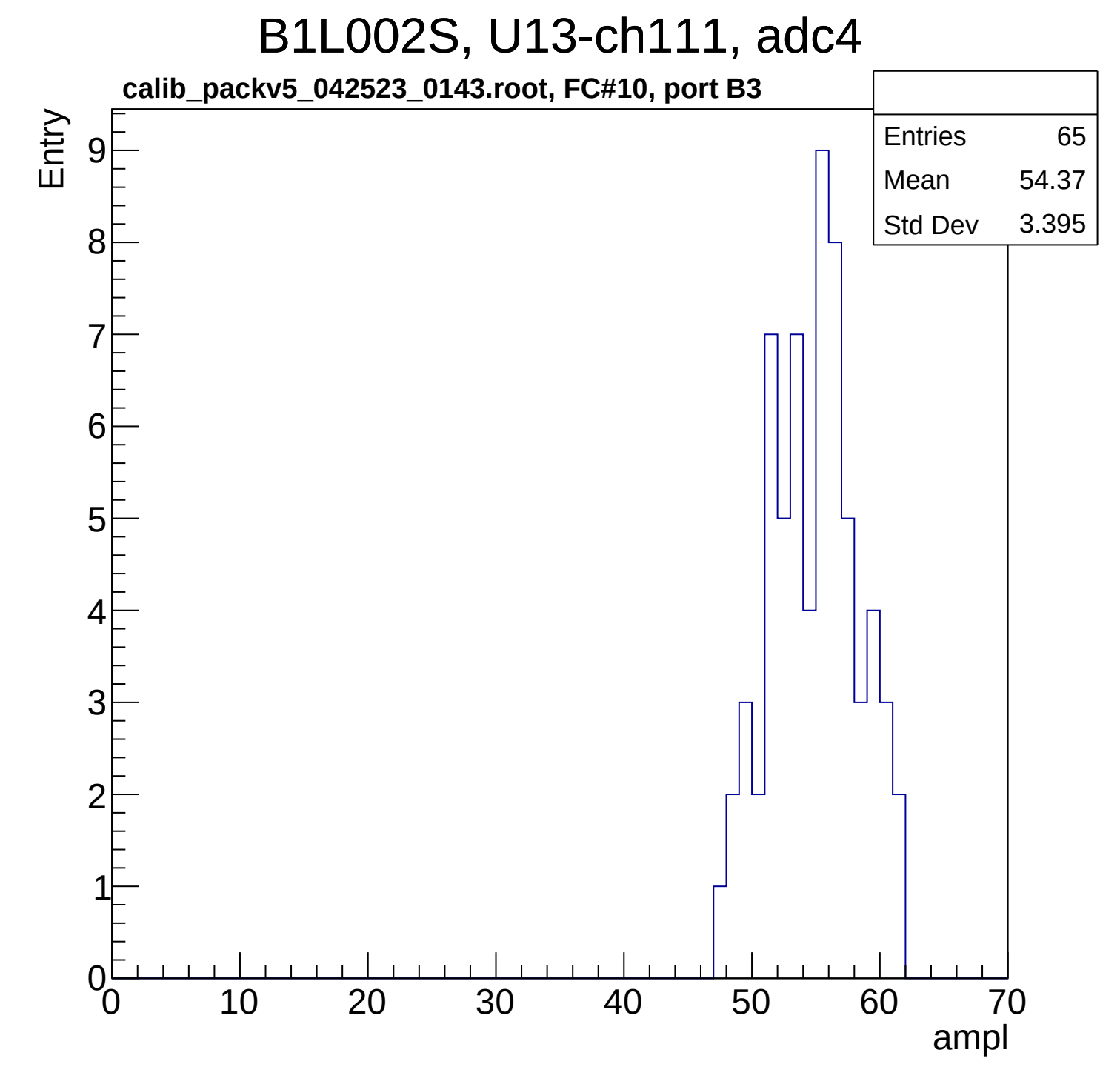
B1L002S, U13-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 54.37
Std Dev 3.395
Entry
ampl
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3
4
5
6
7
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9
0
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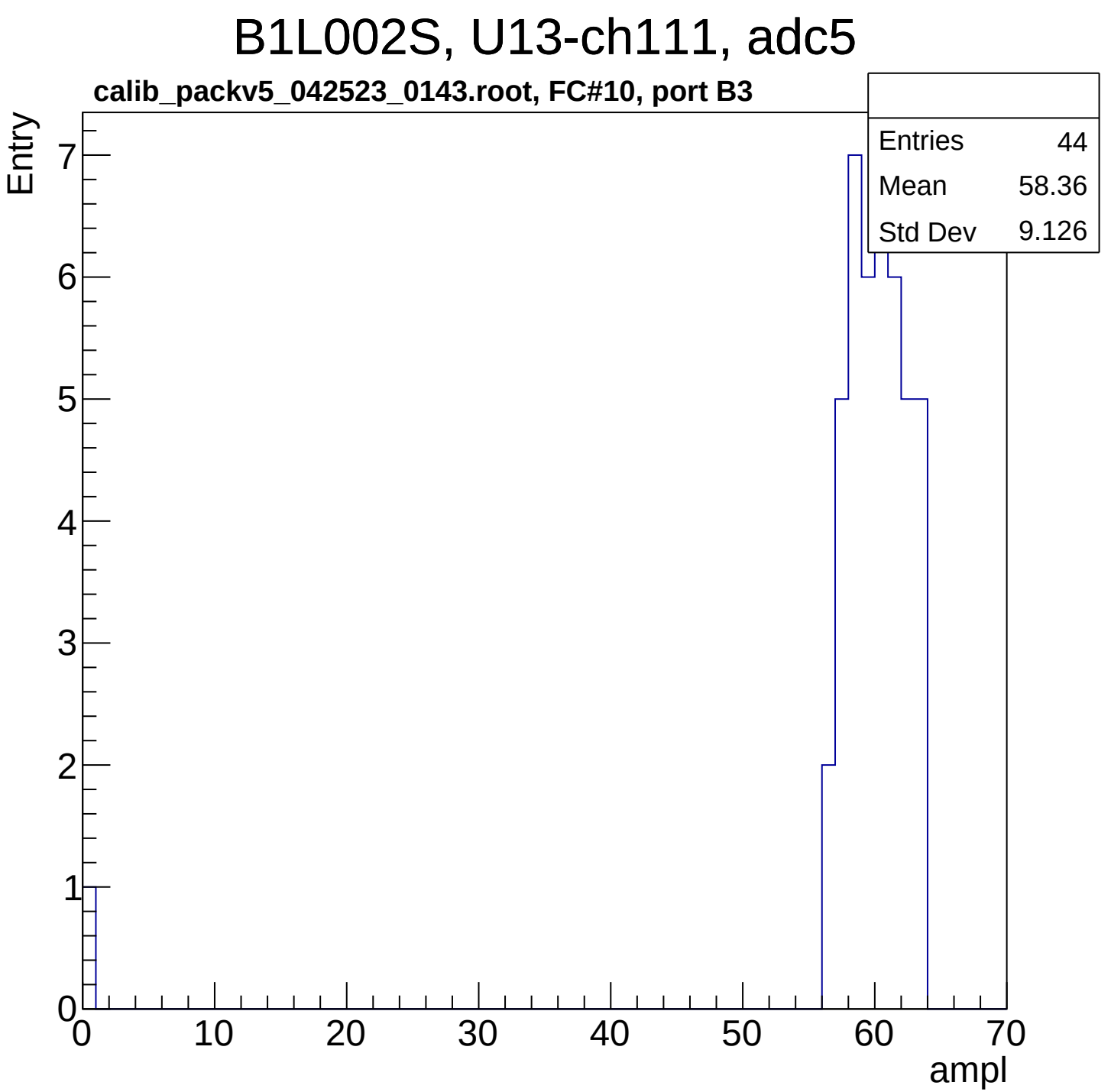
B1L002S, U13-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.36
Std Dev 9.126
Entry
ampl
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0
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40
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60
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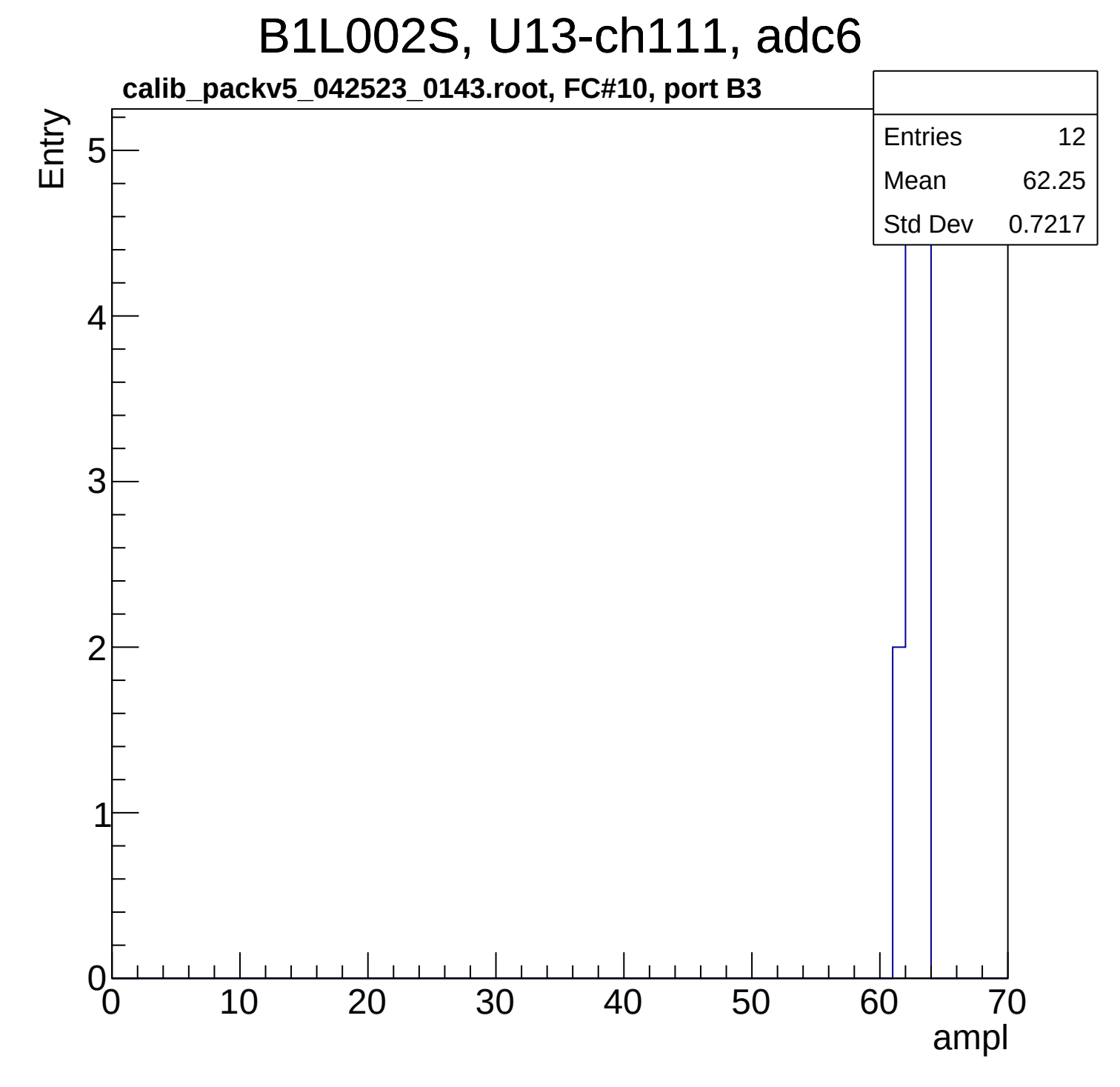
B1L002S, U13-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62.25
Std Dev 0.7217
Entry
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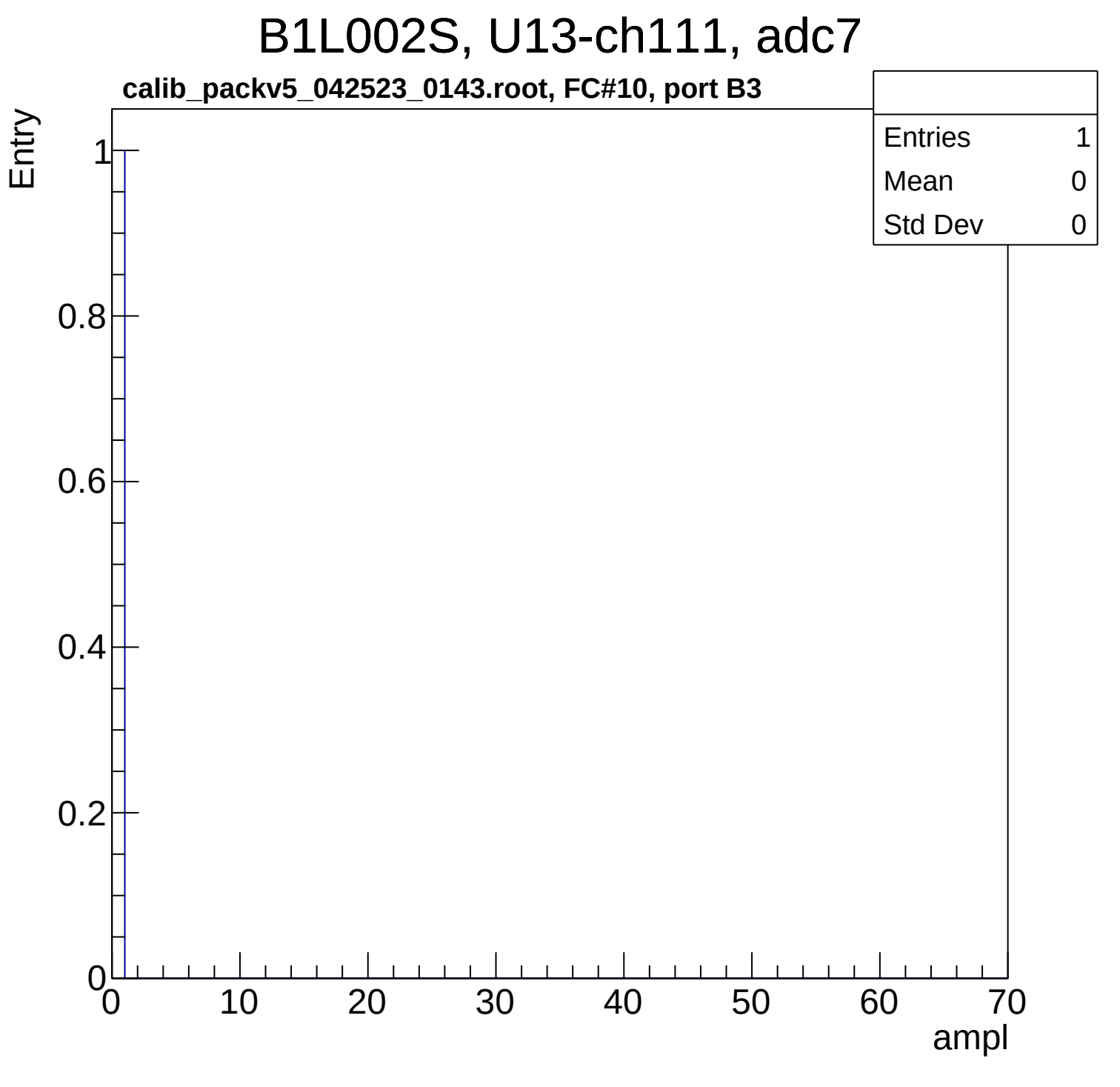
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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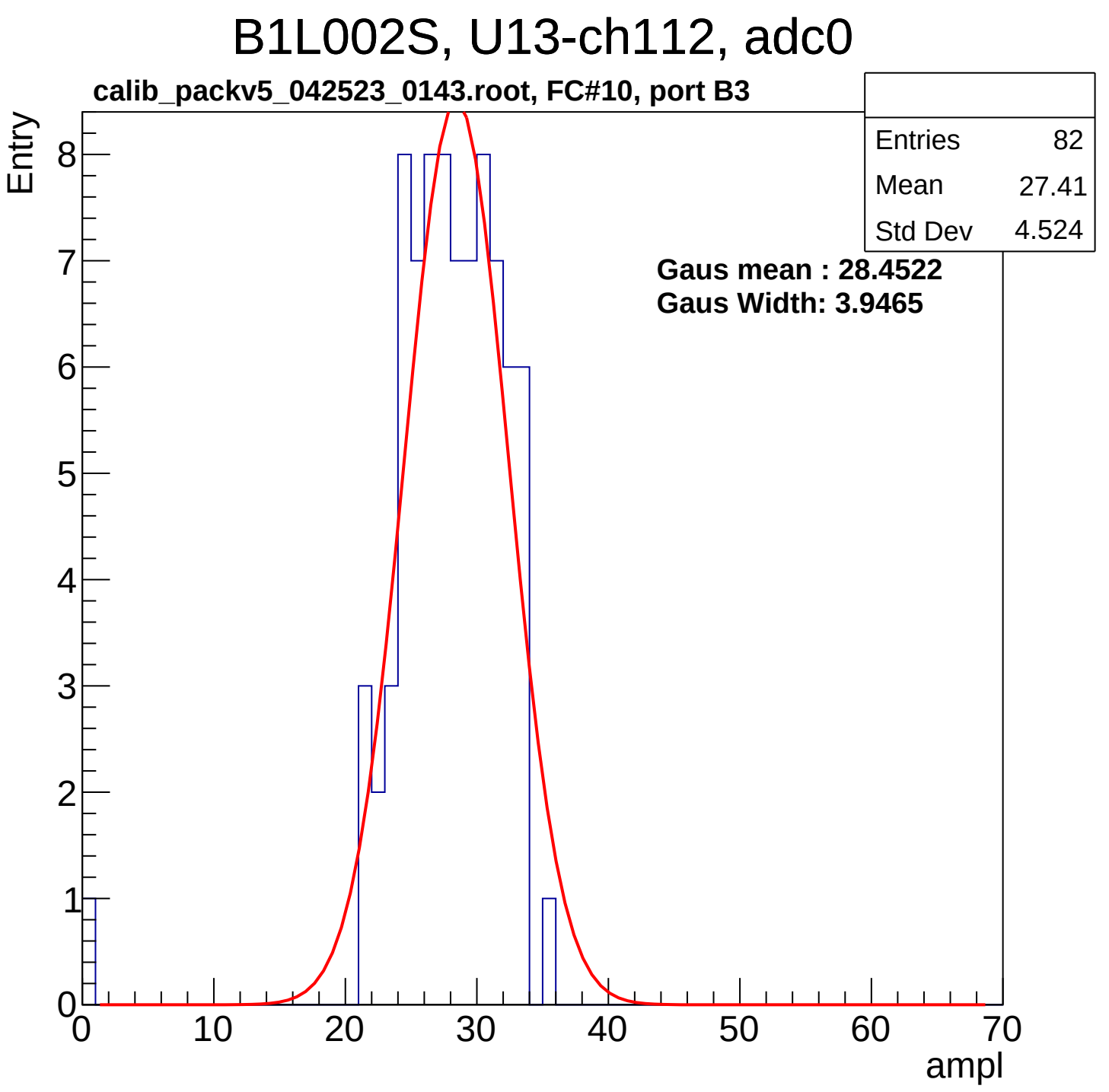
70
ampl


B1L002S, U13-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 27.41
Std Dev 4.524
Gaus mean : 28.4522
Gaus Width: 3.9465
Entry
ampl
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2
3
4
5
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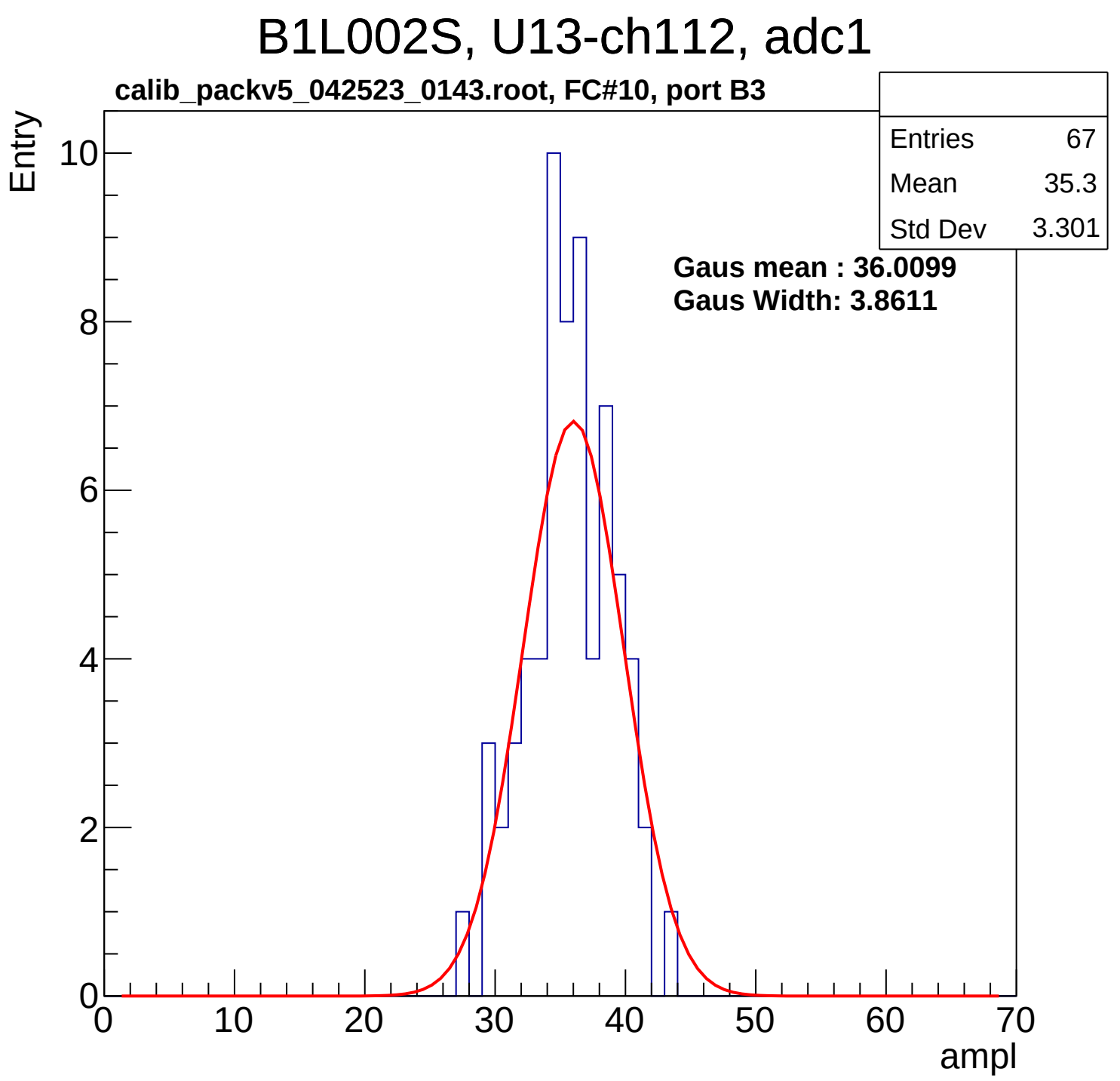
B1L002S, U13-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.3
Std Dev 3.301
Gaus mean : 36.0099
Gaus Width: 3.8611
Entry
ampl
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2
4
6
8
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0
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40
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60
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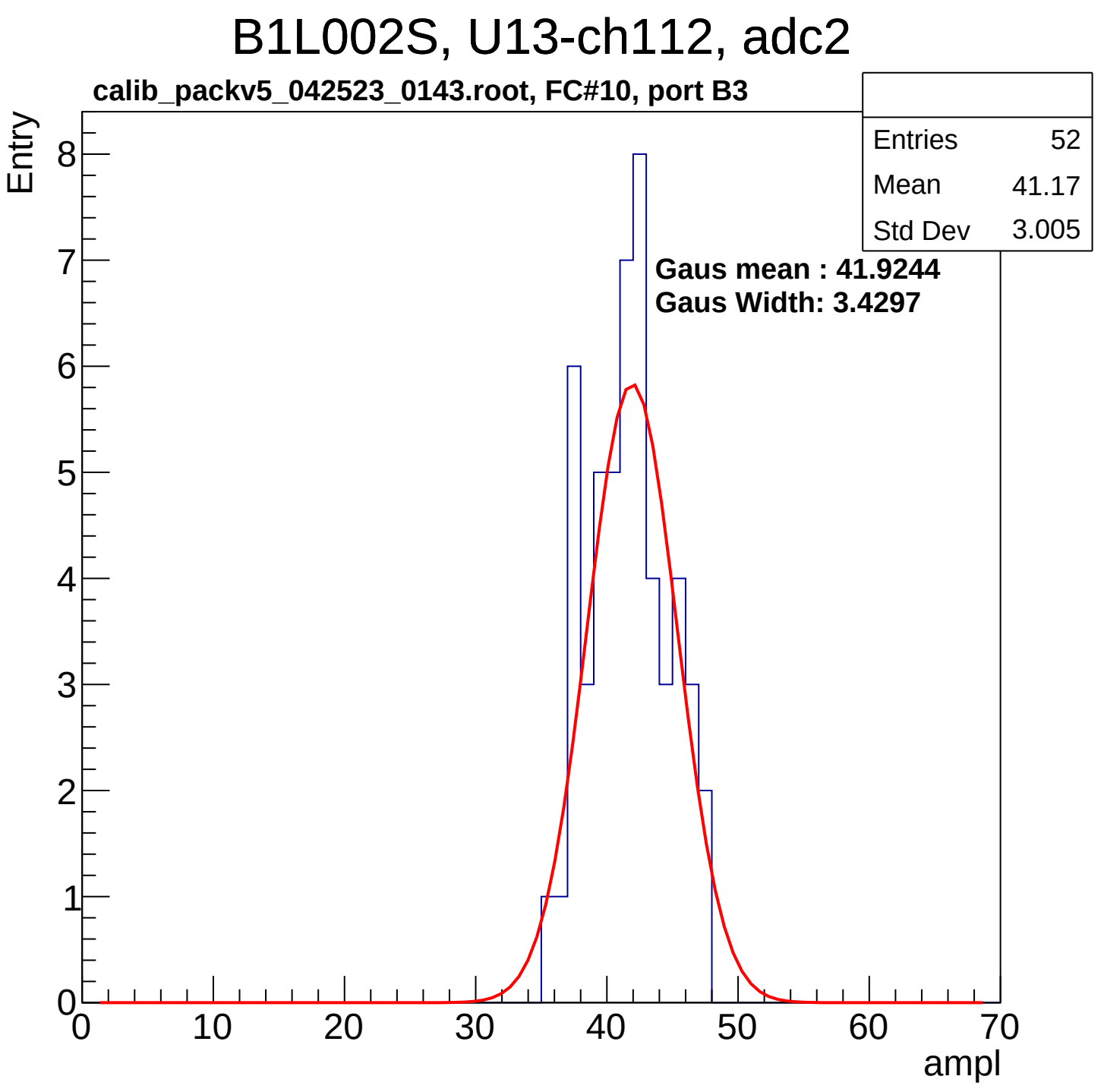
B1L002S, U13-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.17
Std Dev 3.005
Gaus mean : 41.9244
Gaus Width: 3.4297
Entry
ampl
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2
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4
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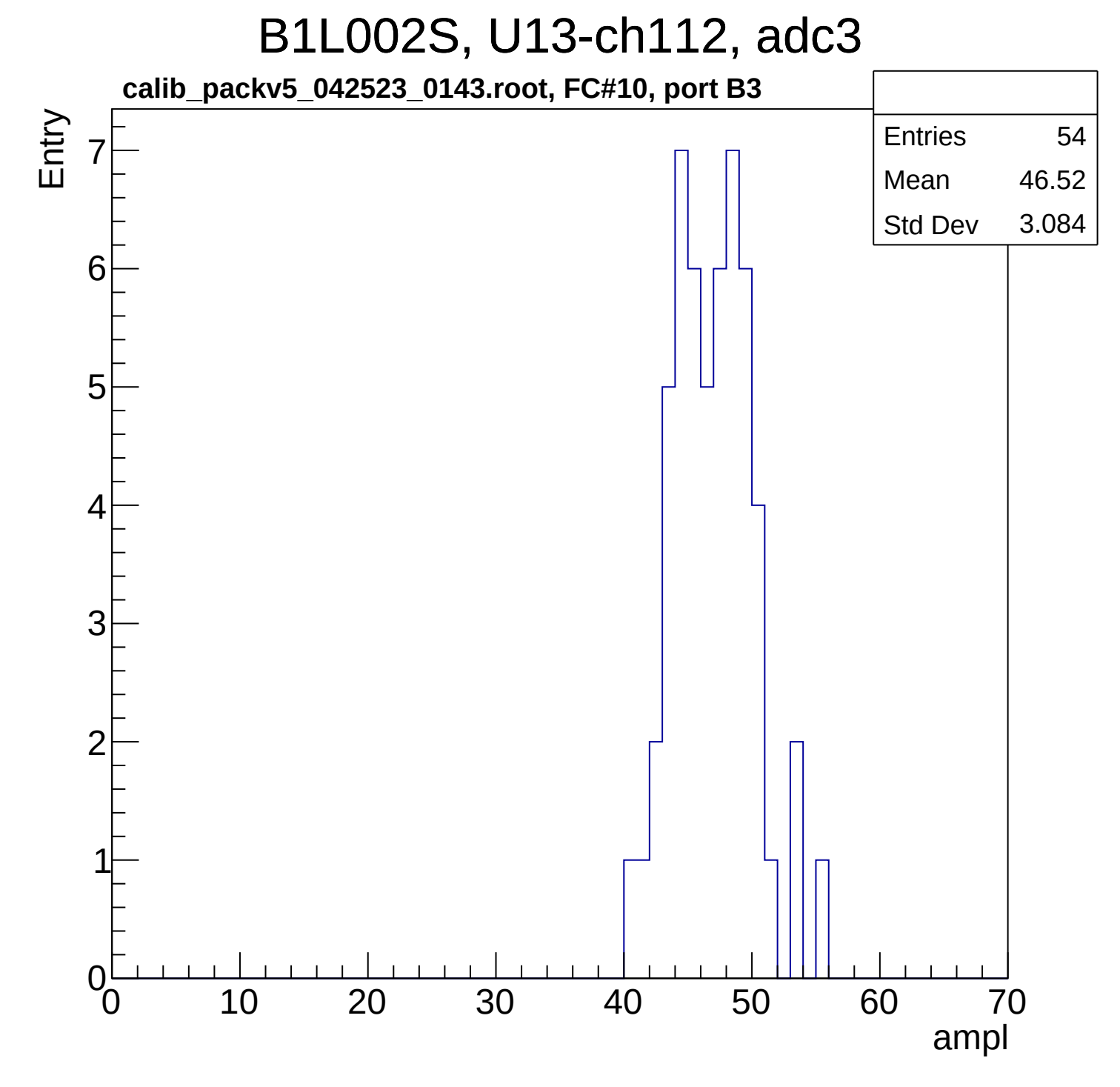
B1L002S, U13-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.52
Std Dev 3.084
Entry
ampl
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1
2
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4
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60
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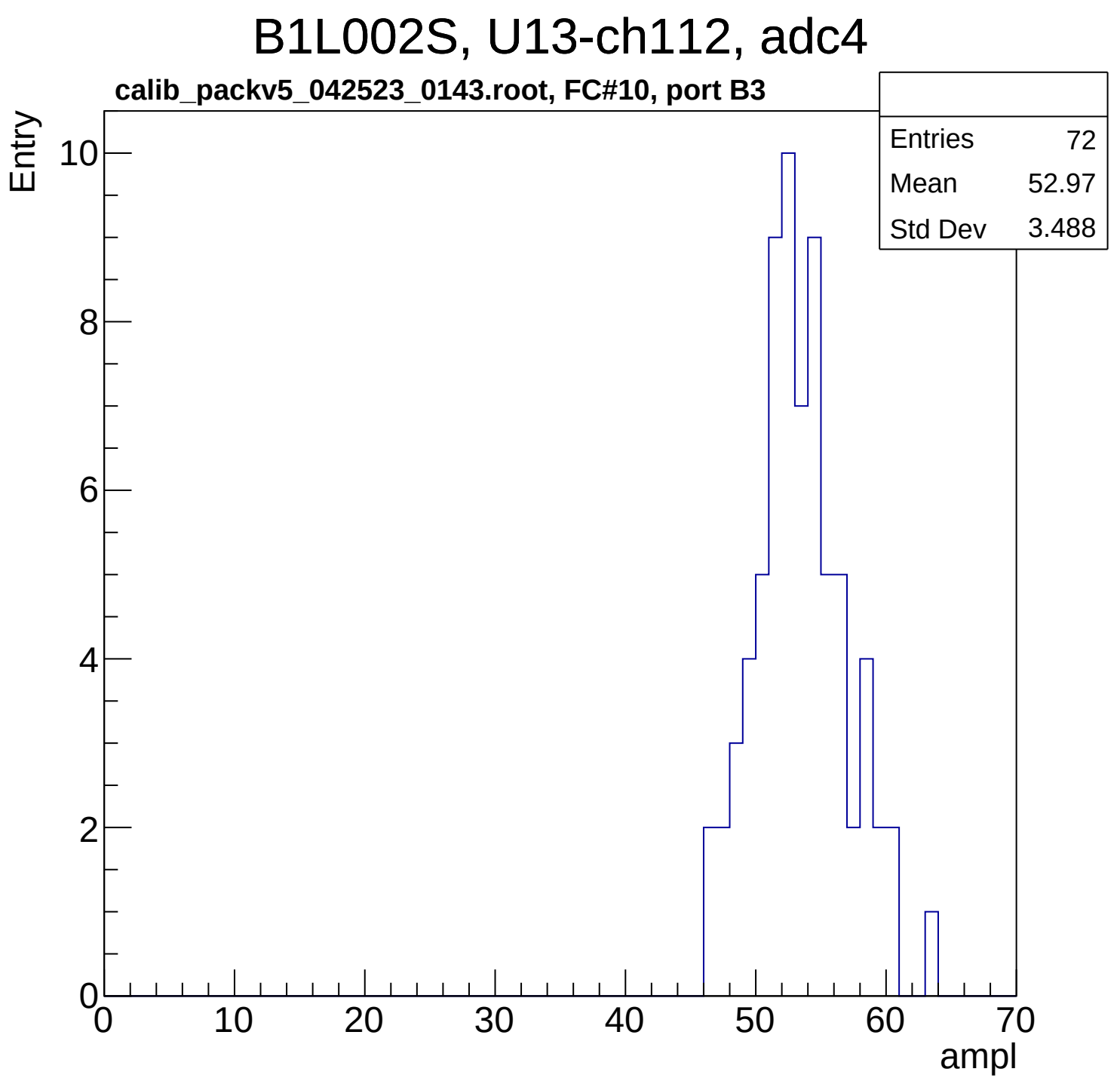
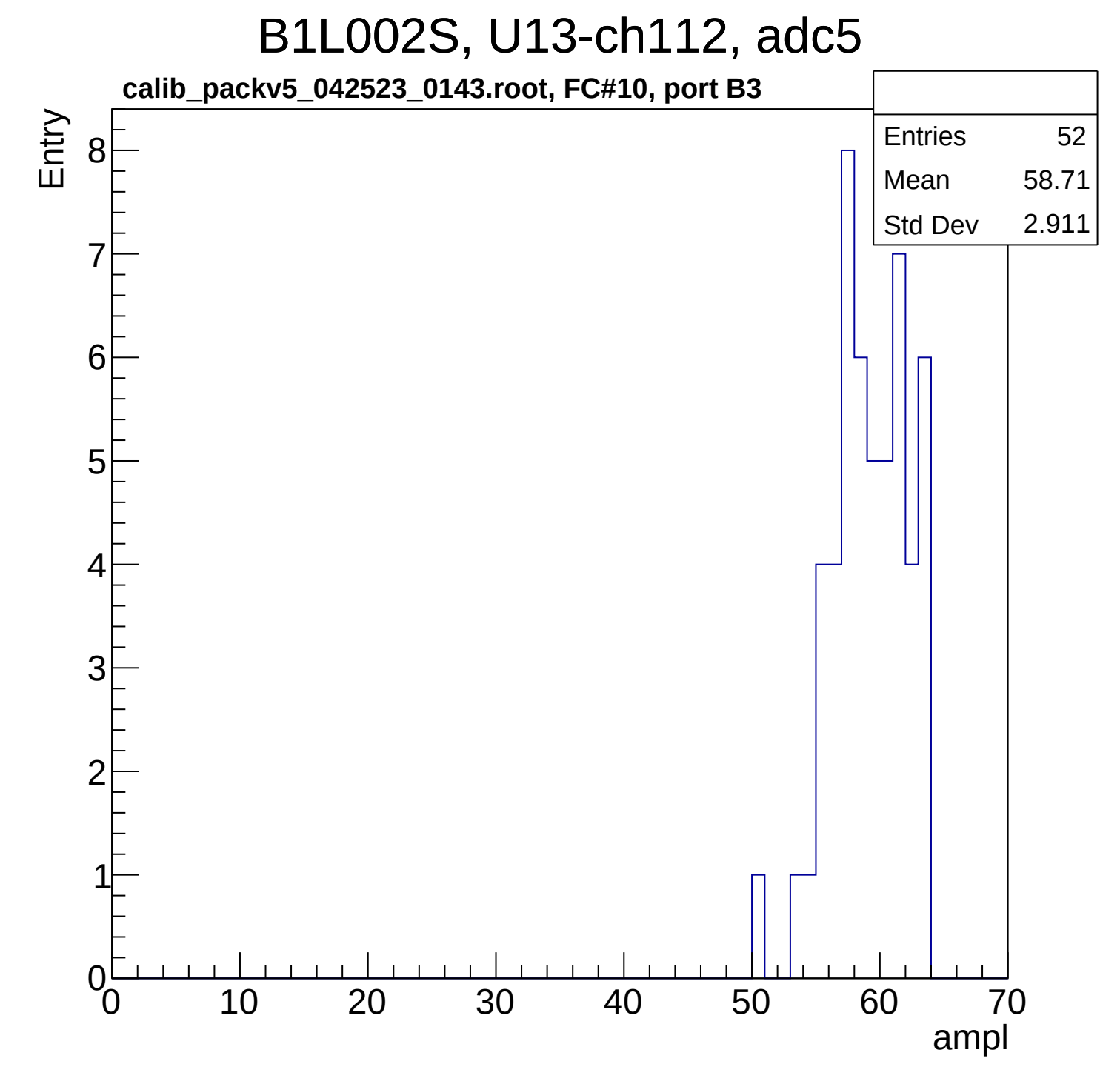

B1L002S, U13-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 52.97
Std Dev 3.488
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.71
Std Dev 2.911
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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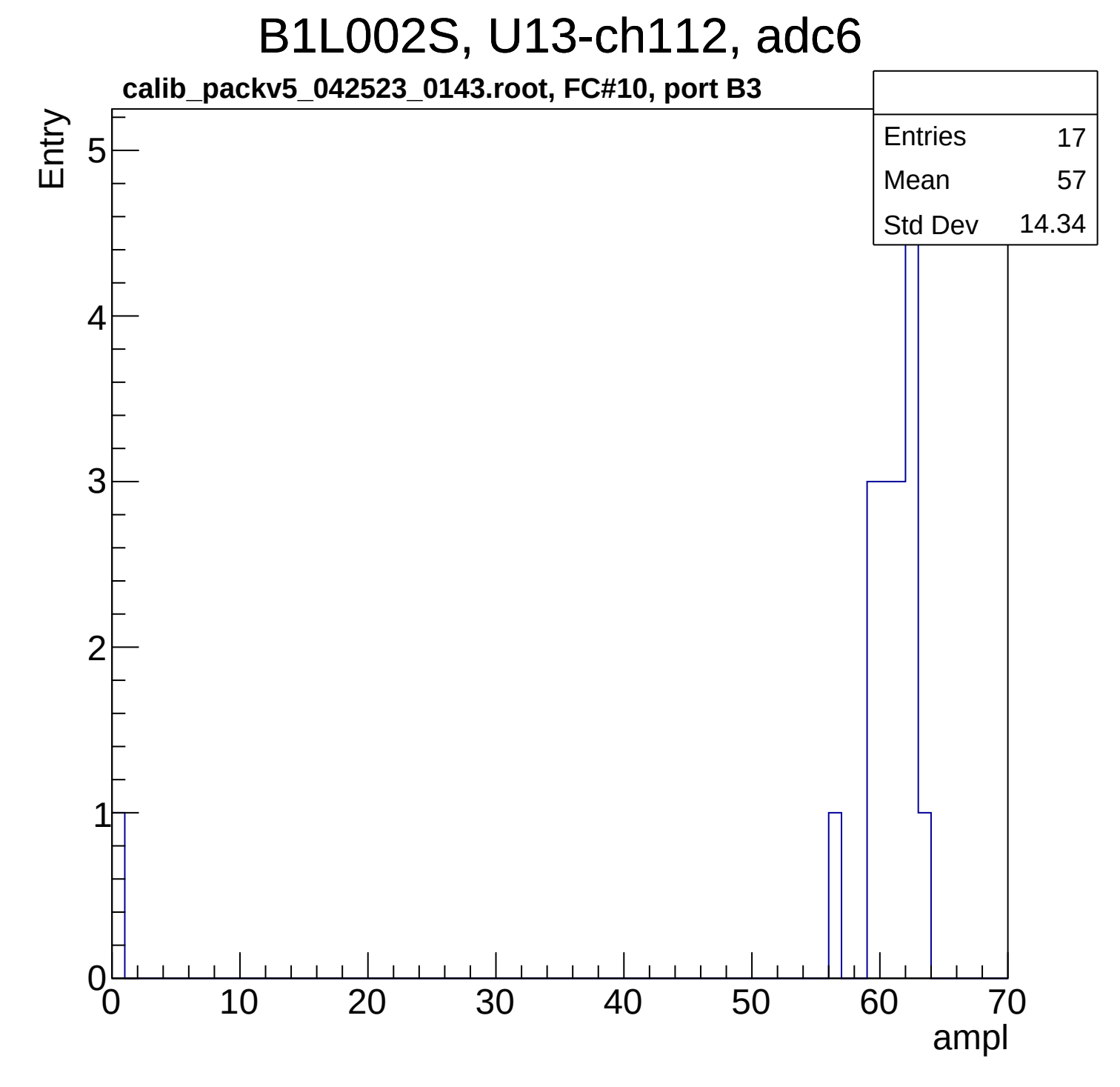
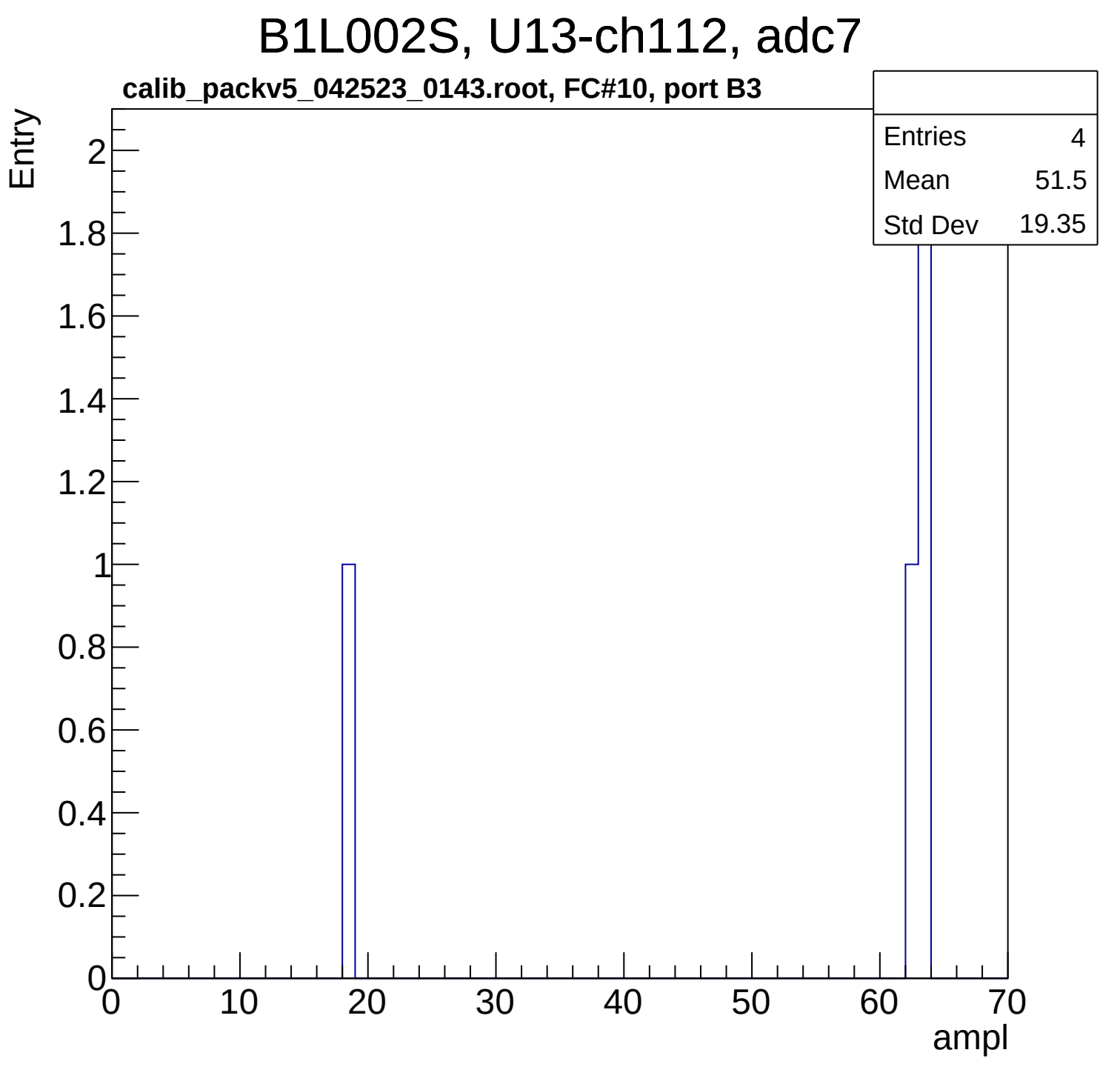

B1L002S, U13-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57
Std Dev 14.34
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U13-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.5
Std Dev 19.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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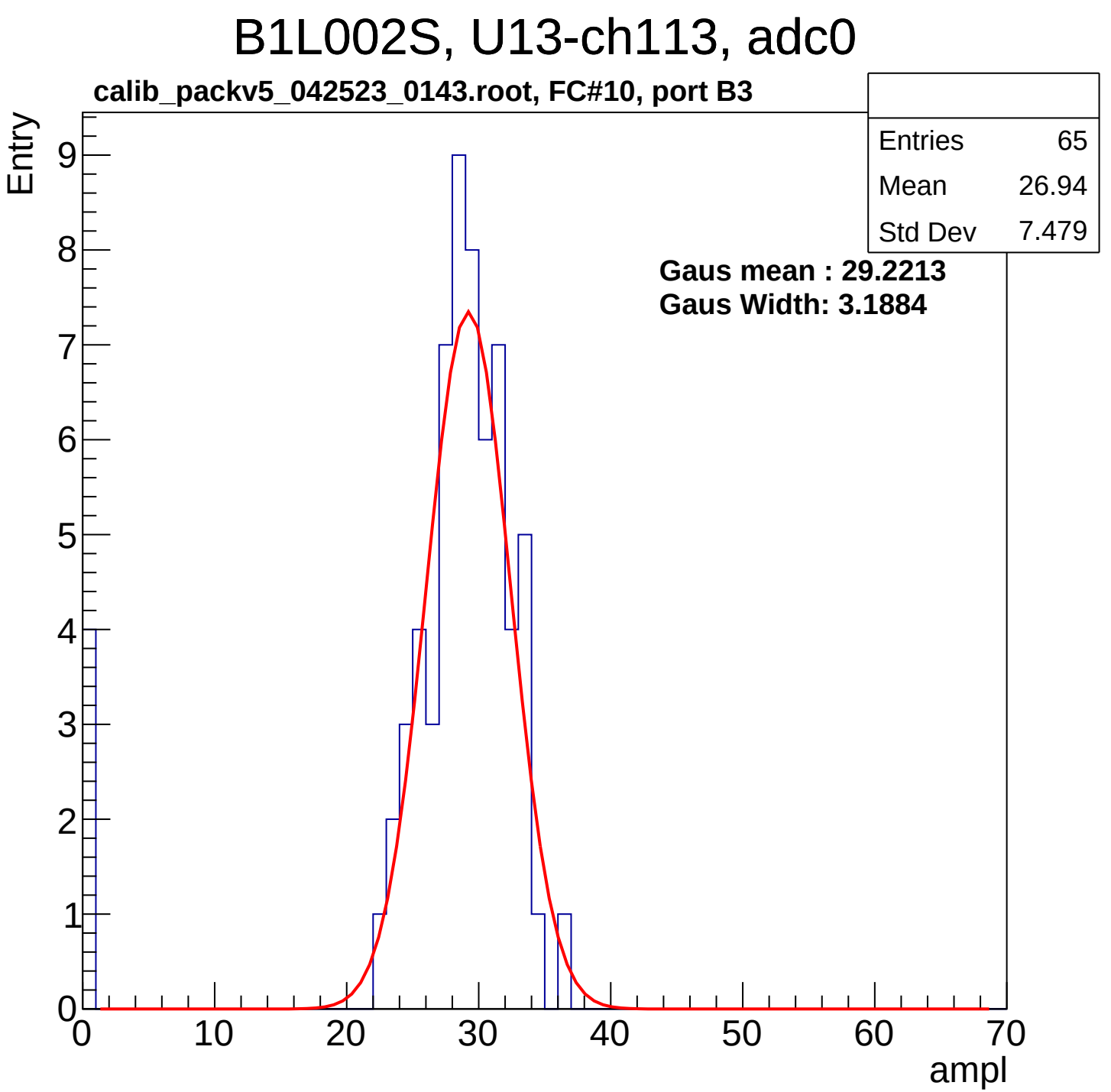
B1L002S, U13-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.94
Std Dev 7.479
Gaus mean : 29.2213
Gaus Width: 3.1884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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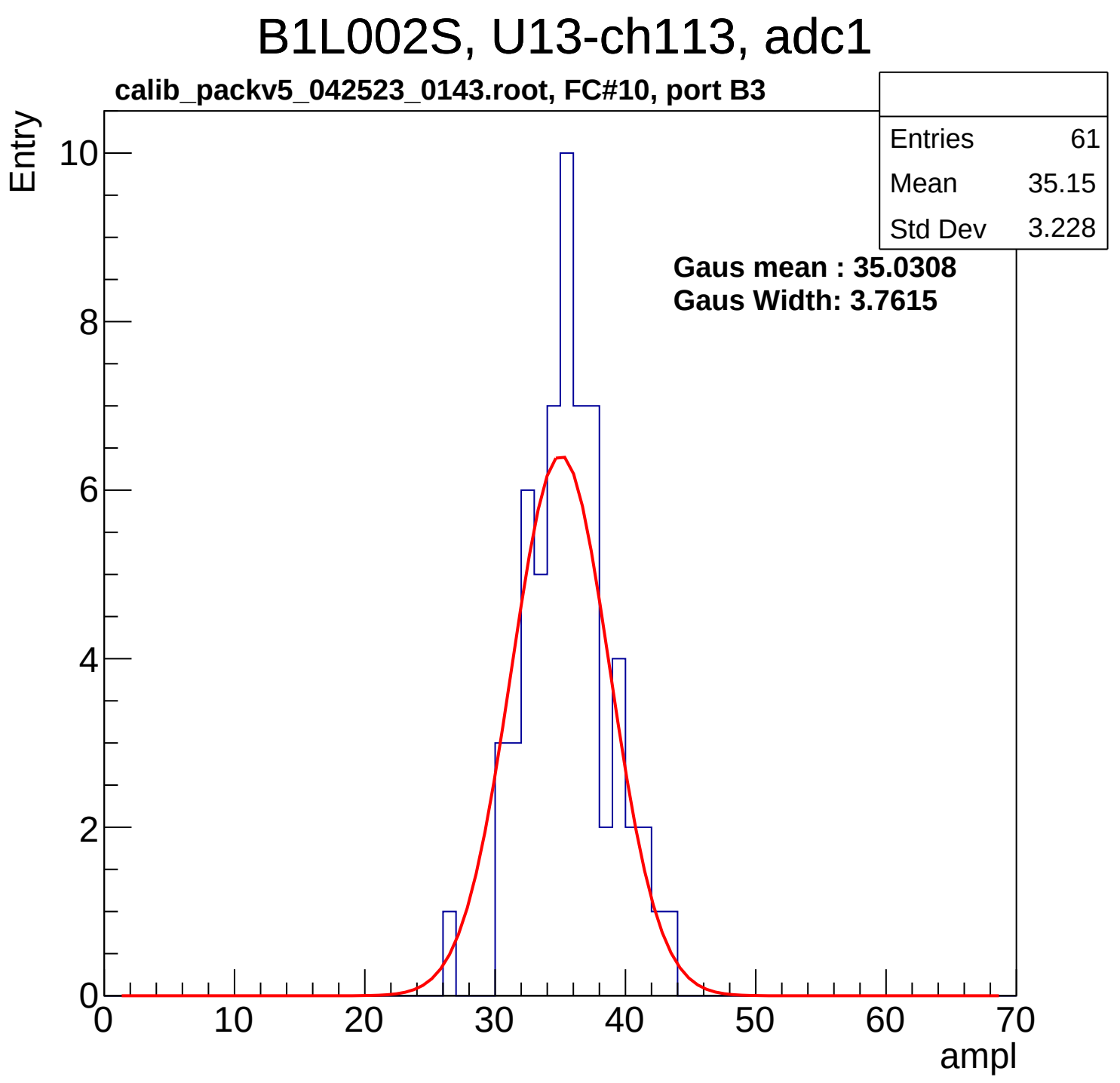
B1L002S, U13-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.15
Std Dev 3.228
Gaus mean : 35.0308
Gaus Width: 3.7615
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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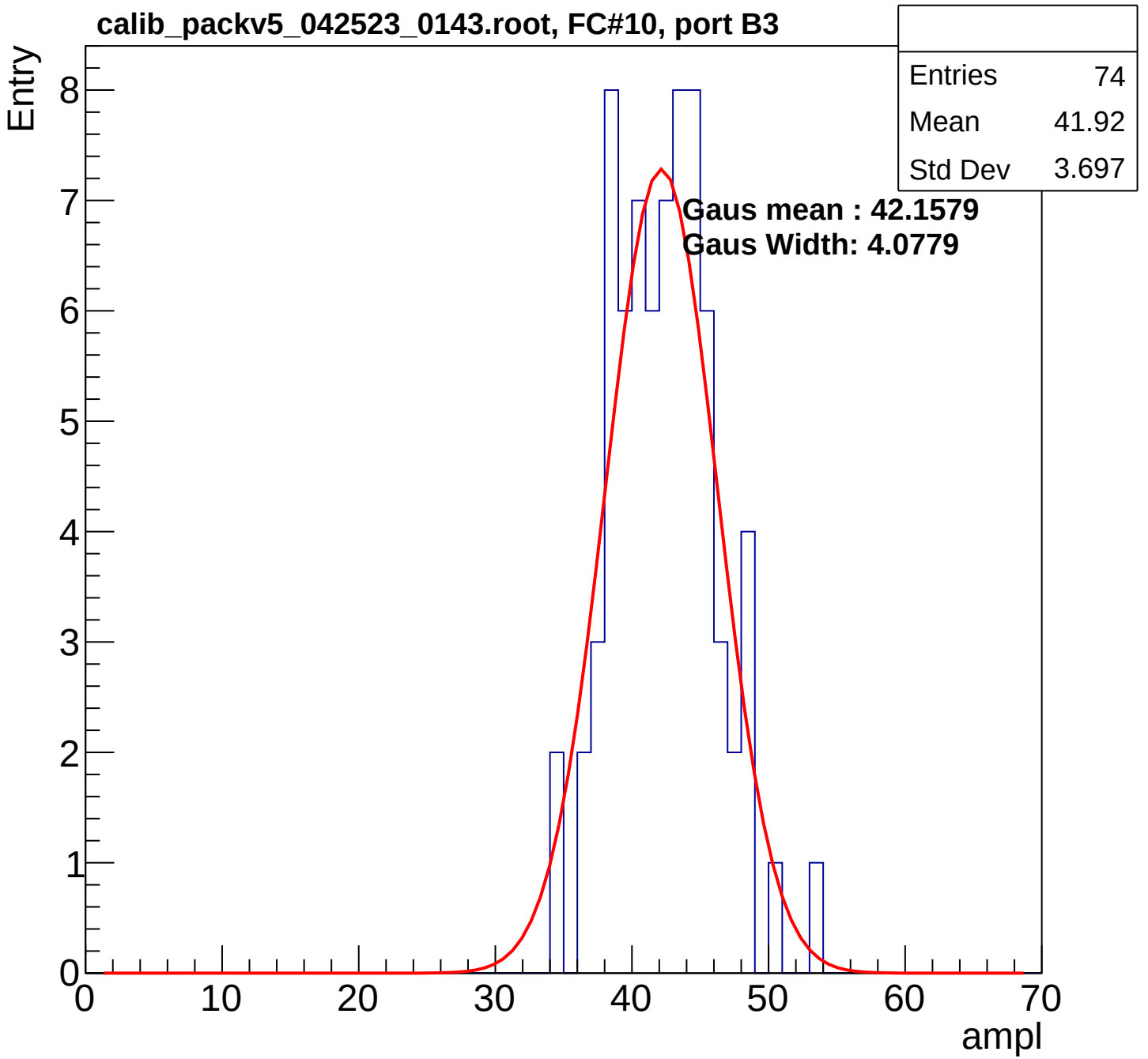
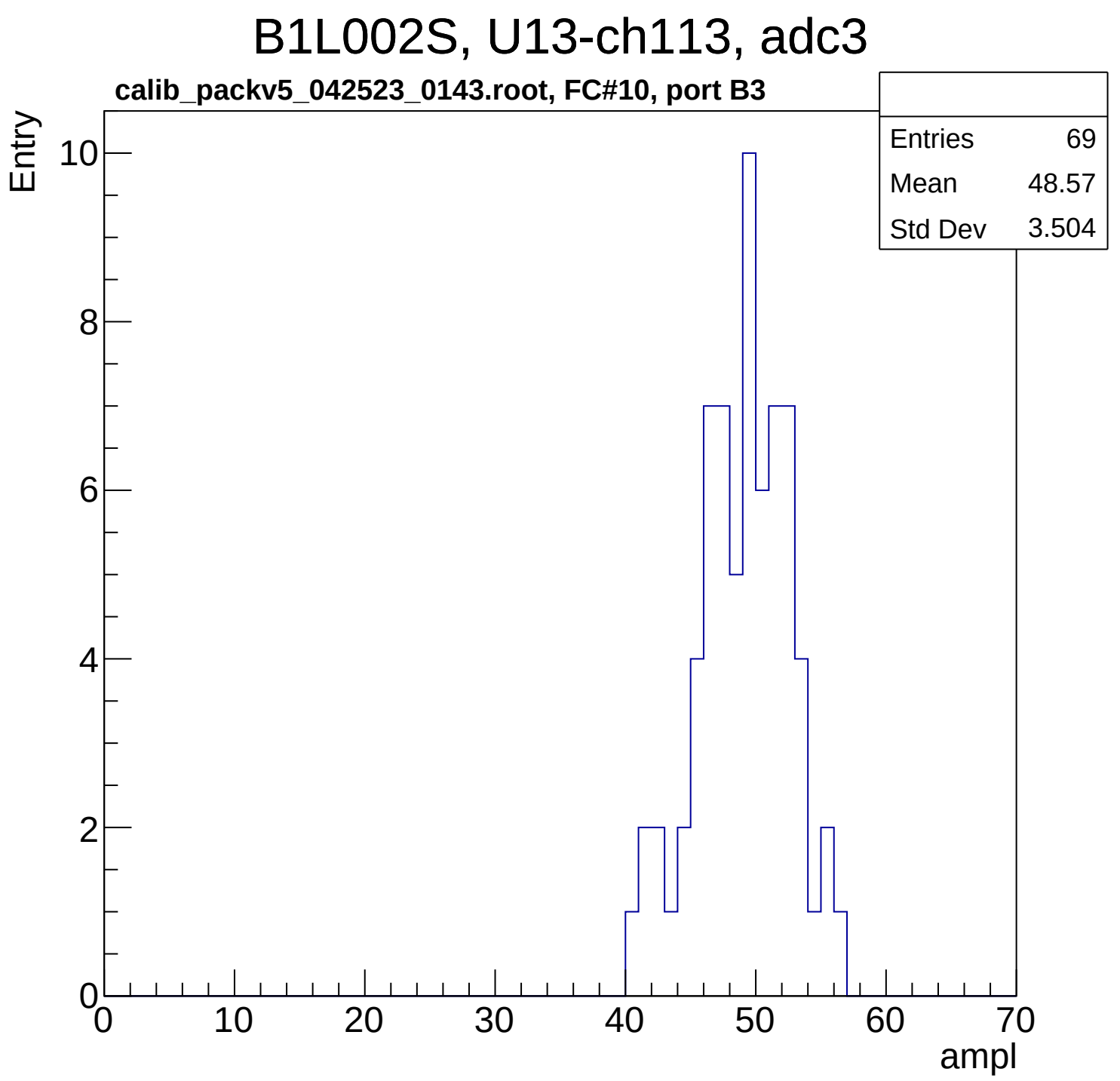
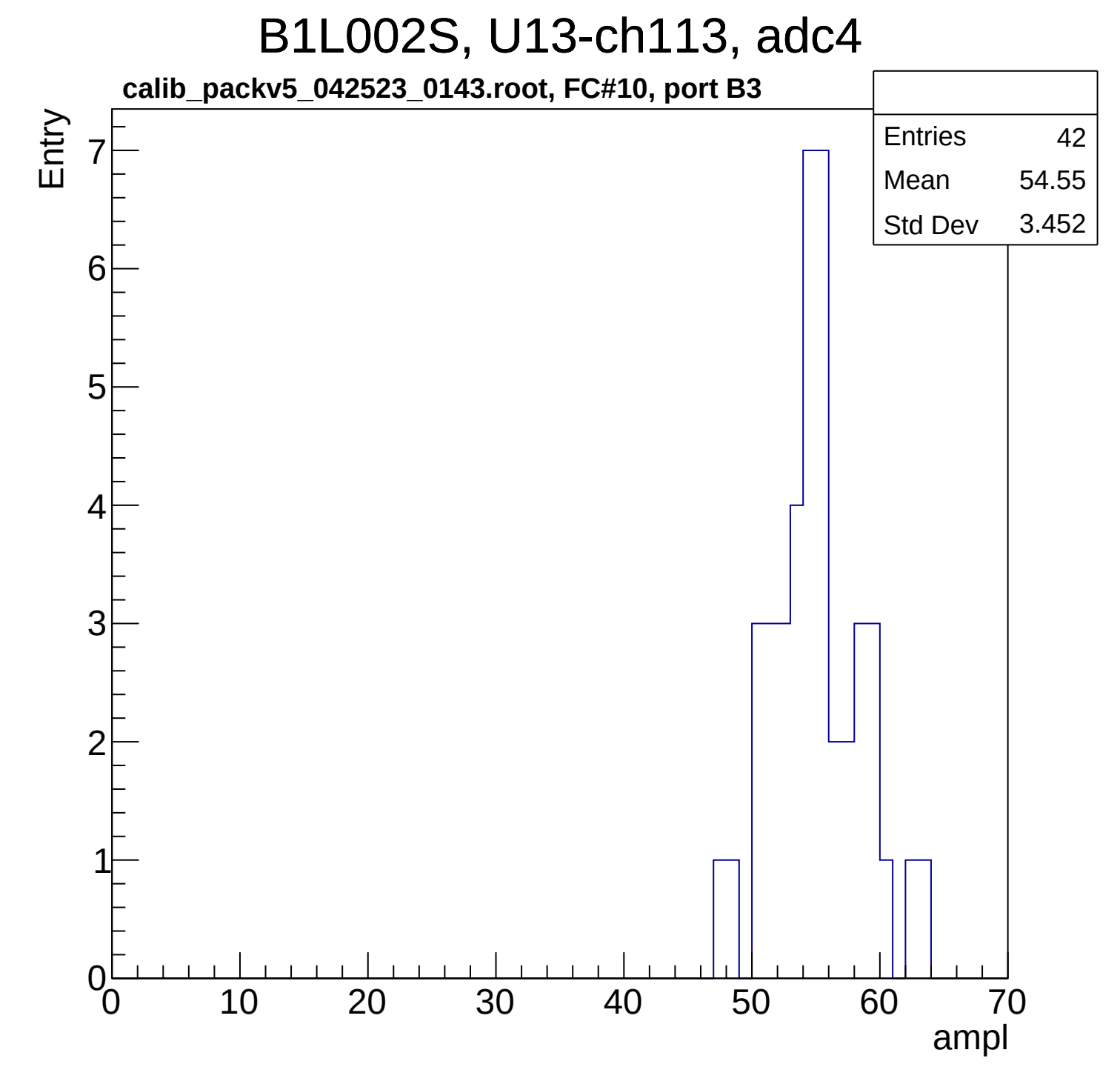
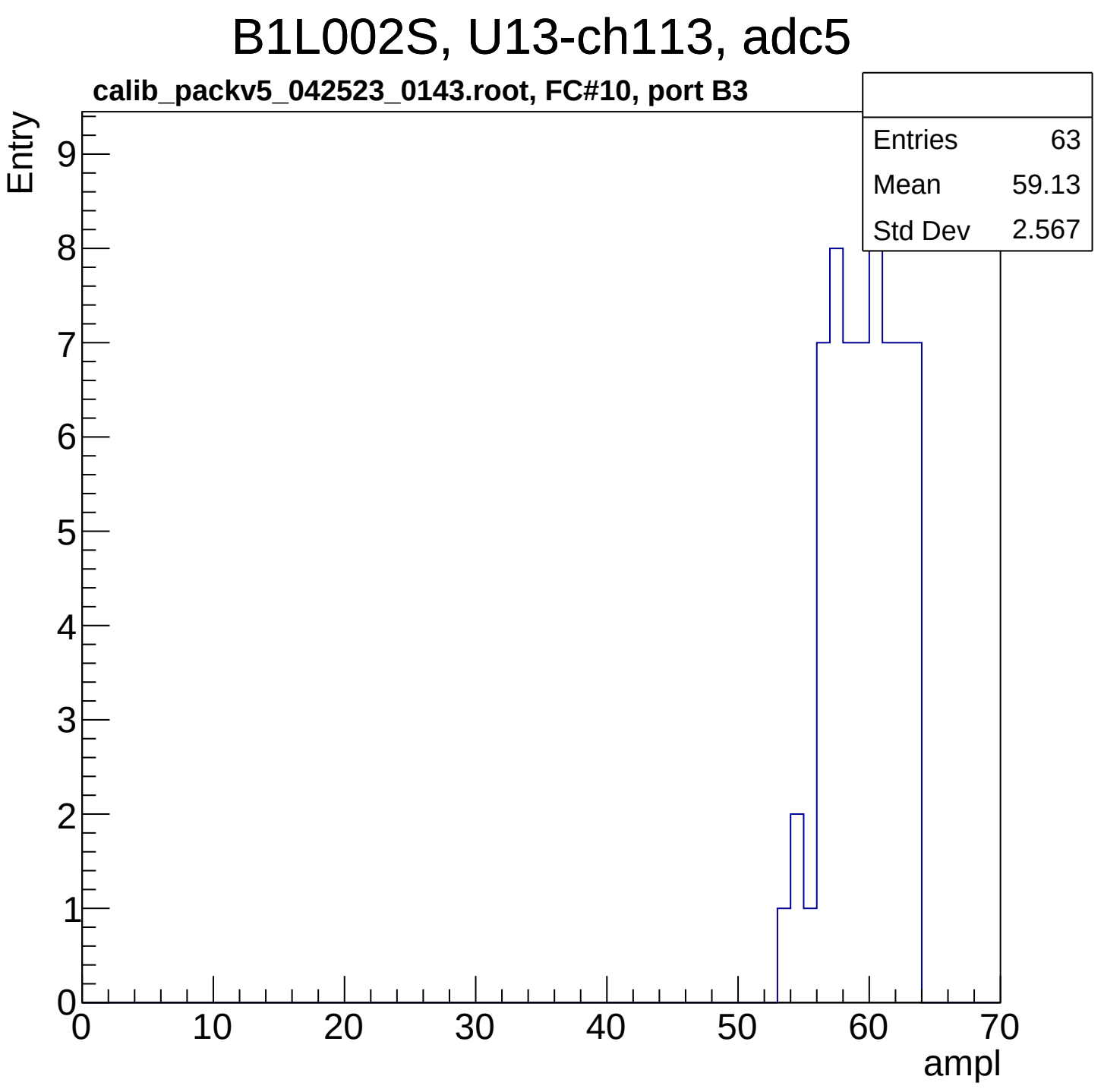
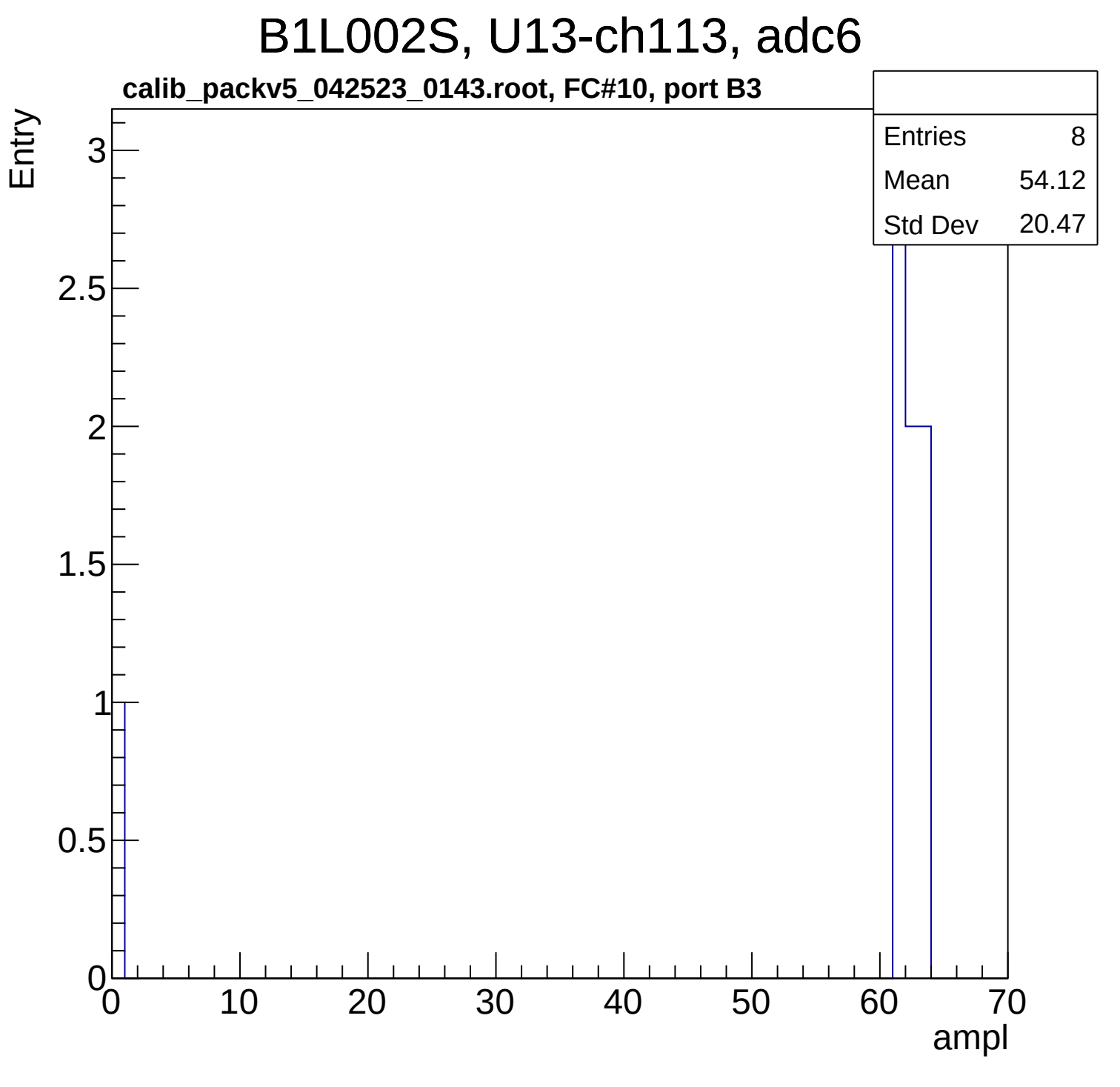

B1L002S, U13-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.92
Std Dev 3.697
Gaus mean : 42.1579
Gaus Width: 4.0779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.57
Std Dev 3.504
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 54.55
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 59.13
Std Dev 2.567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.12
Std Dev 20.47
Entry
ampl
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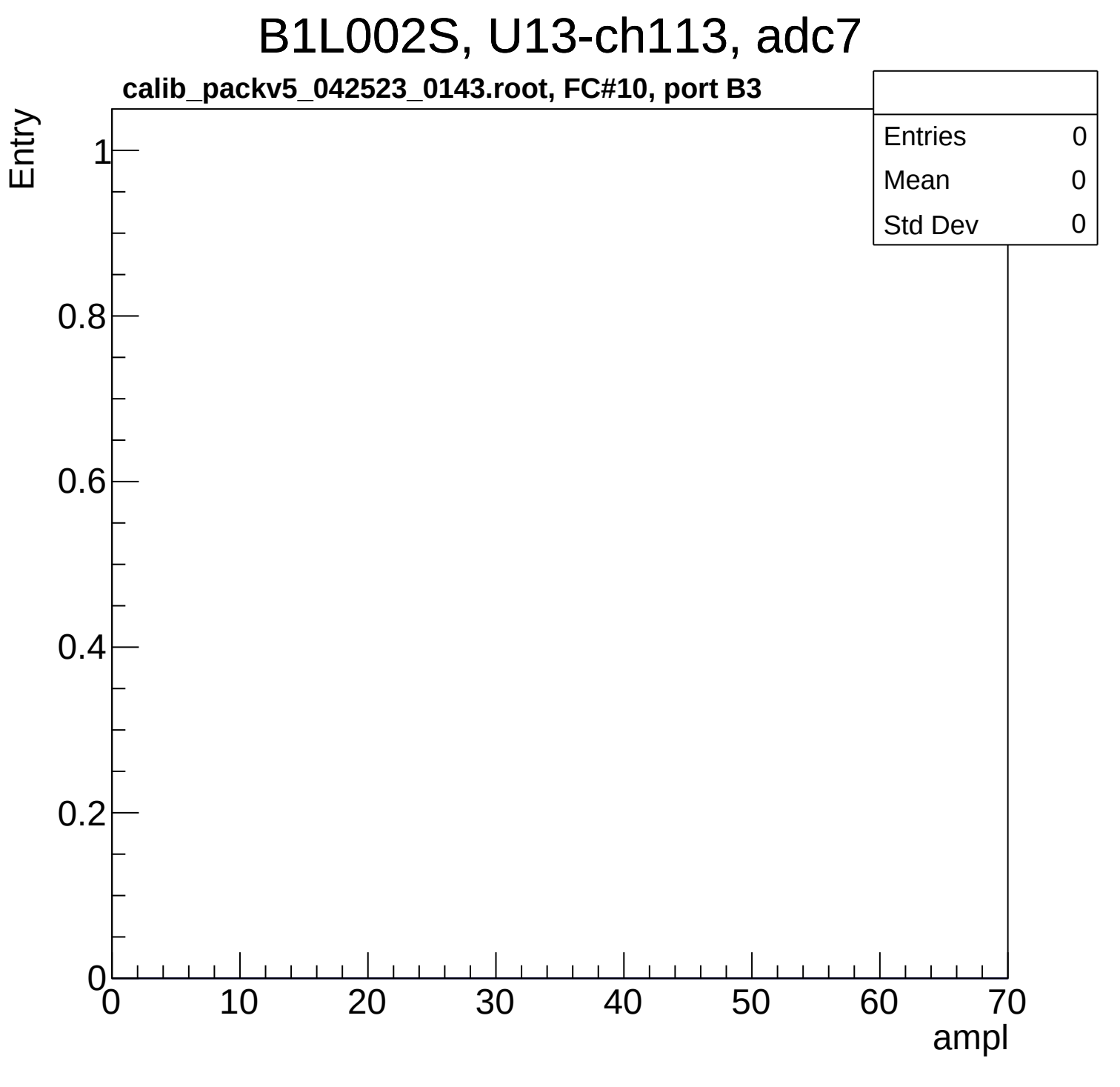
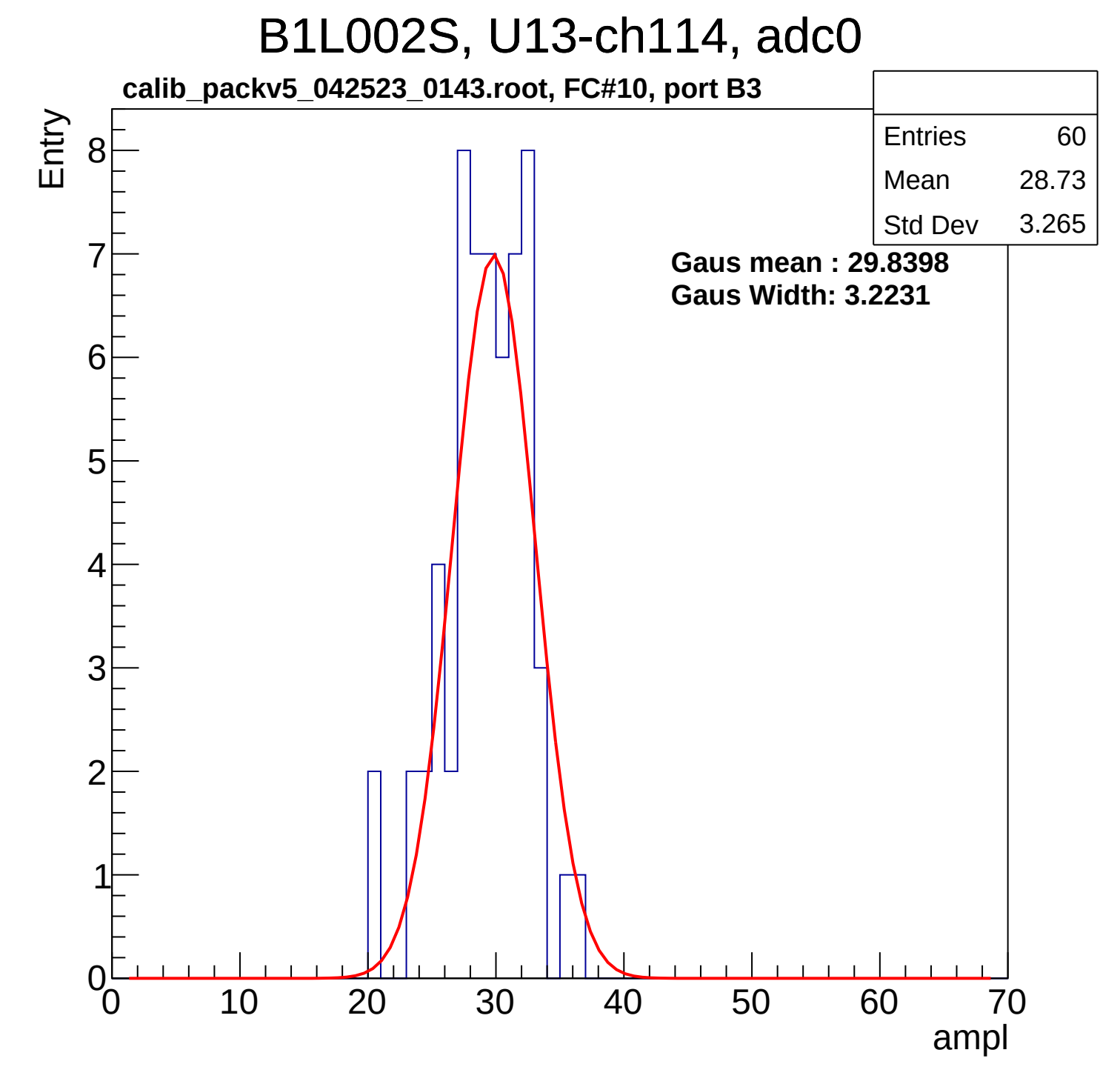
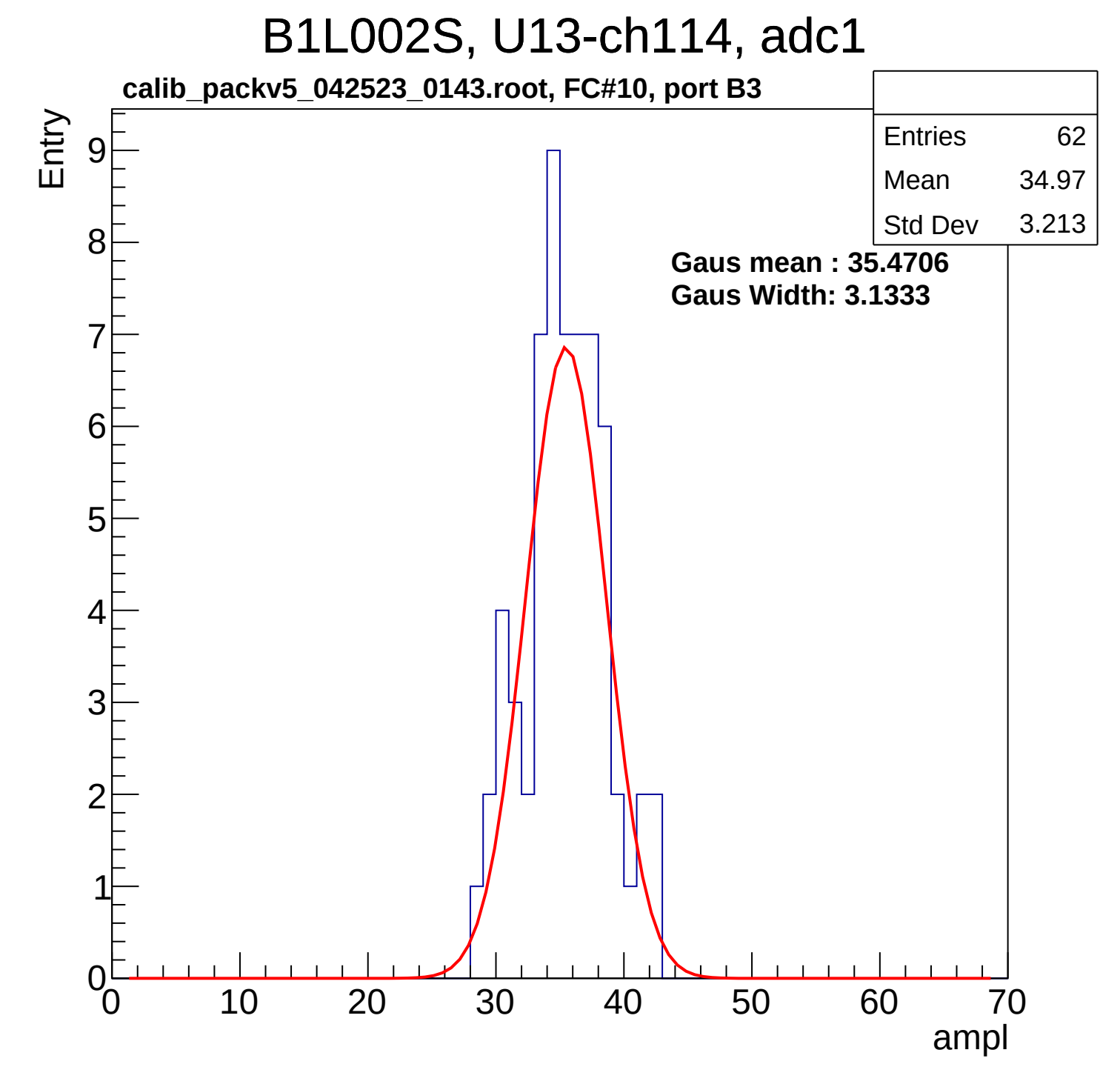
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.73
Std Dev 3.265
Gaus mean : 29.8398
Gaus Width: 3.2231
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.97
Std Dev 3.213
Gaus mean : 35.4706
Gaus Width: 3.1333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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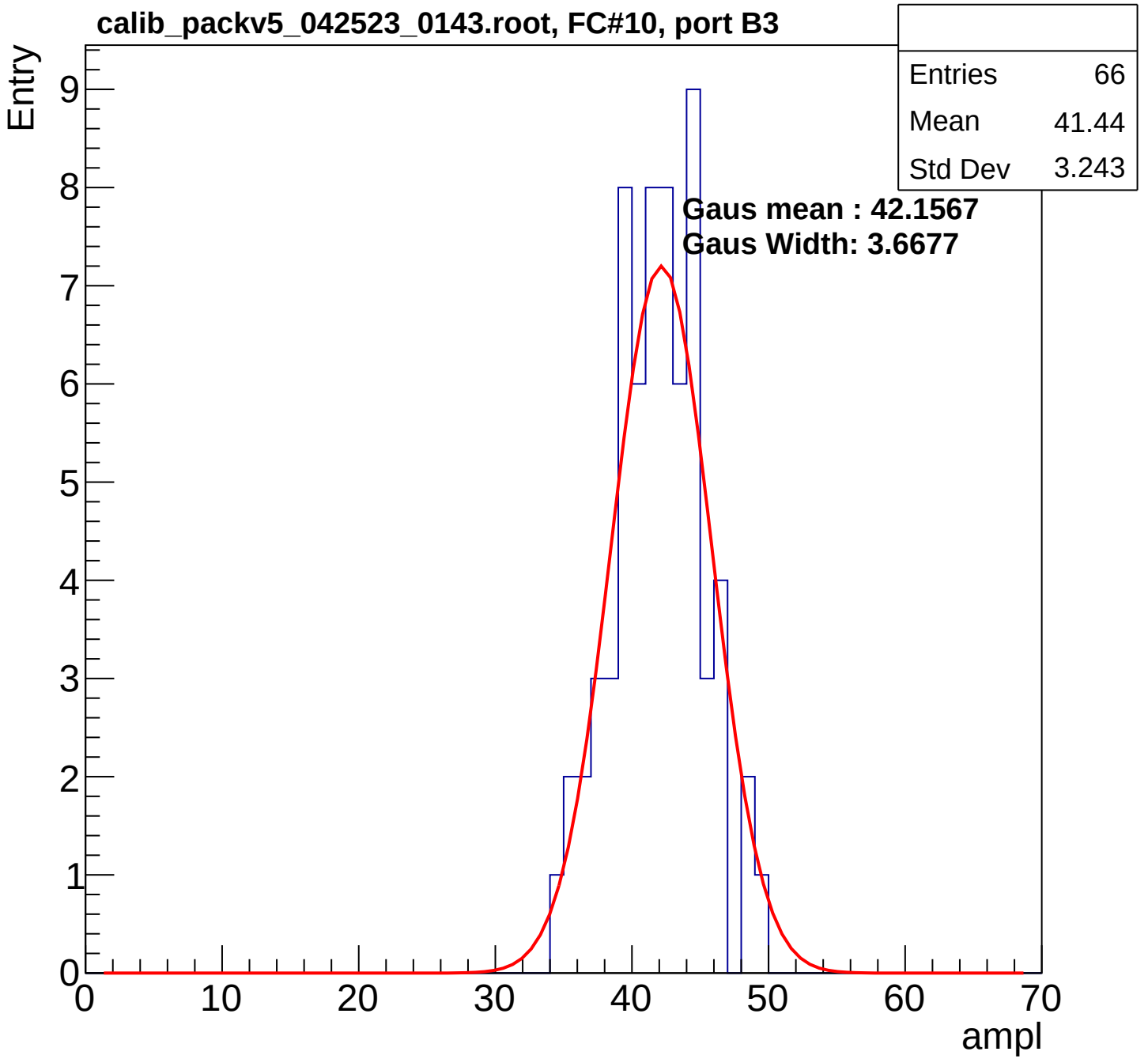
B1L002S, U13-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.44
Std Dev 3.243
Gaus mean : 42.1567
Gaus Width: 3.6677
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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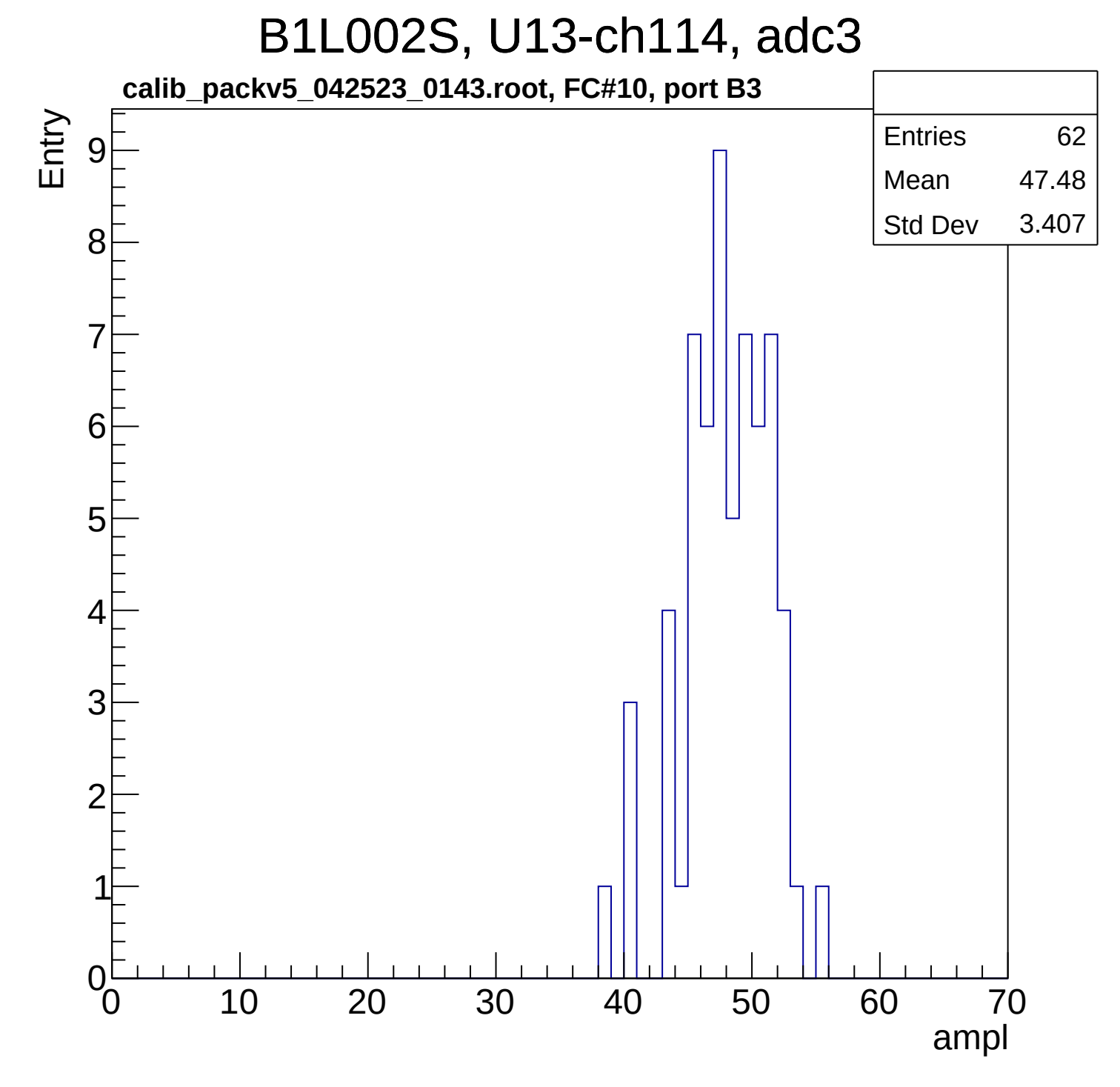
B1L002S, U13-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.48
Std Dev 3.407
Entry
ampl
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4
5
6
7
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0
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60
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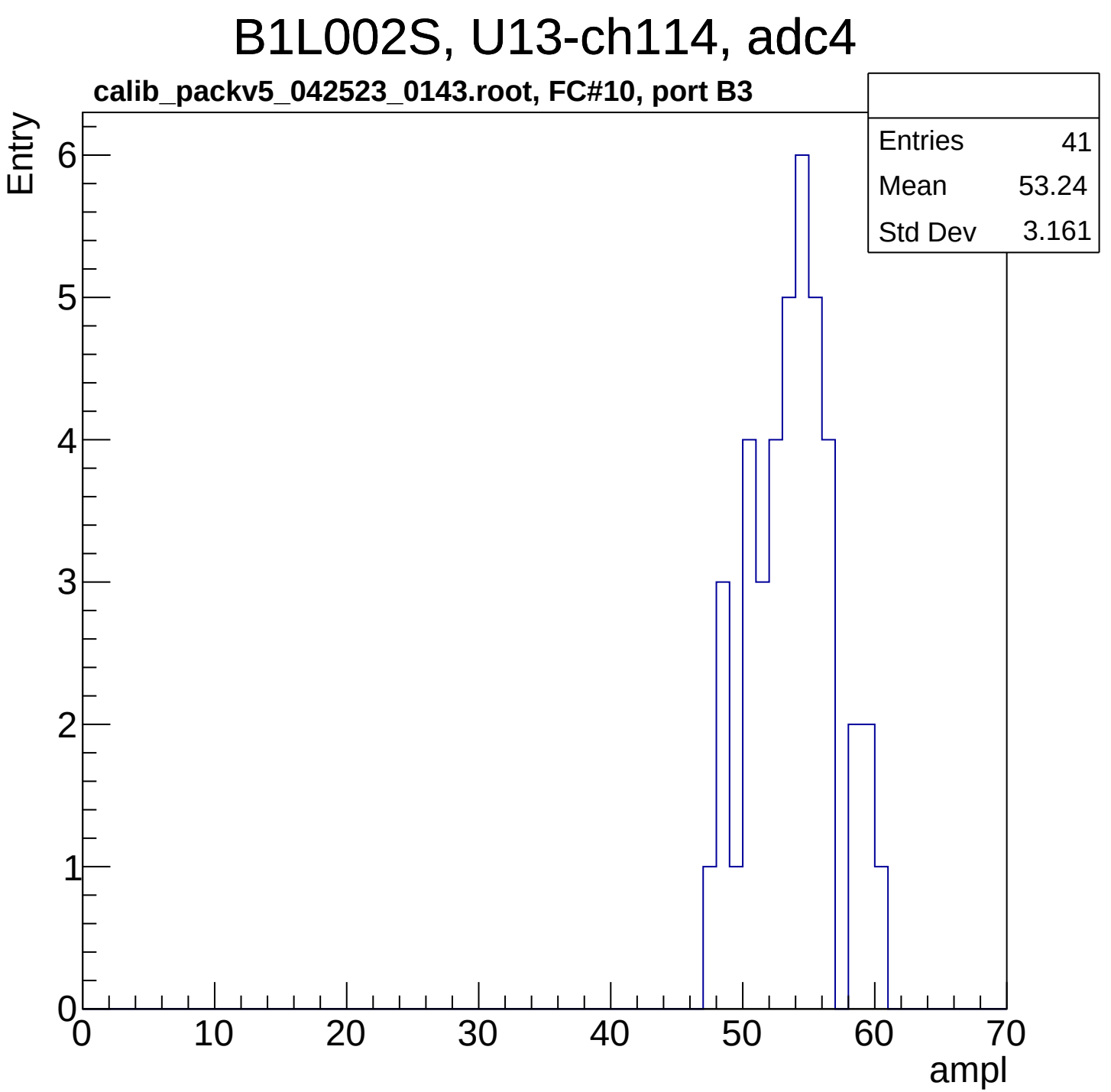
B1L002S, U13-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 53.24
Std Dev 3.161
Entry
0
1
2
3
4
5
6
0
10
20
30
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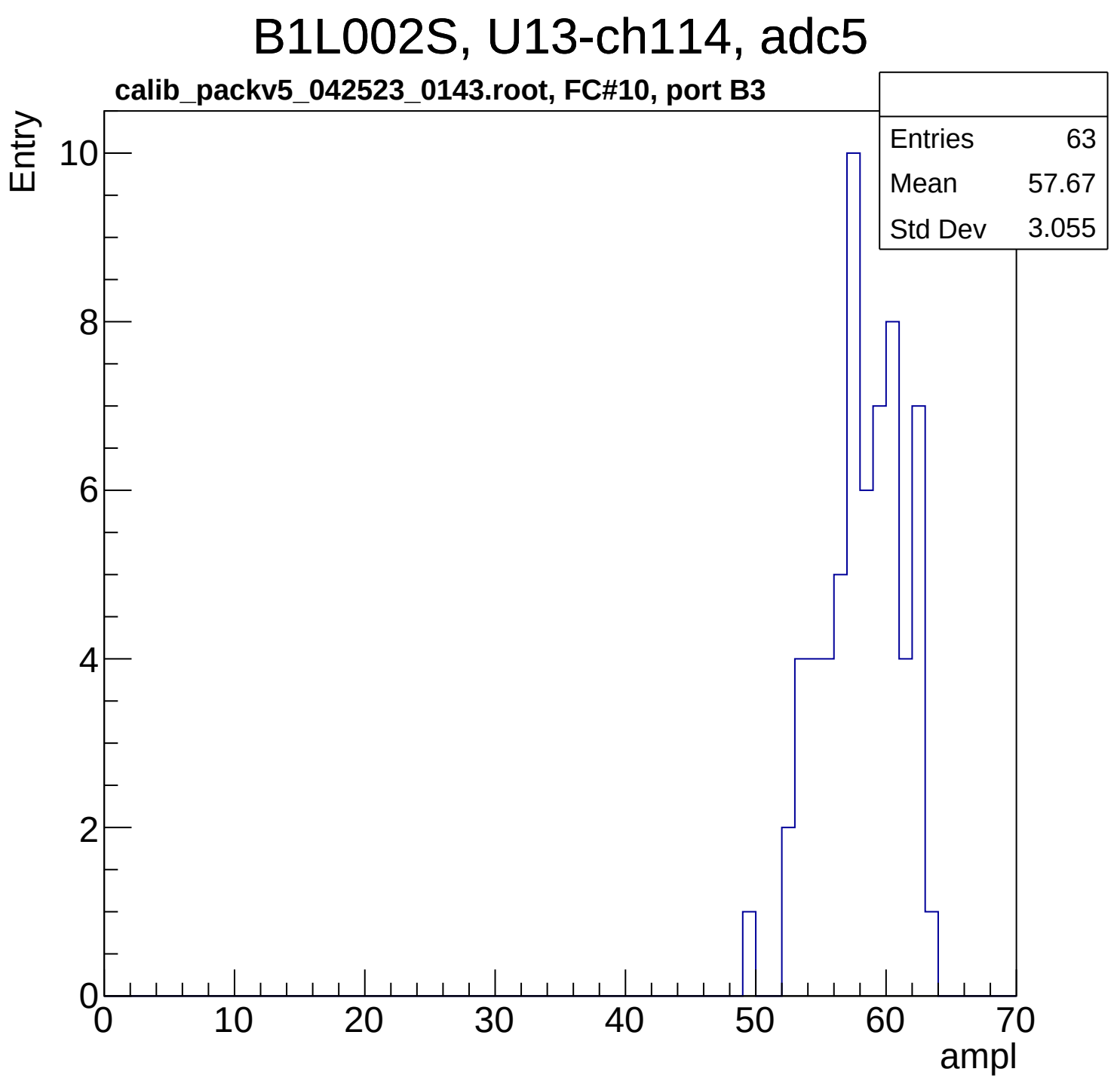
50
60
70
ampl


B1L002S, U13-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.67
Std Dev 3.055
Entry
0
2
4
6
8
10
0
10
20
30
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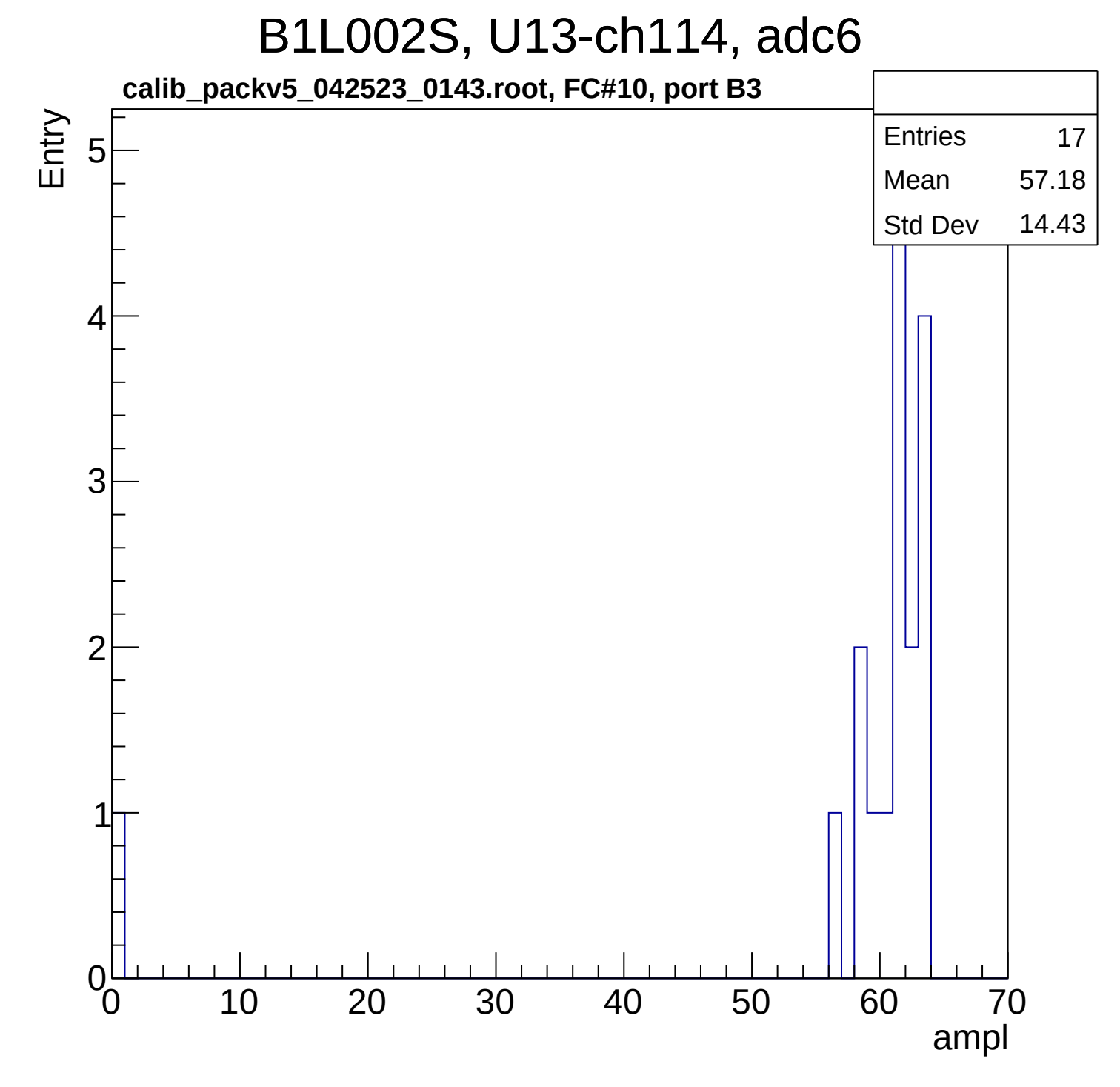
50
60
70
ampl


B1L002S, U13-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.18
Std Dev 14.43
Entry
ampl
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3
4
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0
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20
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40
50
60
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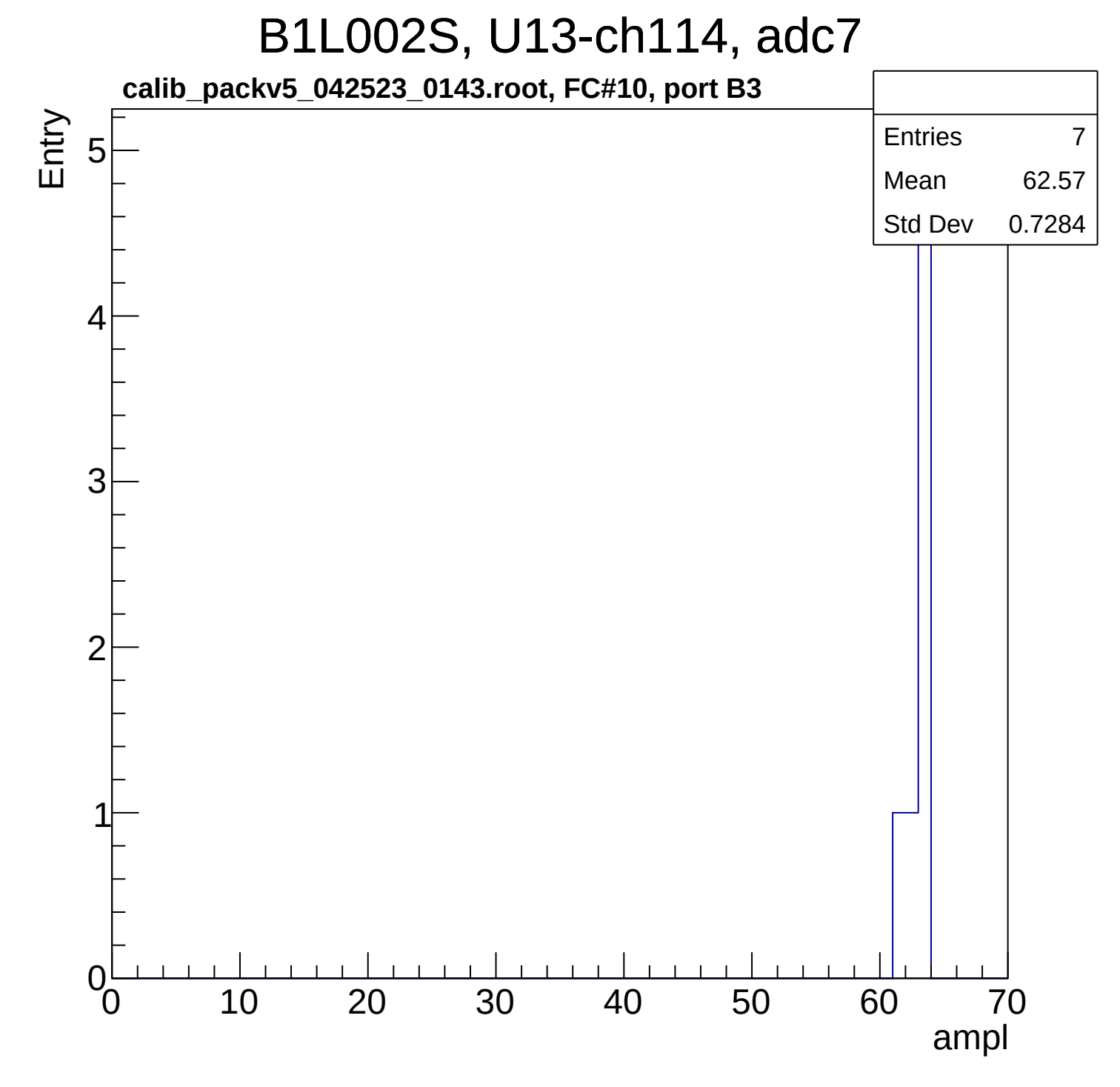
B1L002S, U13-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
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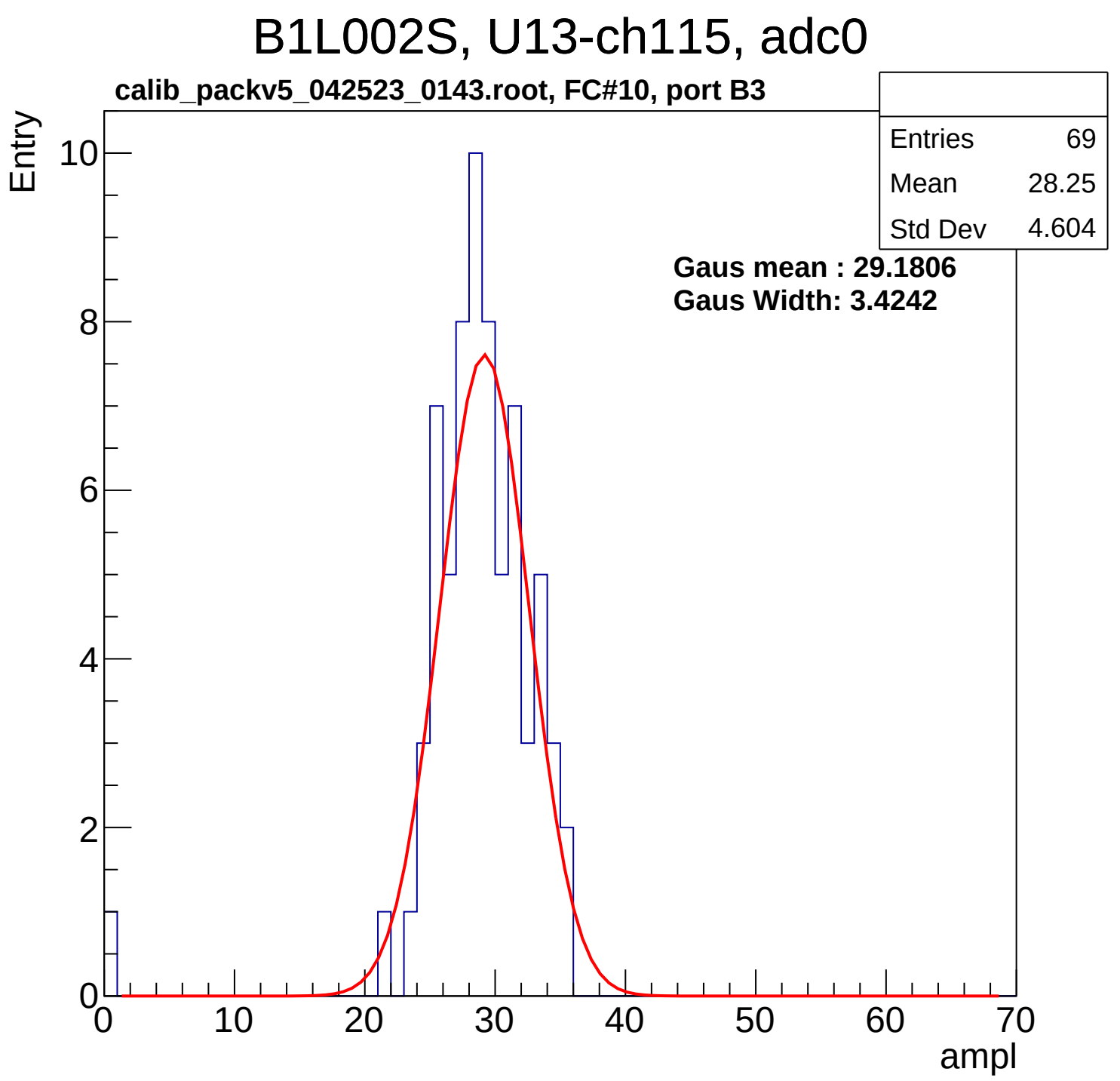
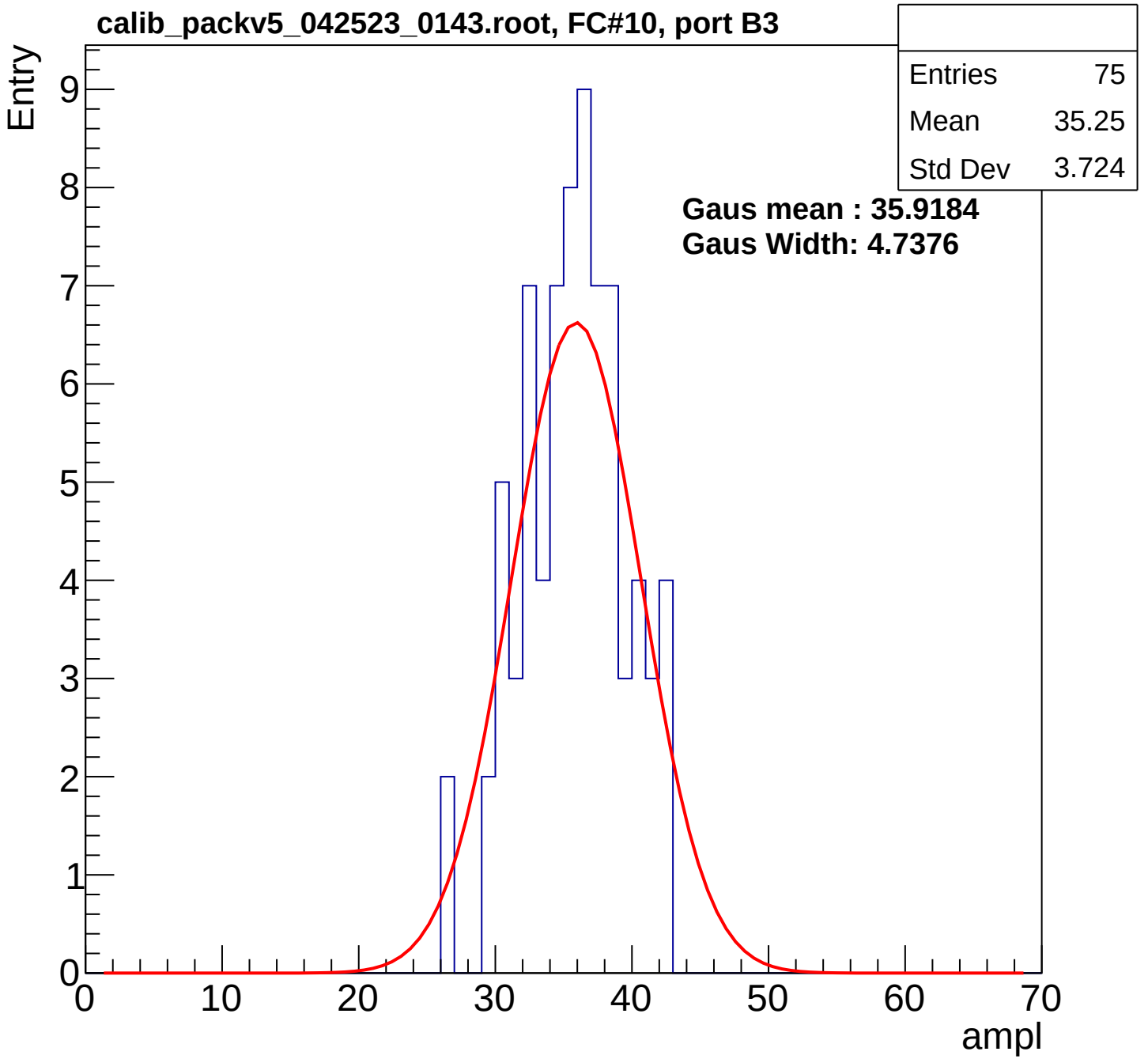
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.25
Std Dev 4.604
Gaus mean : 29.1806
Gaus Width: 3.4242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.25
Std Dev 3.724
Gaus mean : 35.9184
Gaus Width: 4.7376
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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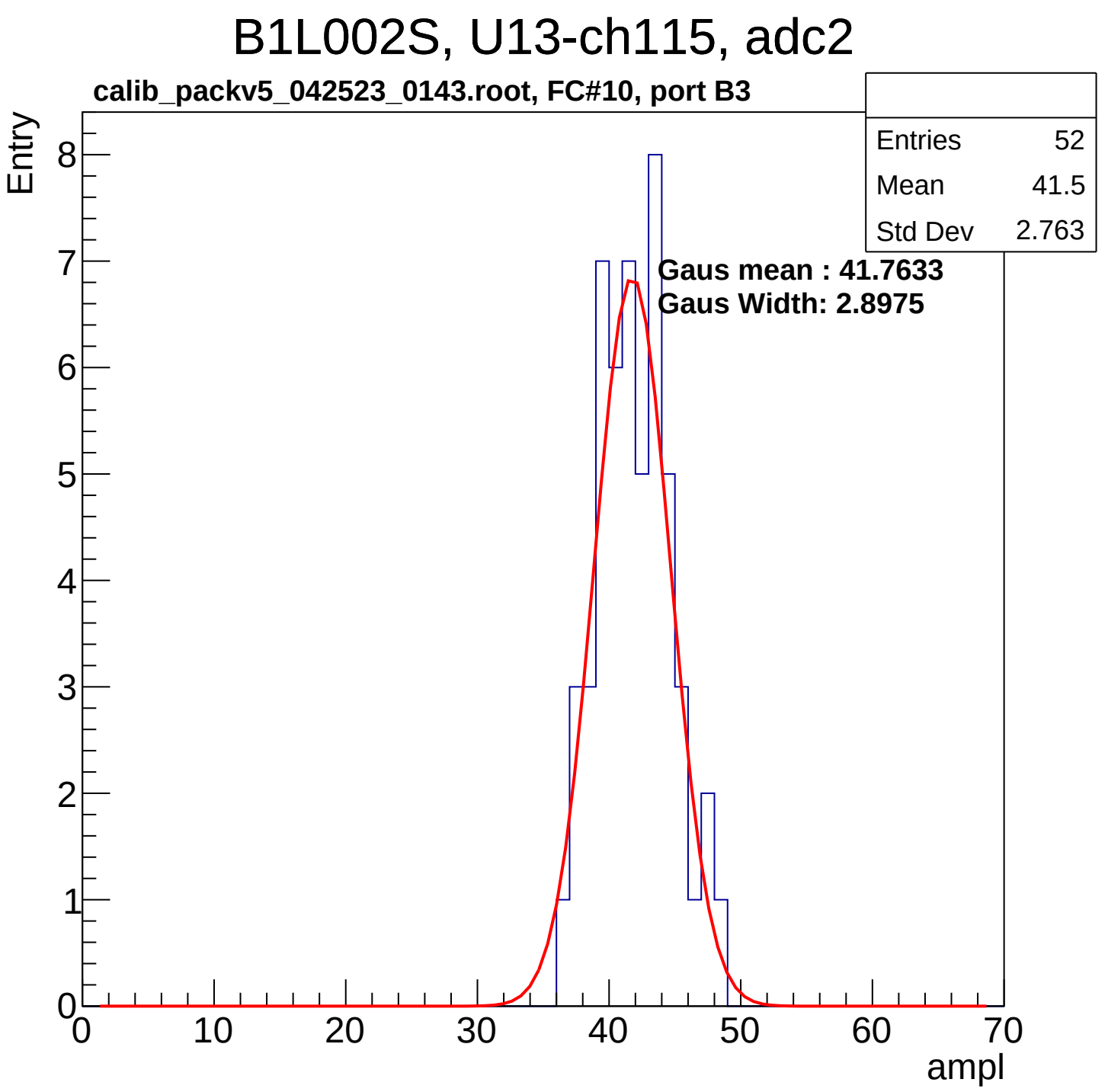
B1L002S, U13-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.5
Std Dev 2.763
Gaus mean : 41.7633
Gaus Width: 2.8975
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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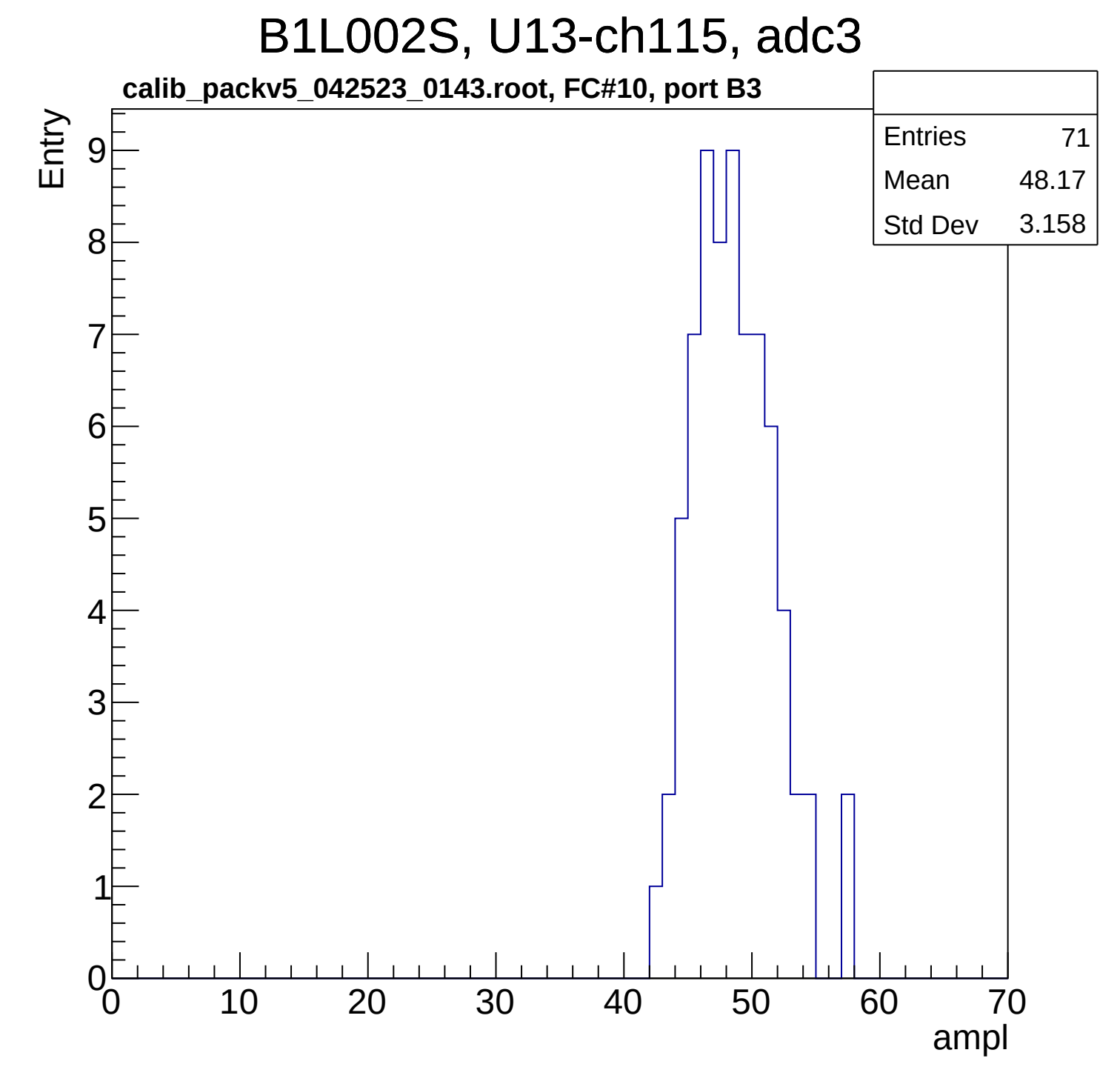
40
50
60
70


B1L002S, U13-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 48.17
Std Dev 3.158
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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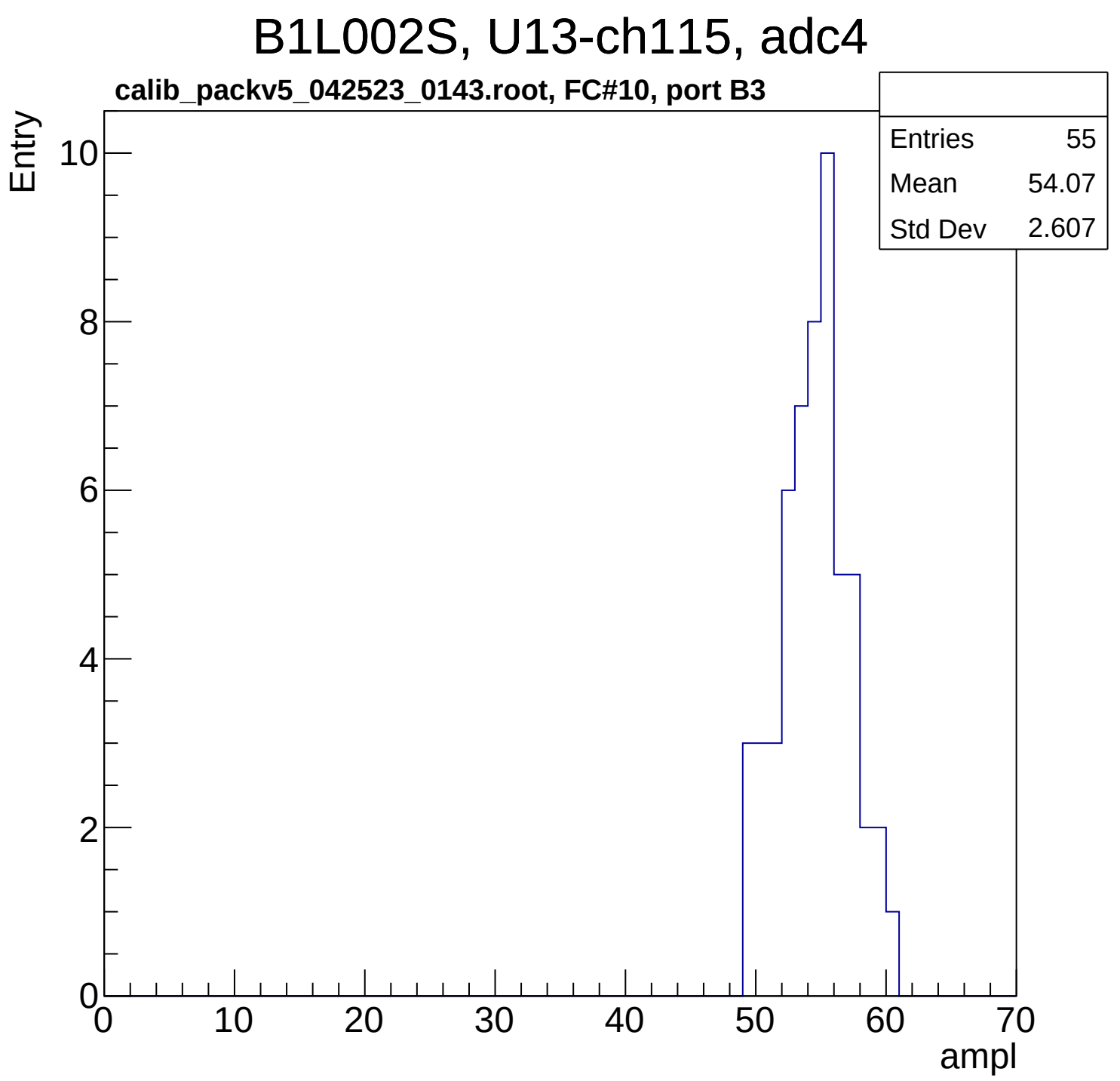
B1L002S, U13-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.07
Std Dev 2.607
Entry
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8
10
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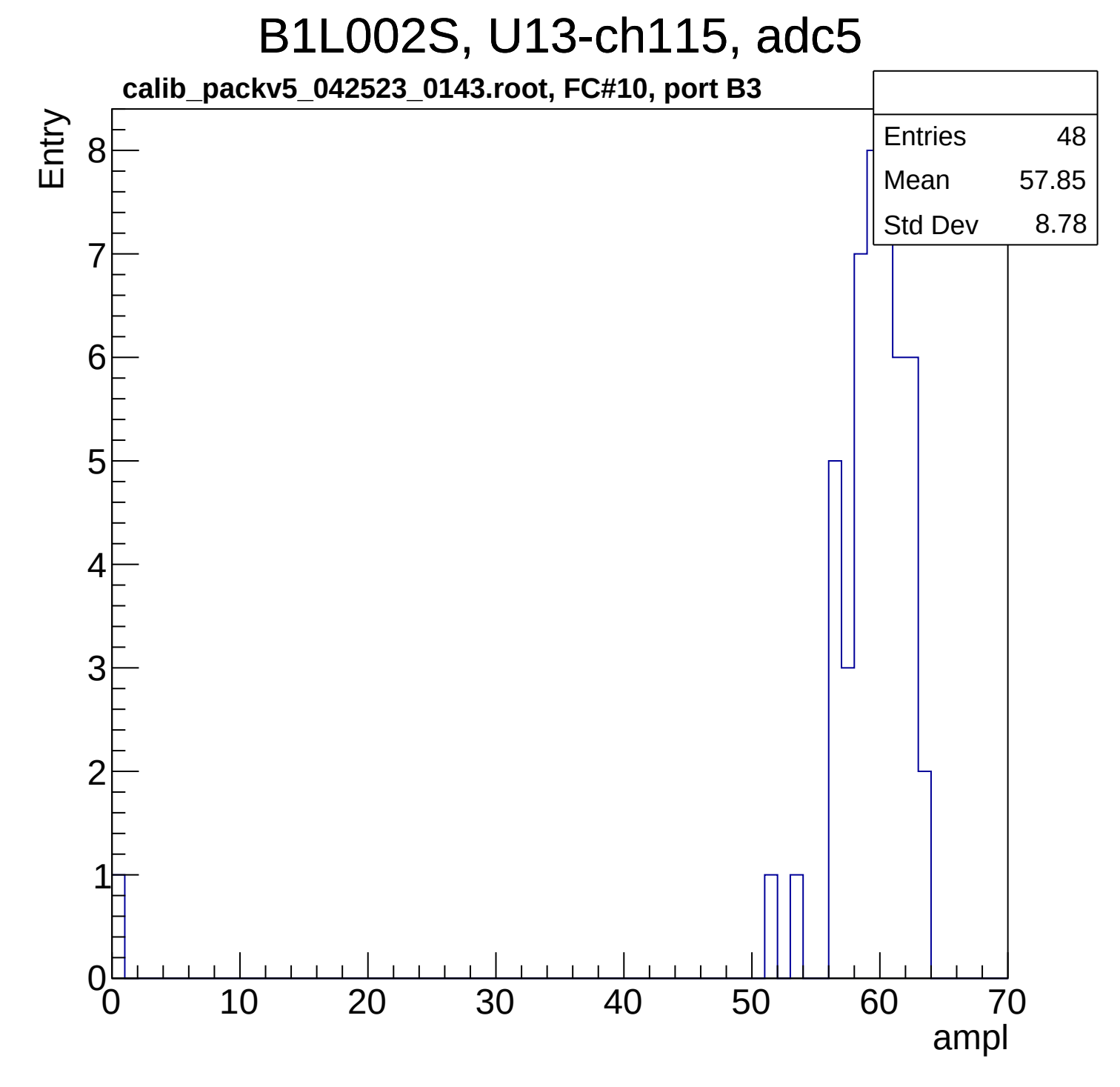
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.85
Std Dev 8.78
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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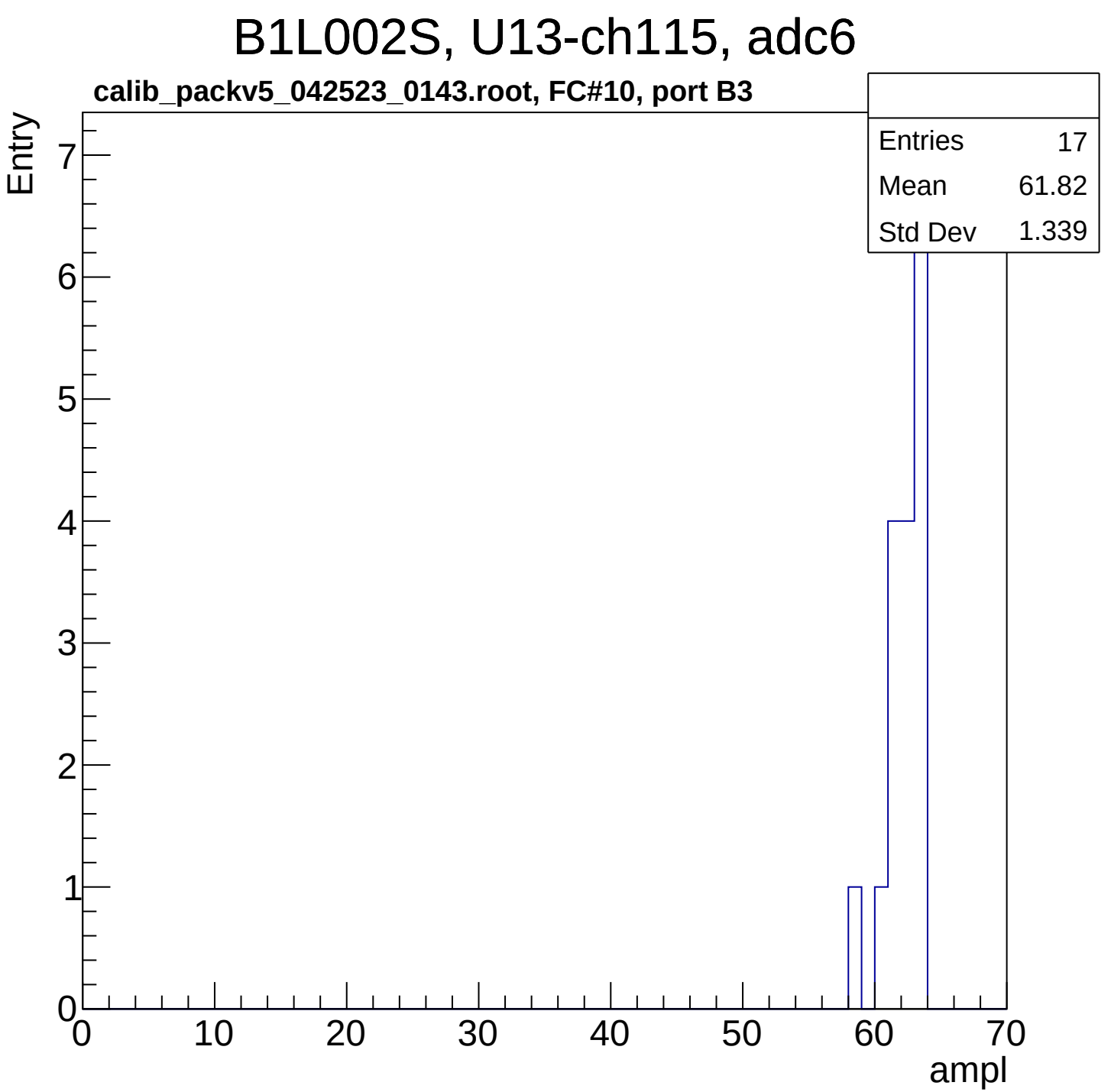
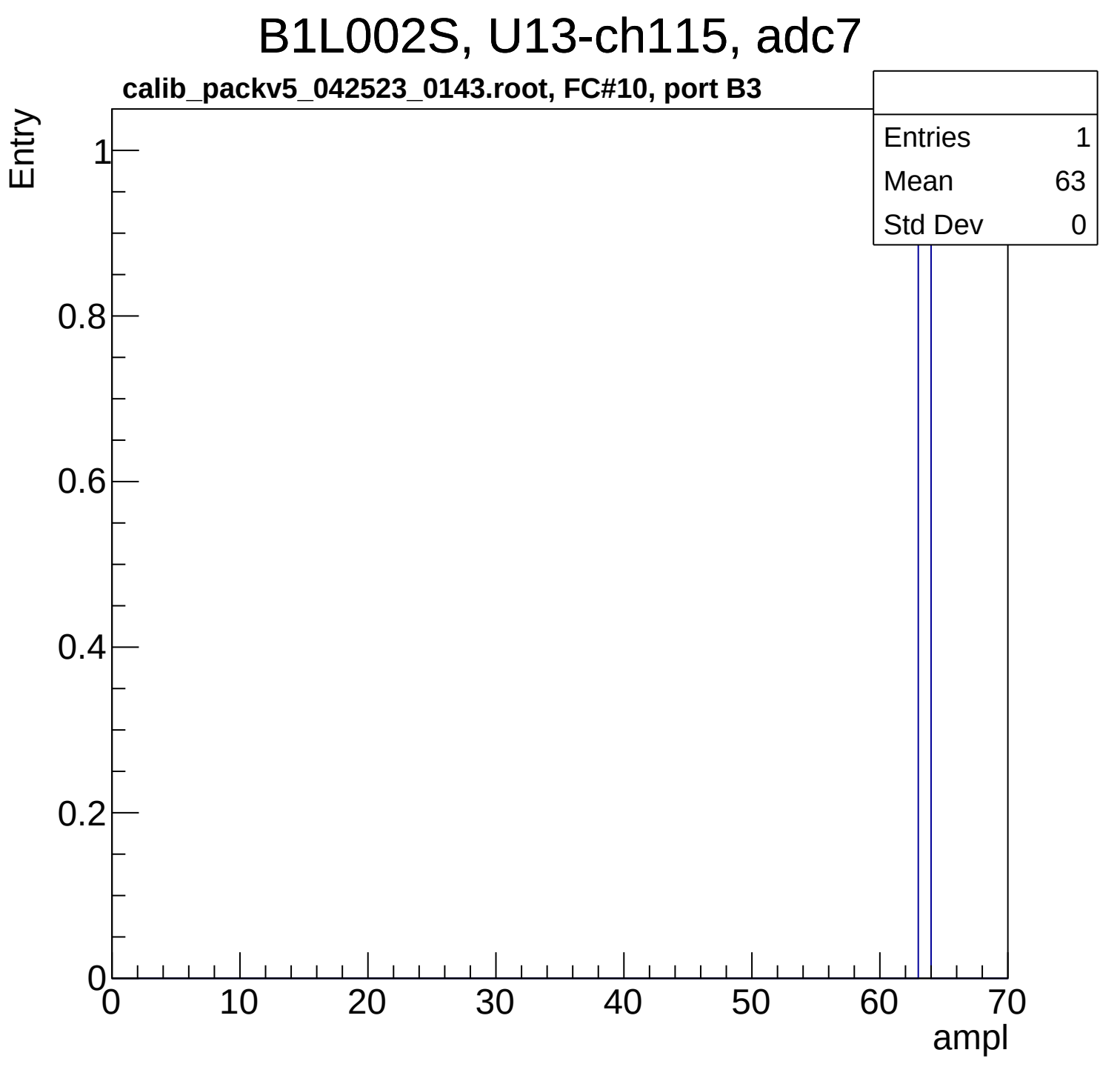

B1L002S, U13-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.82
Std Dev 1.339
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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10
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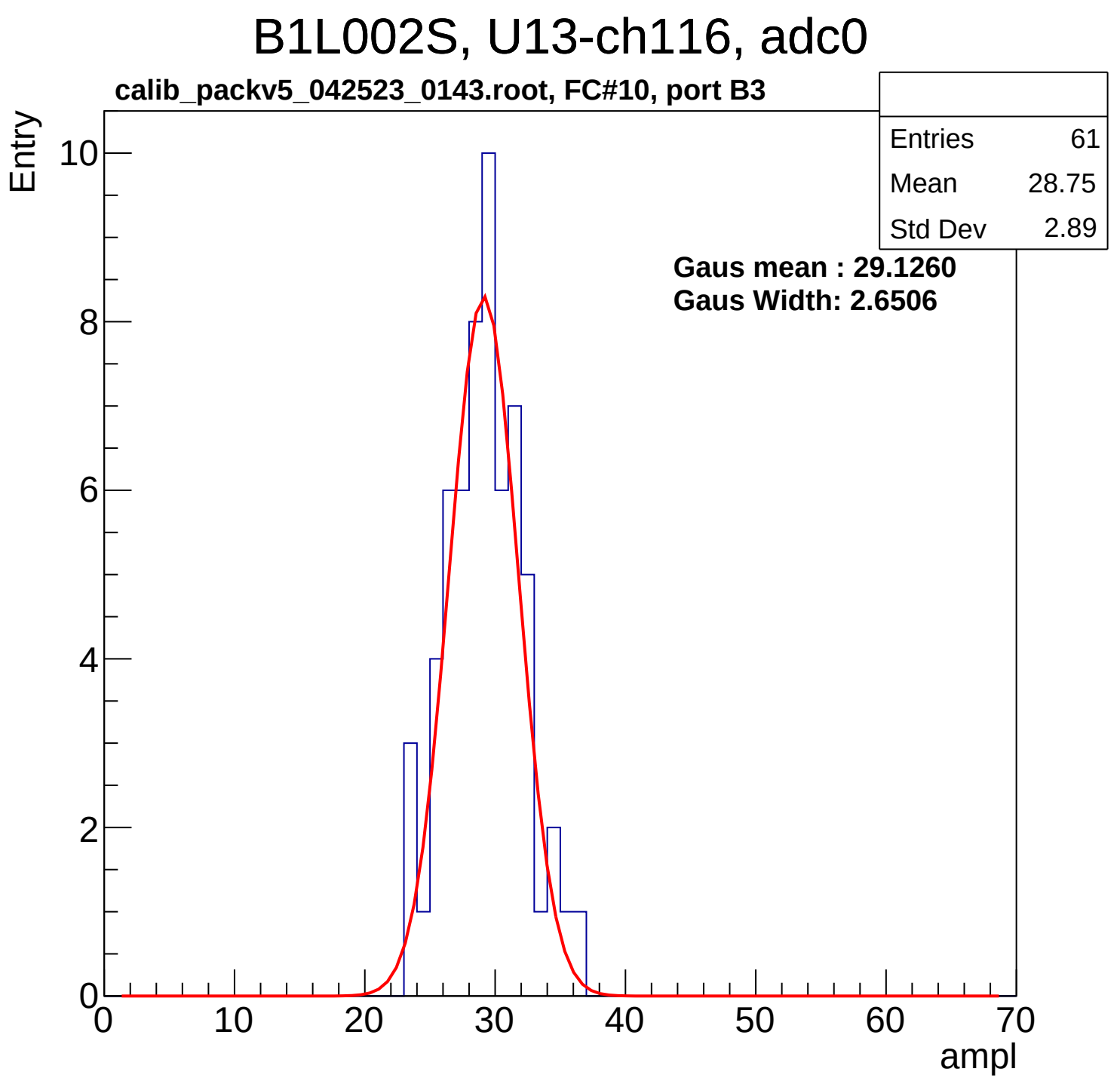
70
ampl


B1L002S, U13-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.75
Std Dev 2.89
Gaus mean : 29.1260
Gaus Width: 2.6506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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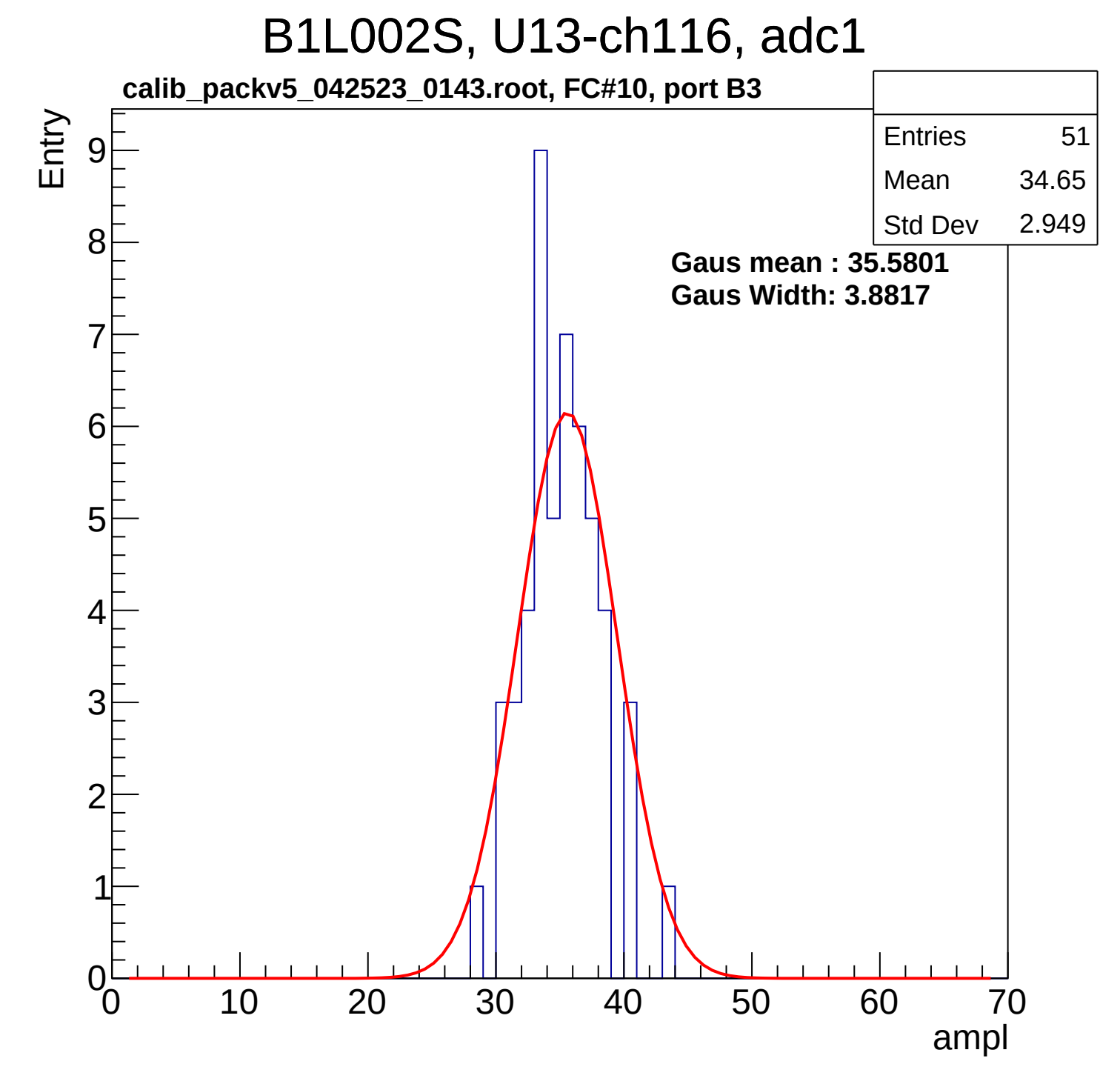
B1L002S, U13-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 34.65
Std Dev 2.949
Gaus mean : 35.5801
Gaus Width: 3.8817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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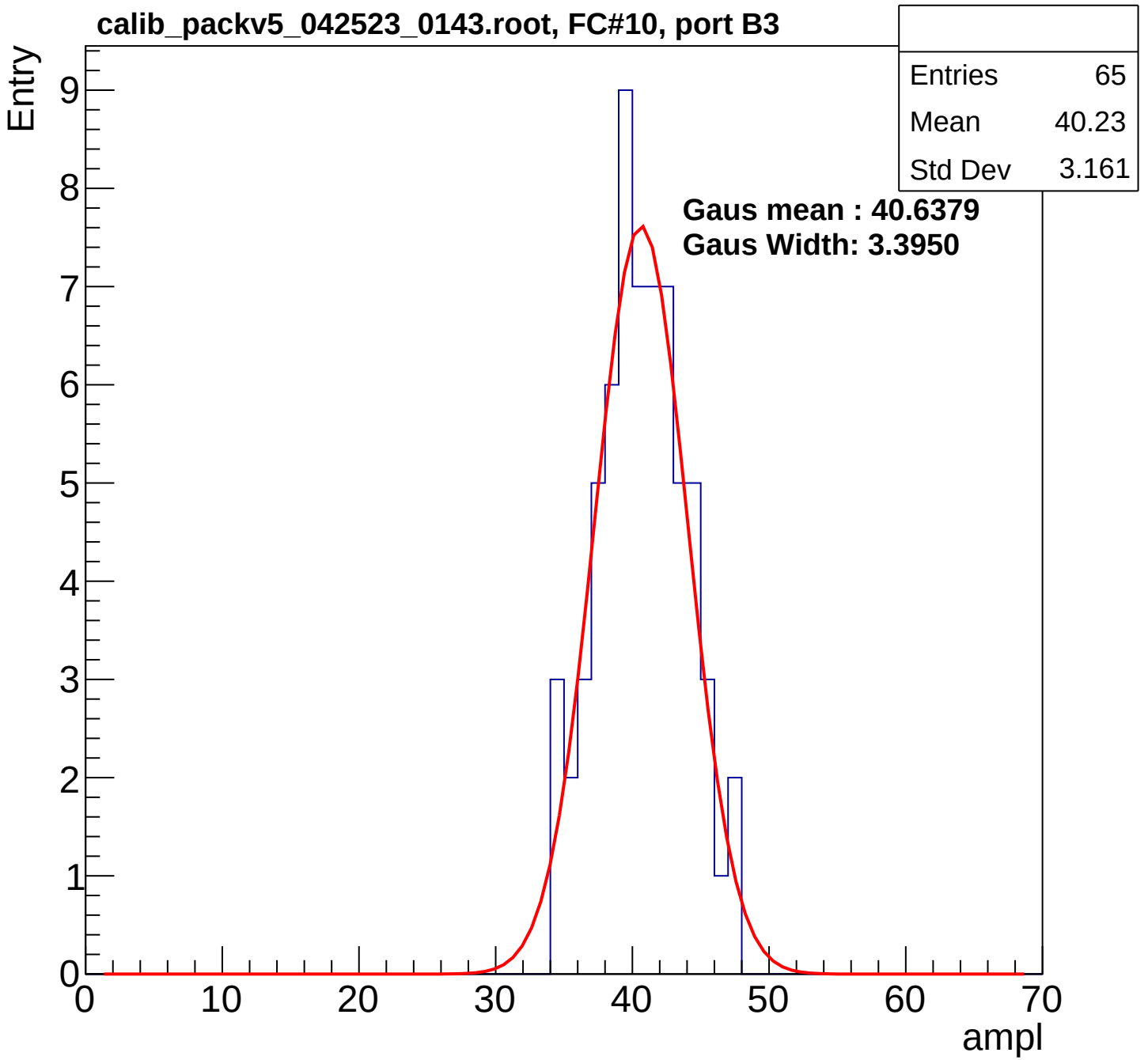
B1L002S, U13-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.23
Std Dev 3.161
Gaus mean : 40.6379
Gaus Width: 3.3950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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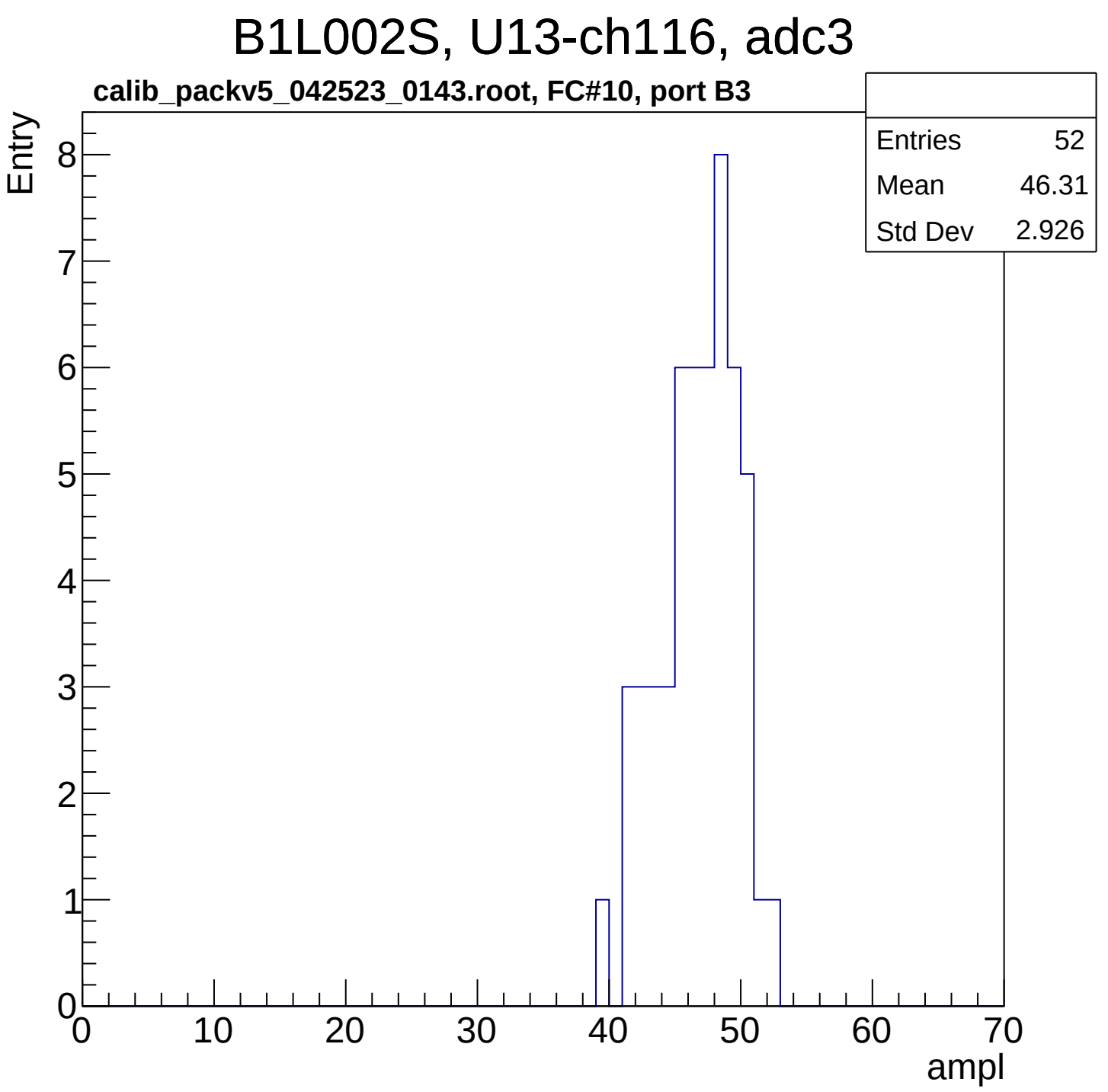
B1L002S, U13-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 46.31
Std Dev 2.926
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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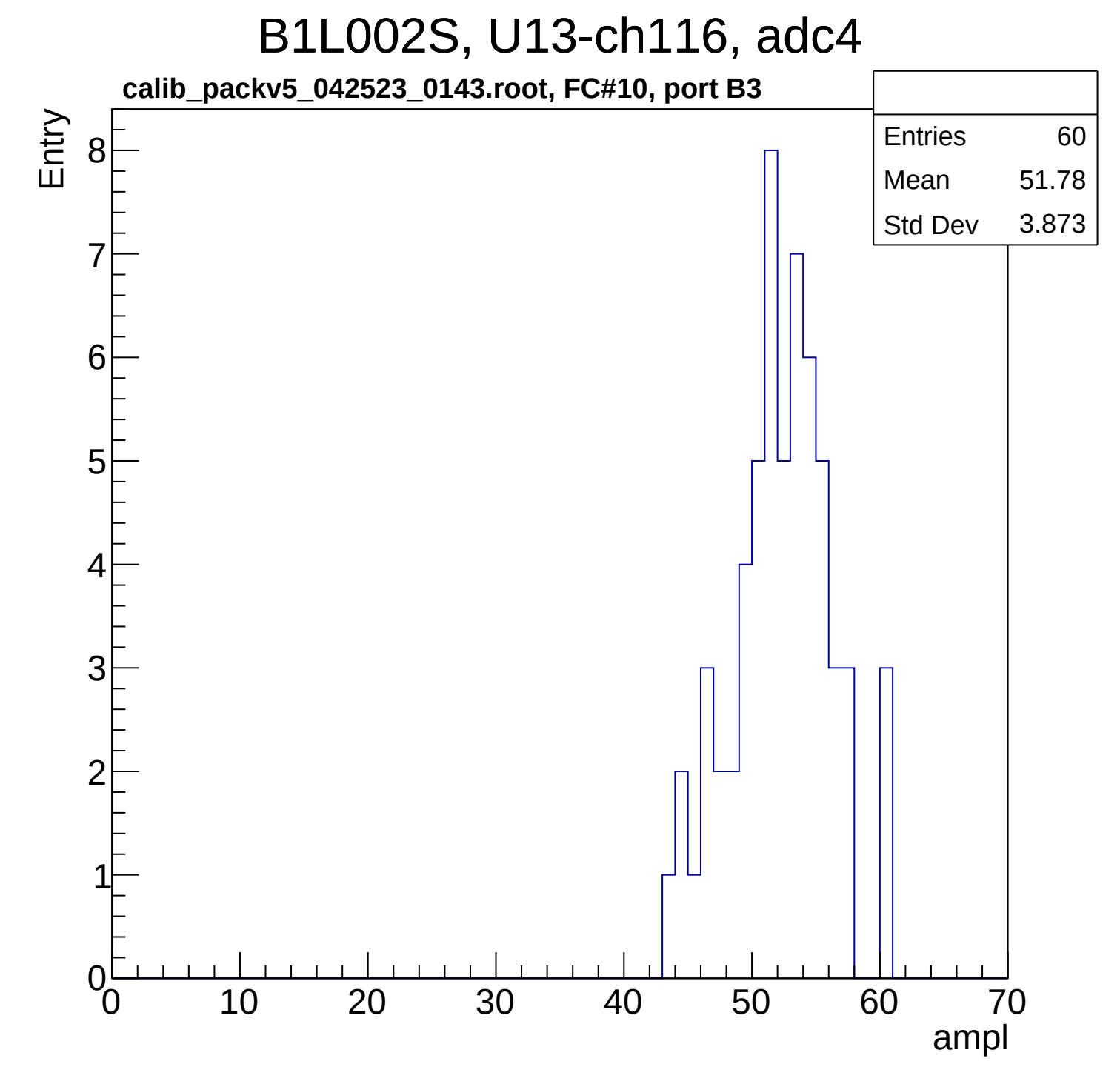
B1L002S, U13-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.78
Std Dev 3.873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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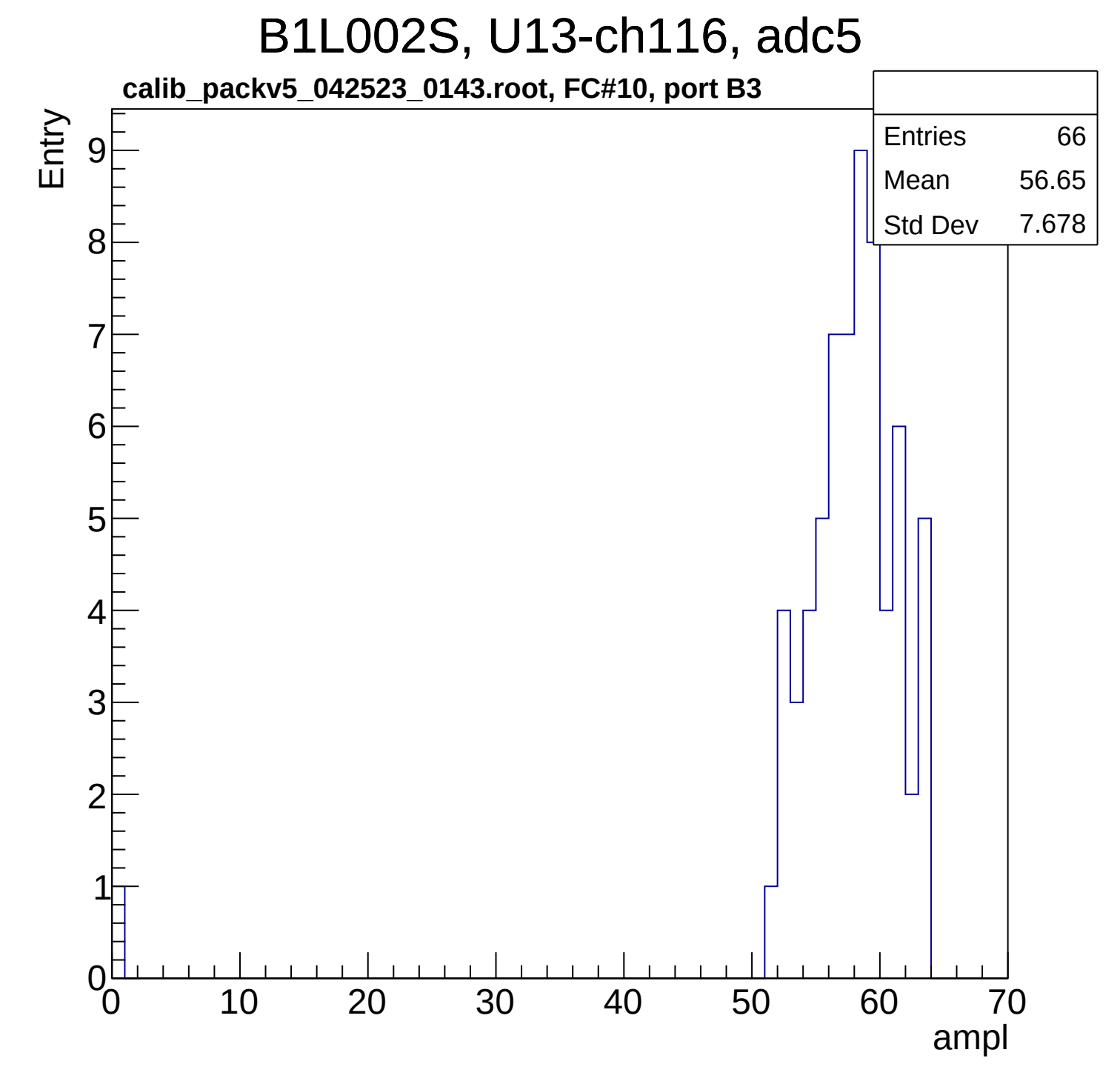
B1L002S, U13-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 56.65
Std Dev 7.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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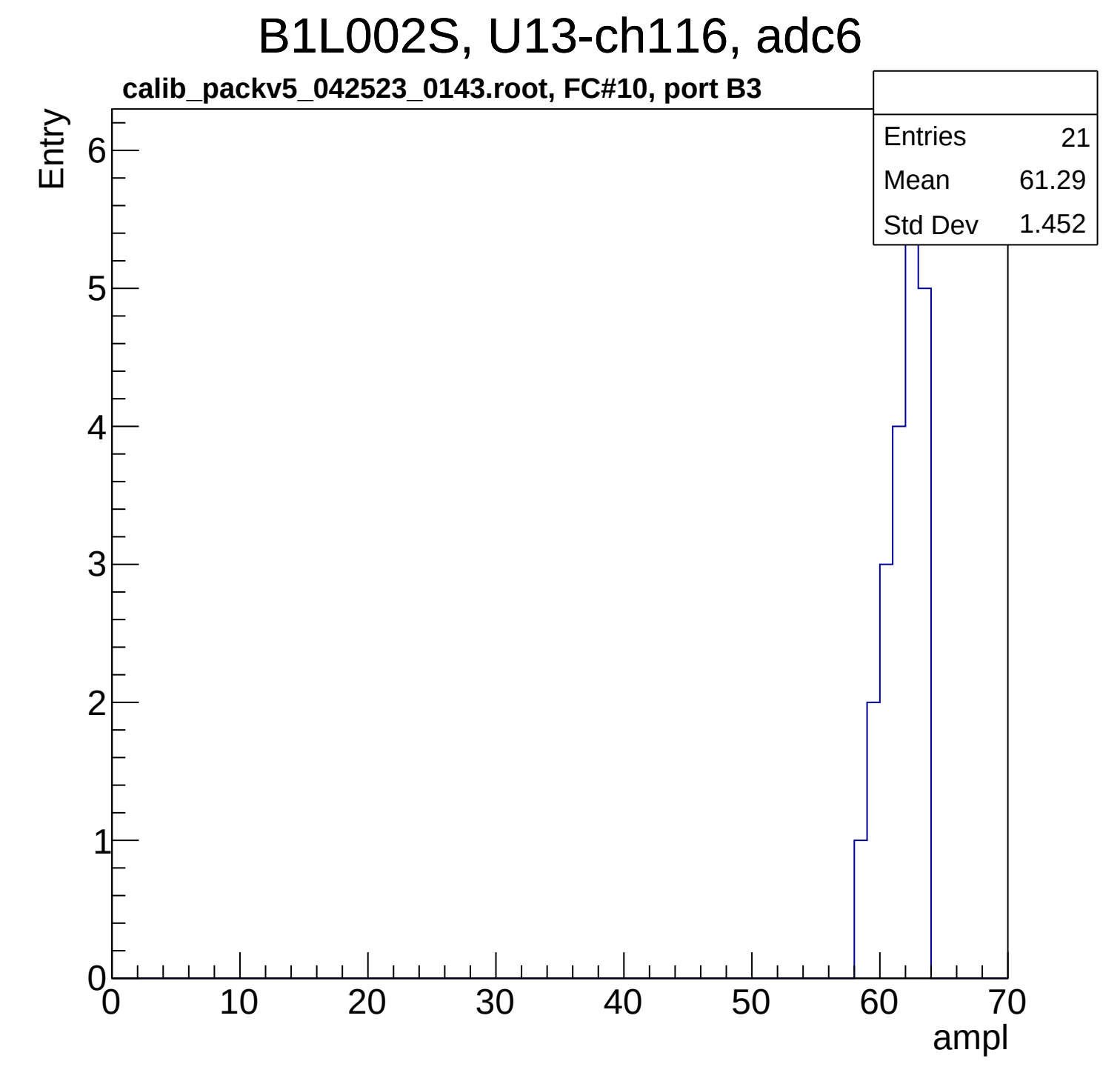
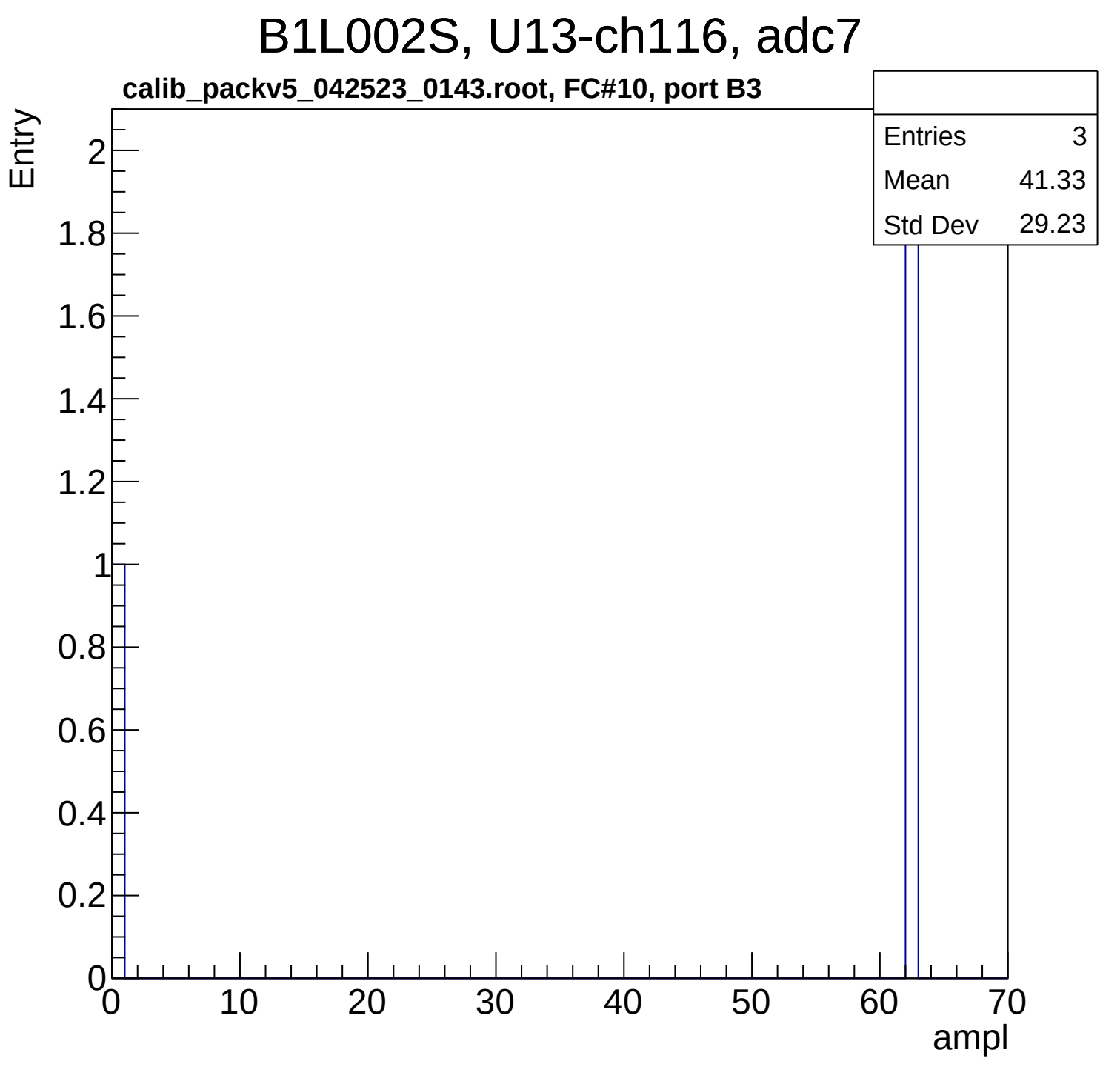
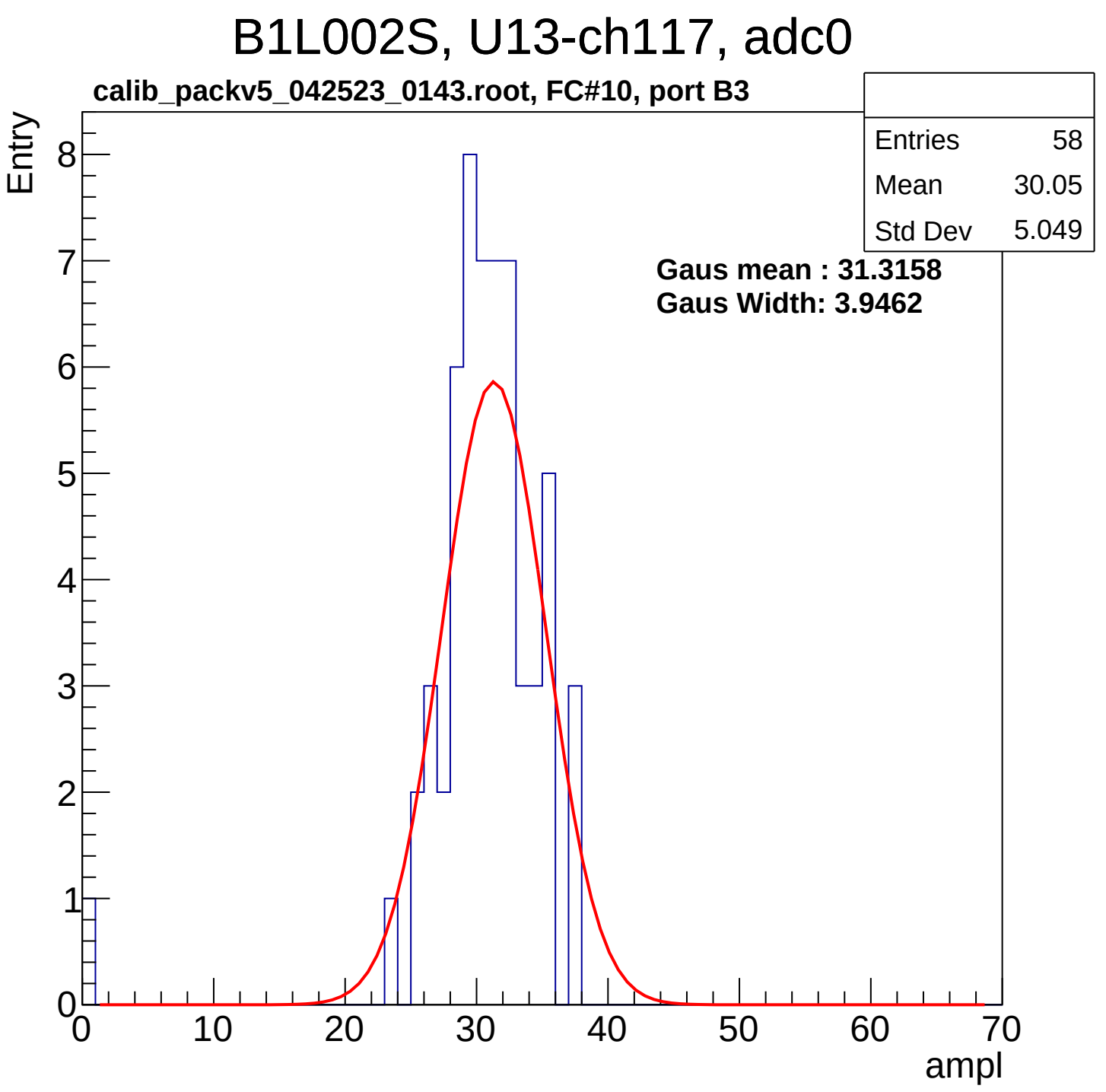
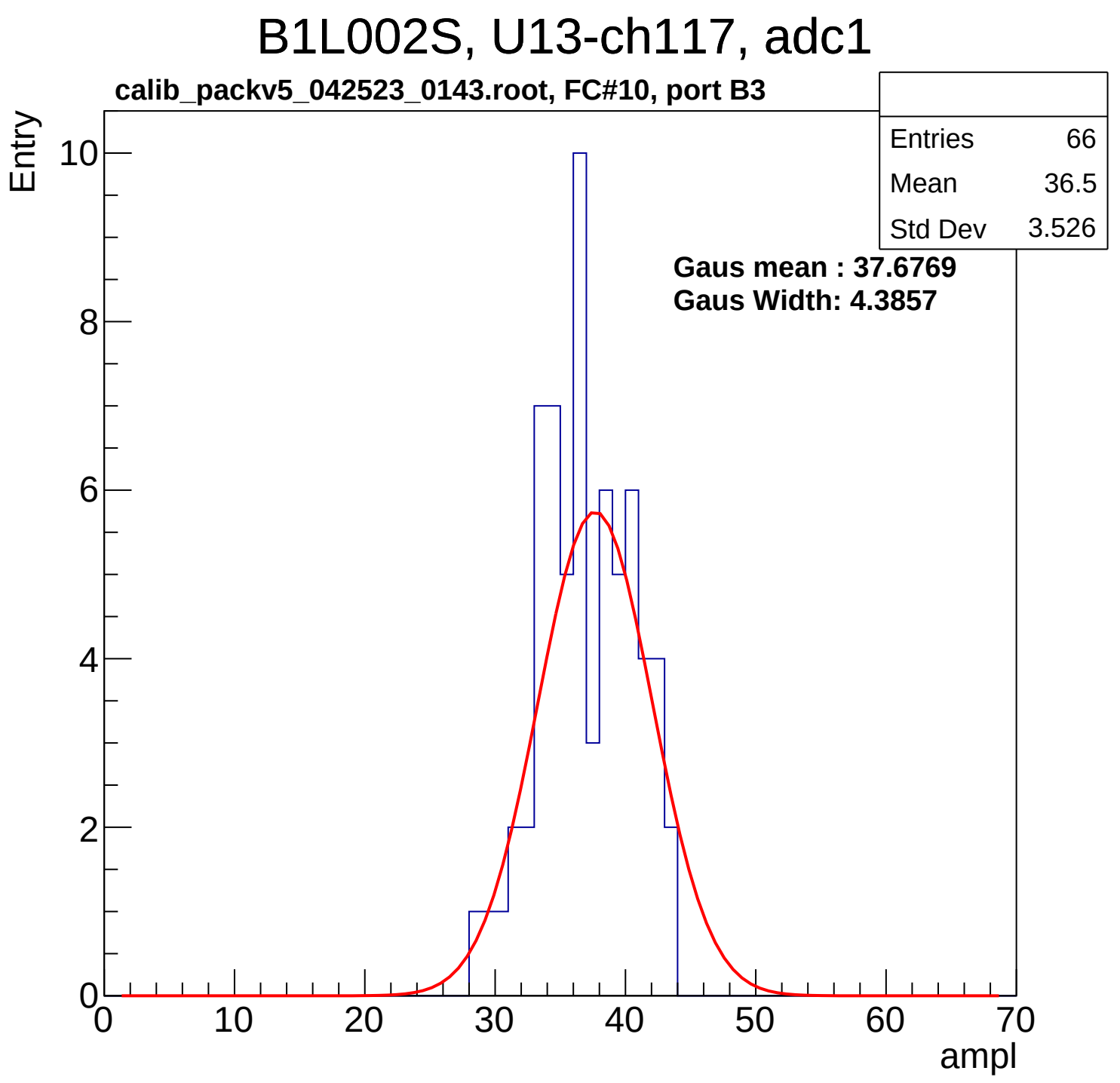

B1L002S, U13-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.29
Std Dev 1.452
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 30.05
Std Dev 5.049
Gaus mean : 31.3158
Gaus Width: 3.9462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 36.5
Std Dev 3.526
Gaus mean : 37.6769
Gaus Width: 4.3857
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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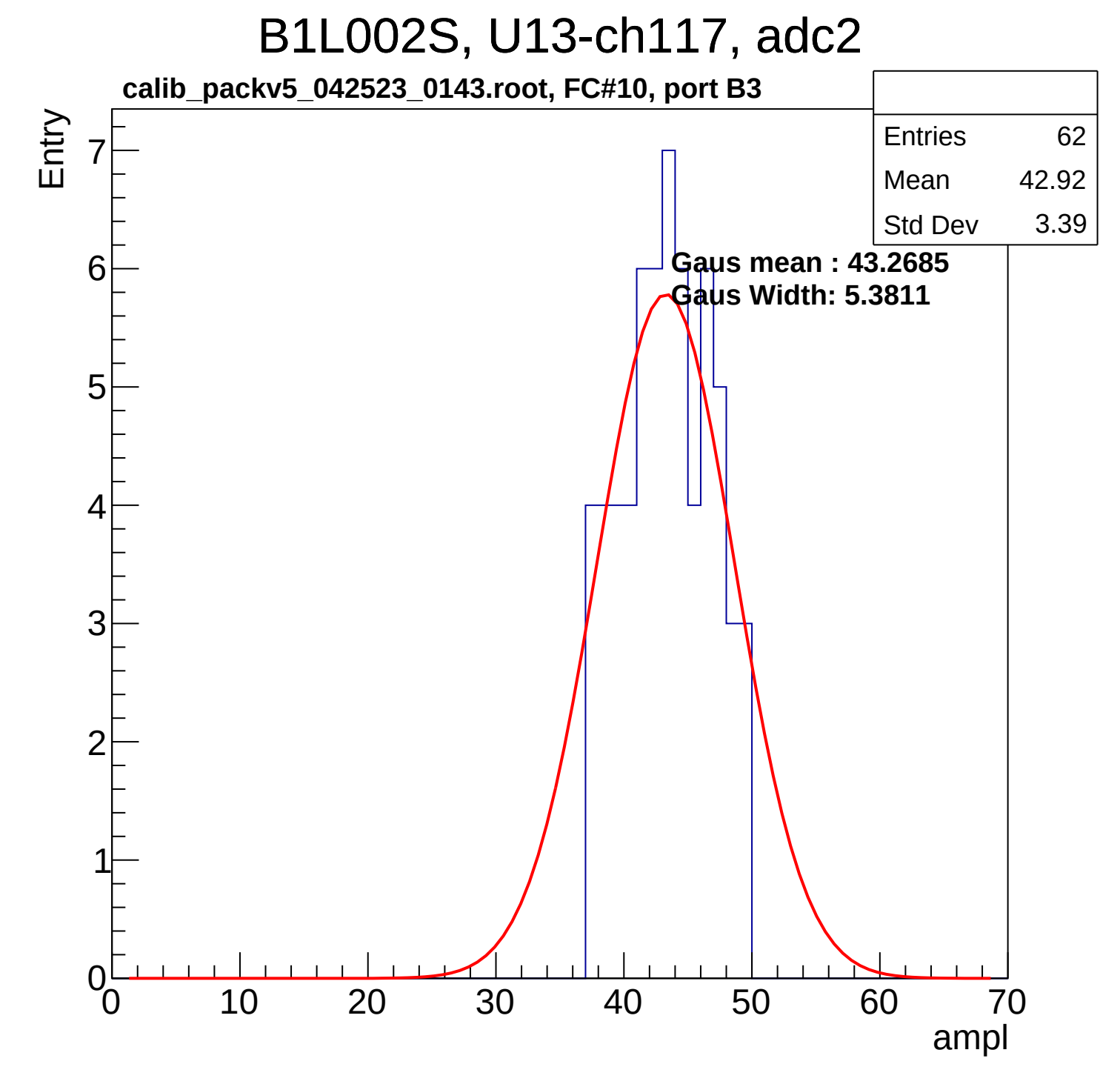
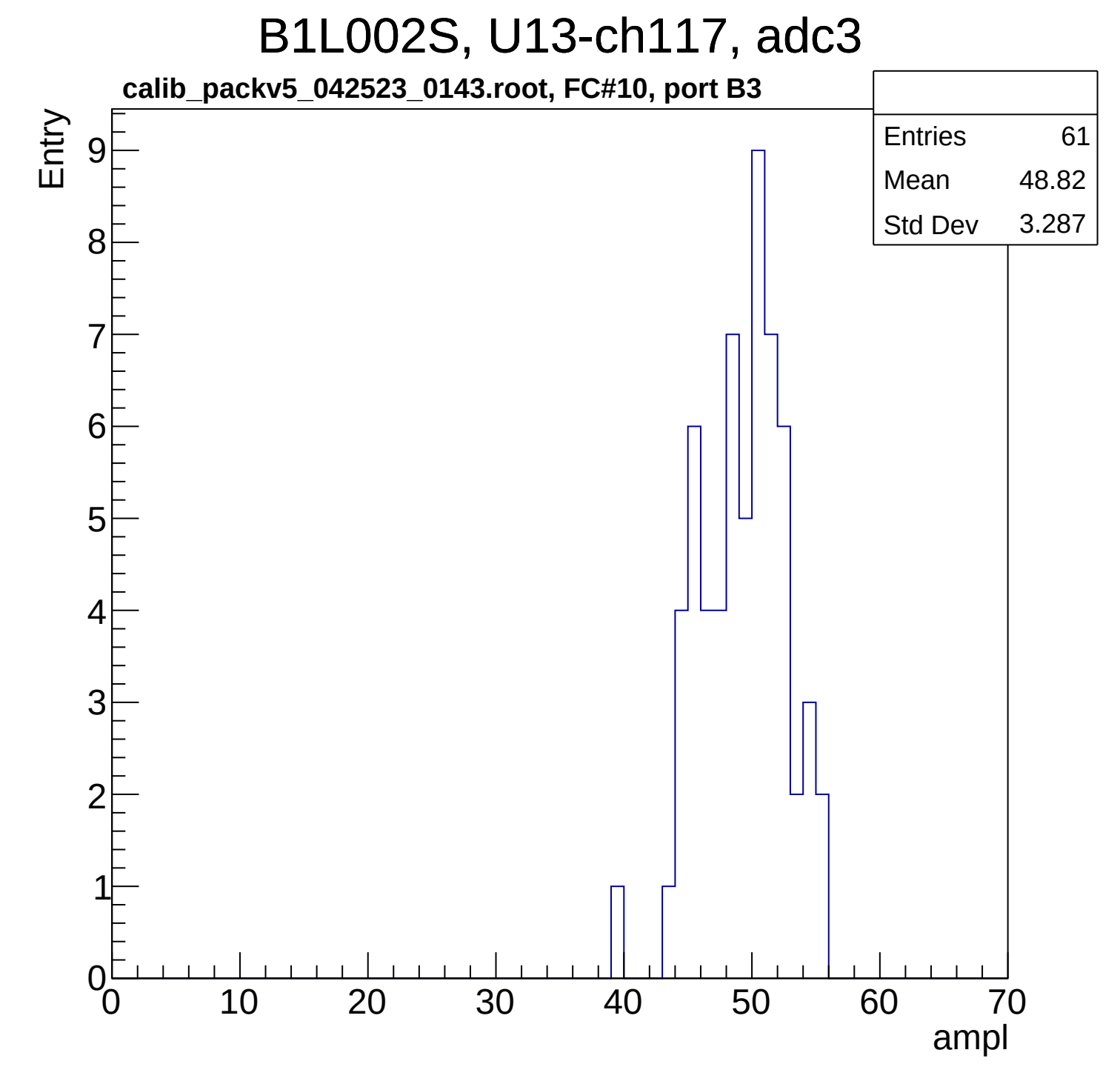

B1L002S, U13-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.92
Std Dev 3.39
Gaus mean : 43.2685
Gaus Width: 5.3811
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.82
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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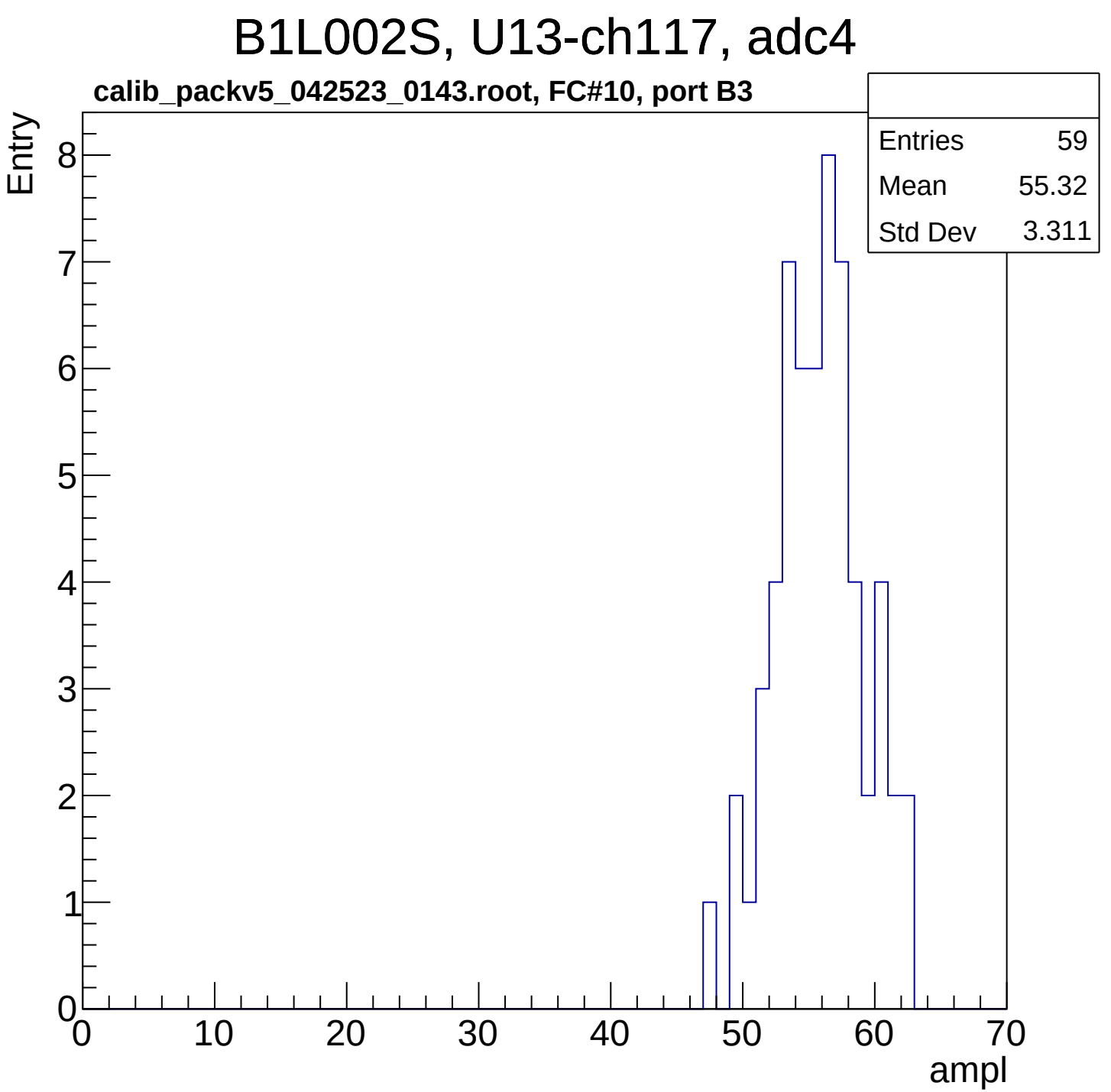
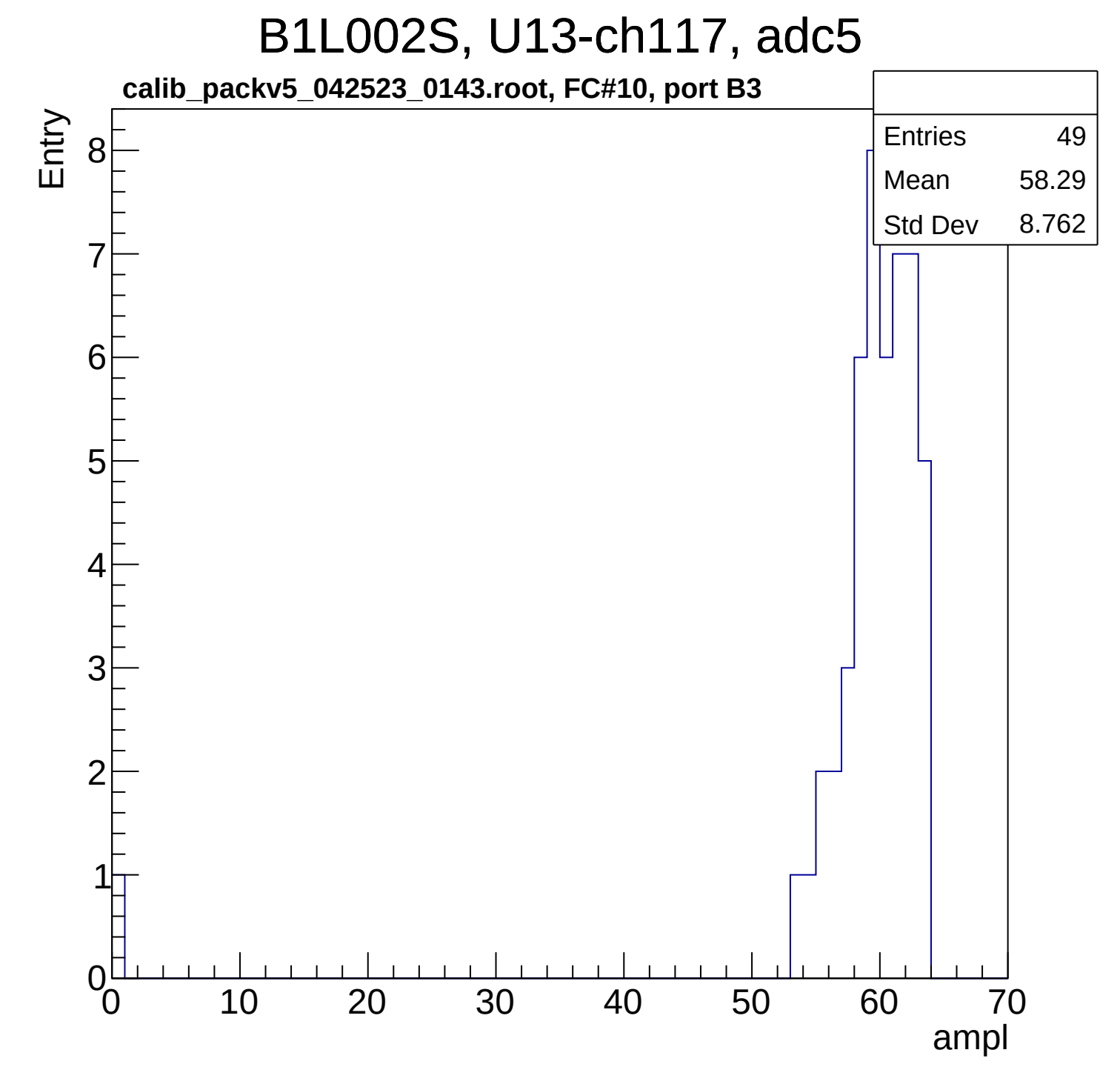
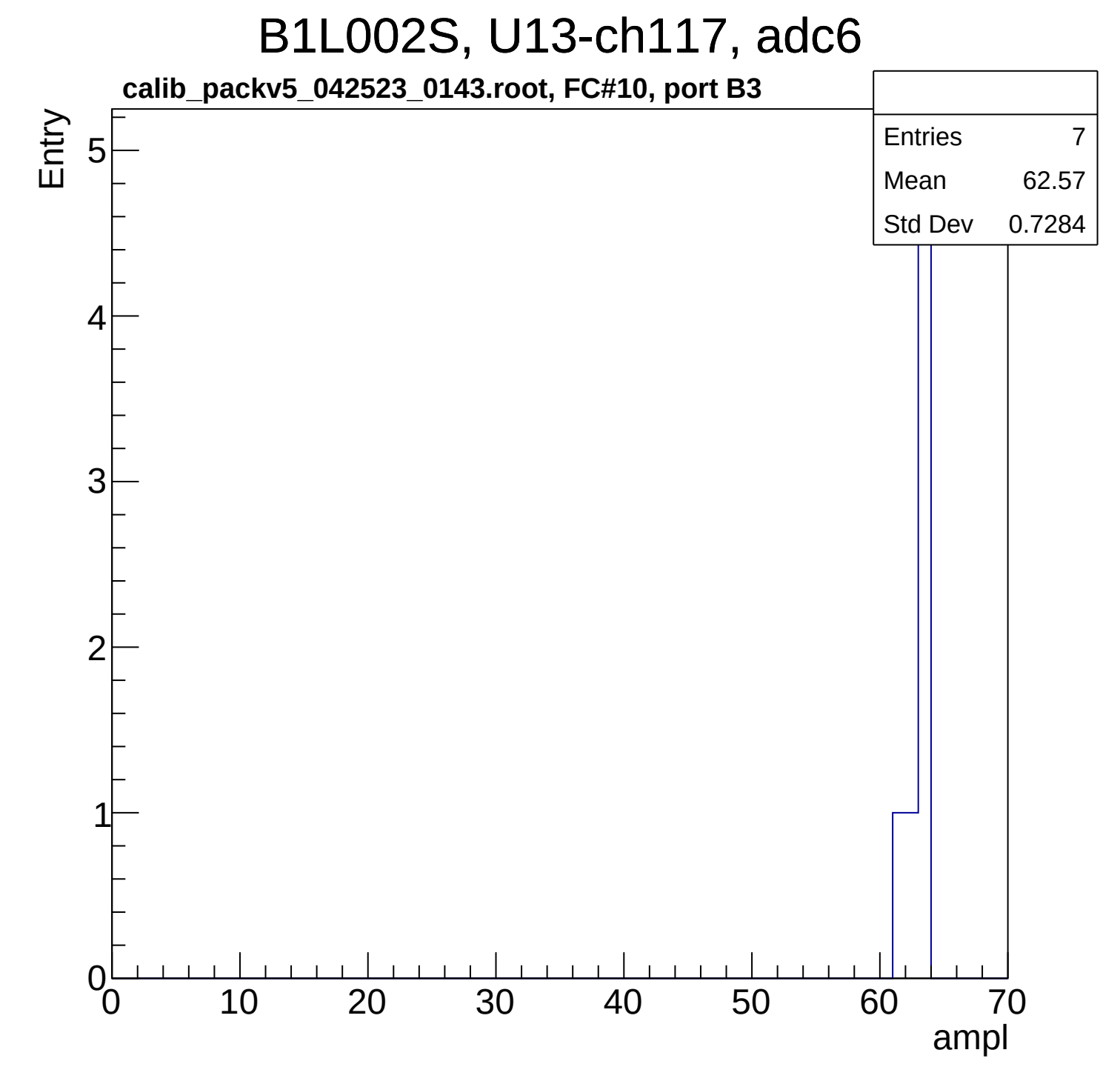
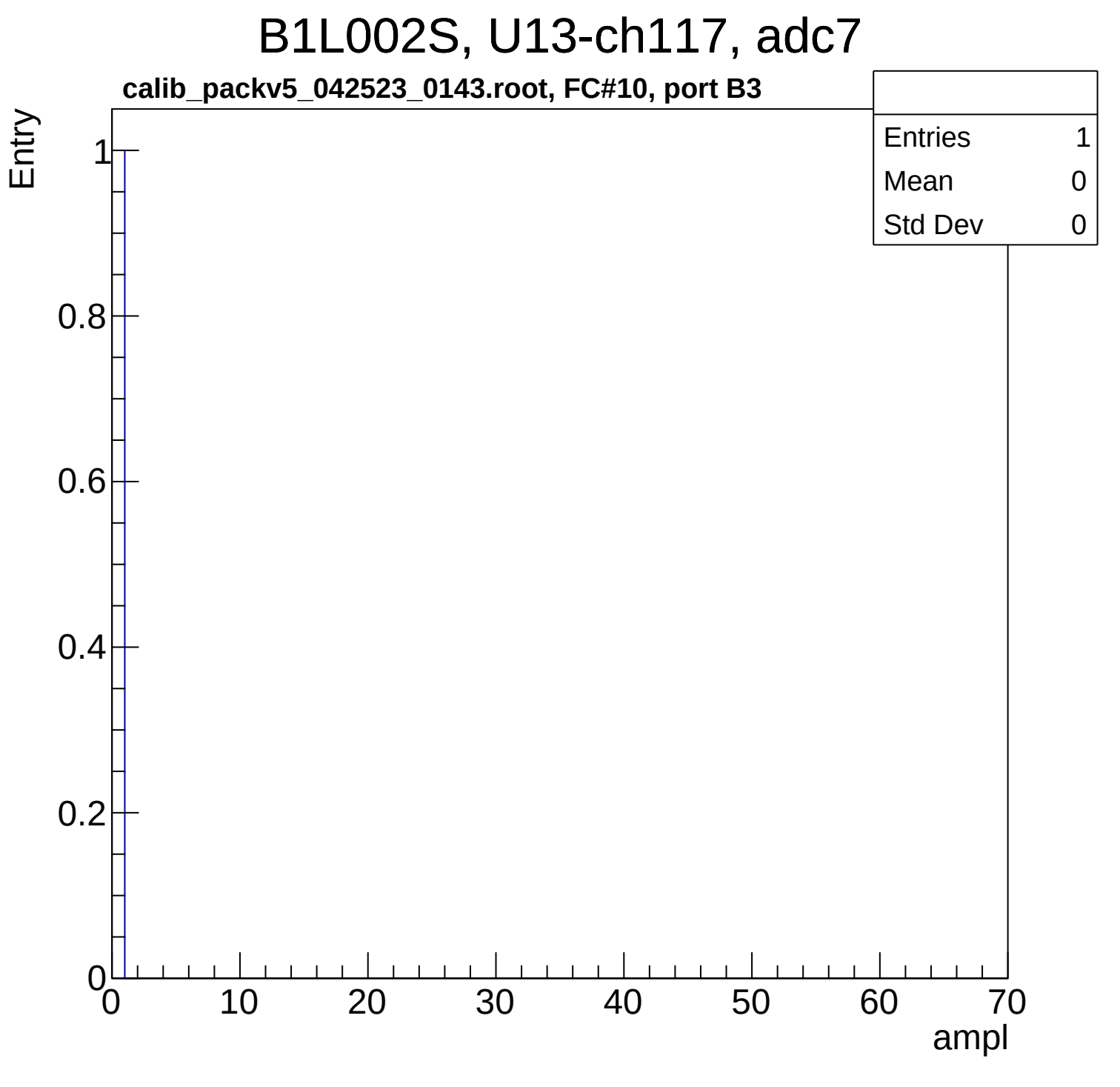
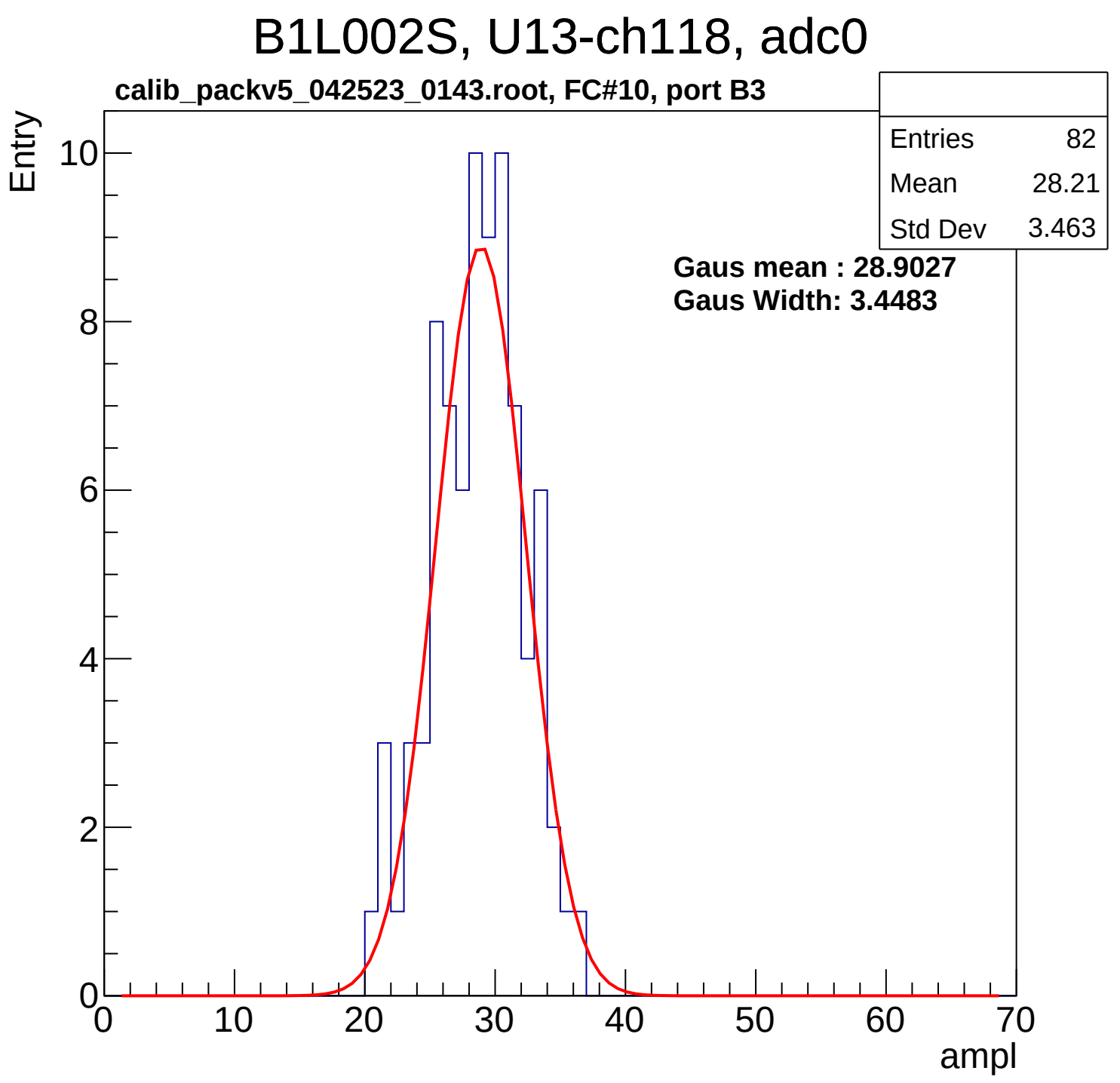
60
70


B1L002S, U13-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 55.32
Std Dev 3.311
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.29
Std Dev 8.762
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.21
Std Dev 3.463
Gaus mean : 28.9027
Gaus Width: 3.4483
Entry
ampl
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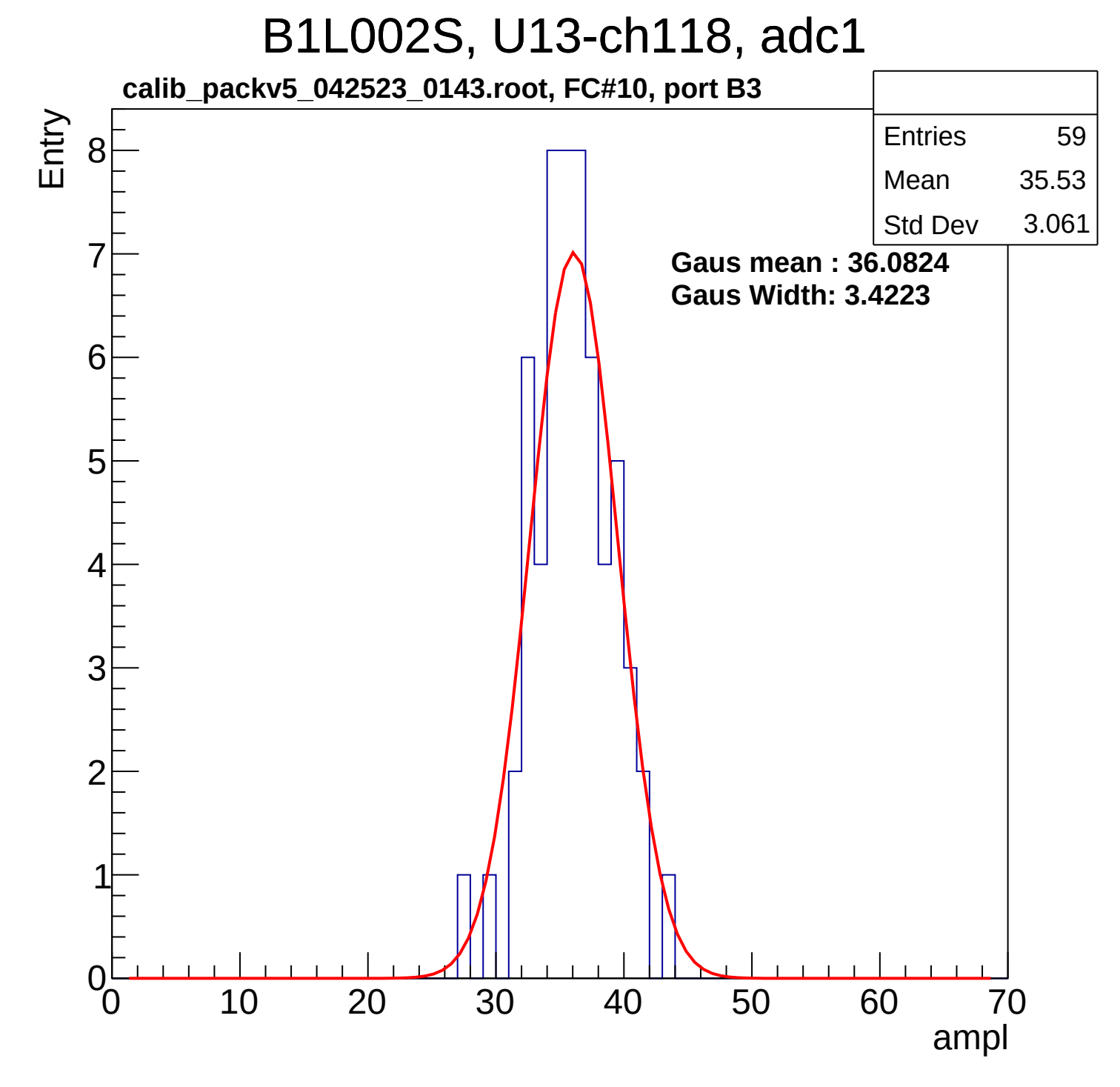
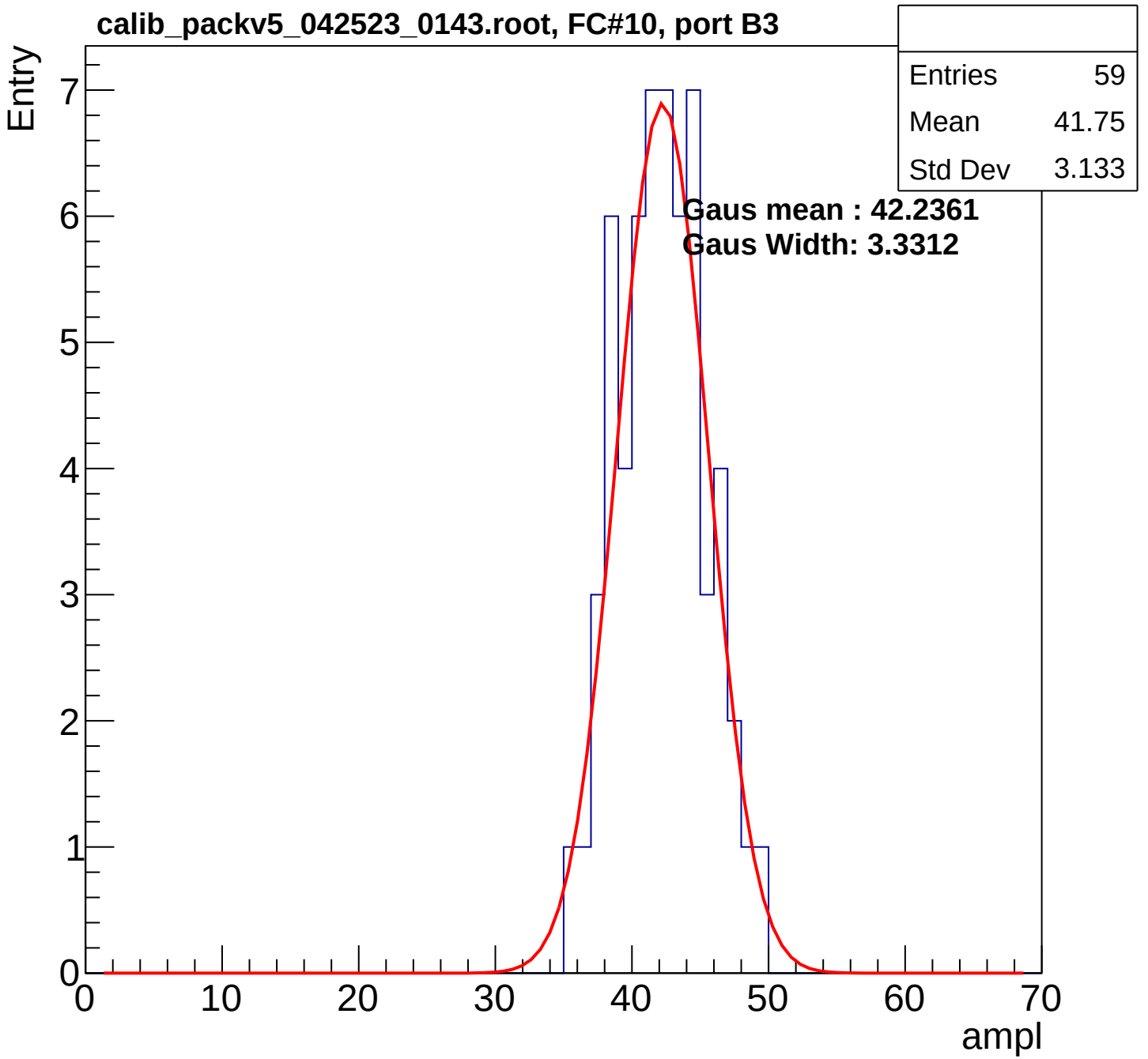
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.53
Std Dev 3.061
Gaus mean : 36.0824
Gaus Width: 3.4223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.75
Std Dev 3.133
Gaus mean : 42.2361
Gaus Width: 3.3312
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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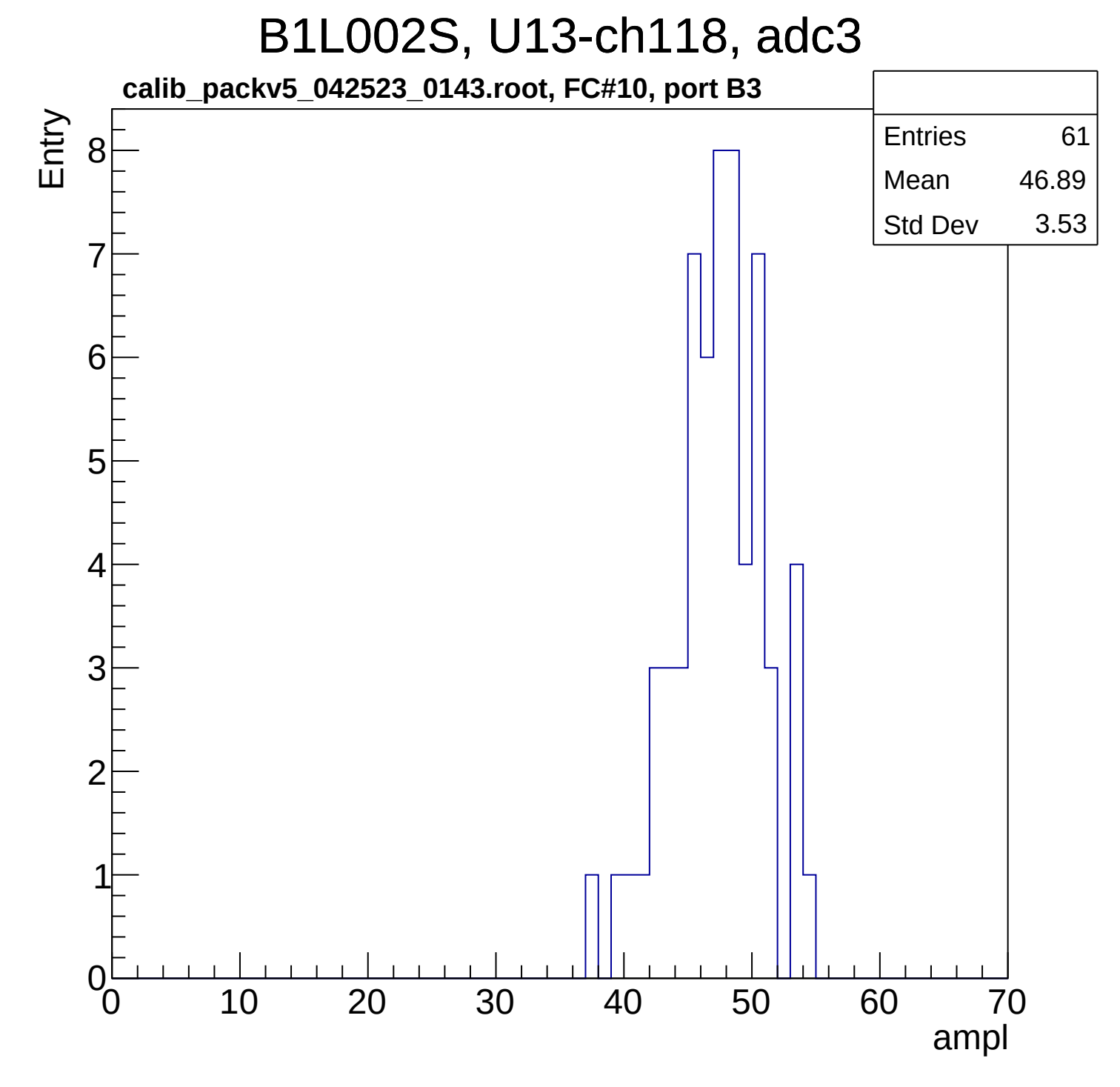
B1L002S, U13-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.89
Std Dev 3.53
Entry
ampl
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2
3
4
5
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0
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60
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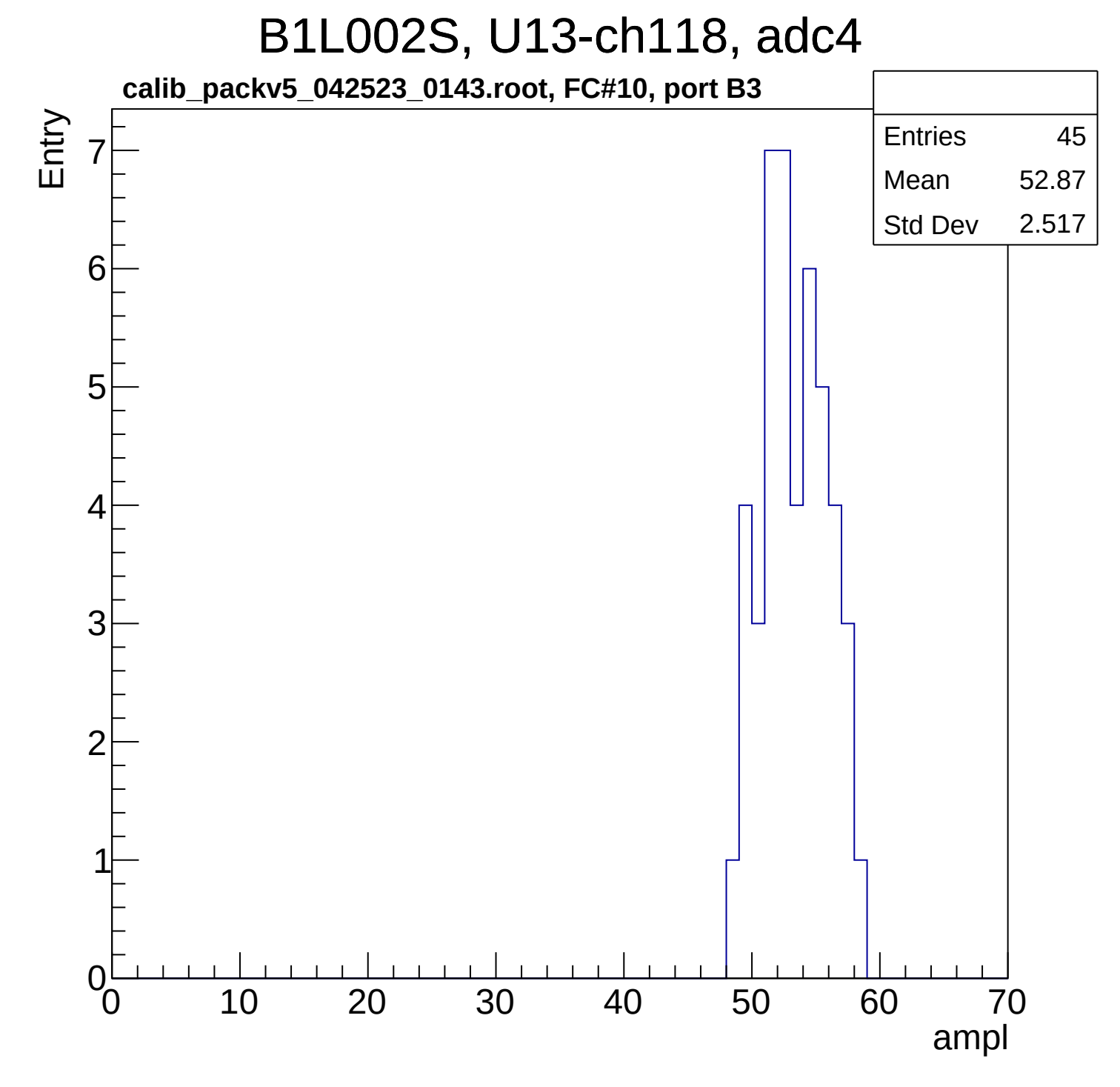
B1L002S, U13-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52.87
Std Dev 2.517
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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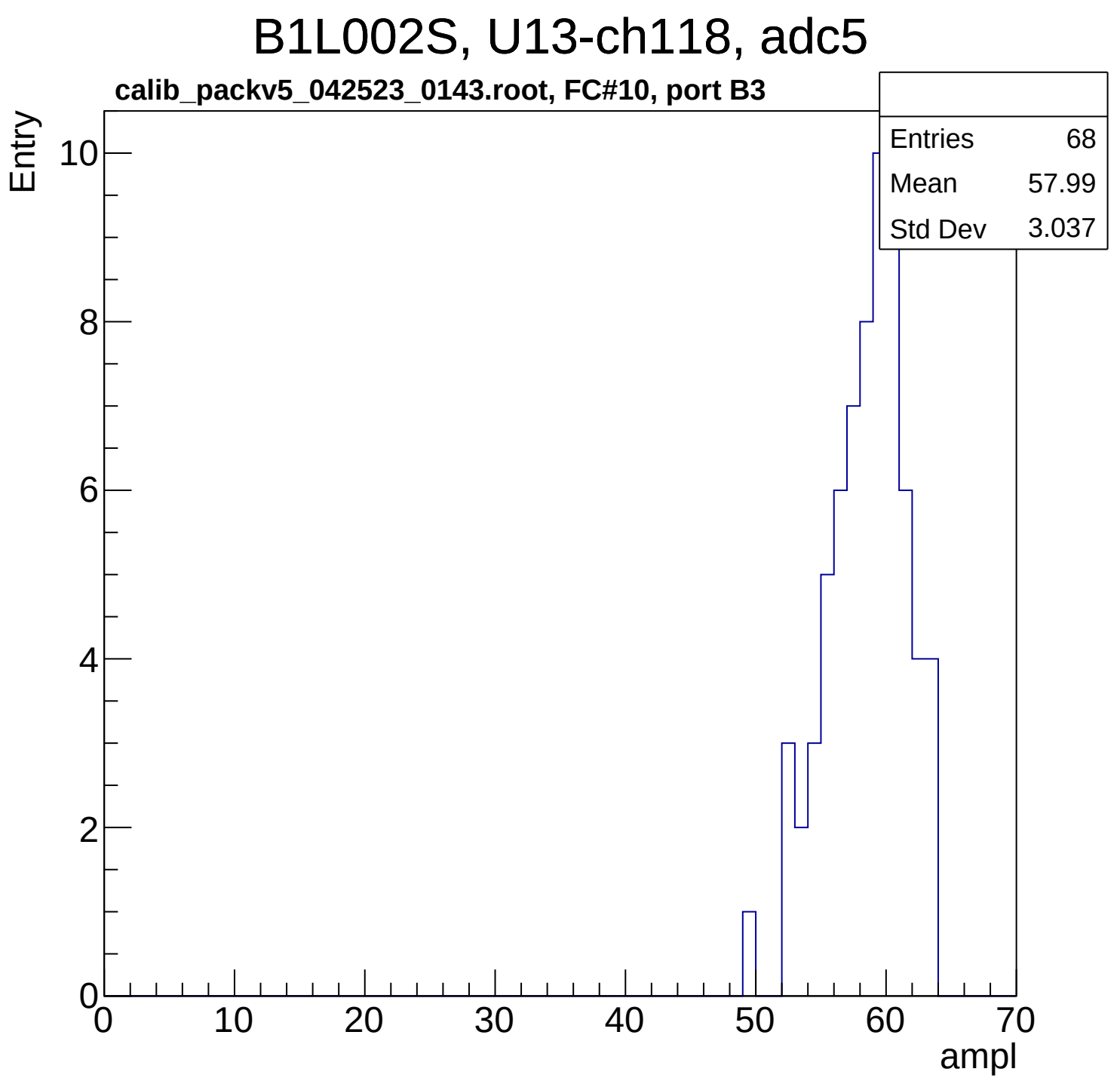
B1L002S, U13-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.99
Std Dev 3.037
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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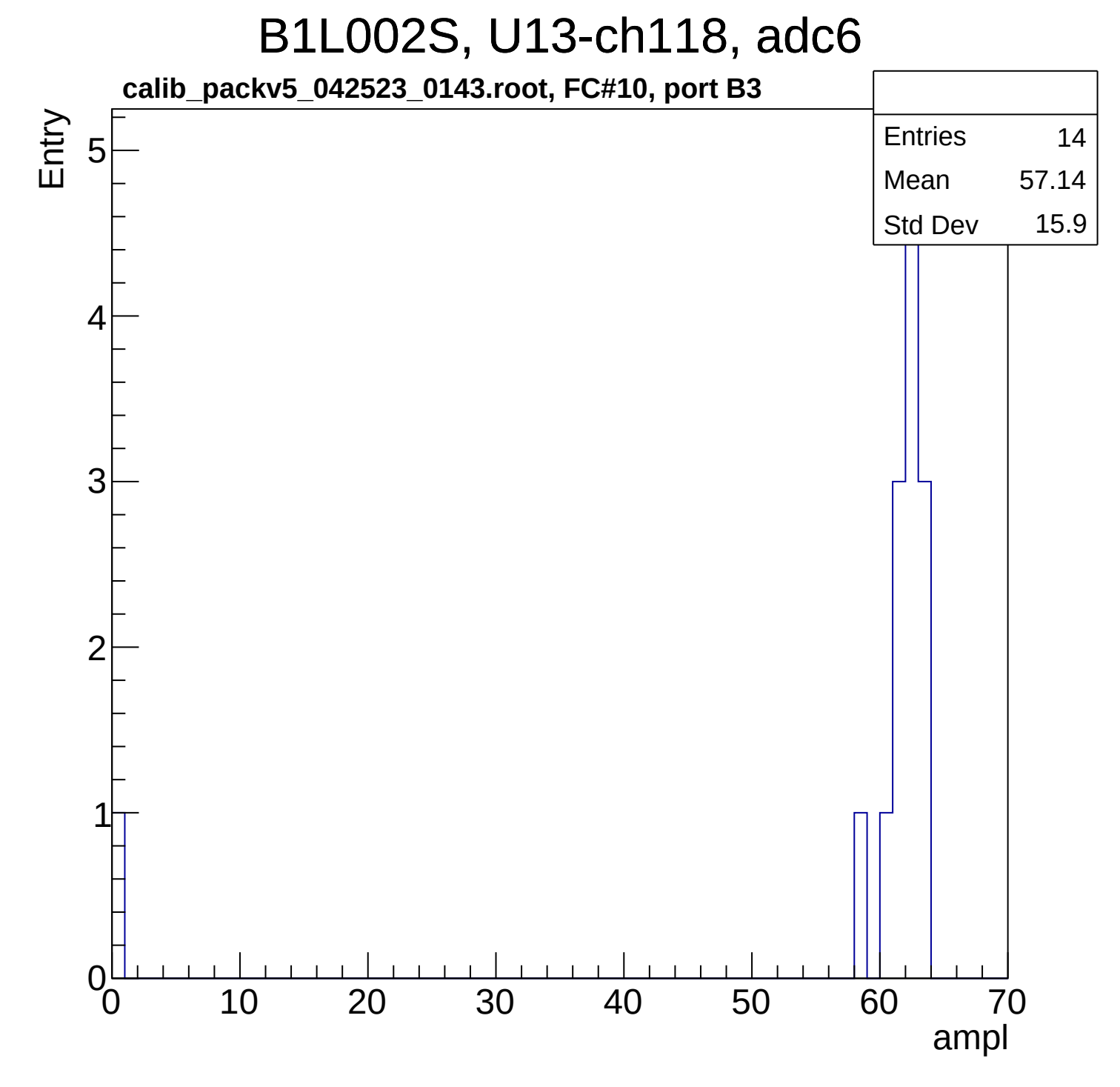
B1L002S, U13-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.14
Std Dev 15.9
Entry
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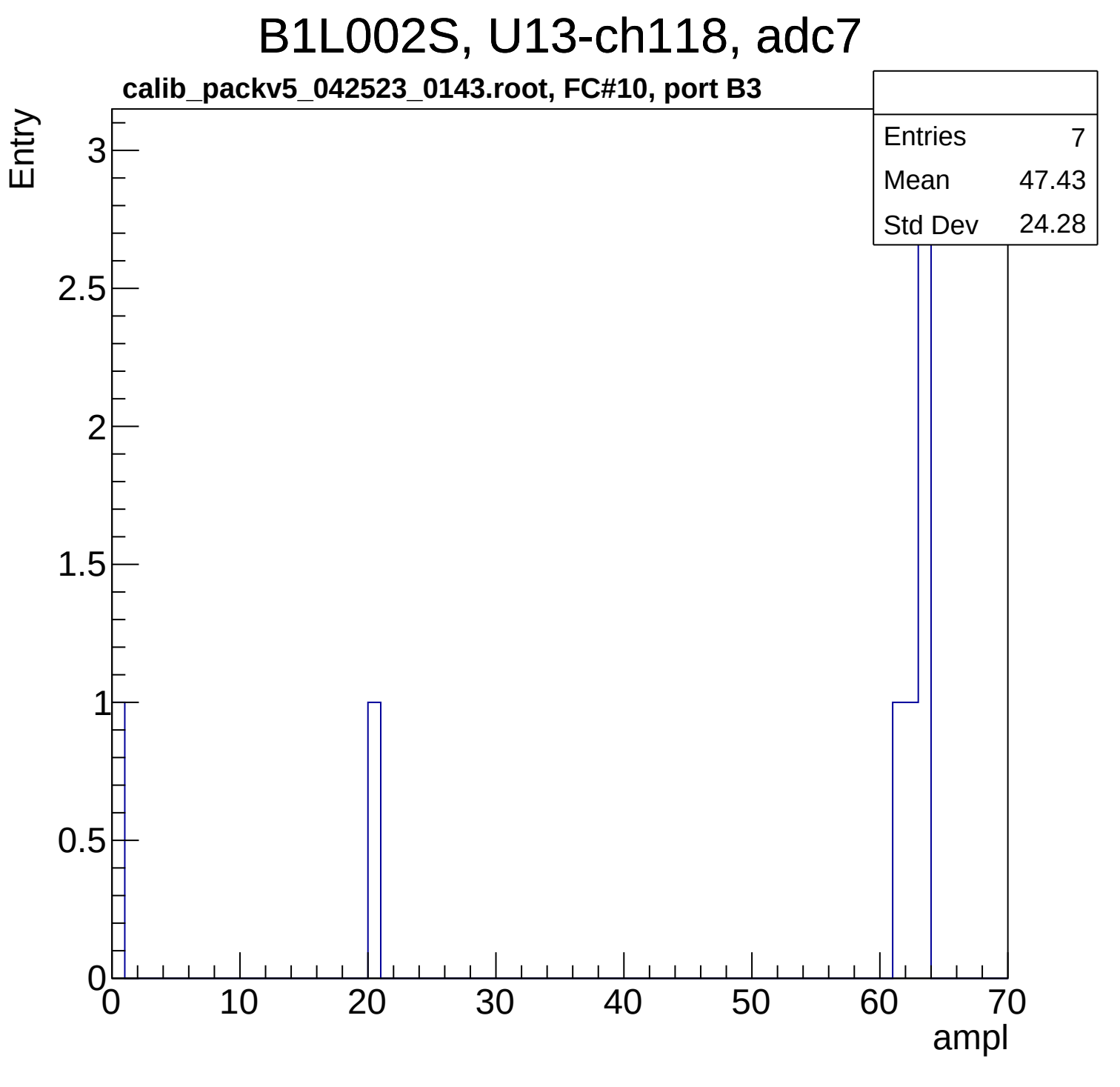
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 47.43
Std Dev 24.28
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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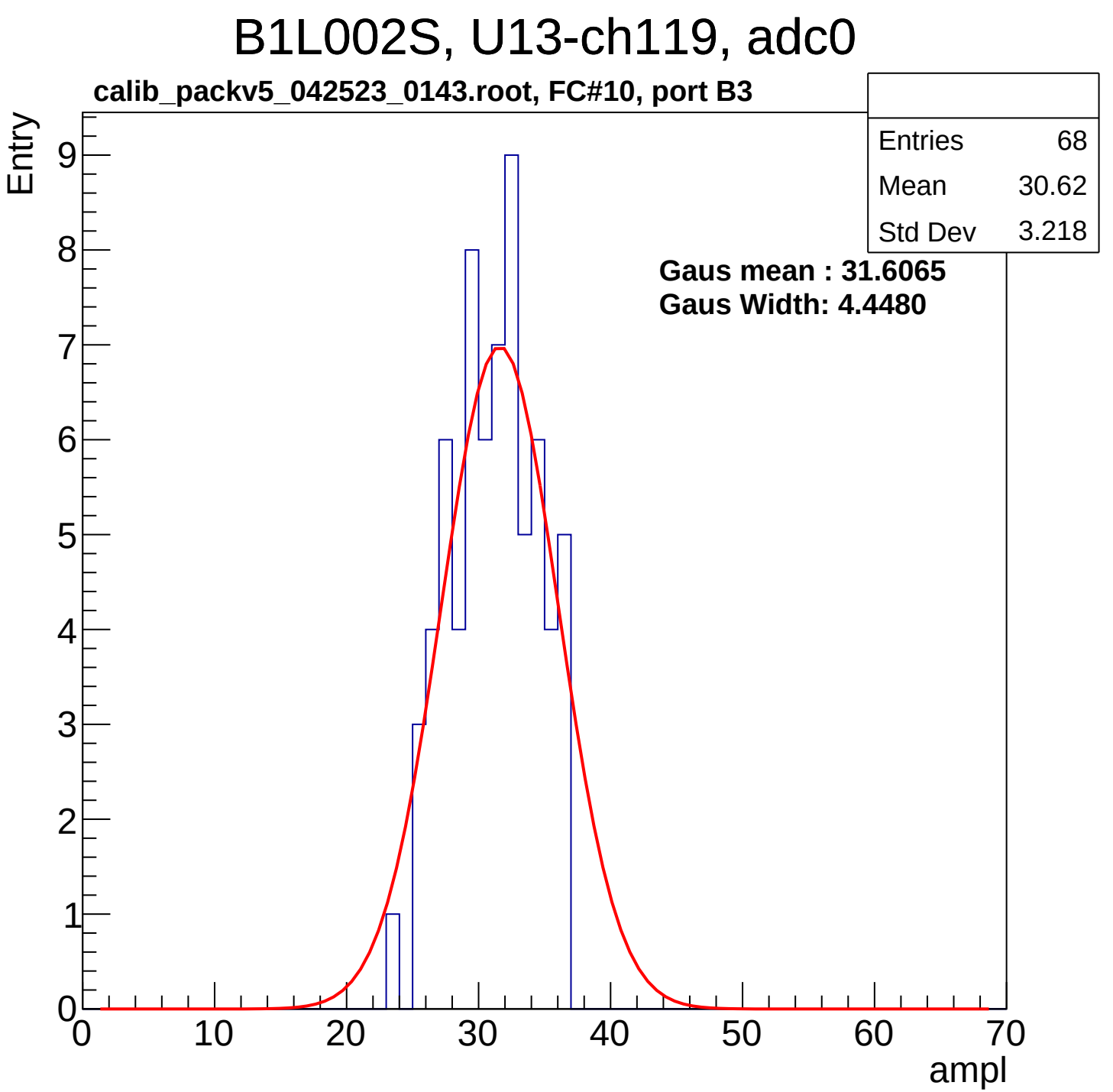
60
70


B1L002S, U13-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 30.62
Std Dev 3.218
Gaus mean : 31.6065
Gaus Width: 4.4480
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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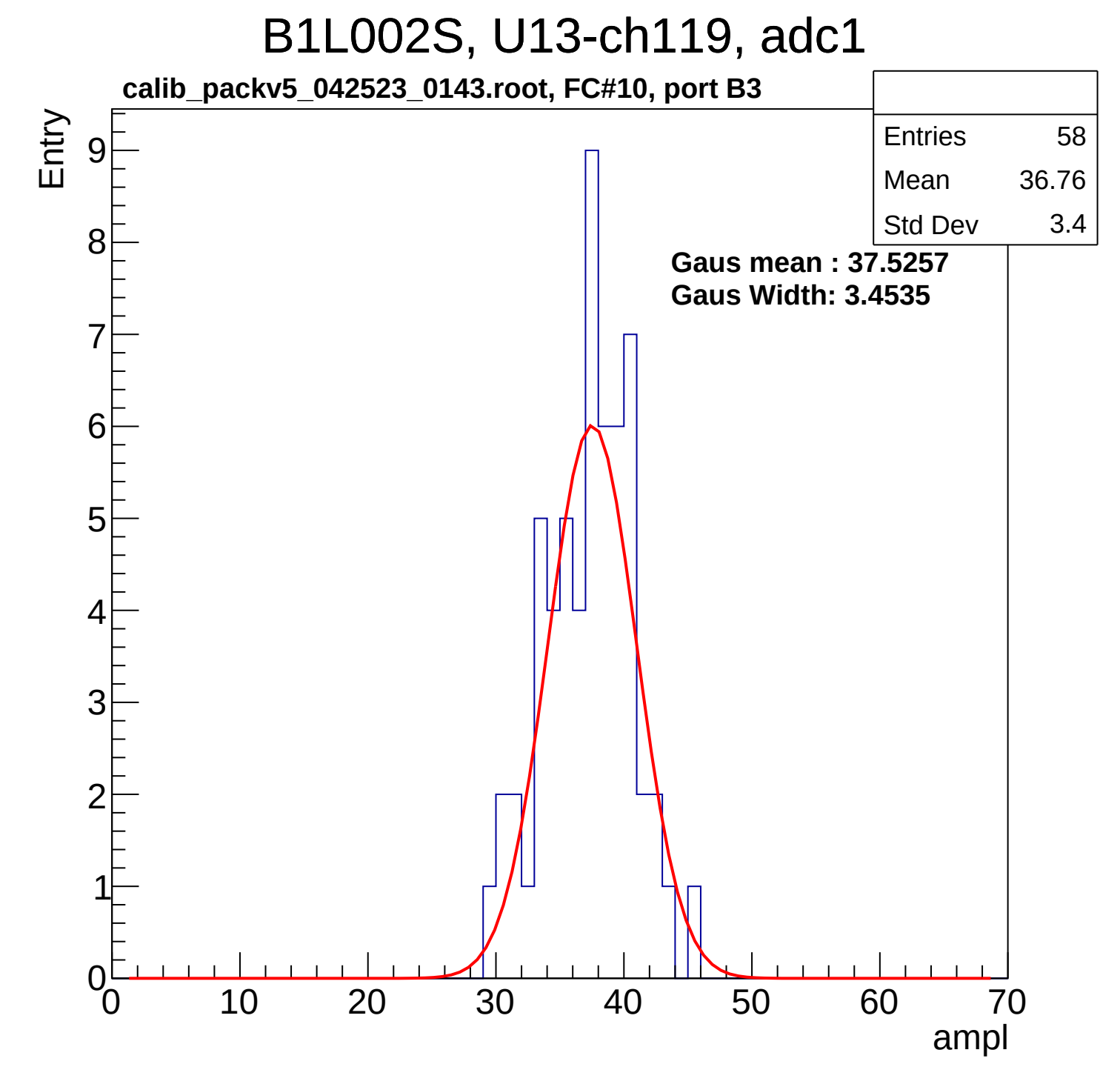
B1L002S, U13-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 36.76
Std Dev 3.4
Gaus mean : 37.5257
Gaus Width: 3.4535
Entry
ampl
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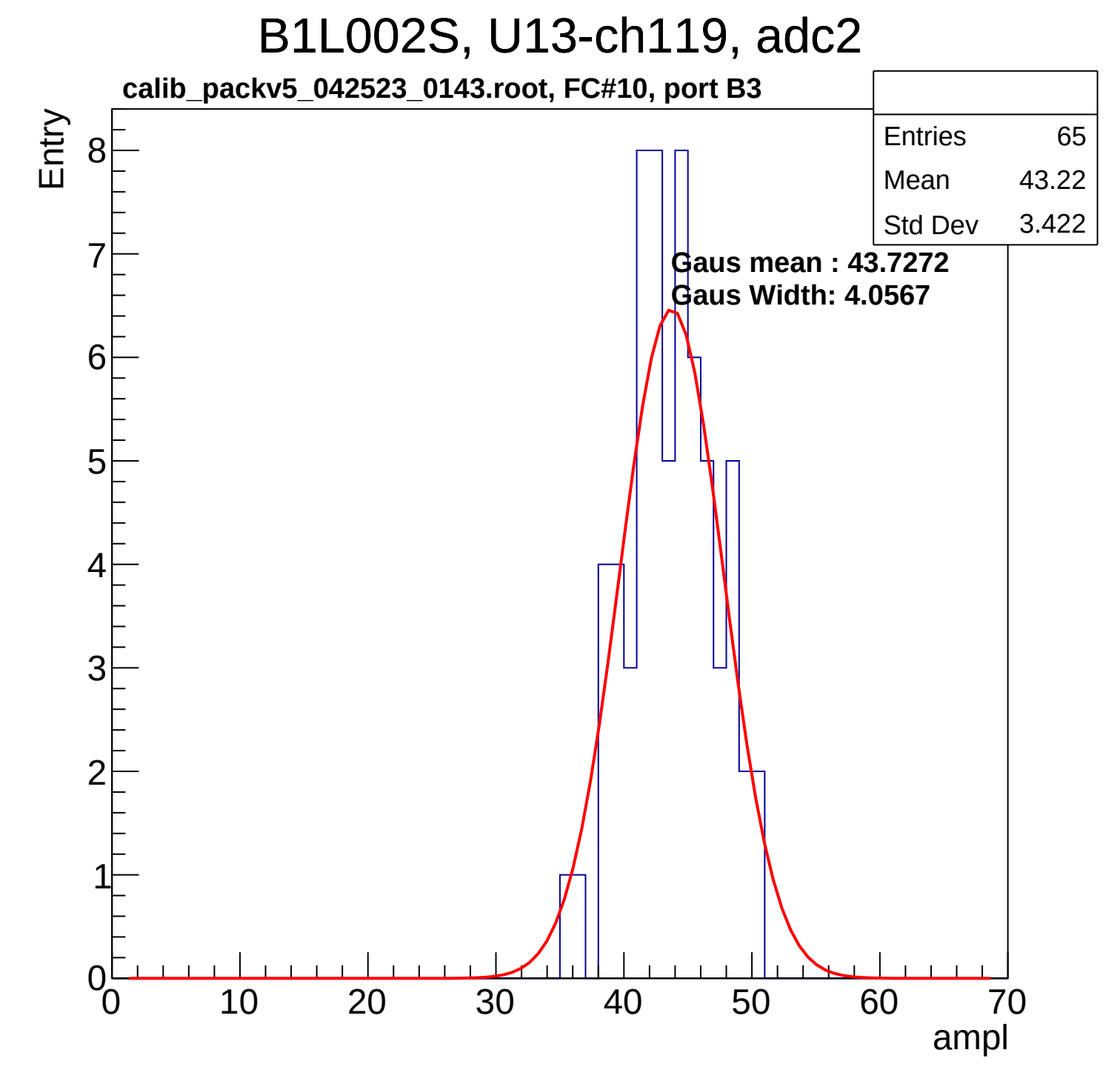
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 43.22
Std Dev 3.422
Gaus mean : 43.7272
Gaus Width: 4.0567
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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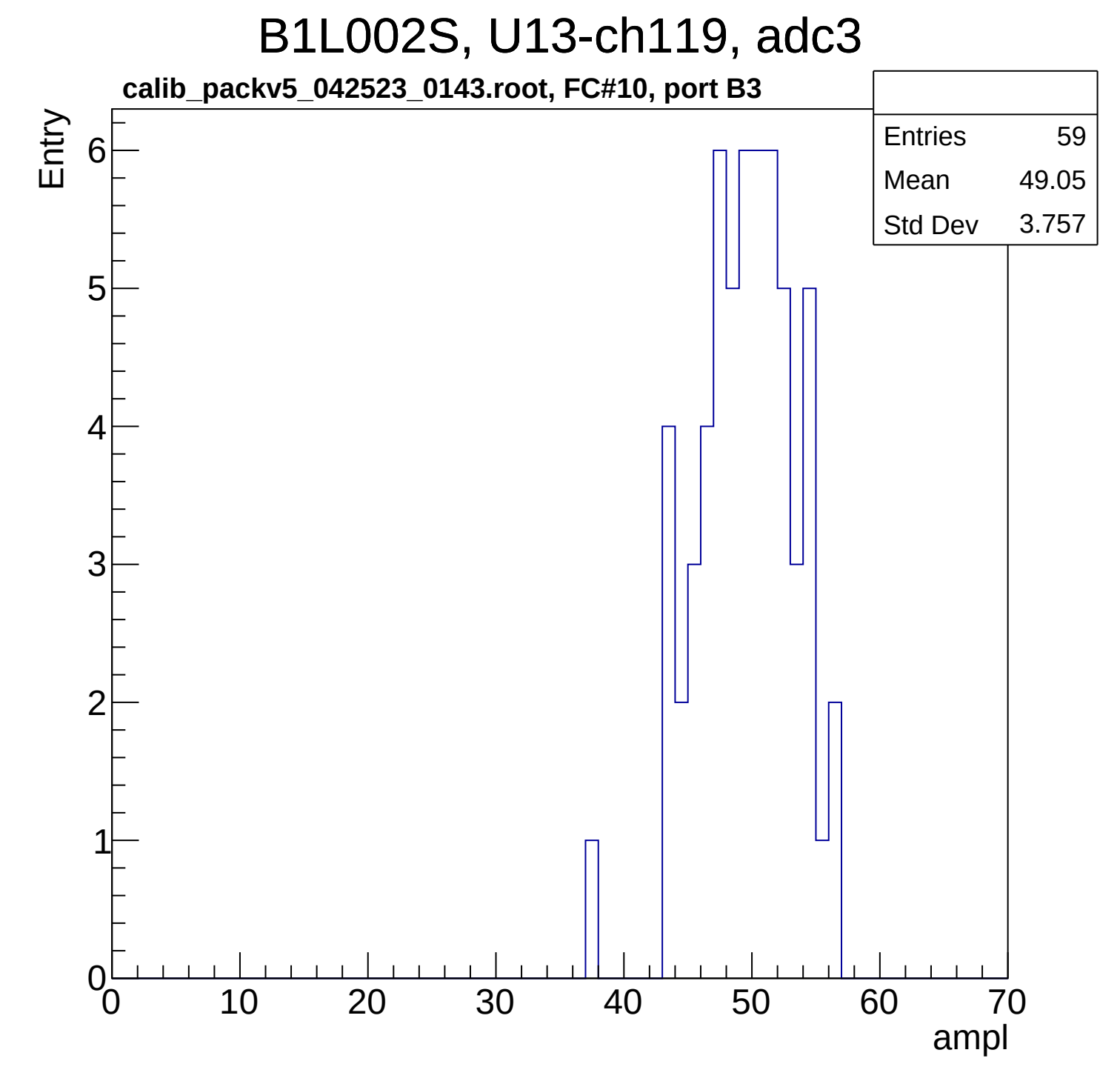
B1L002S, U13-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 49.05
Std Dev 3.757
Entry
ampl
0
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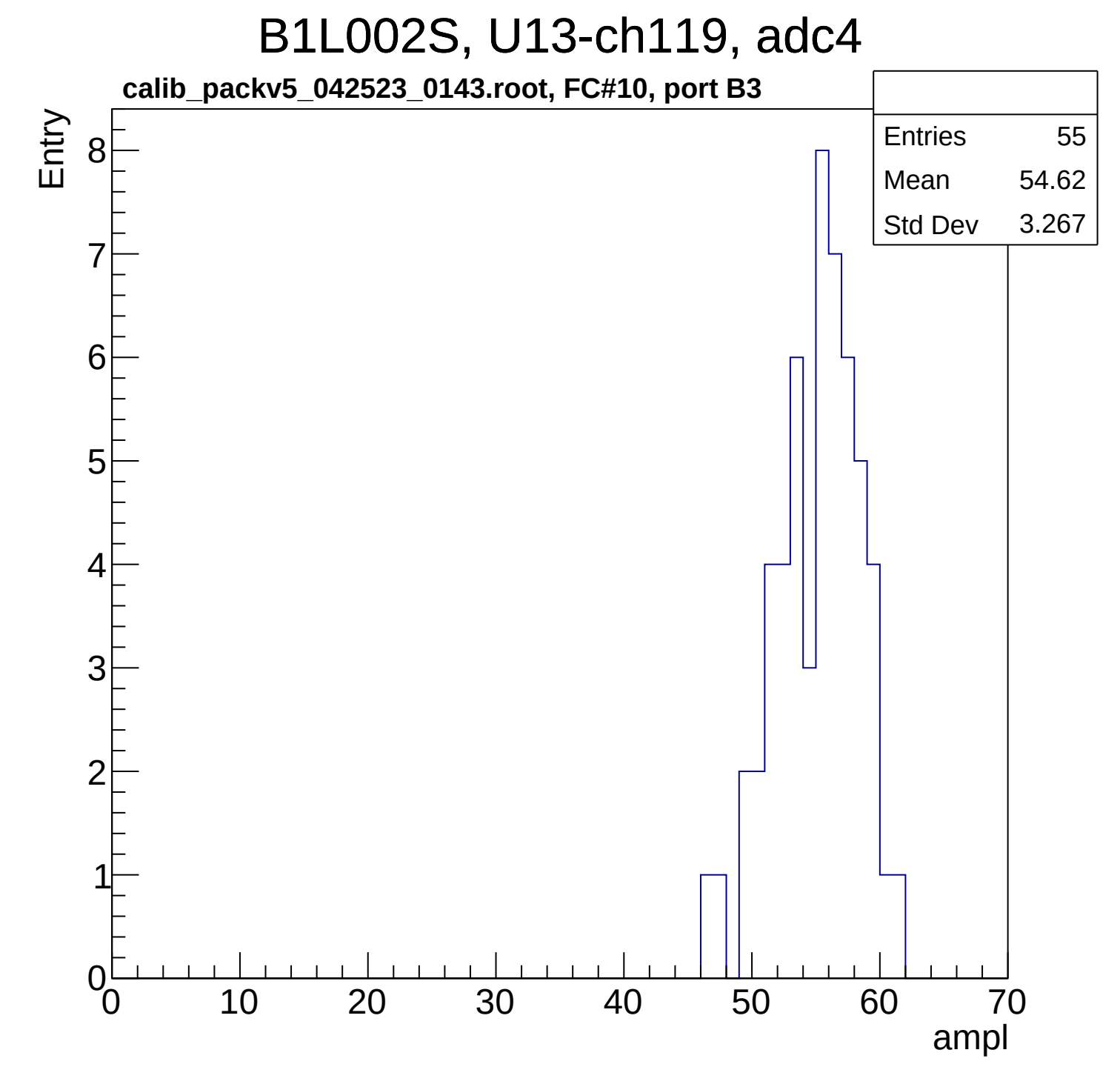
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U13-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.62
Std Dev 3.267
Entry
ampl
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1
2
3
4
5
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7
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0
10
20
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40
50
60
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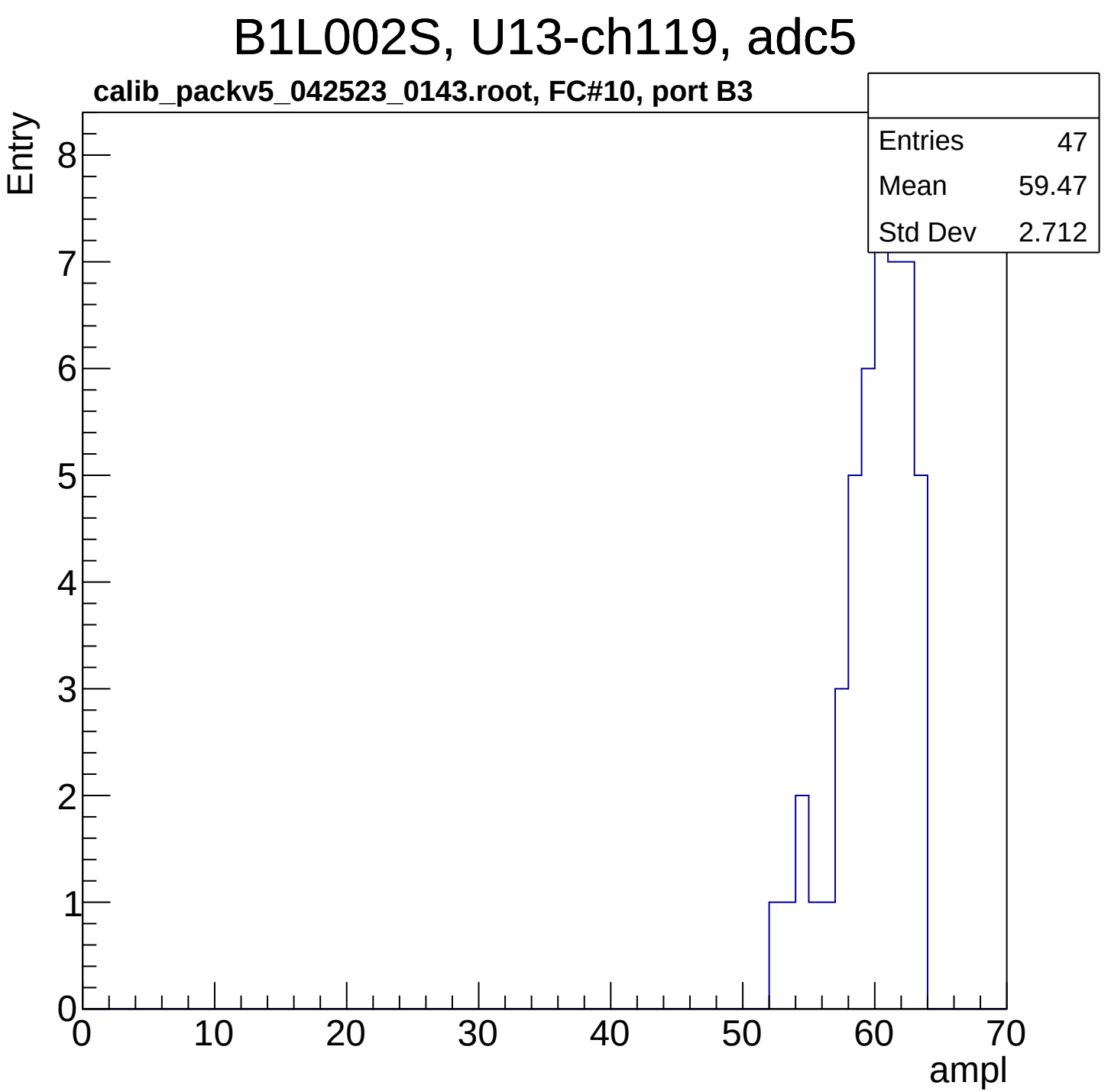
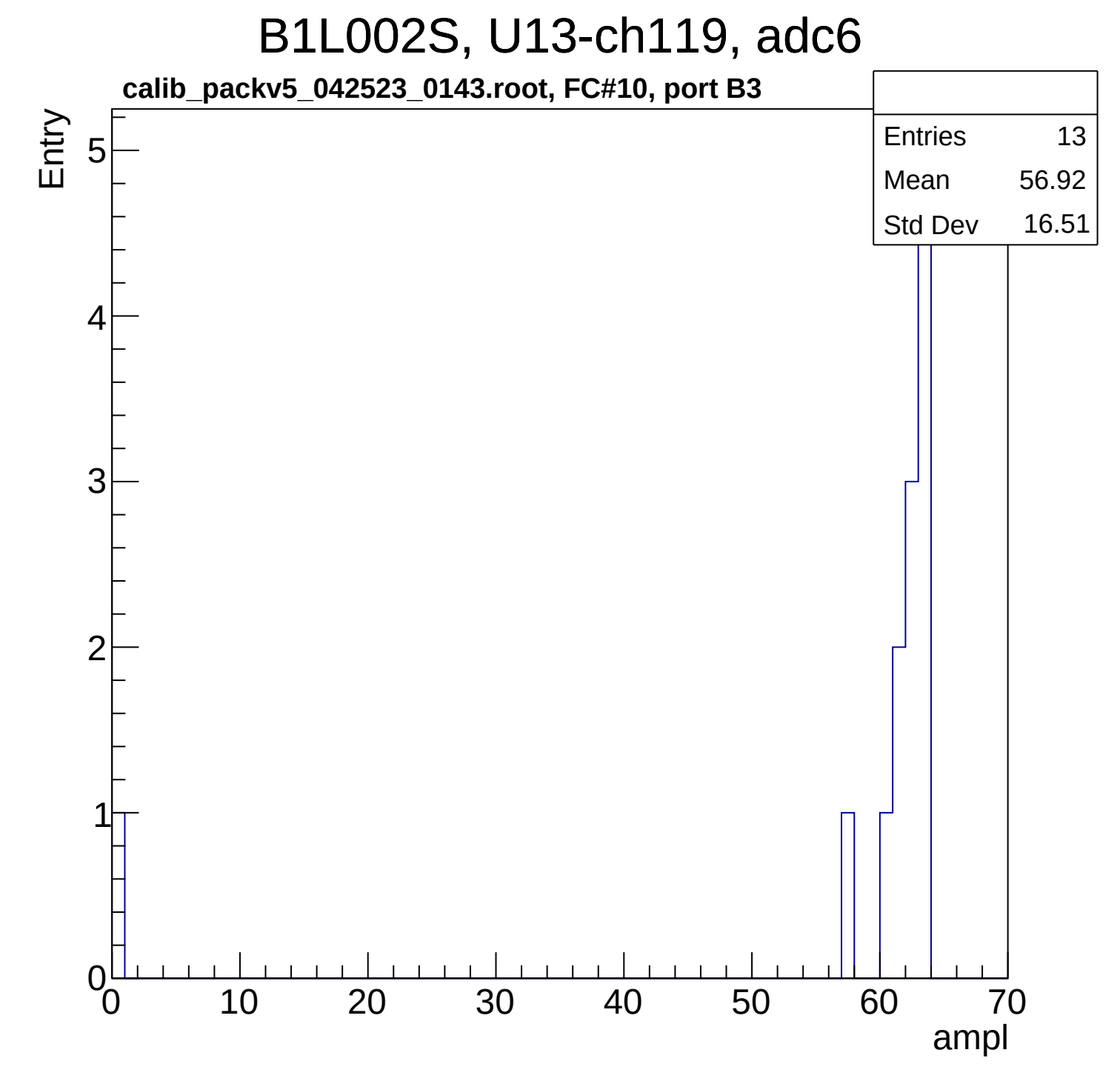
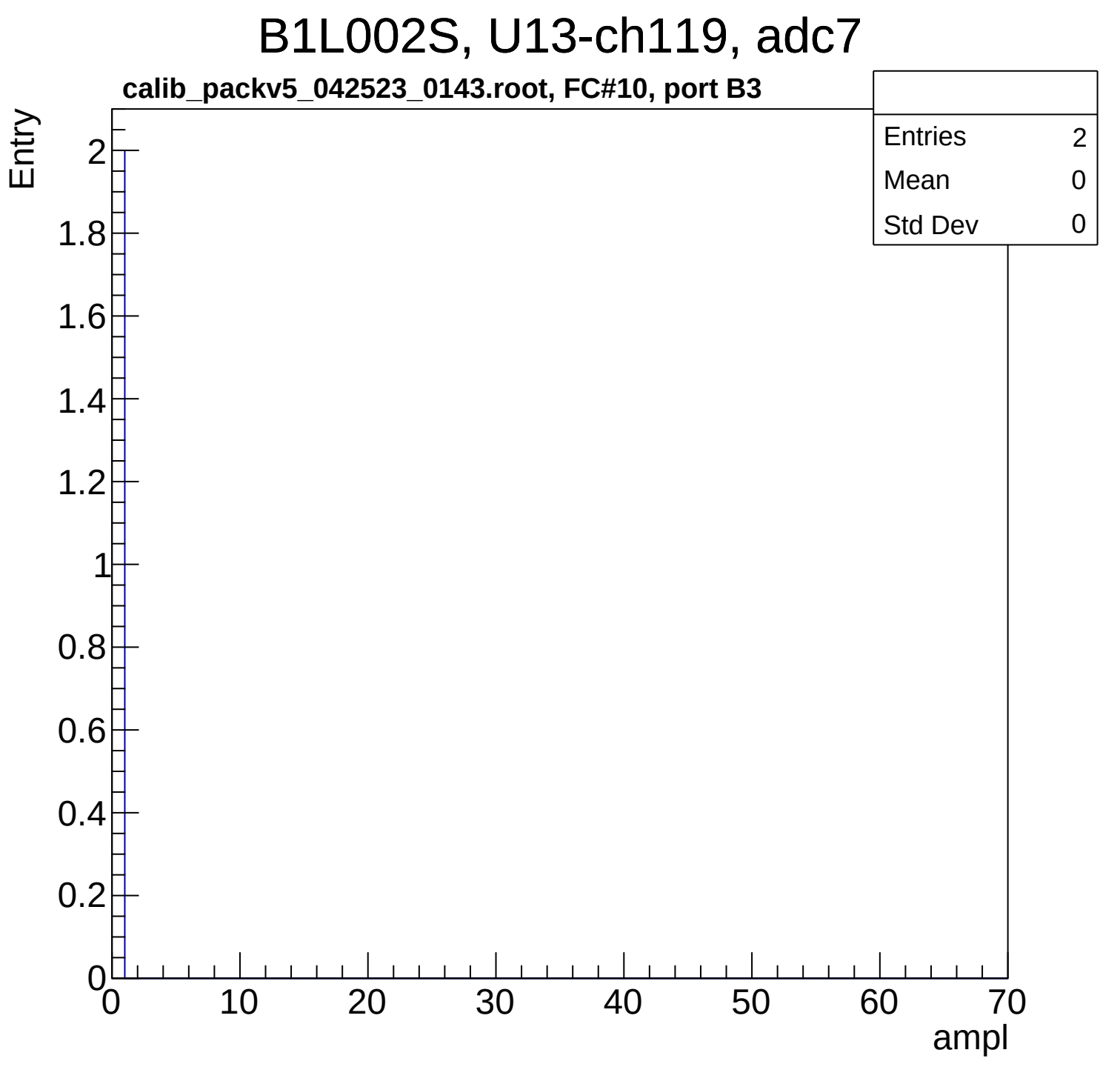

B1L002S, U13-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.47
Std Dev 2.712
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 56.92
Std Dev 16.51
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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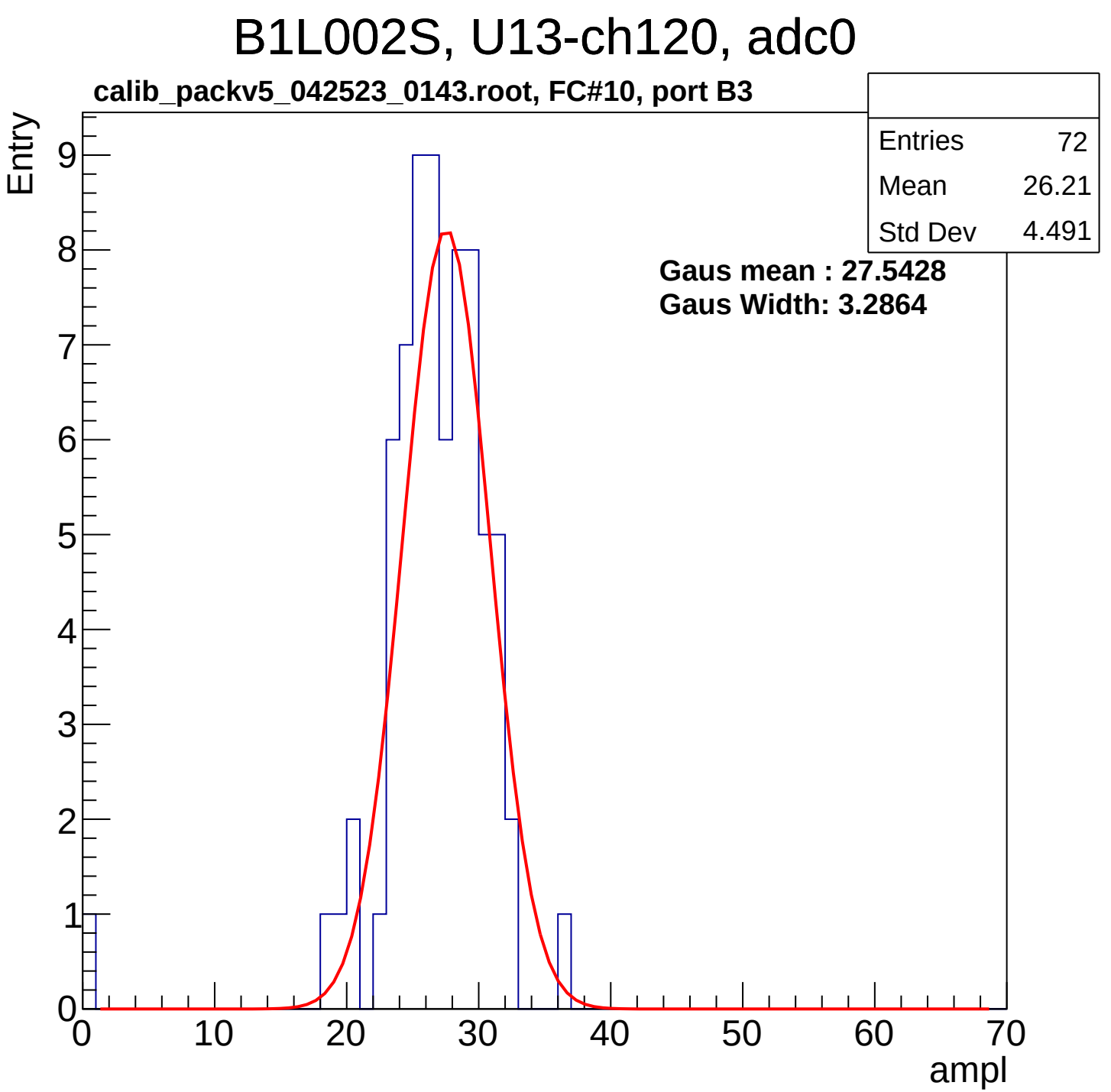
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.21
Std Dev 4.491
Gaus mean : 27.5428
Gaus Width: 3.2864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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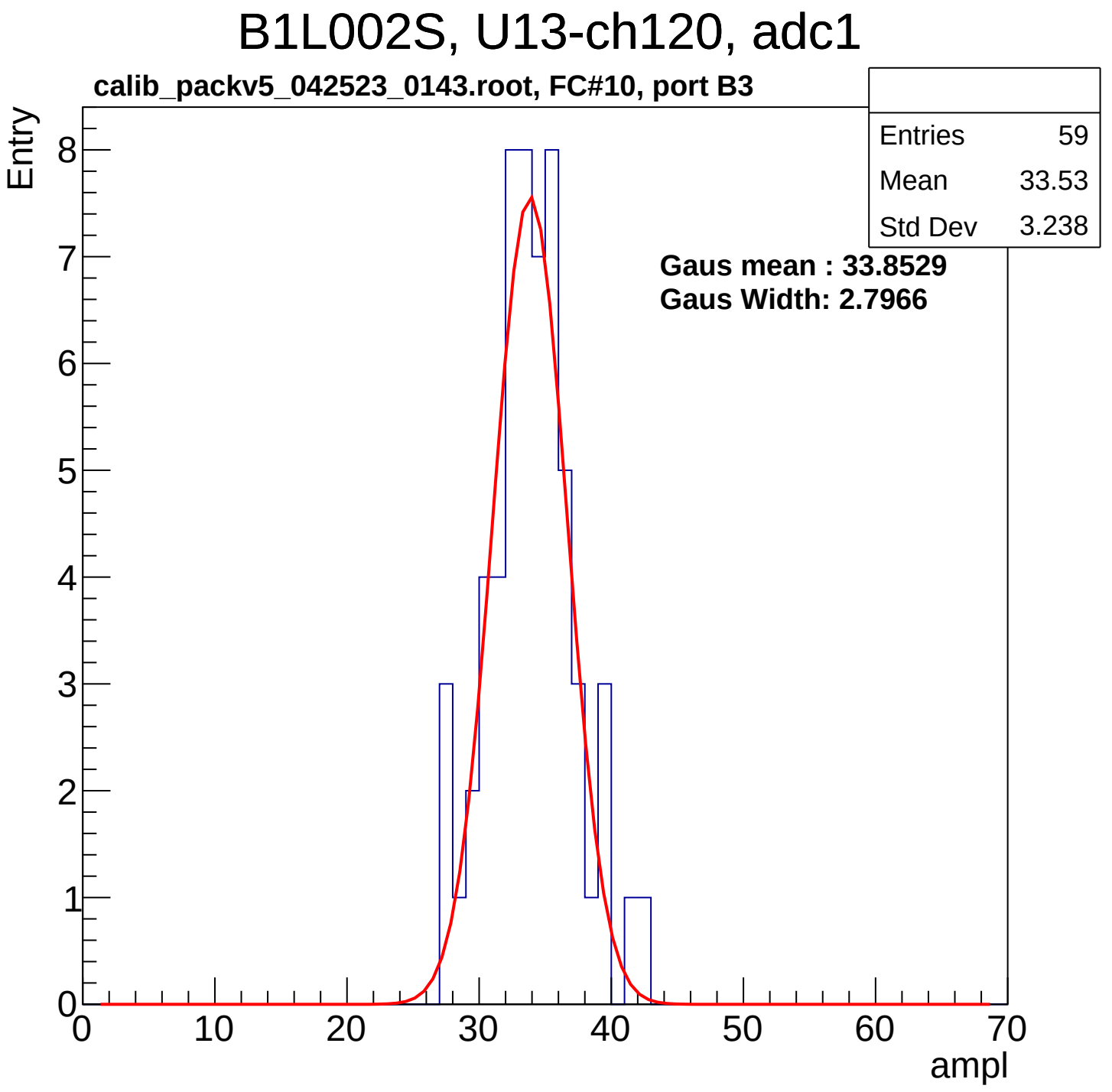
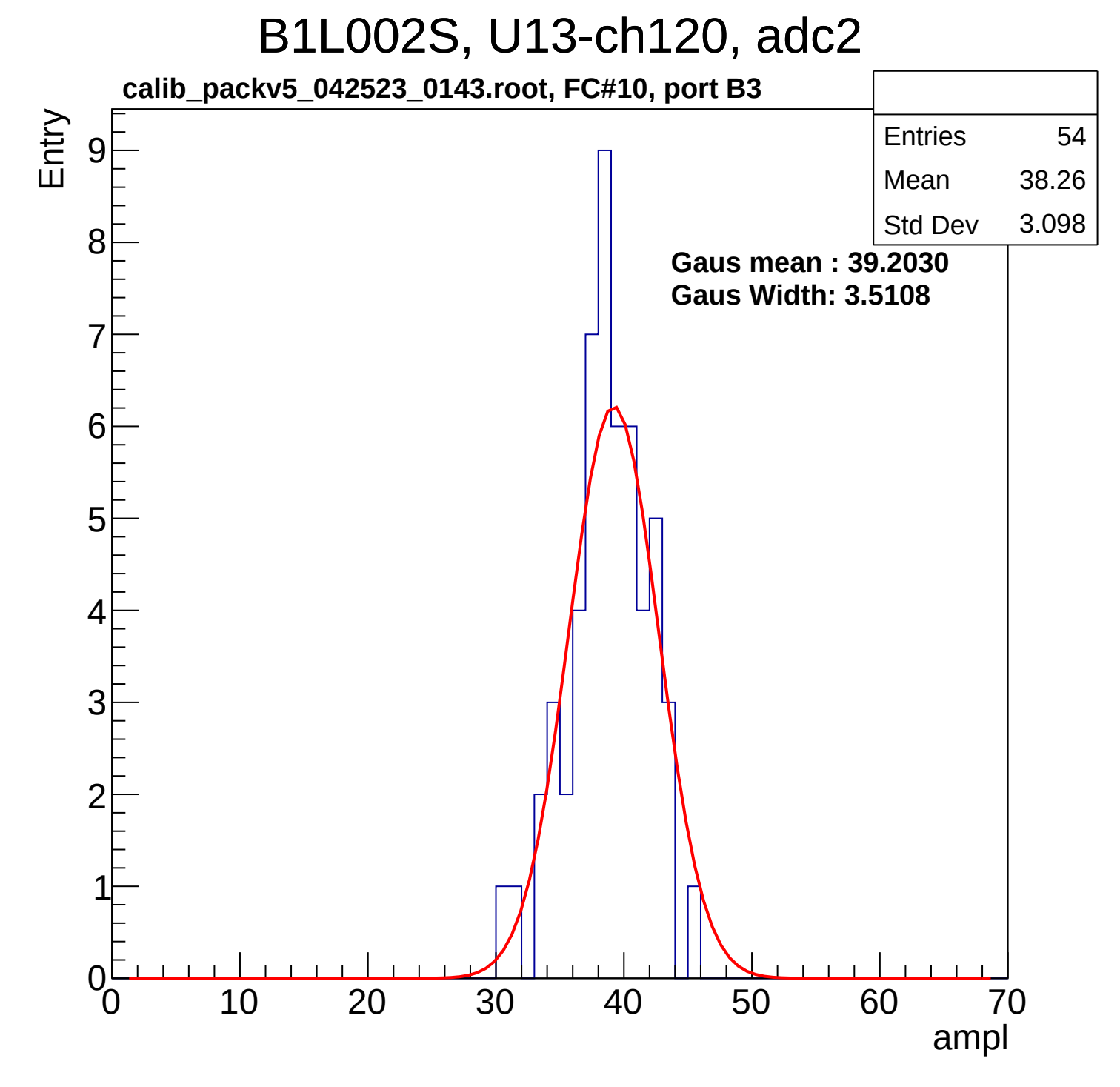

B1L002S, U13-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 33.53
Std Dev 3.238
Gaus mean : 33.8529
Gaus Width: 2.7966
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 38.26
Std Dev 3.098
Gaus mean : 39.2030
Gaus Width: 3.5108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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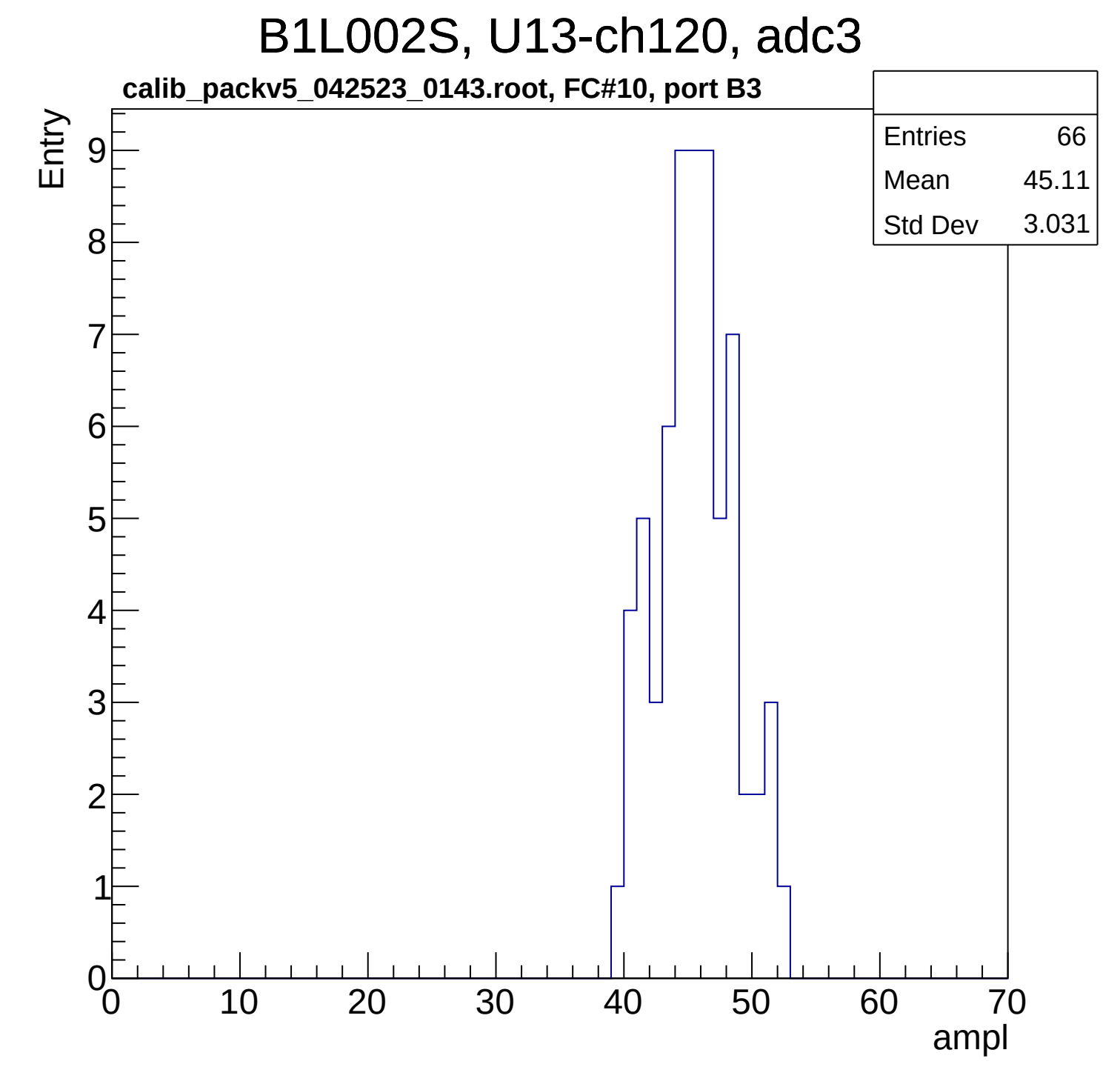
B1L002S, U13-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.11
Std Dev 3.031
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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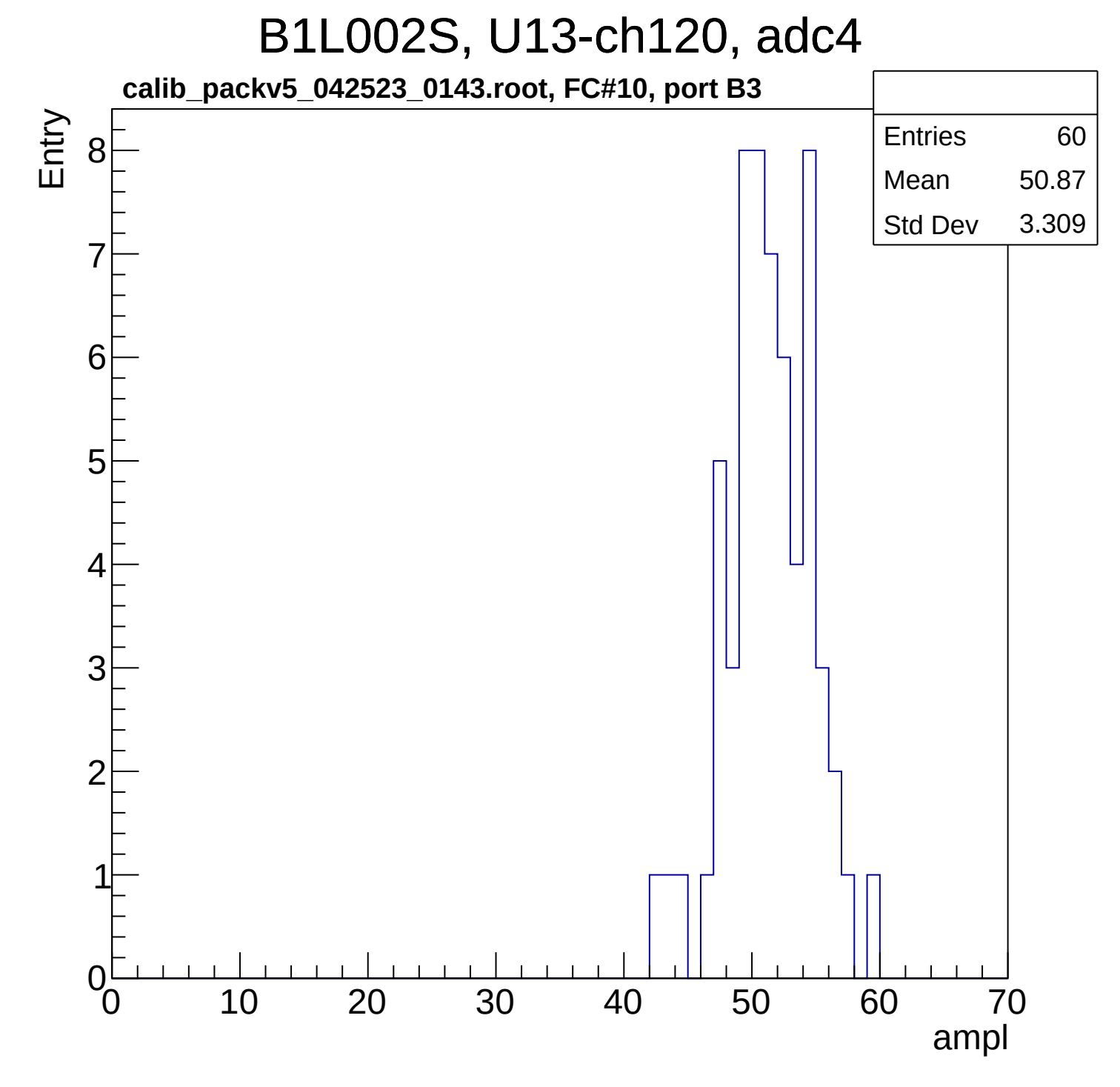
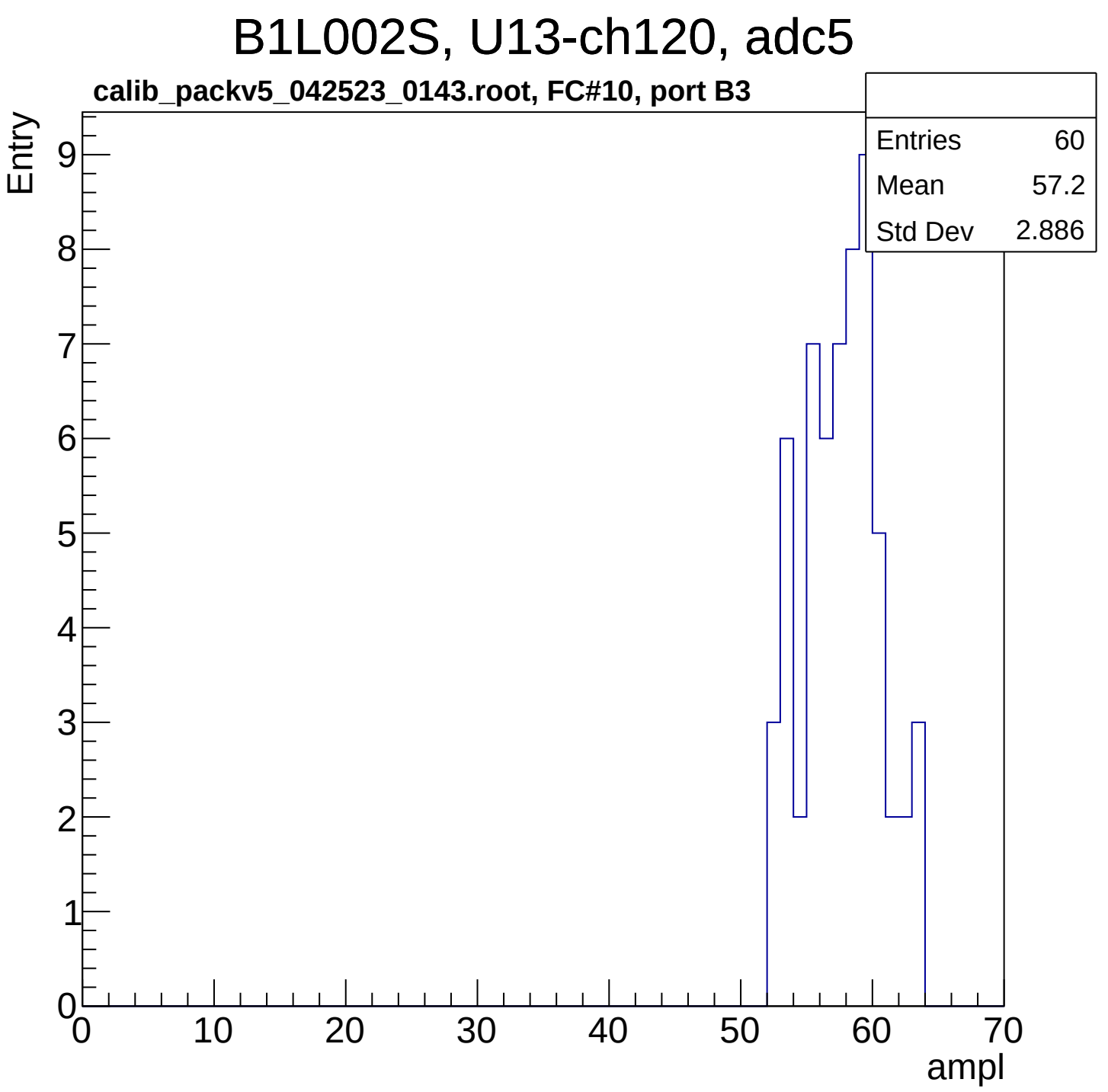
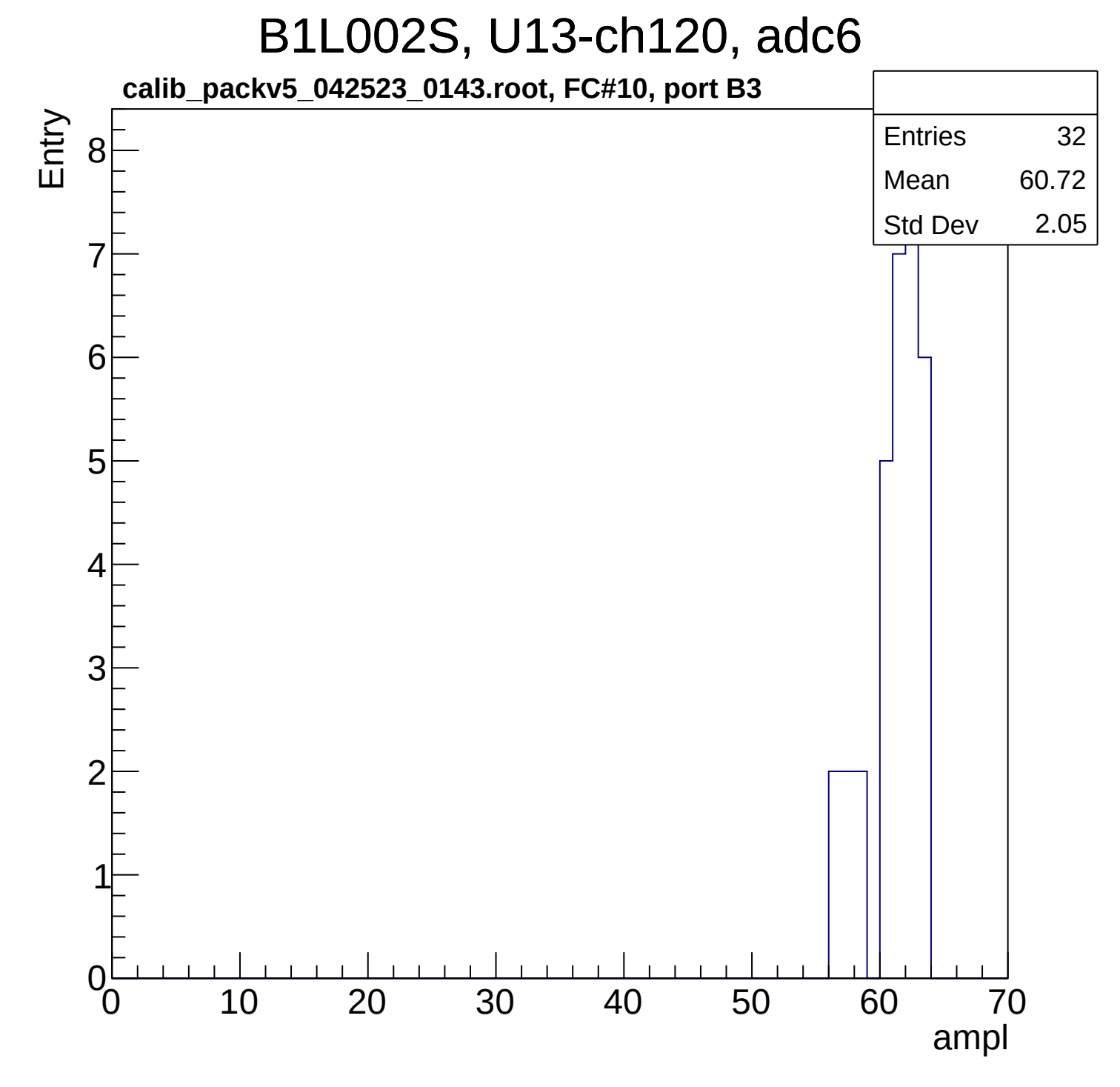
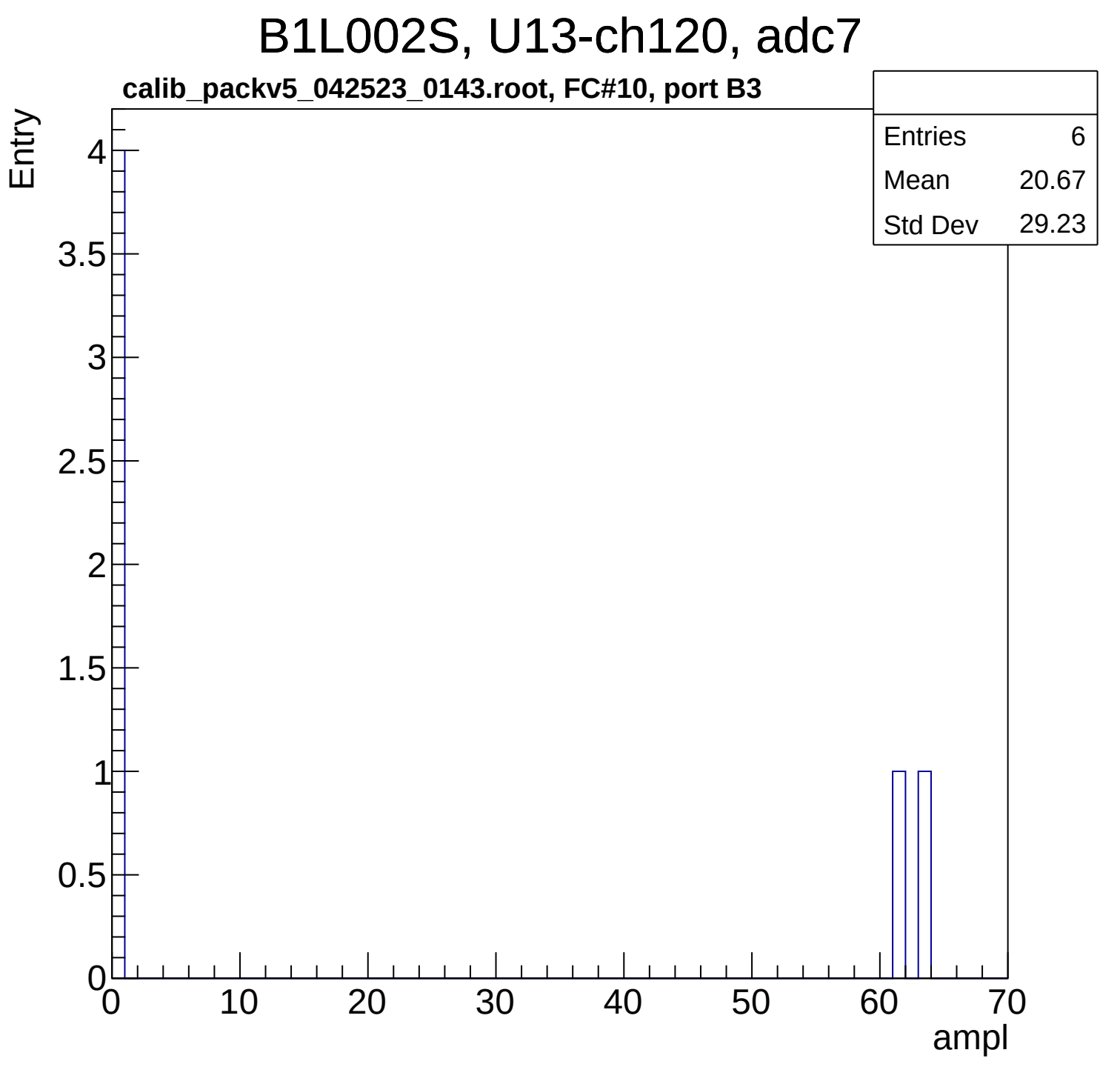
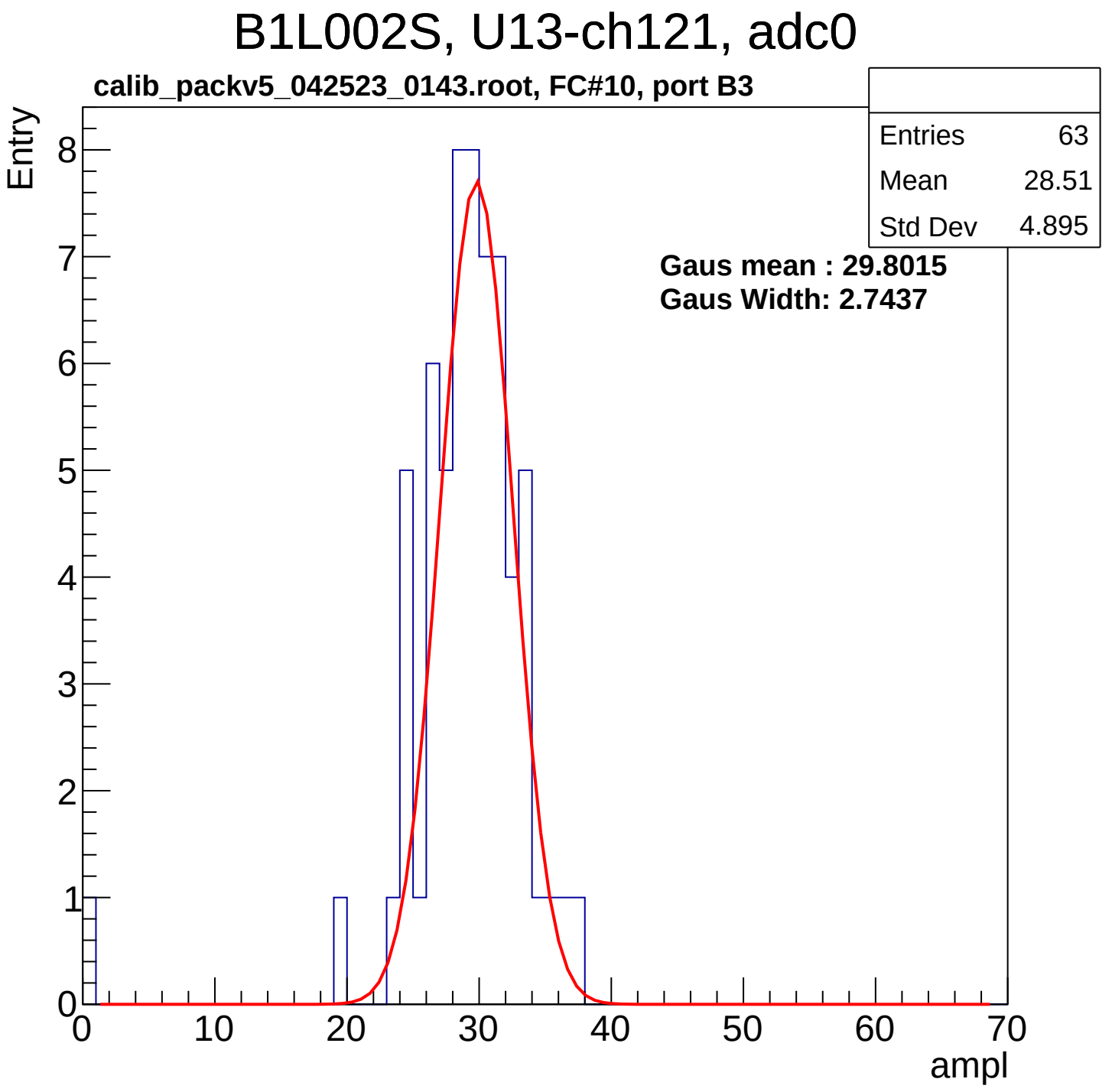
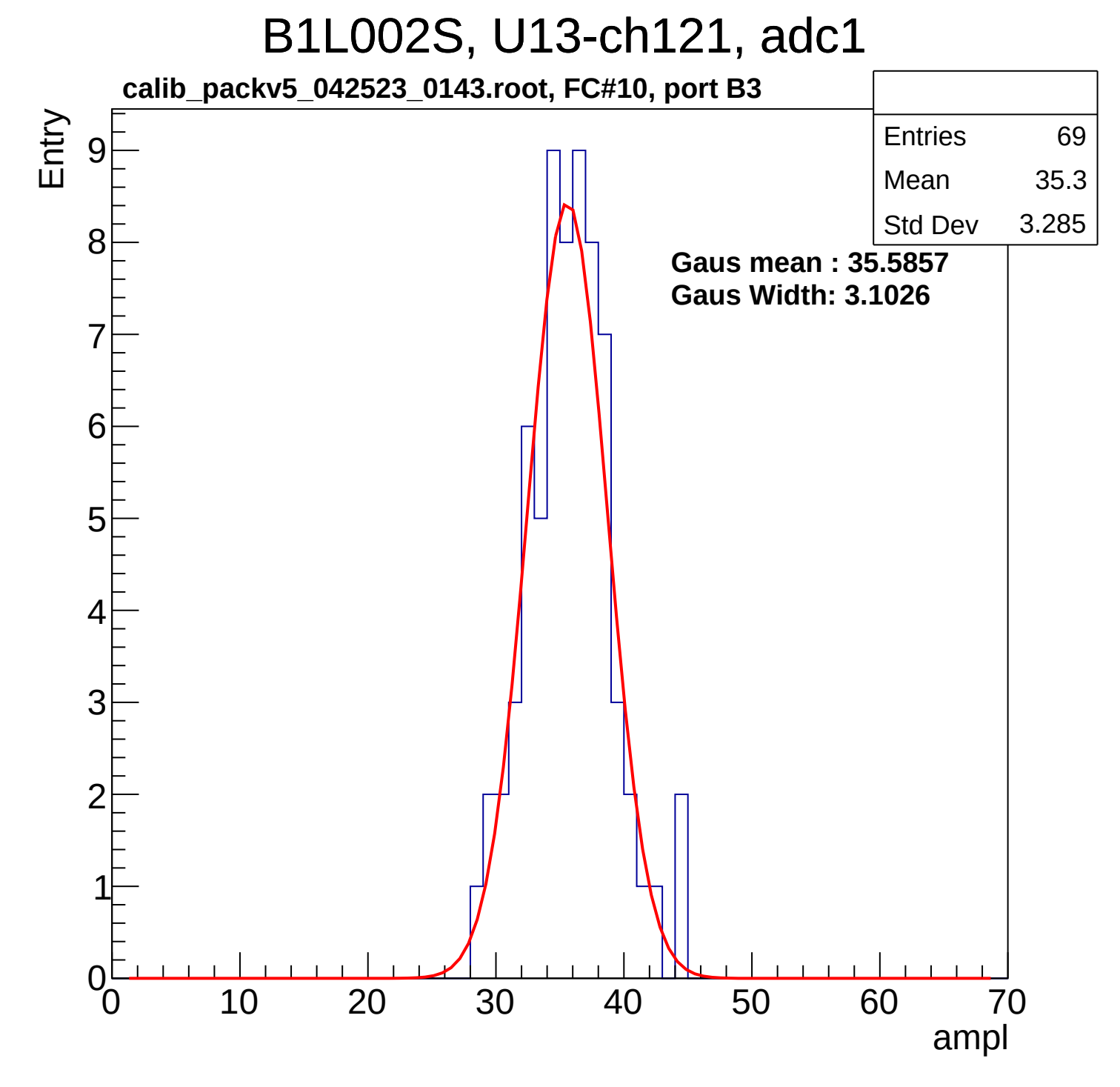

B1L002S, U13-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 50.87
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

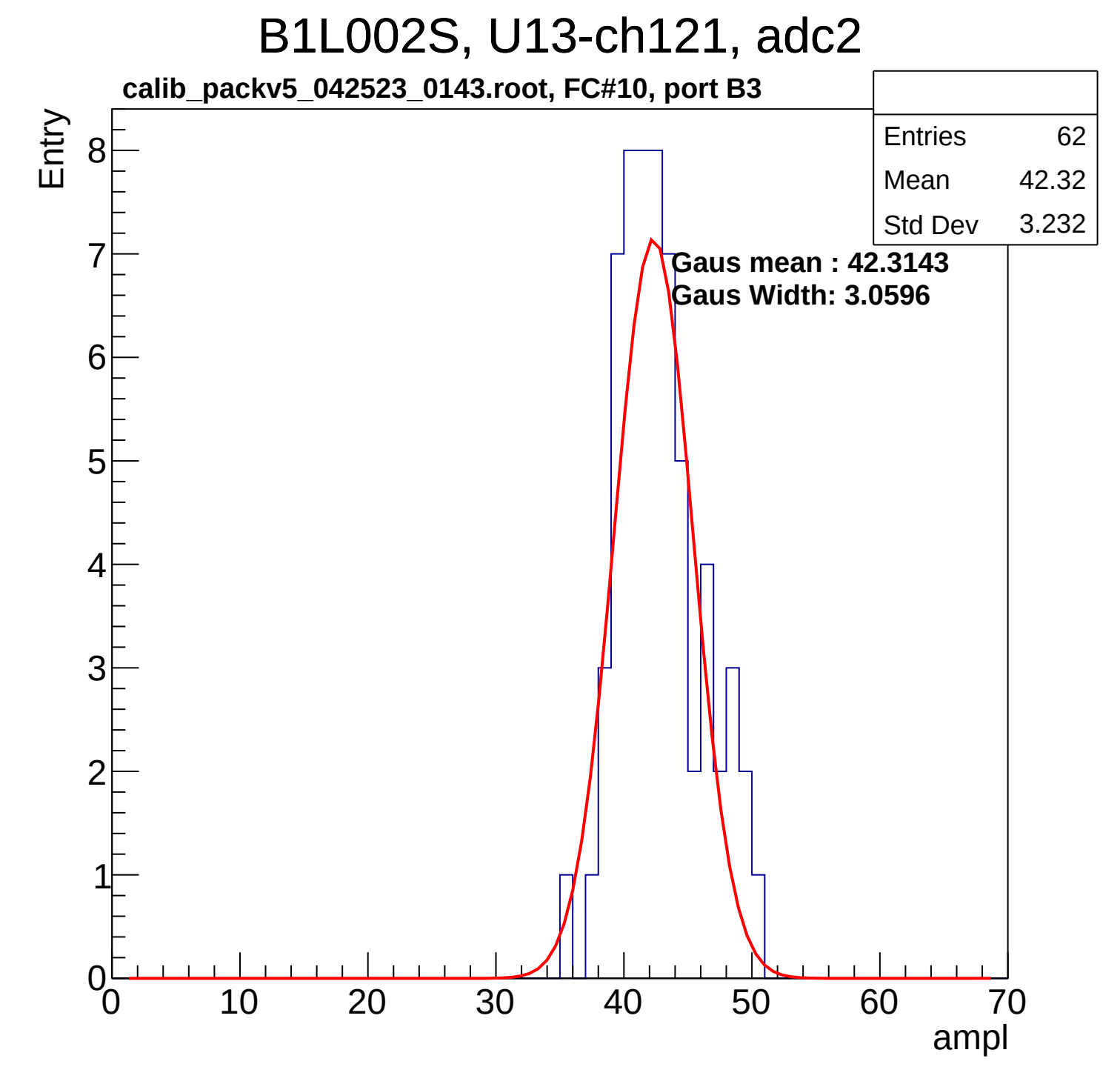

B1L002S, U13-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.2
Std Dev 2.886
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

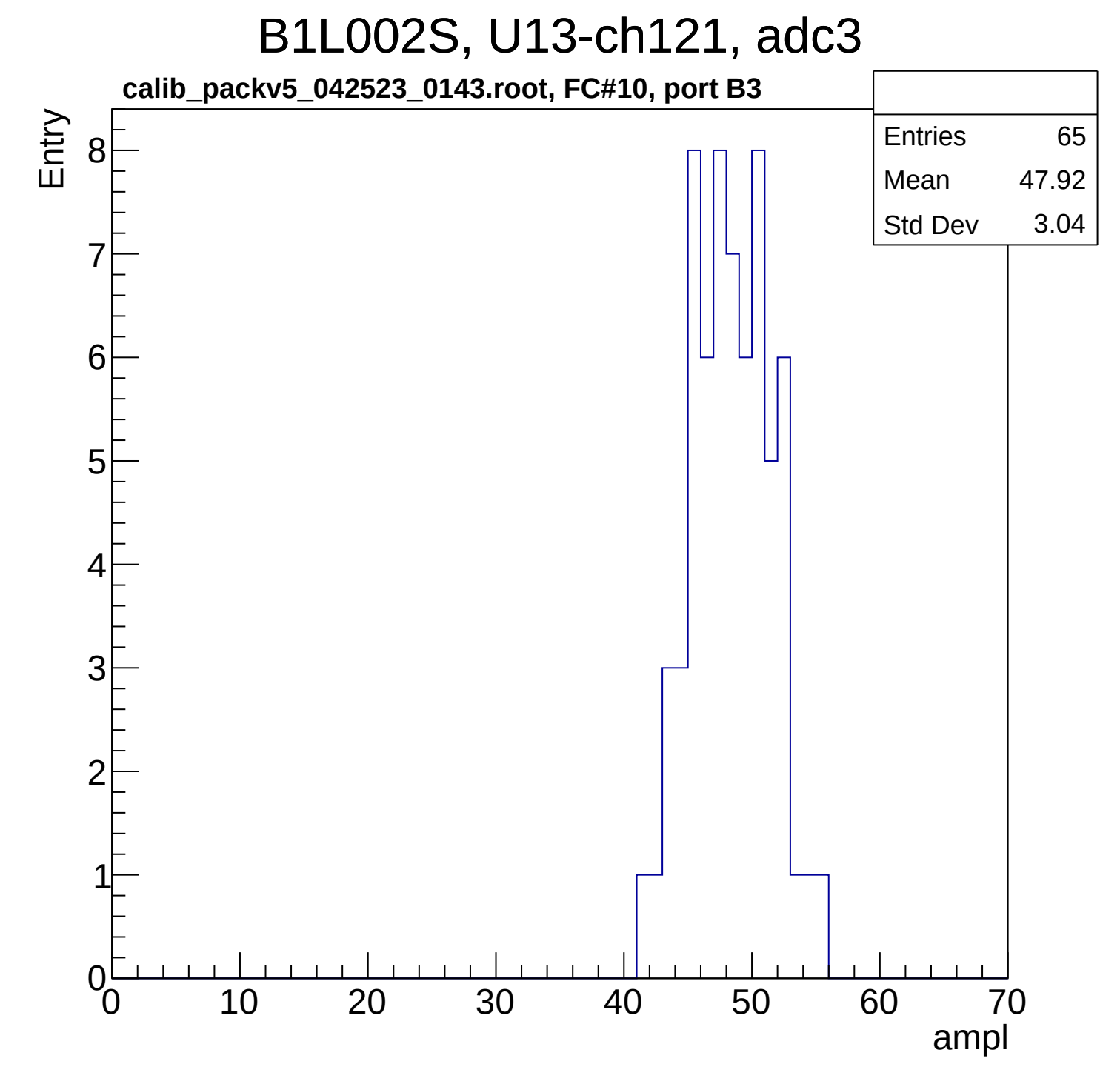

B1L002S, U13-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 60.72
Std Dev 2.05
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

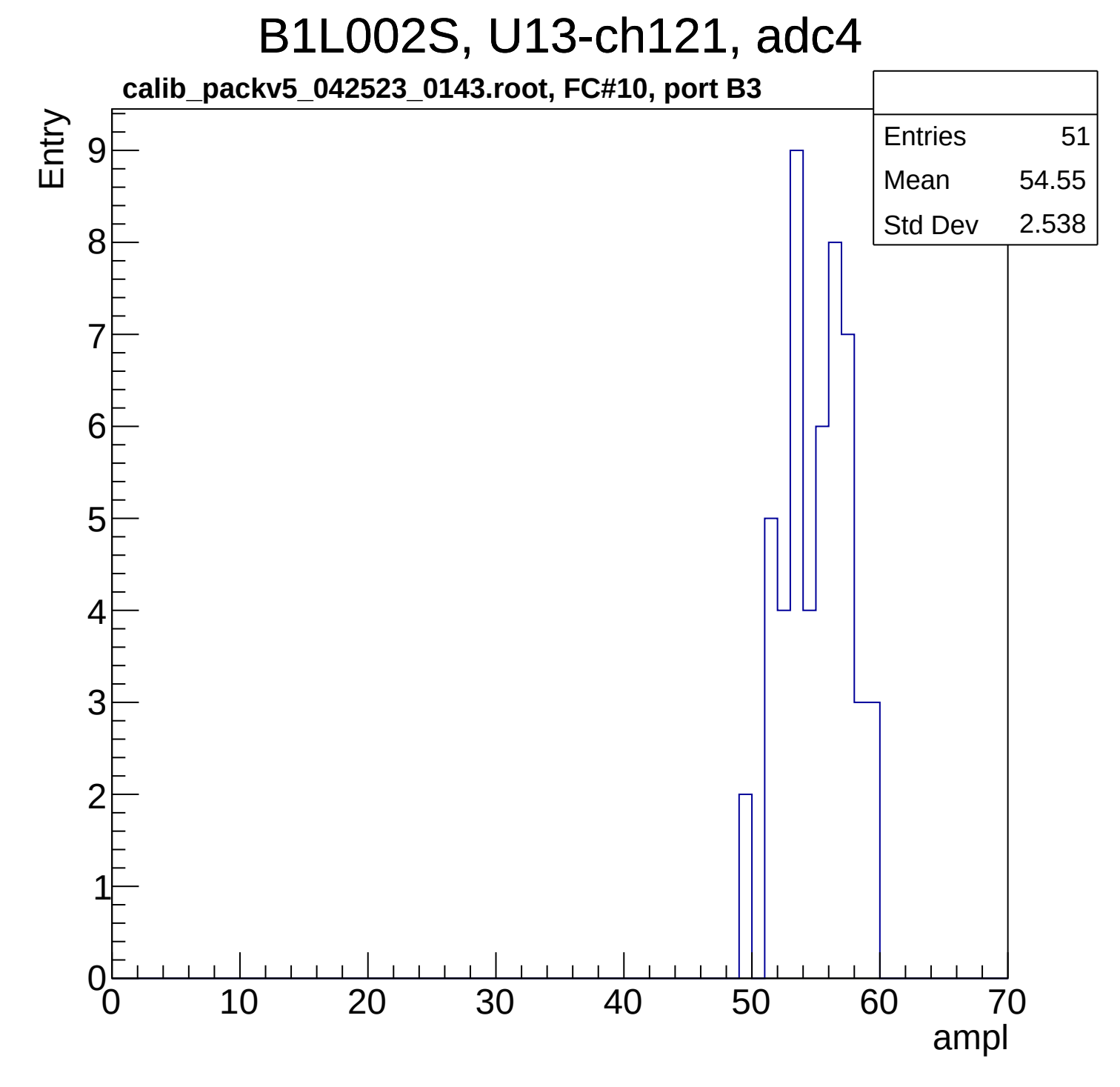

B1L002S, U13-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

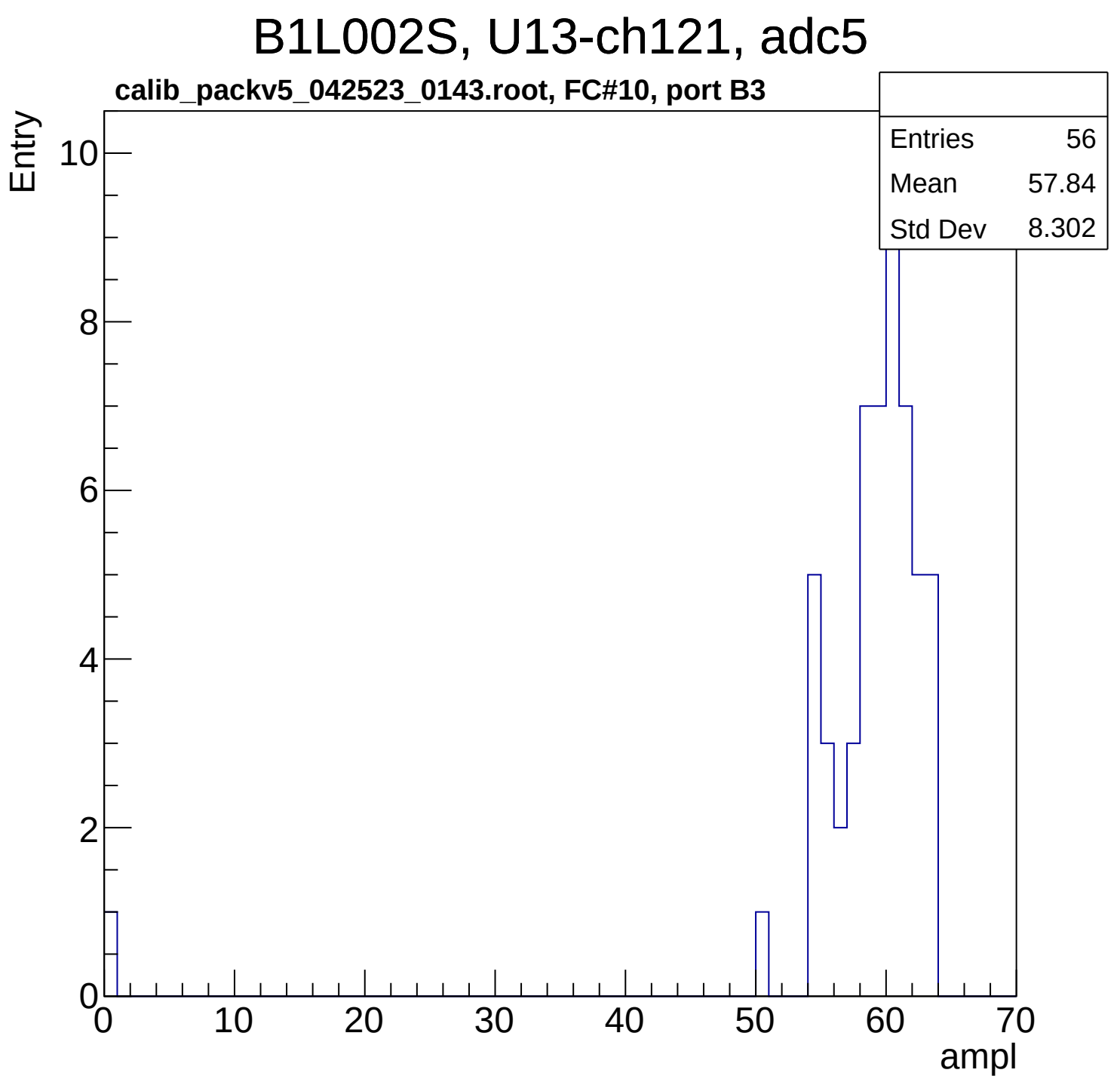

B1L002S, U13-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.51
Std Dev 4.895
Gaus mean : 29.8015
Gaus Width: 2.7437
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

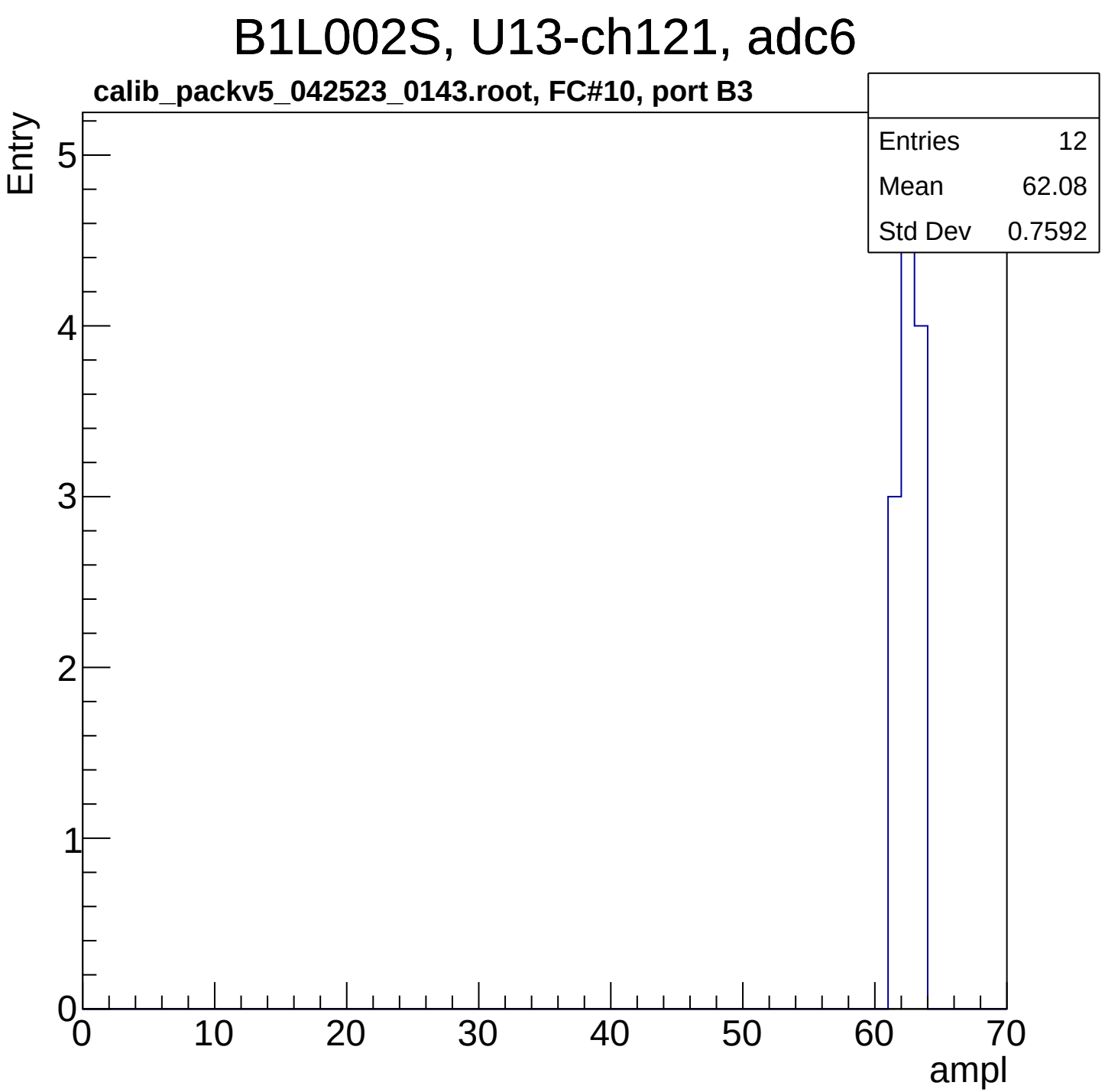

B1L002S, U13-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.3
Std Dev 3.285
Gaus mean : 35.5857
Gaus Width: 3.1026
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

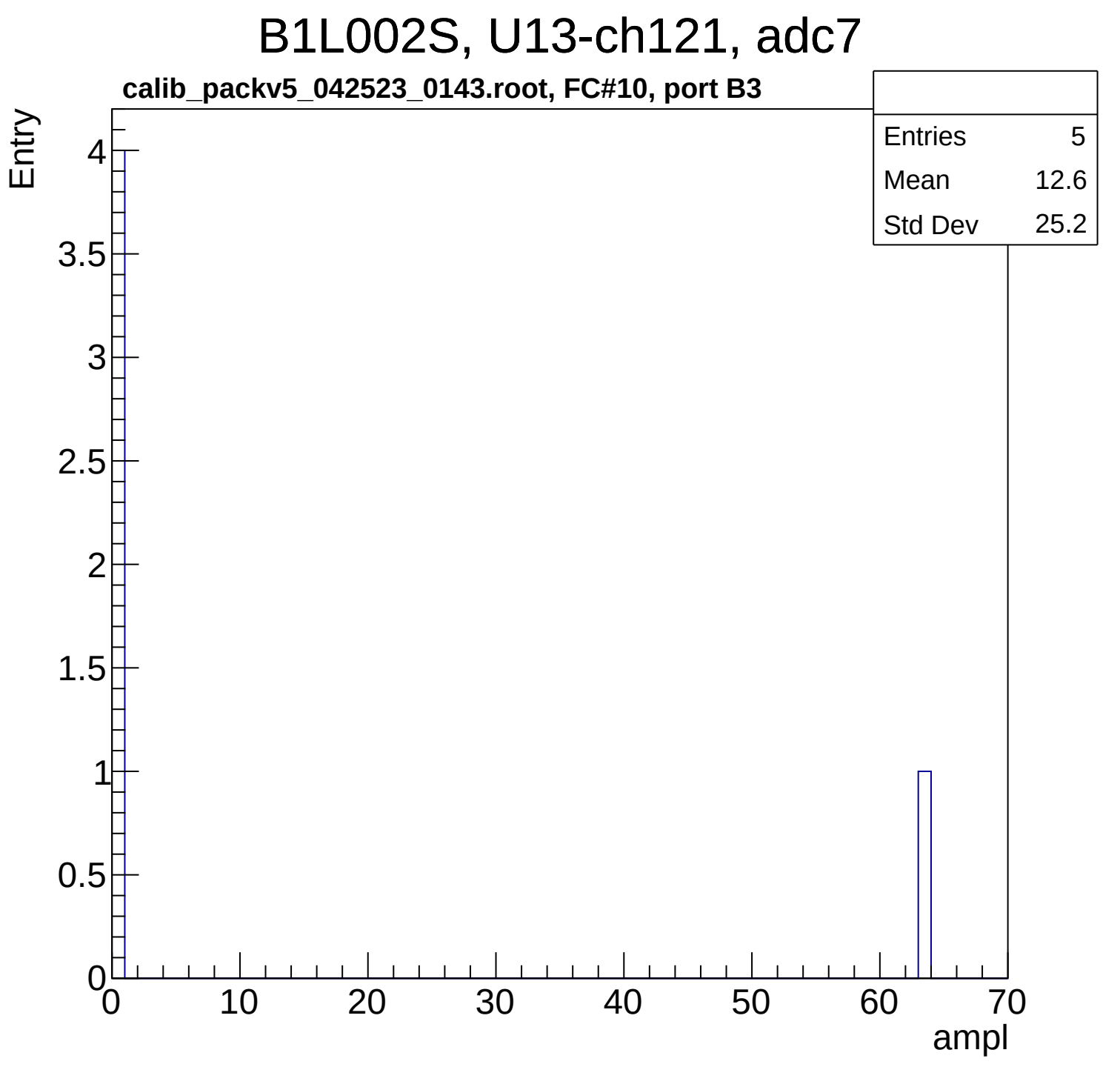

B1L002S, U13-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.32
Std Dev 3.232
Gaus mean : 42.3143
Gaus Width: 3.0596
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

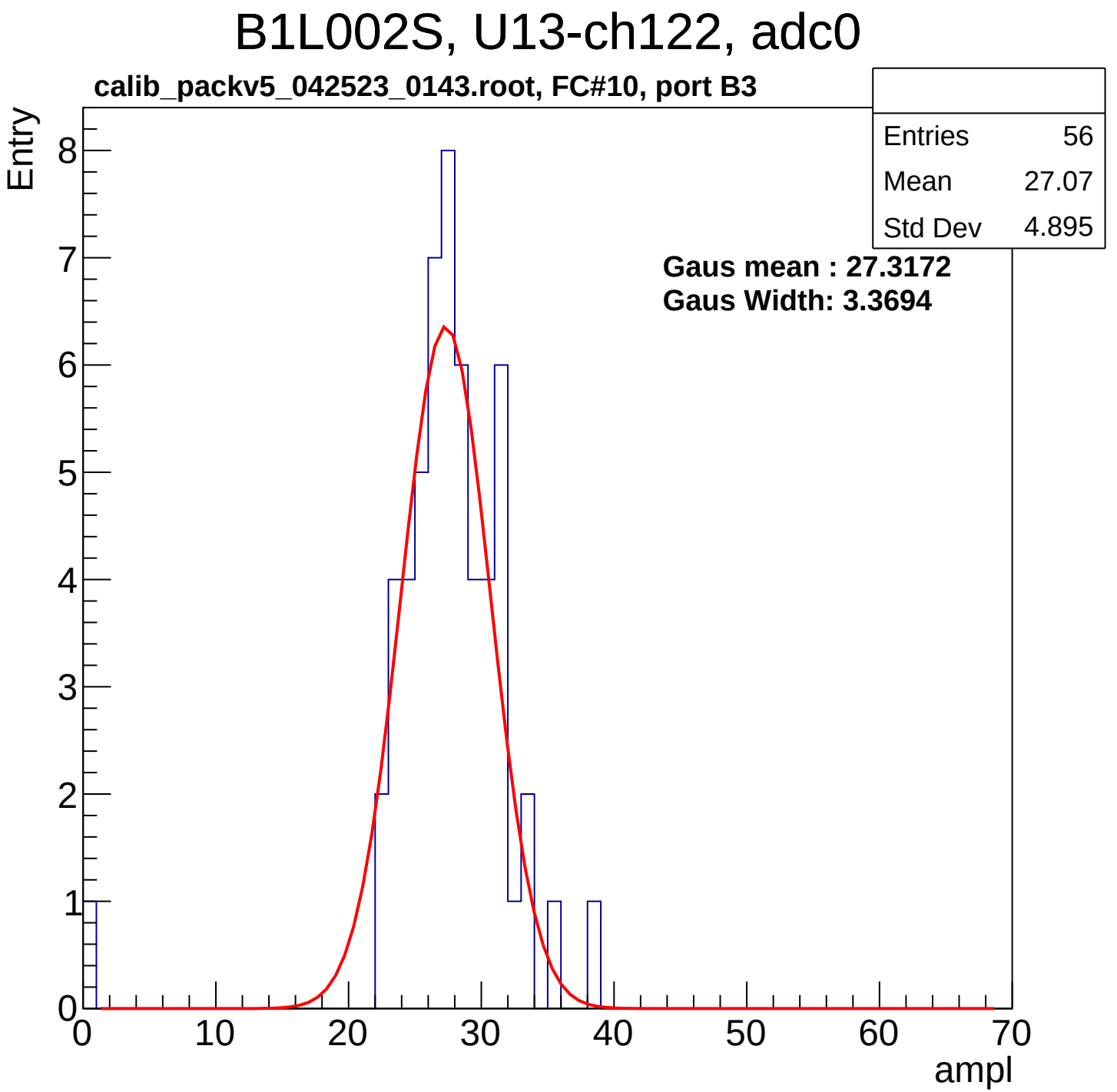

B1L002S, U13-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.92
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

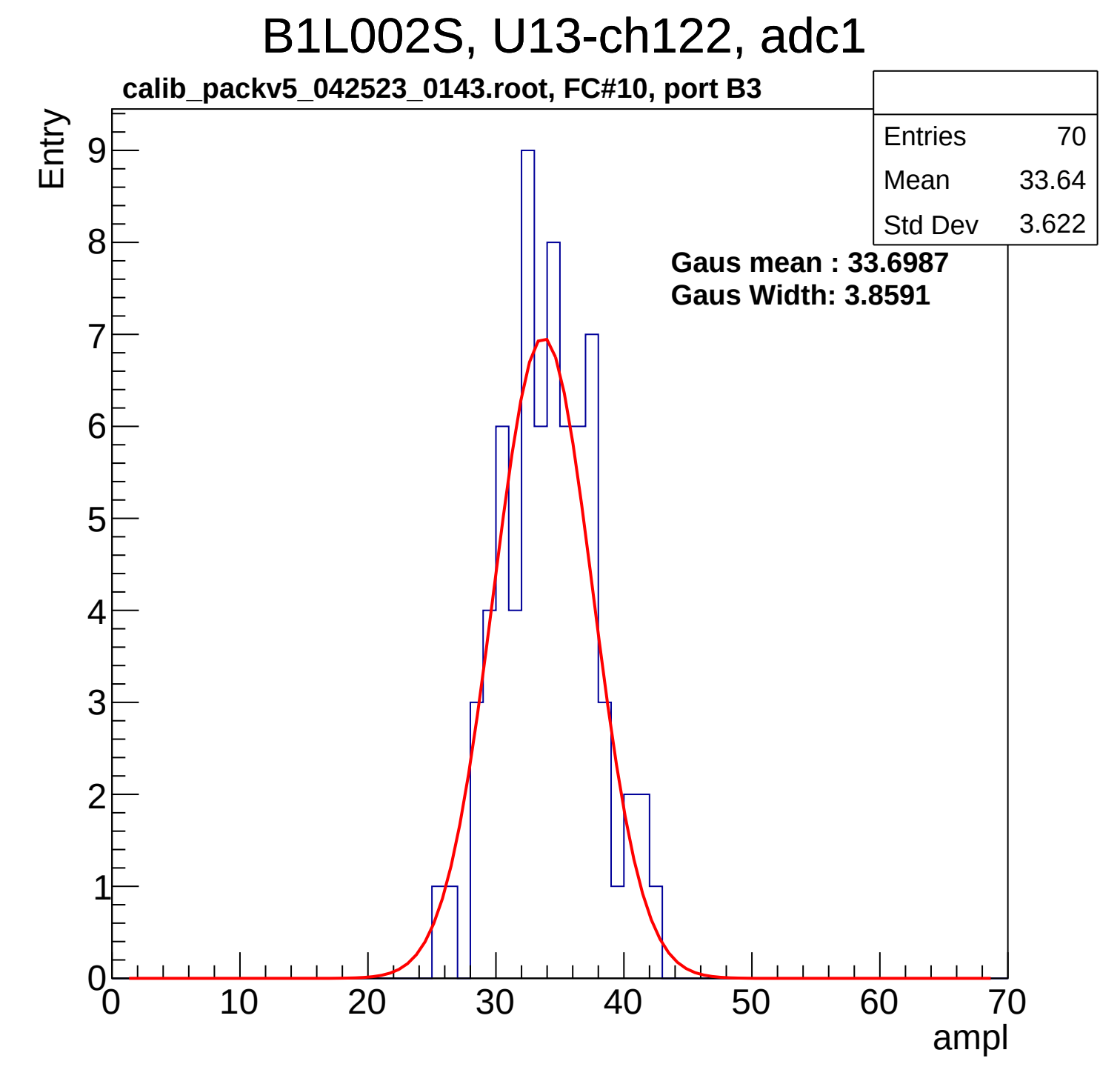

B1L002S, U13-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.55
Std Dev 2.538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

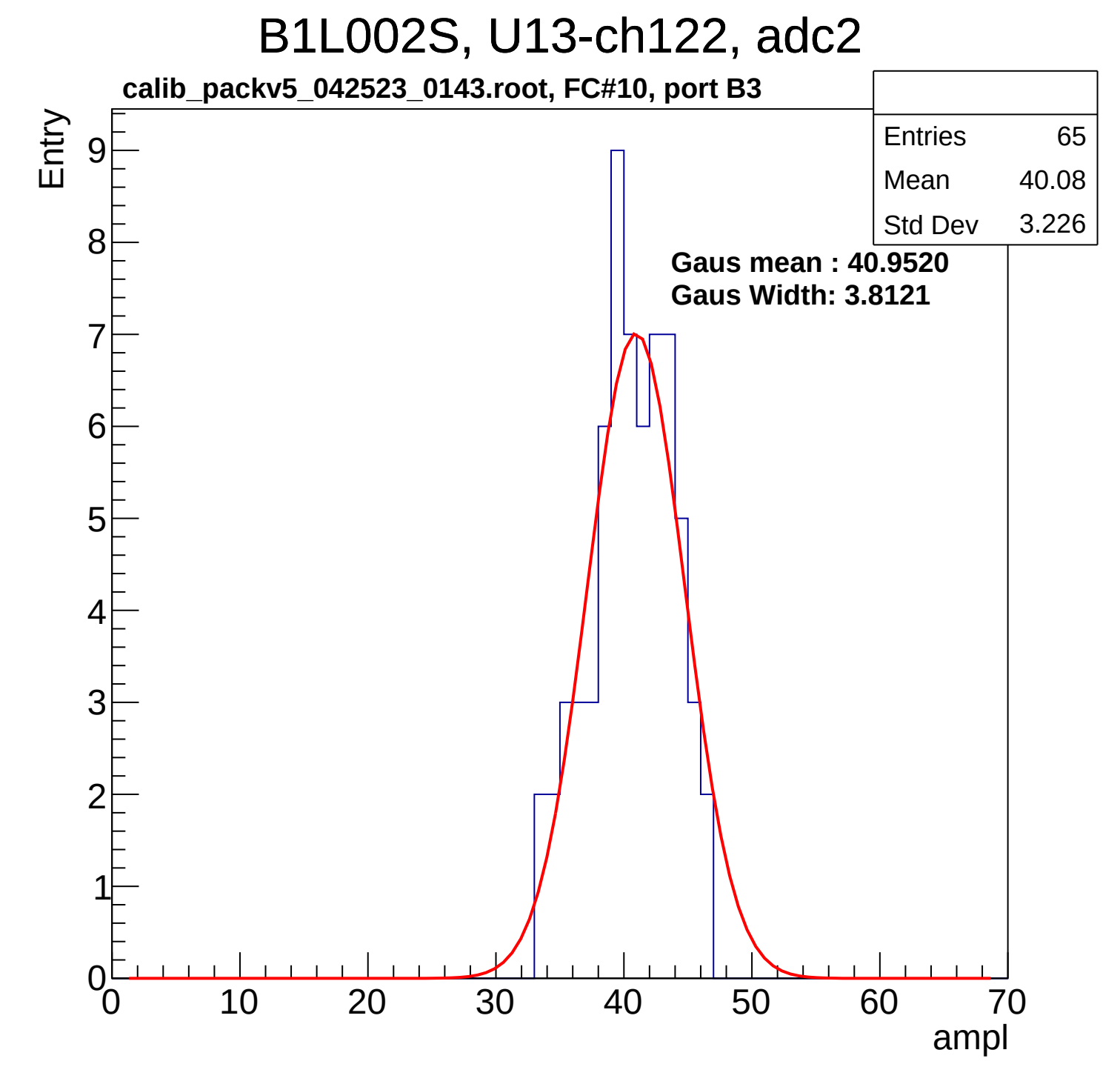

B1L002S, U13-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.84
Std Dev 8.302
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62.08
Std Dev 0.7592
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 12.6
Std Dev 25.2
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.07
Std Dev 4.895
Gaus mean : 27.3172
Gaus Width: 3.3694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.64
Std Dev 3.622
Gaus mean : 33.6987
Gaus Width: 3.8591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.08
Std Dev 3.226
Gaus mean : 40.9520
Gaus Width: 3.8121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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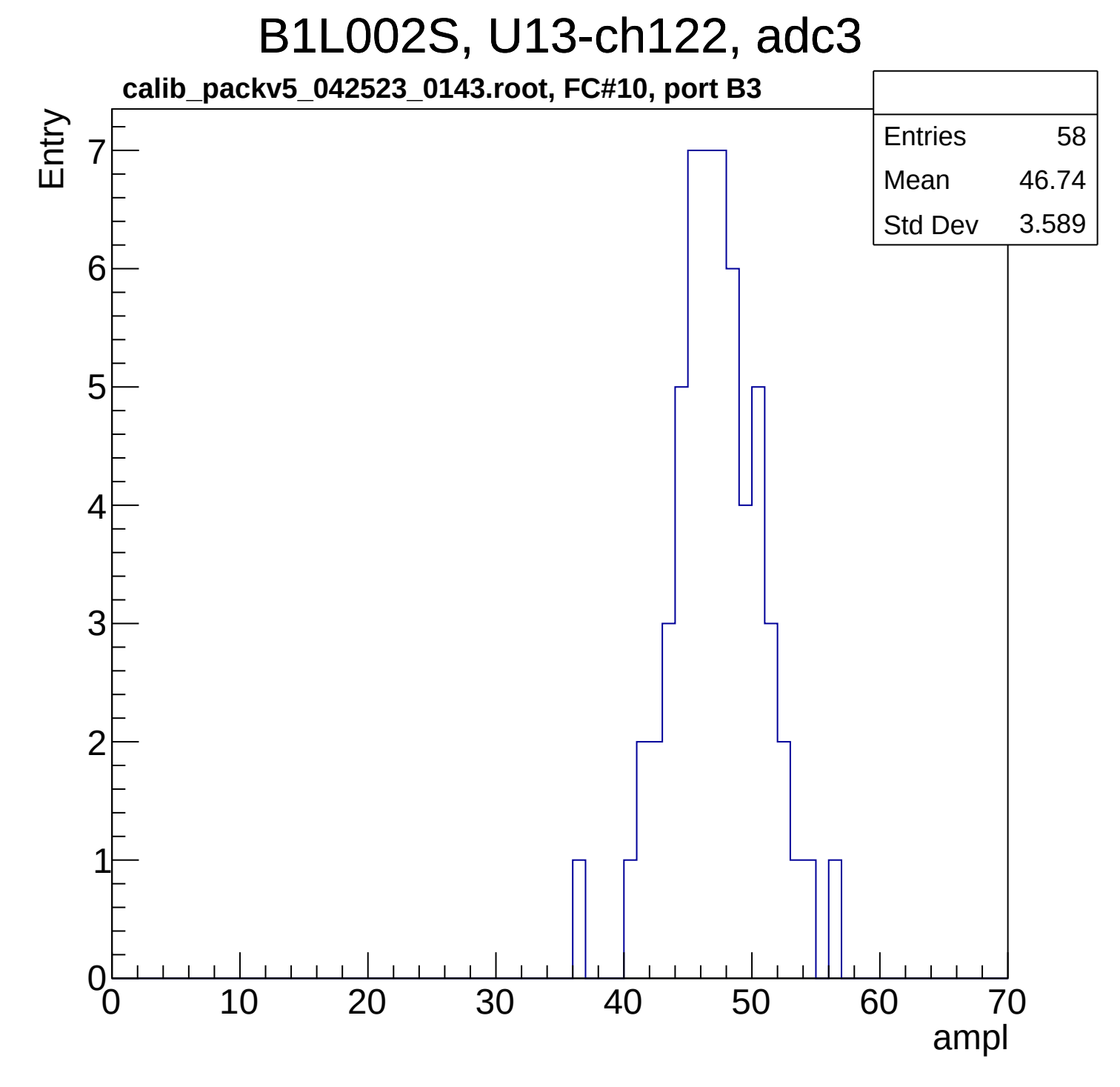
B1L002S, U13-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.74
Std Dev 3.589
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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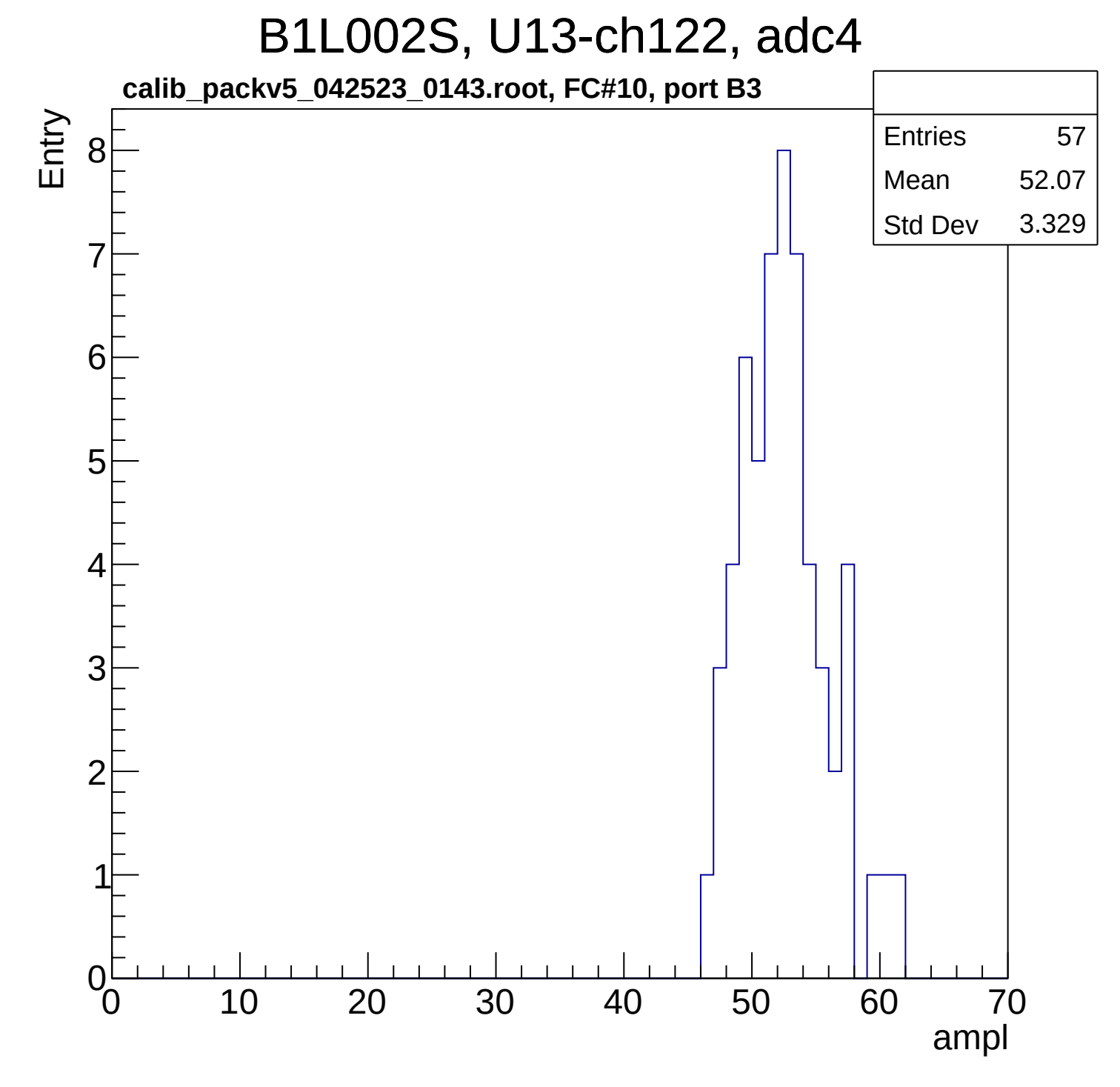
B1L002S, U13-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.07
Std Dev 3.329
Entry
ampl
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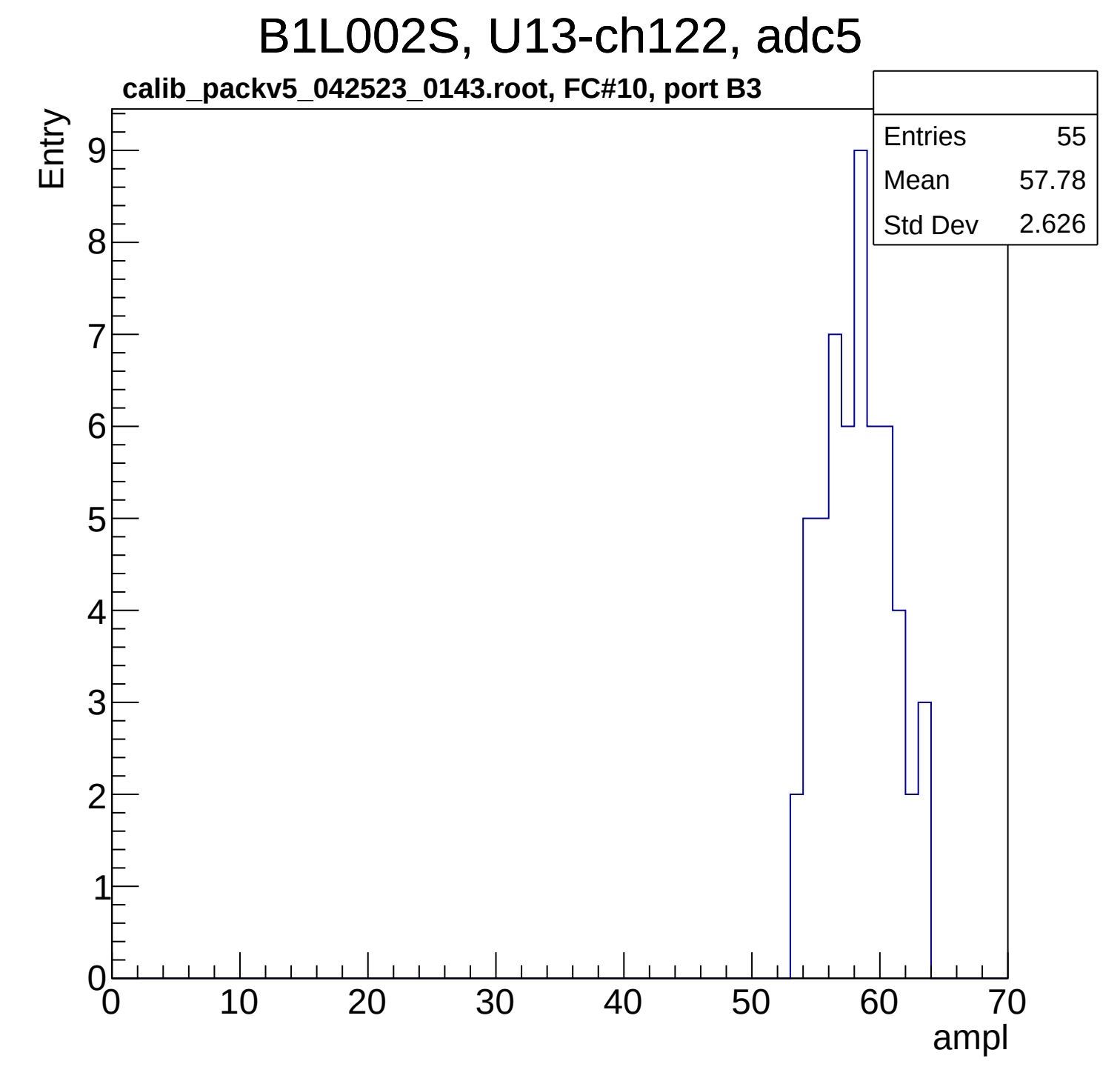
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.78
Std Dev 2.626
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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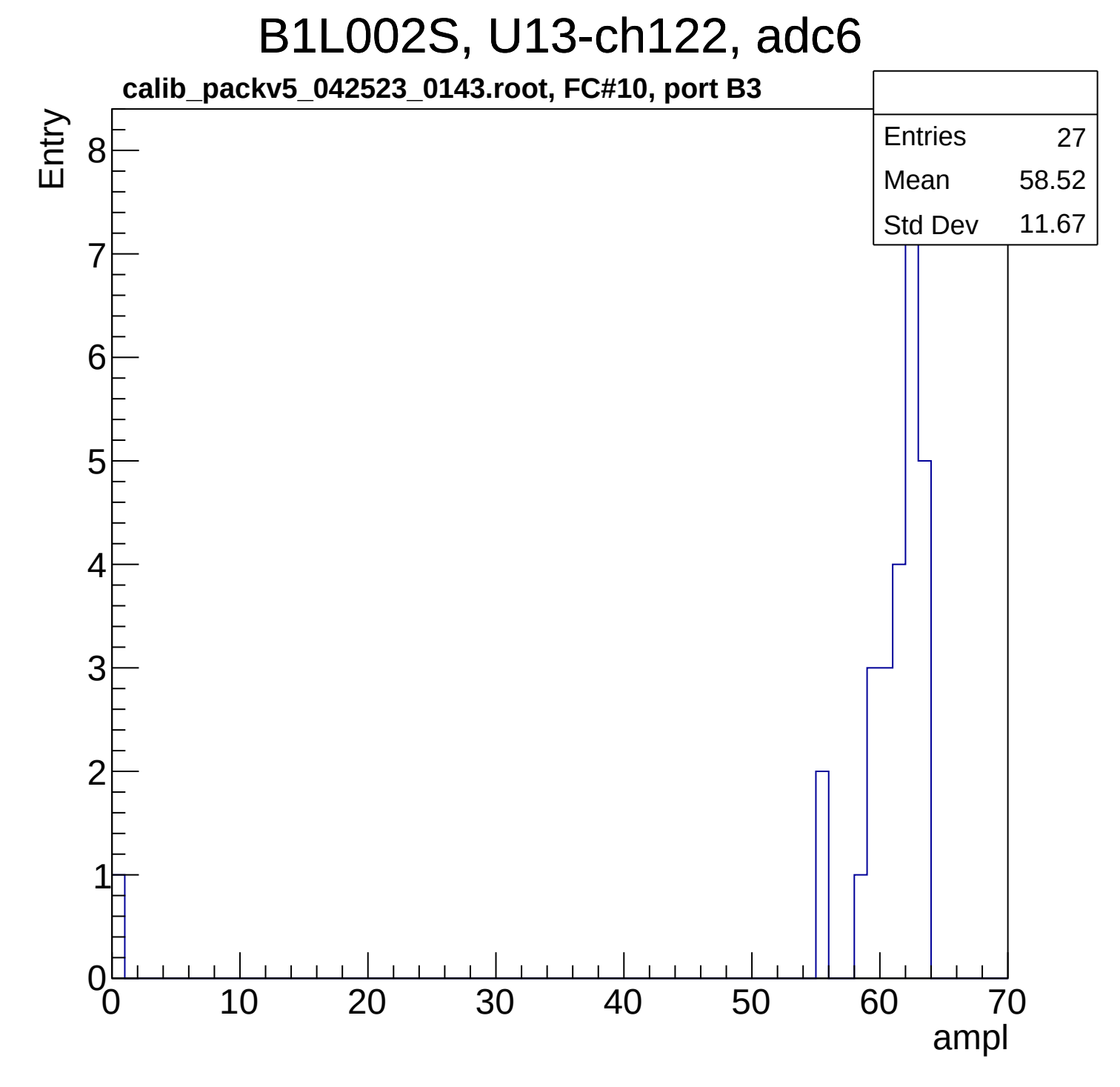
60
70


B1L002S, U13-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.52
Std Dev 11.67
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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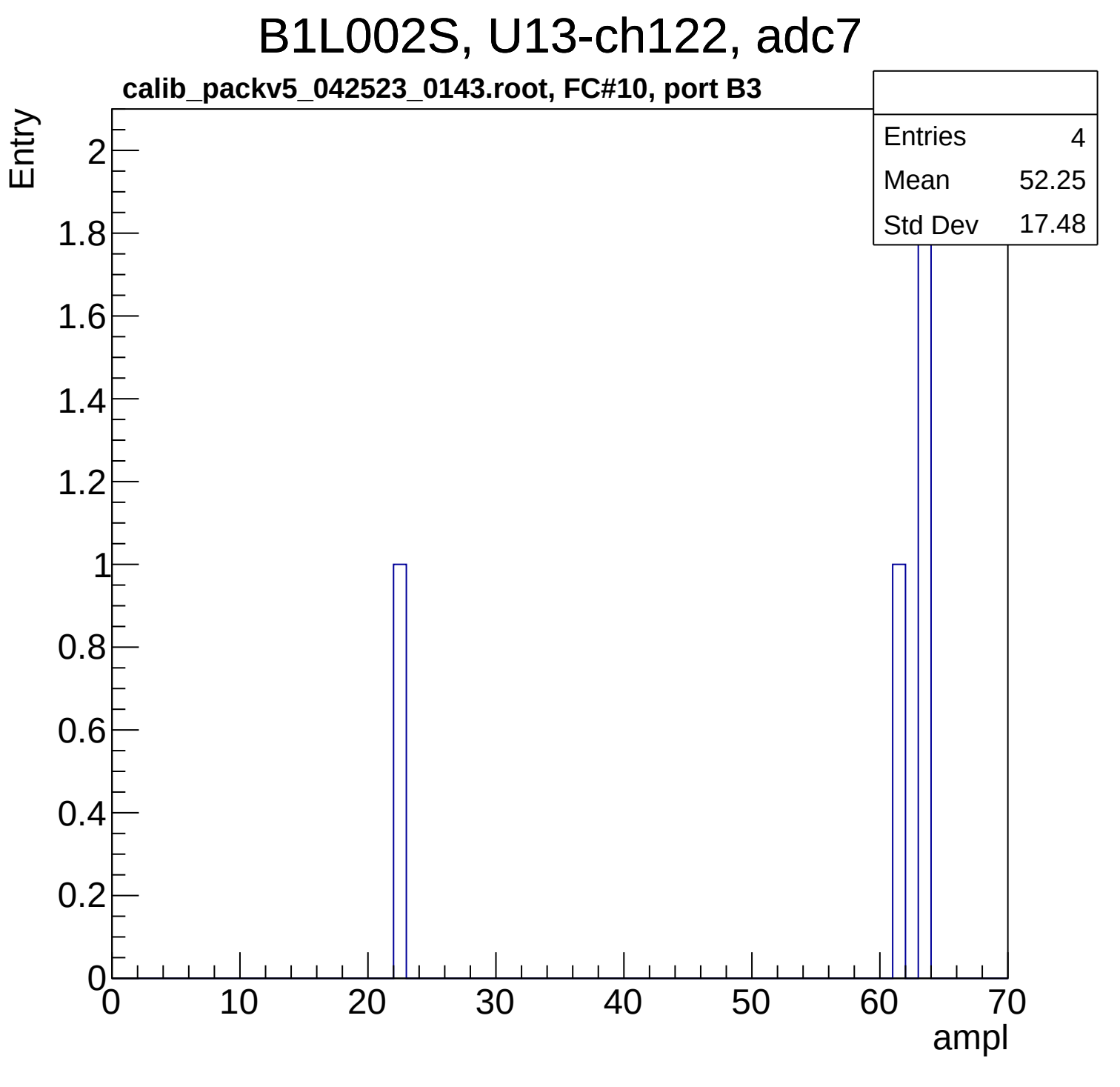
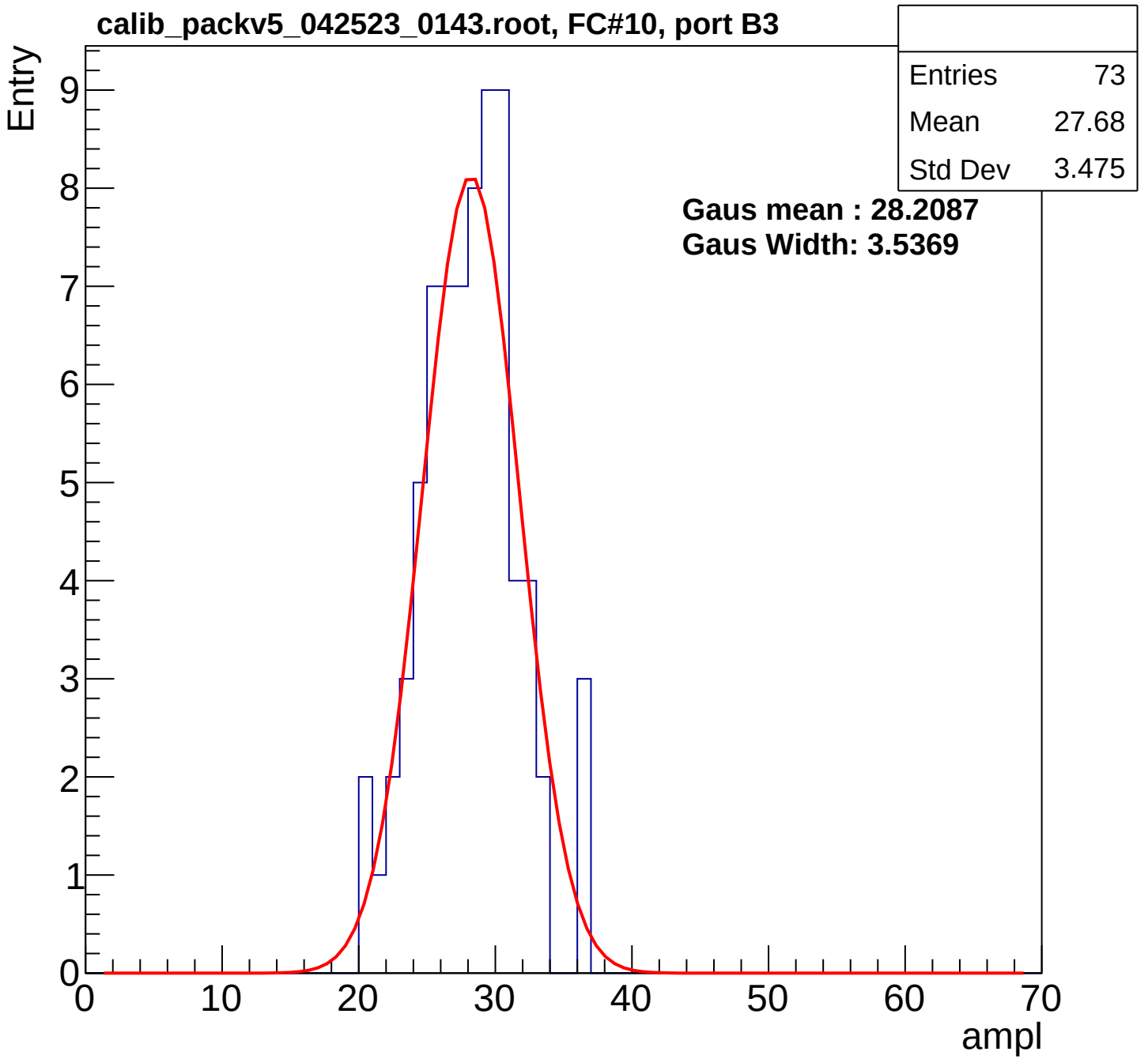
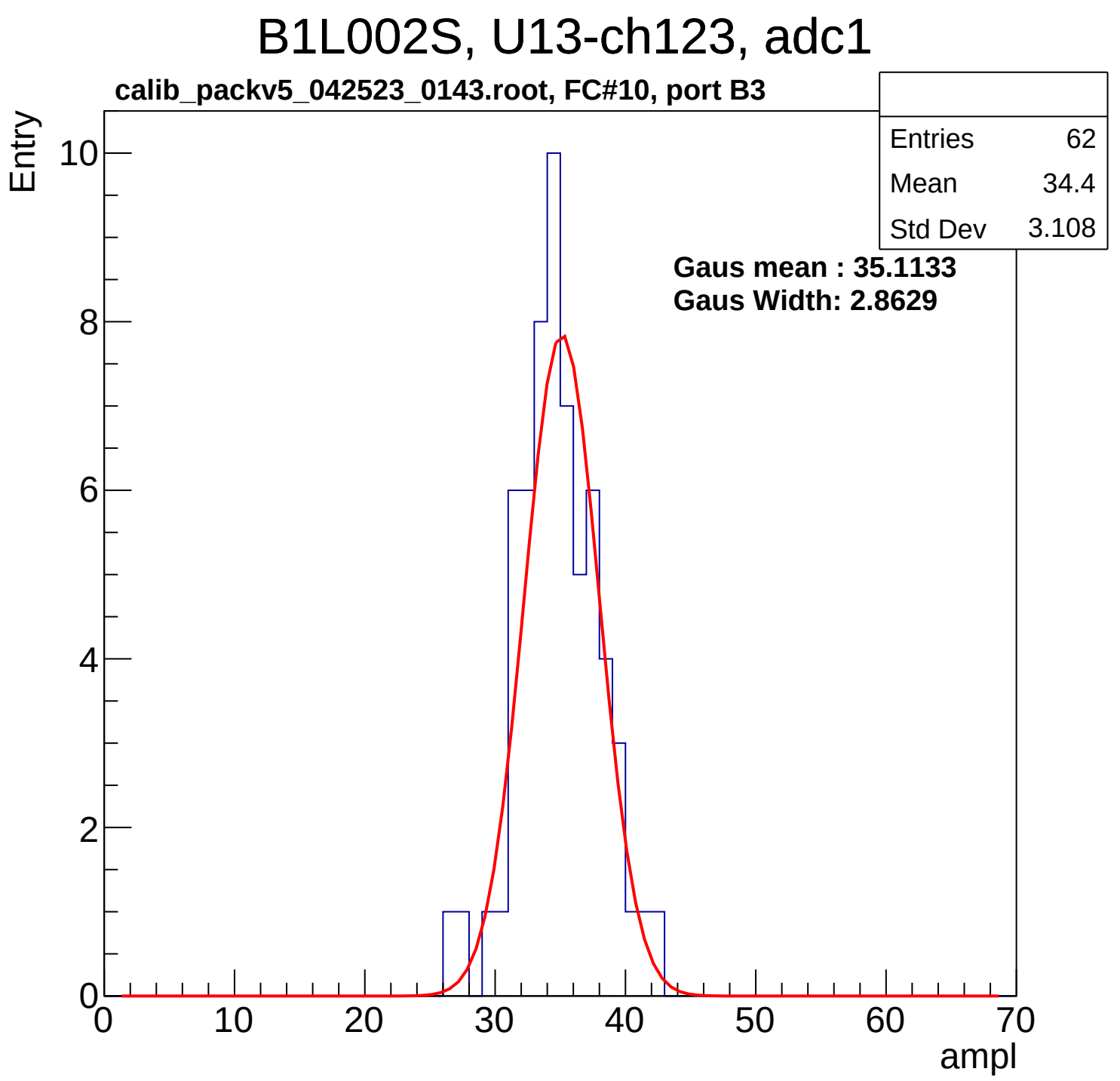
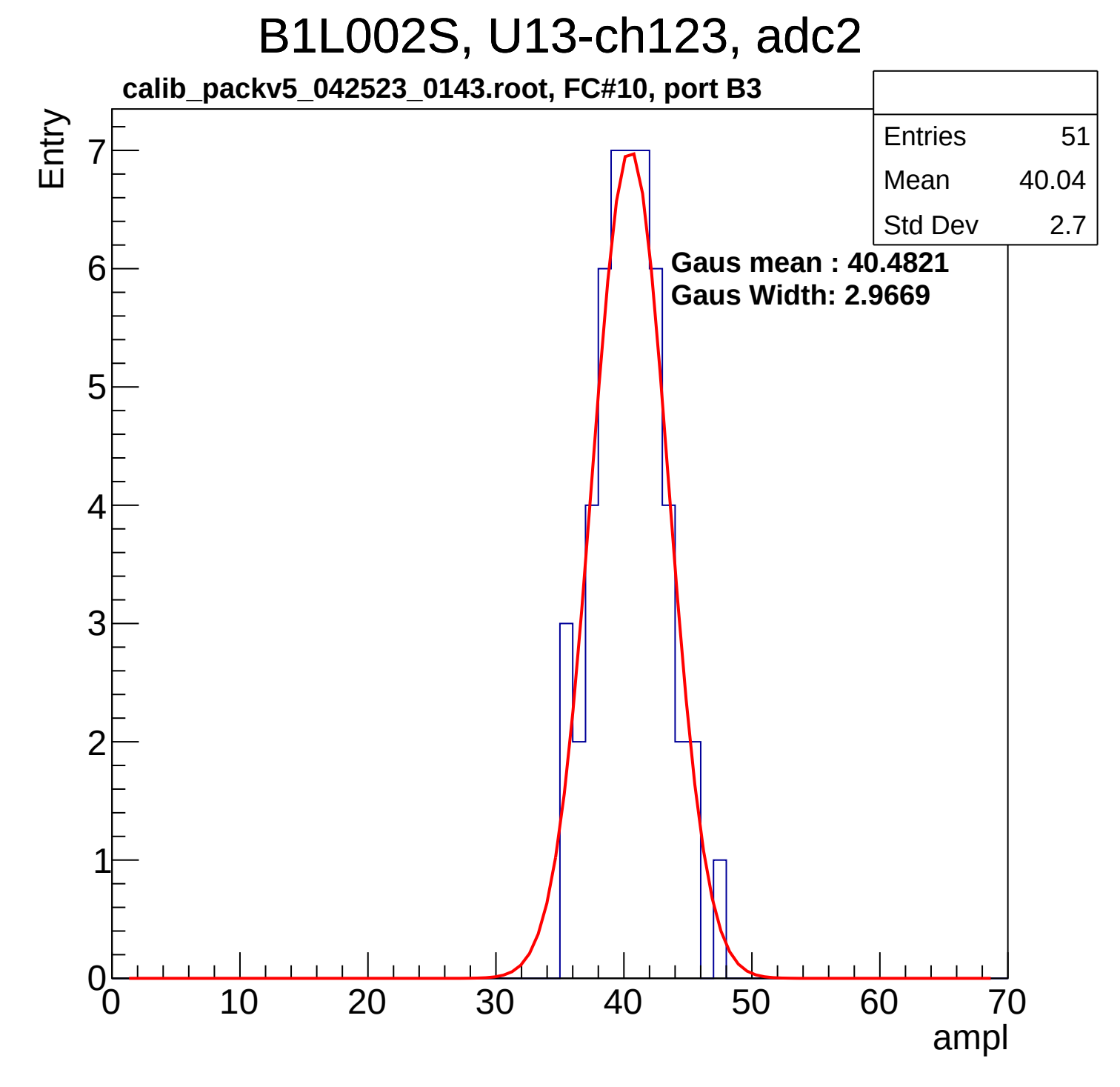
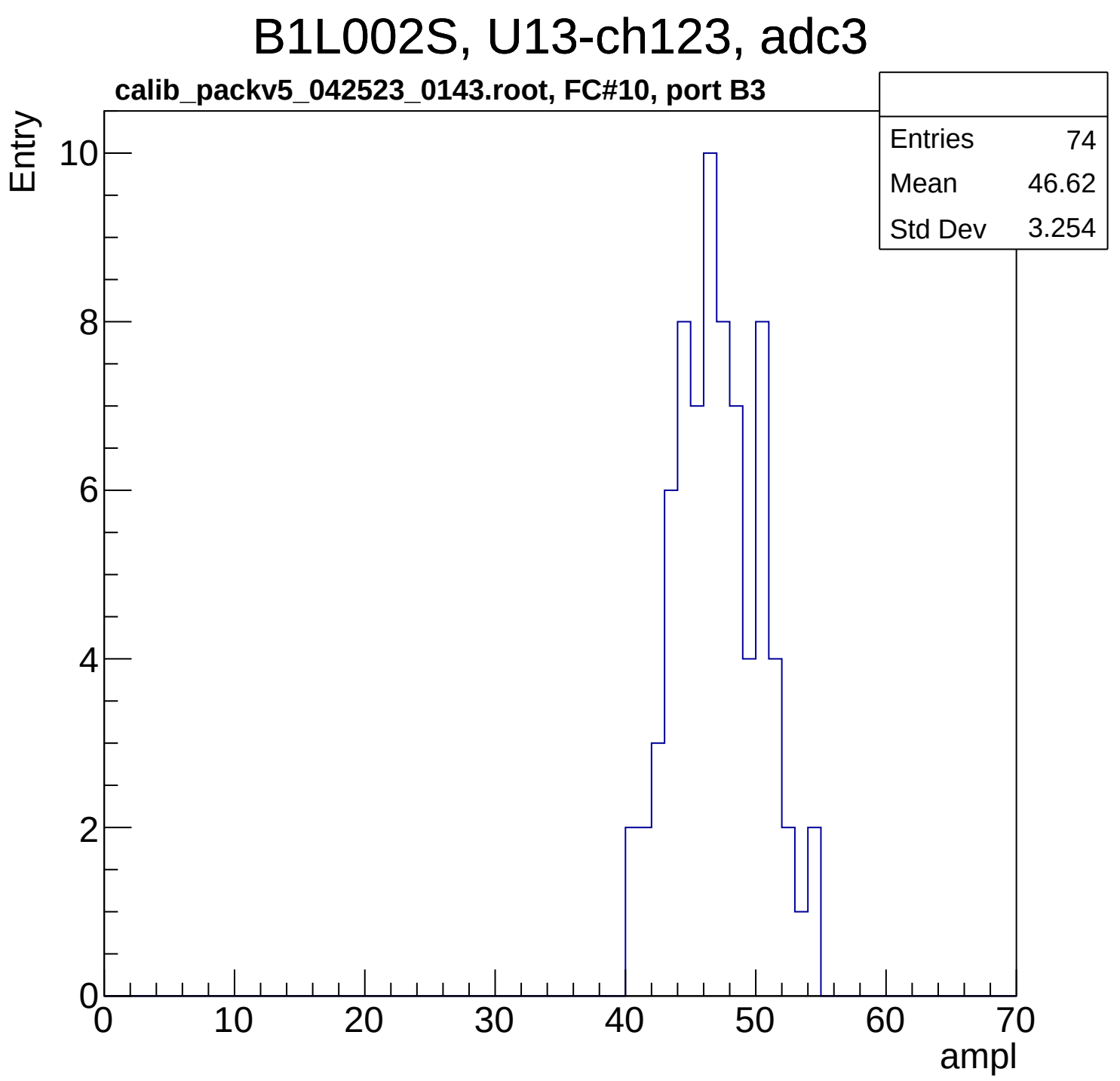

B1L002S, U13-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 52.25
Std Dev 17.48
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.68
Std Dev 3.475
Gaus mean : 28.2087
Gaus Width: 3.5369
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.4
Std Dev 3.108
Gaus mean : 35.1133
Gaus Width: 2.8629
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U13-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 40.04
Std Dev 2.7
Gaus mean : 40.4821
Gaus Width: 2.9669
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.62
Std Dev 3.254
Entry
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4
6
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10
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10
20
30
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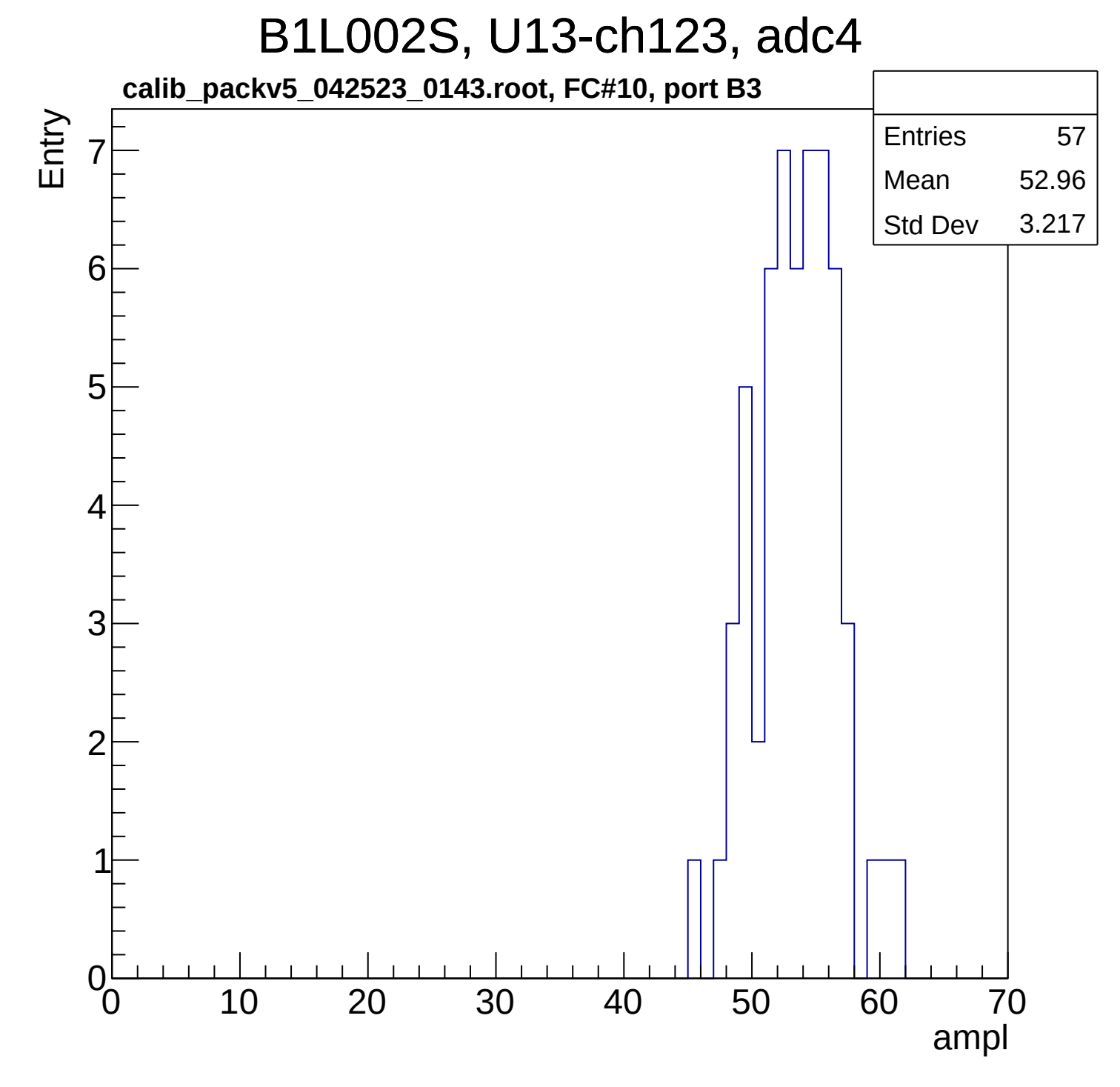
50
60
70
ampl


B1L002S, U13-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.96
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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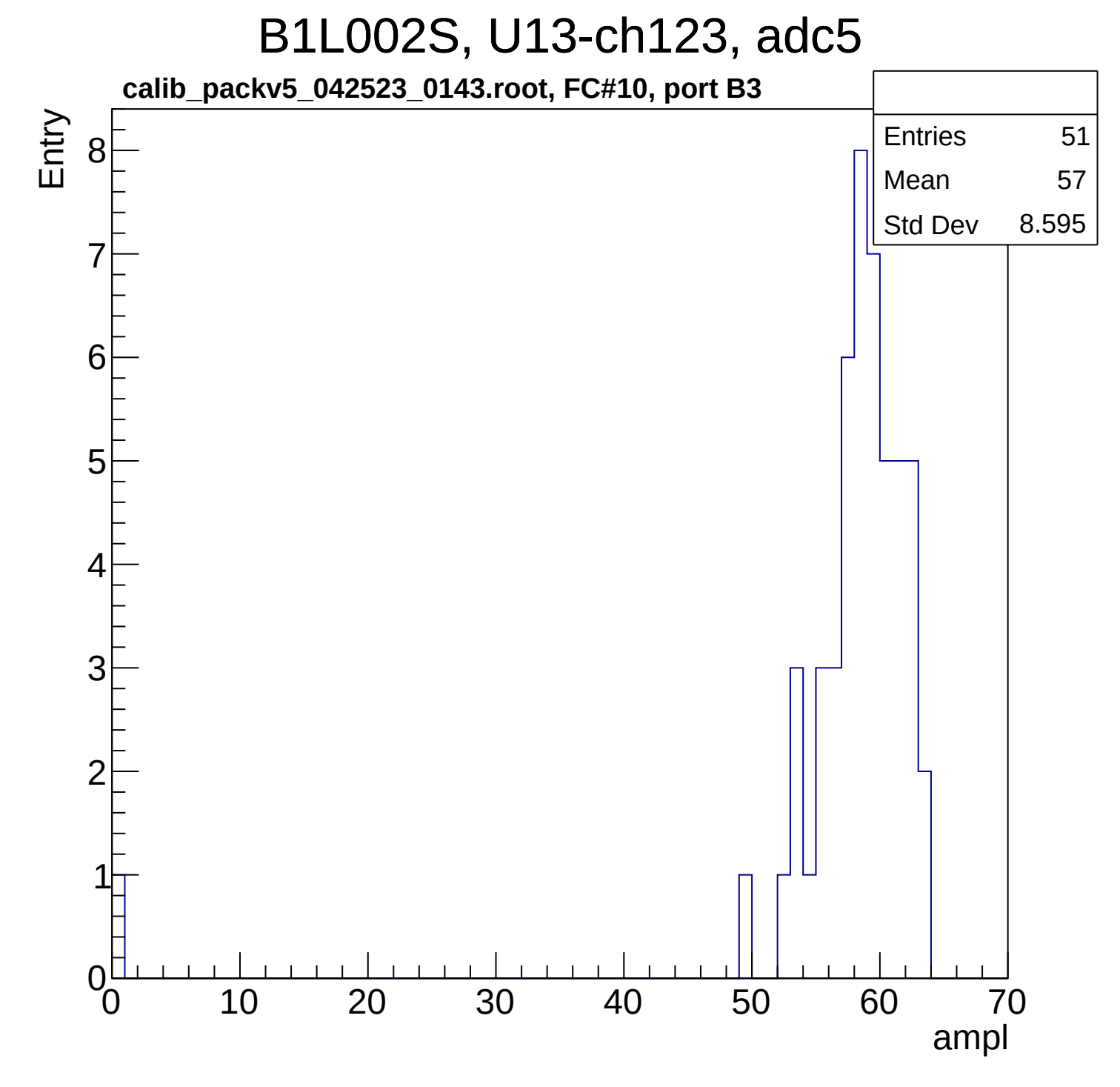
60
70


B1L002S, U13-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57
Std Dev 8.595
Entry
ampl
0
1
2
3
4
5
6
7
8
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20
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60
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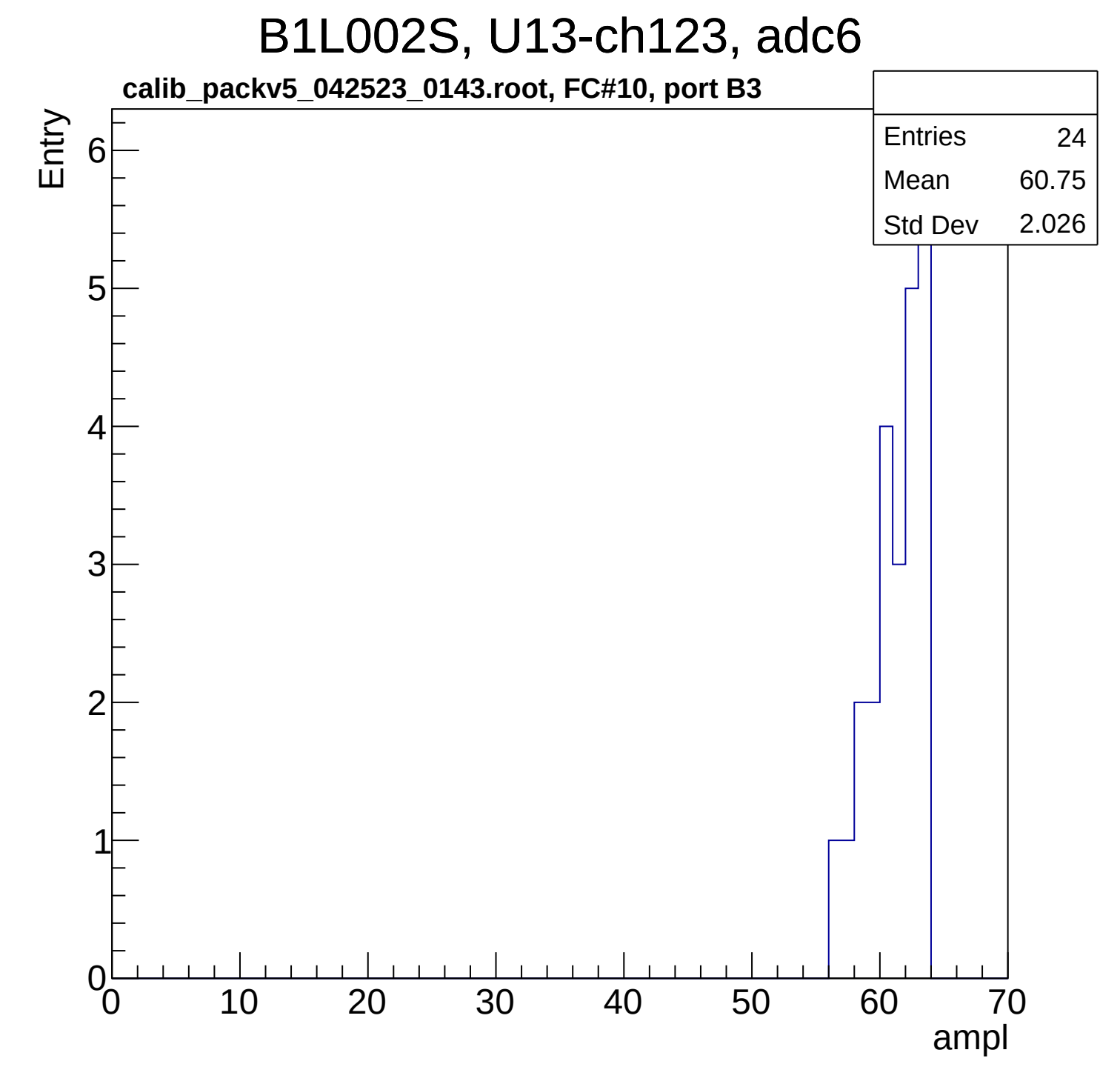
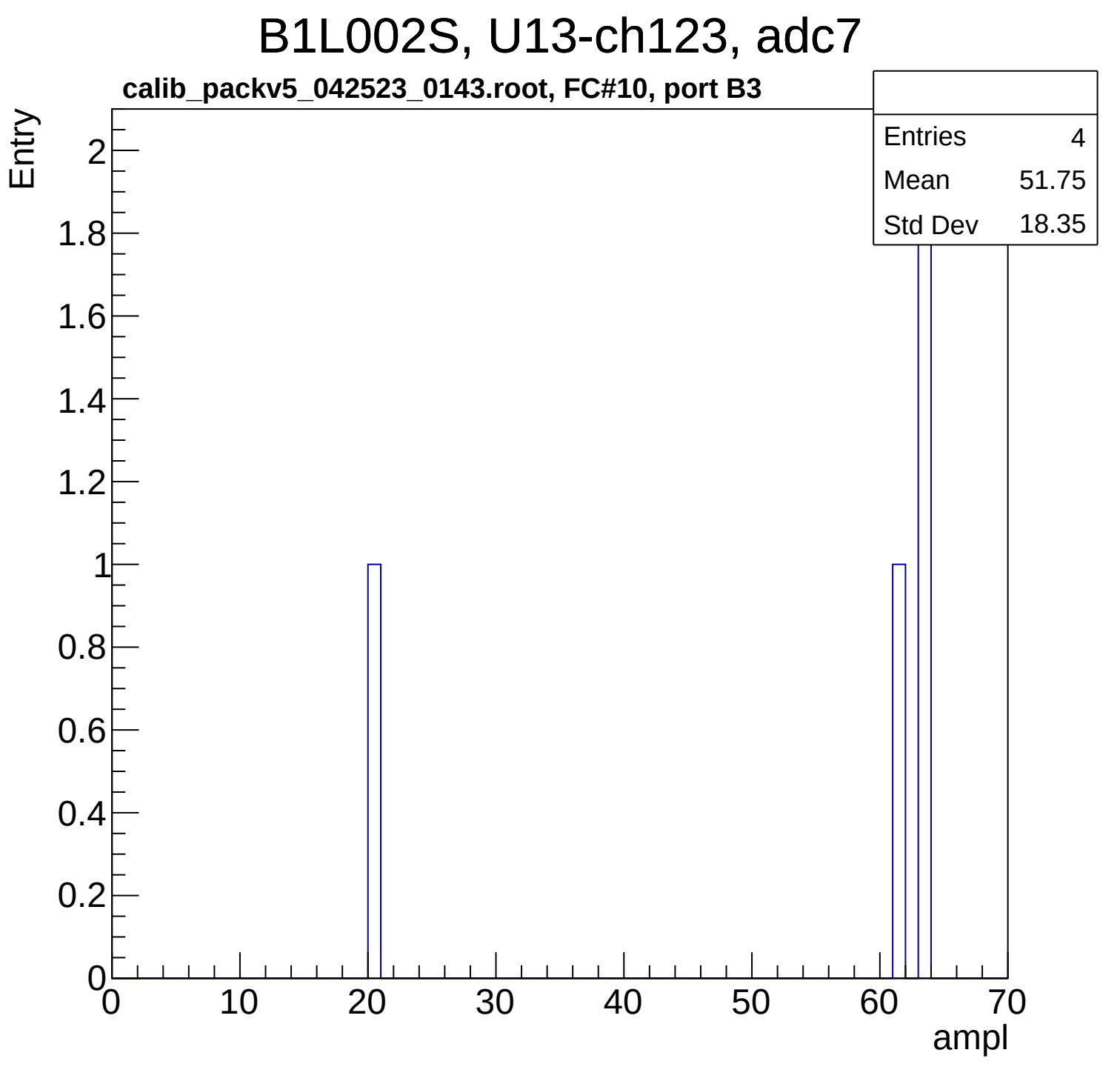

B1L002S, U13-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 60.75
Std Dev 2.026
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.75
Std Dev 18.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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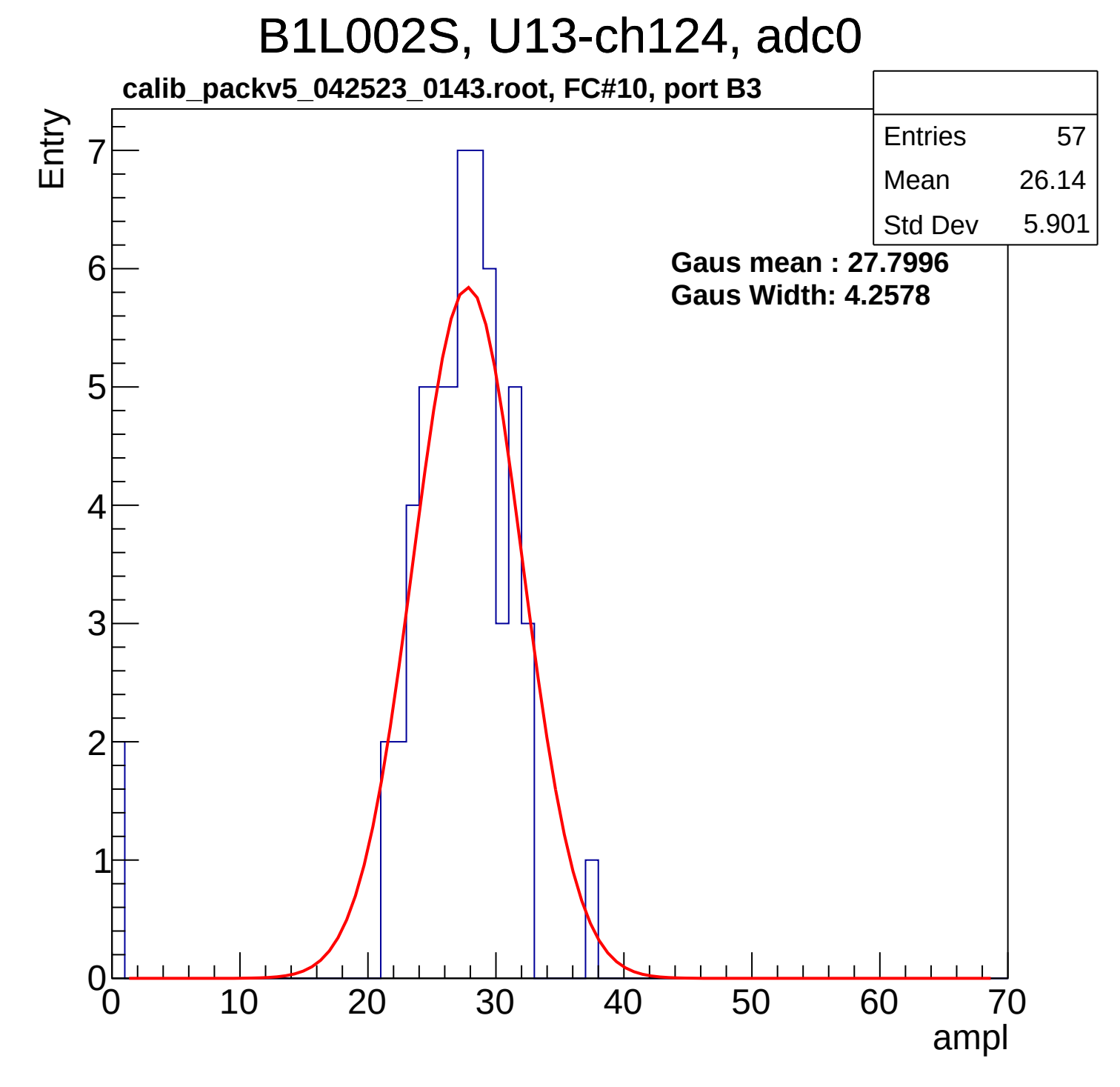
B1L002S, U13-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.14
Std Dev 5.901
Gaus mean : 27.7996
Gaus Width: 4.2578
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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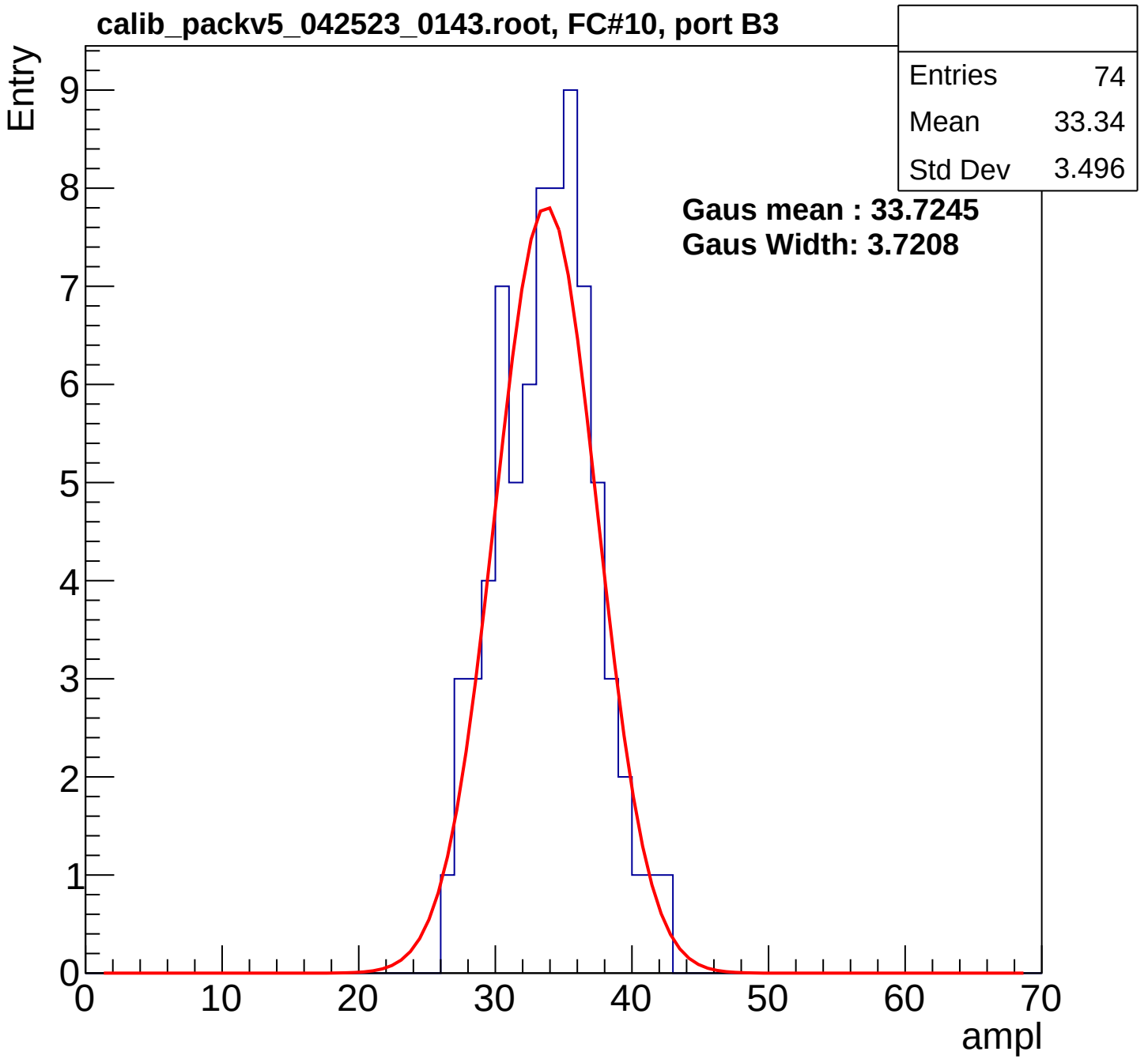
B1L002S, U13-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.34
Std Dev 3.496
Gaus mean : 33.7245
Gaus Width: 3.7208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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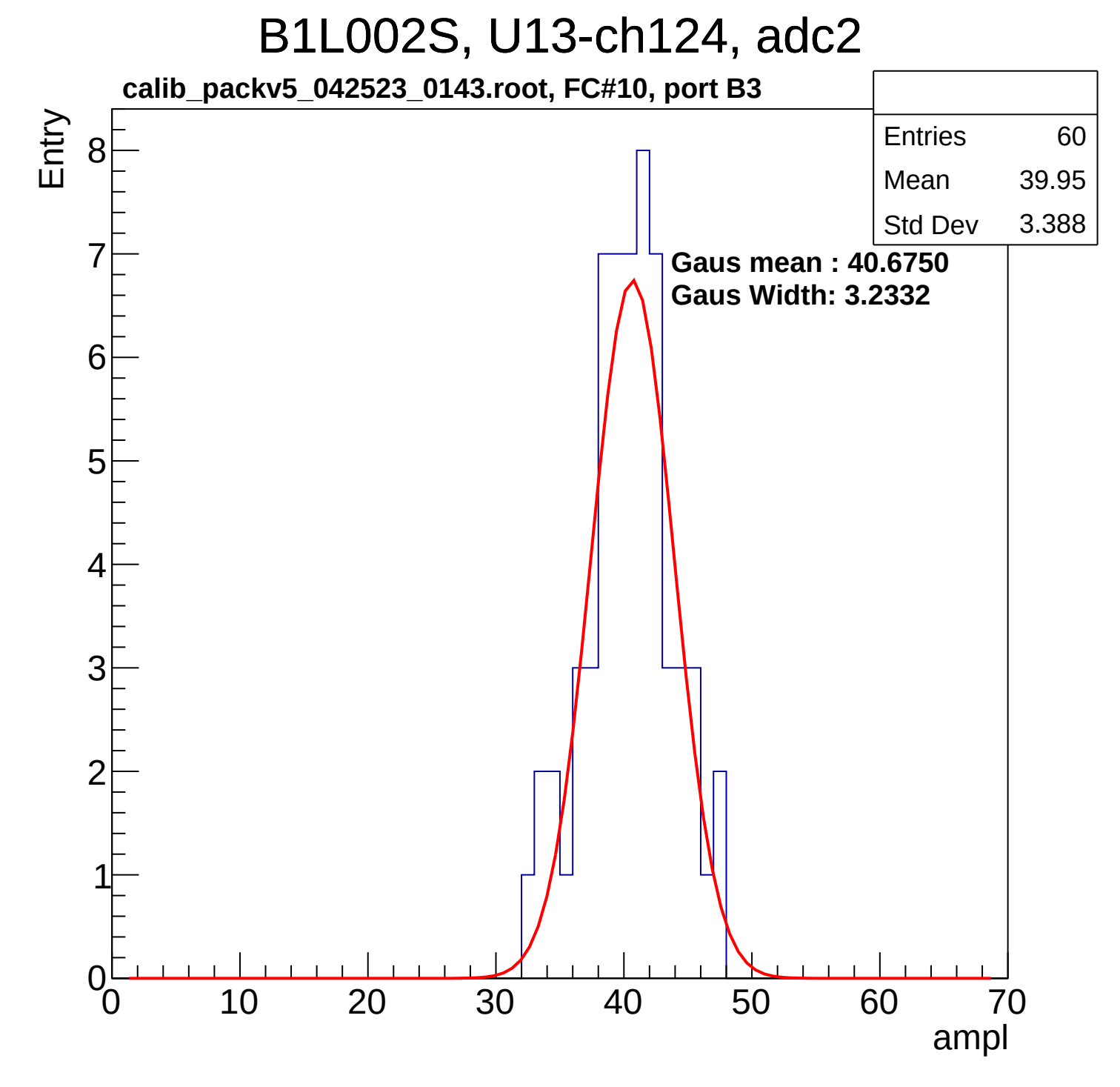
B1L002S, U13-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 39.95
Std Dev 3.388
Gaus mean : 40.6750
Gaus Width: 3.2332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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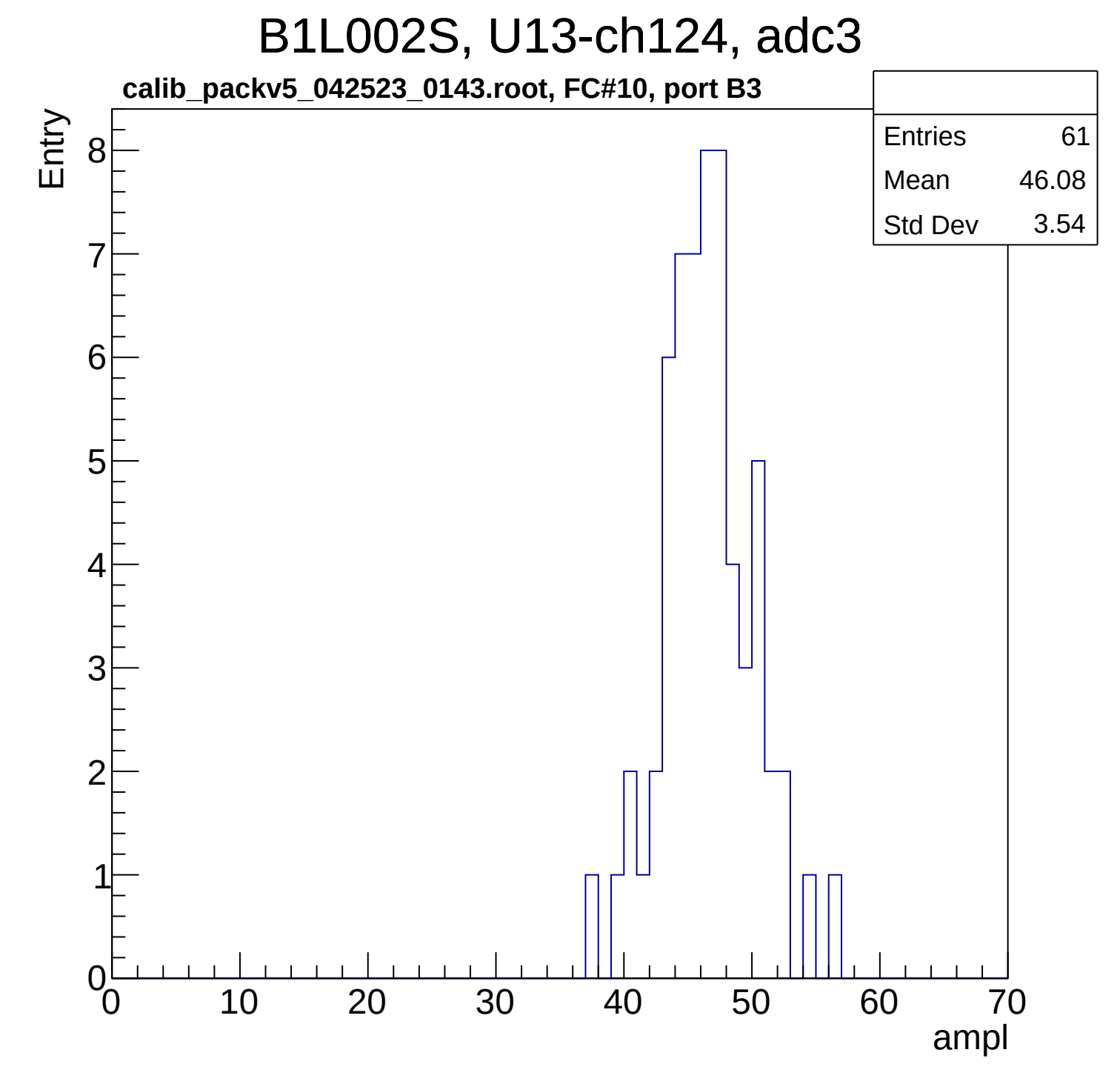
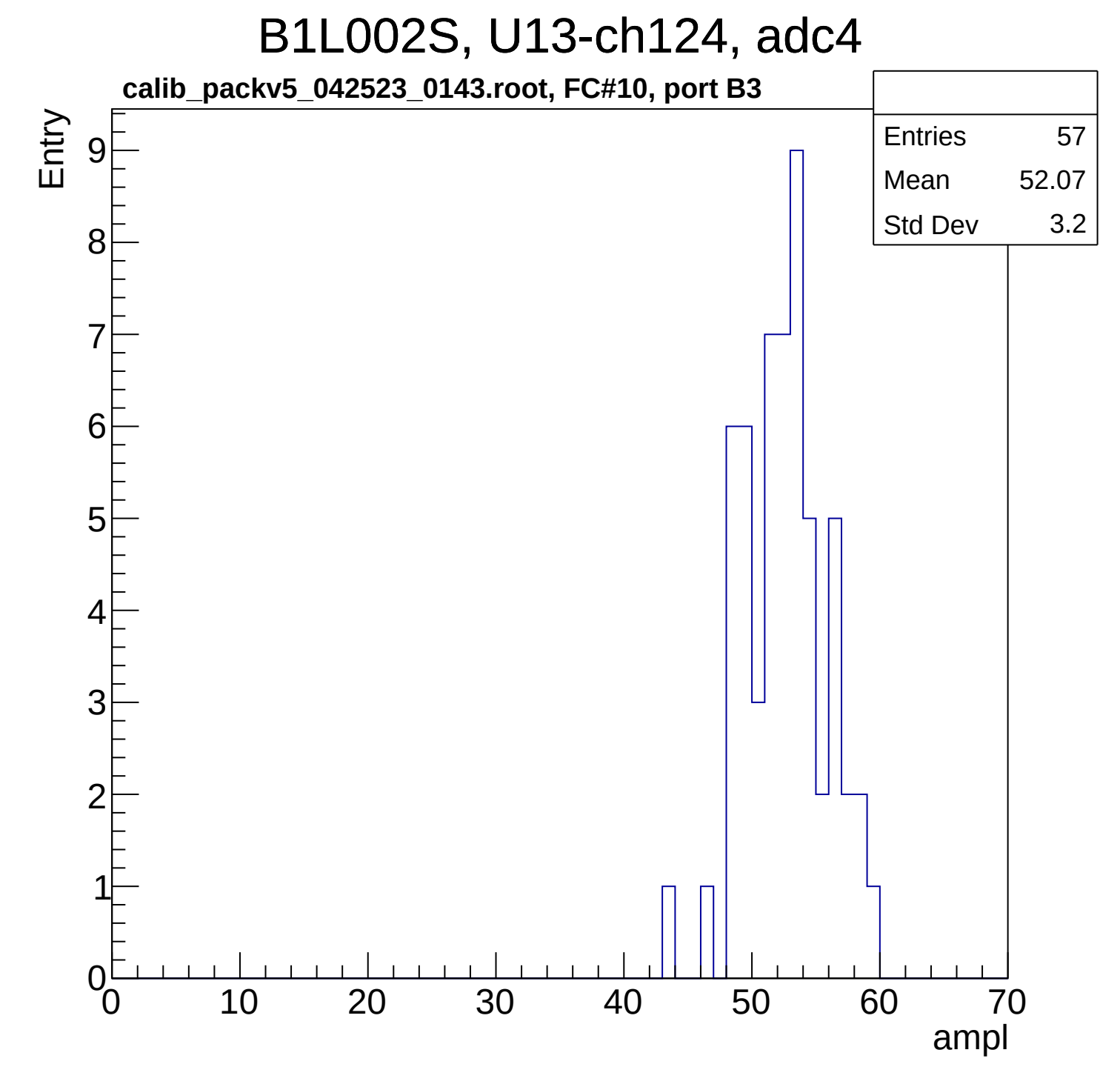

B1L002S, U13-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.08
Std Dev 3.54
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.07
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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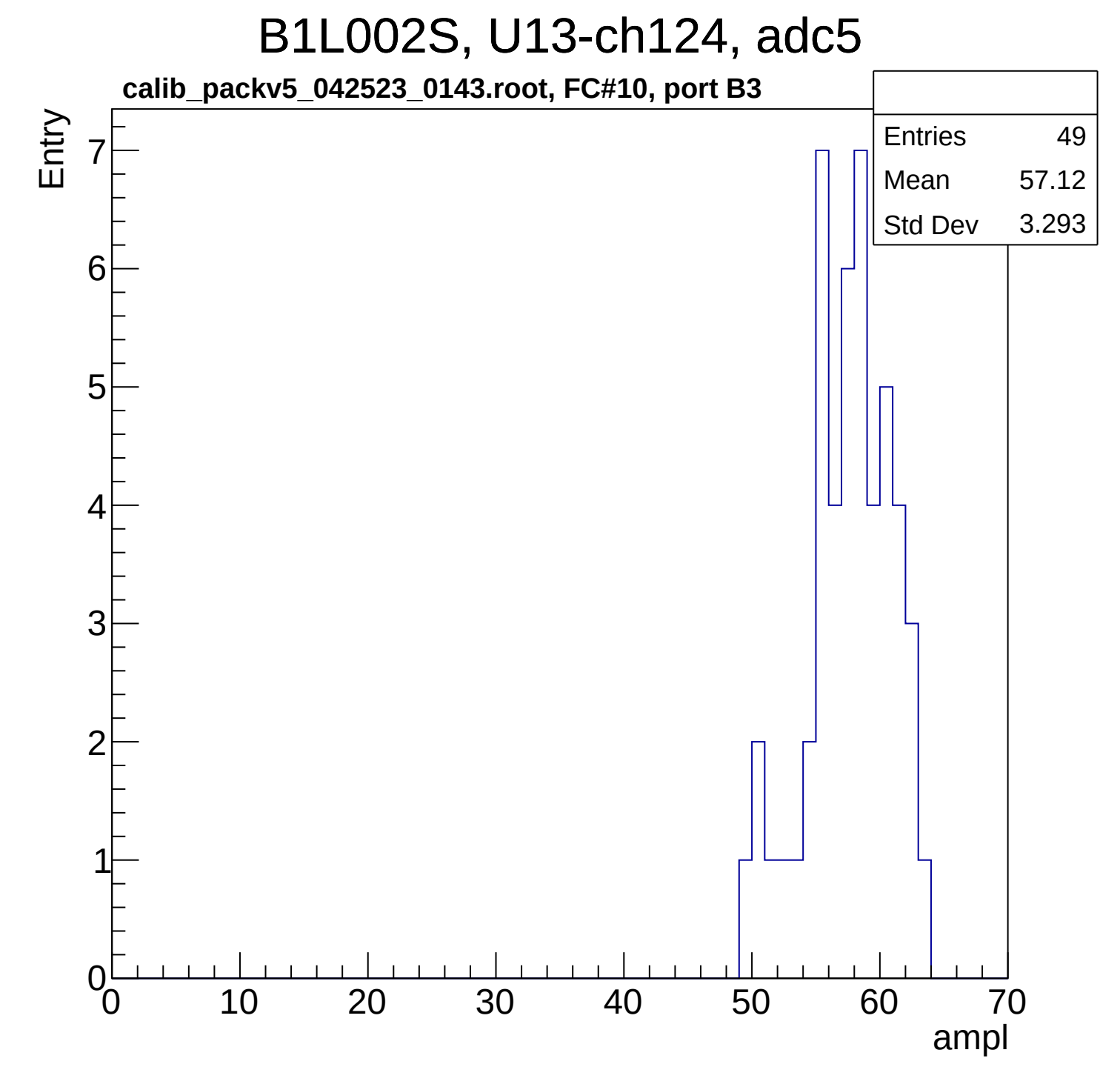
B1L002S, U13-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.12
Std Dev 3.293
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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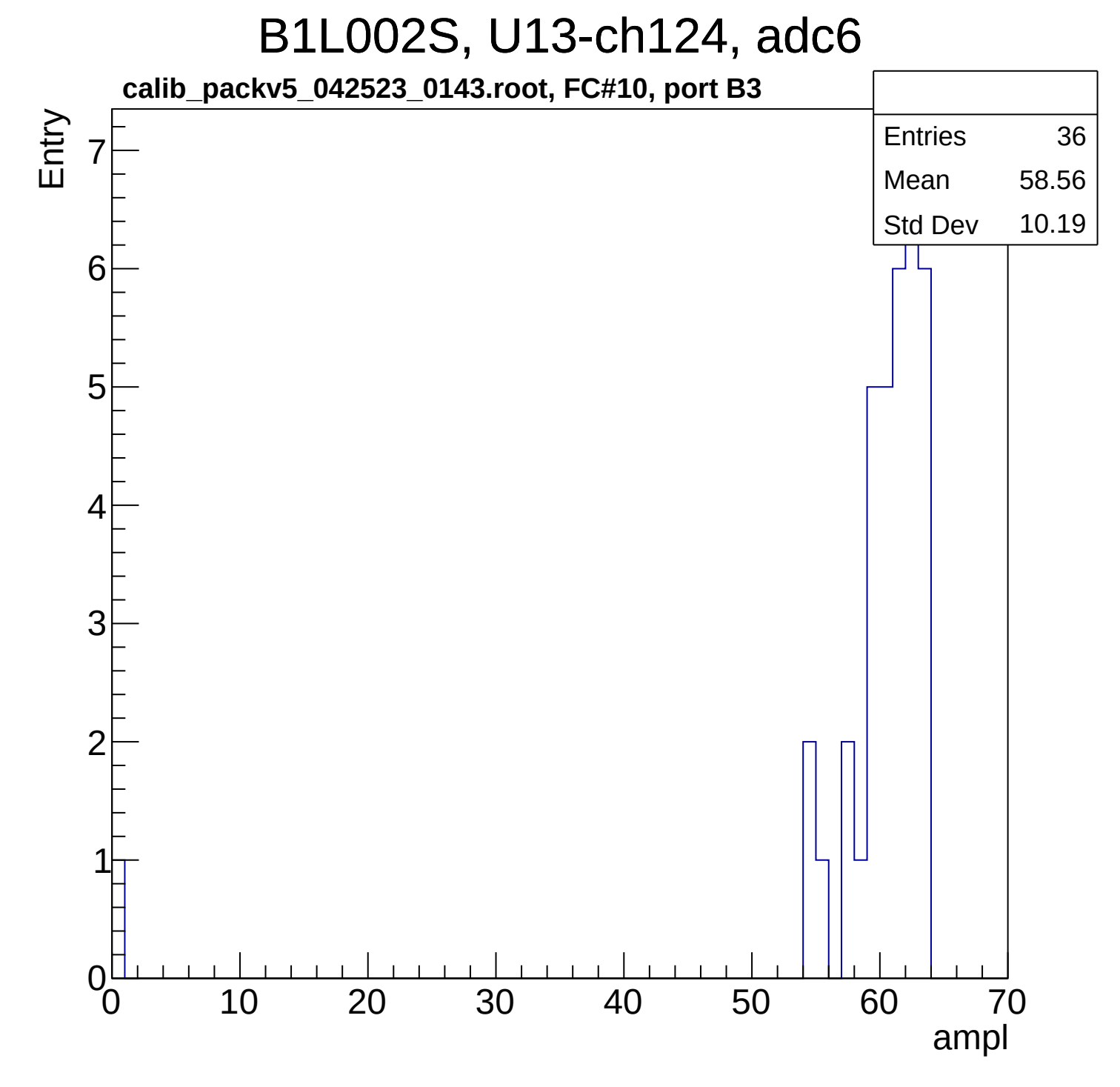
40
50
60
70


B1L002S, U13-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 58.56
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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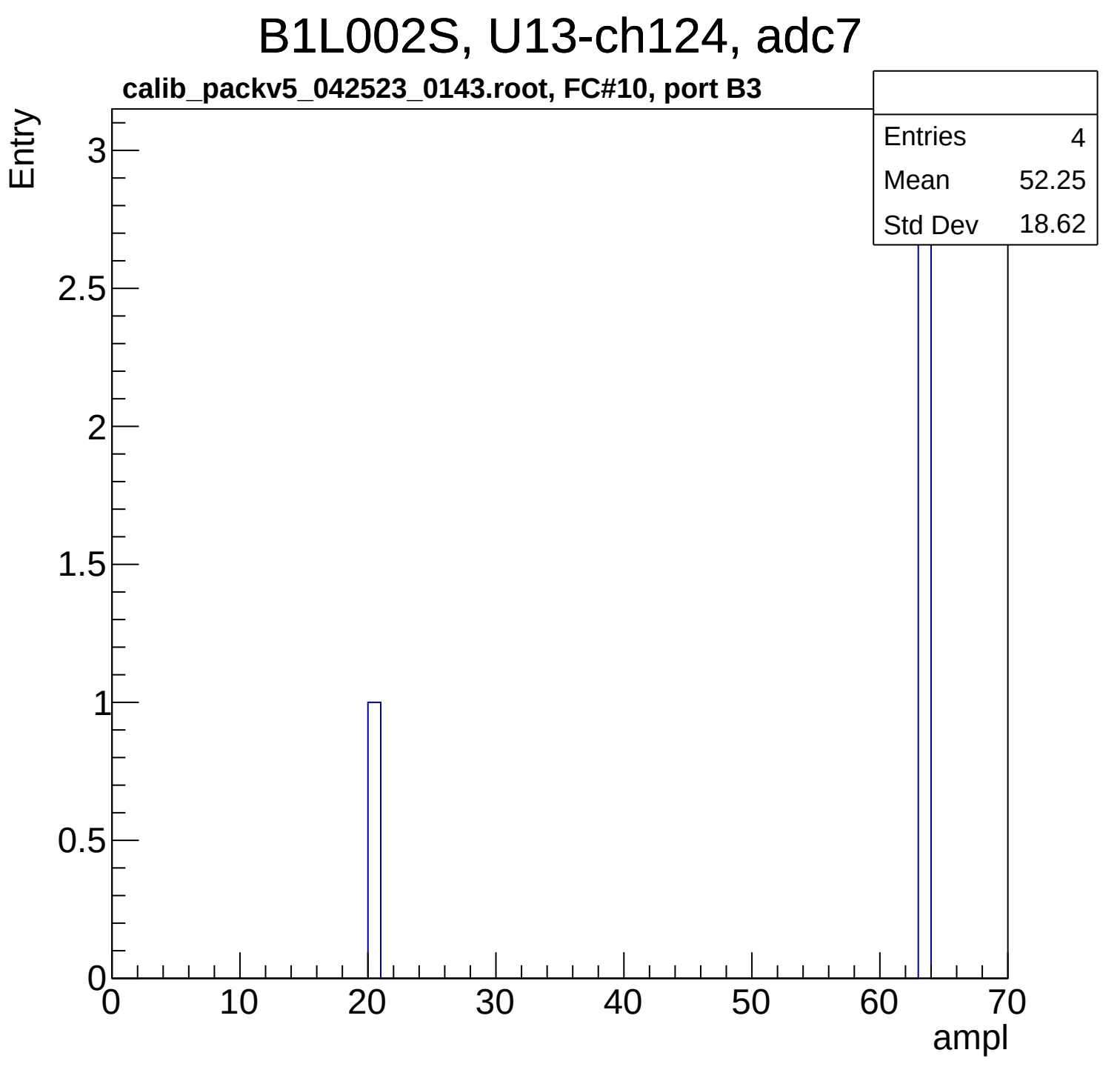
B1L002S, U13-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 52.25
Std Dev 18.62
Entry
ampl
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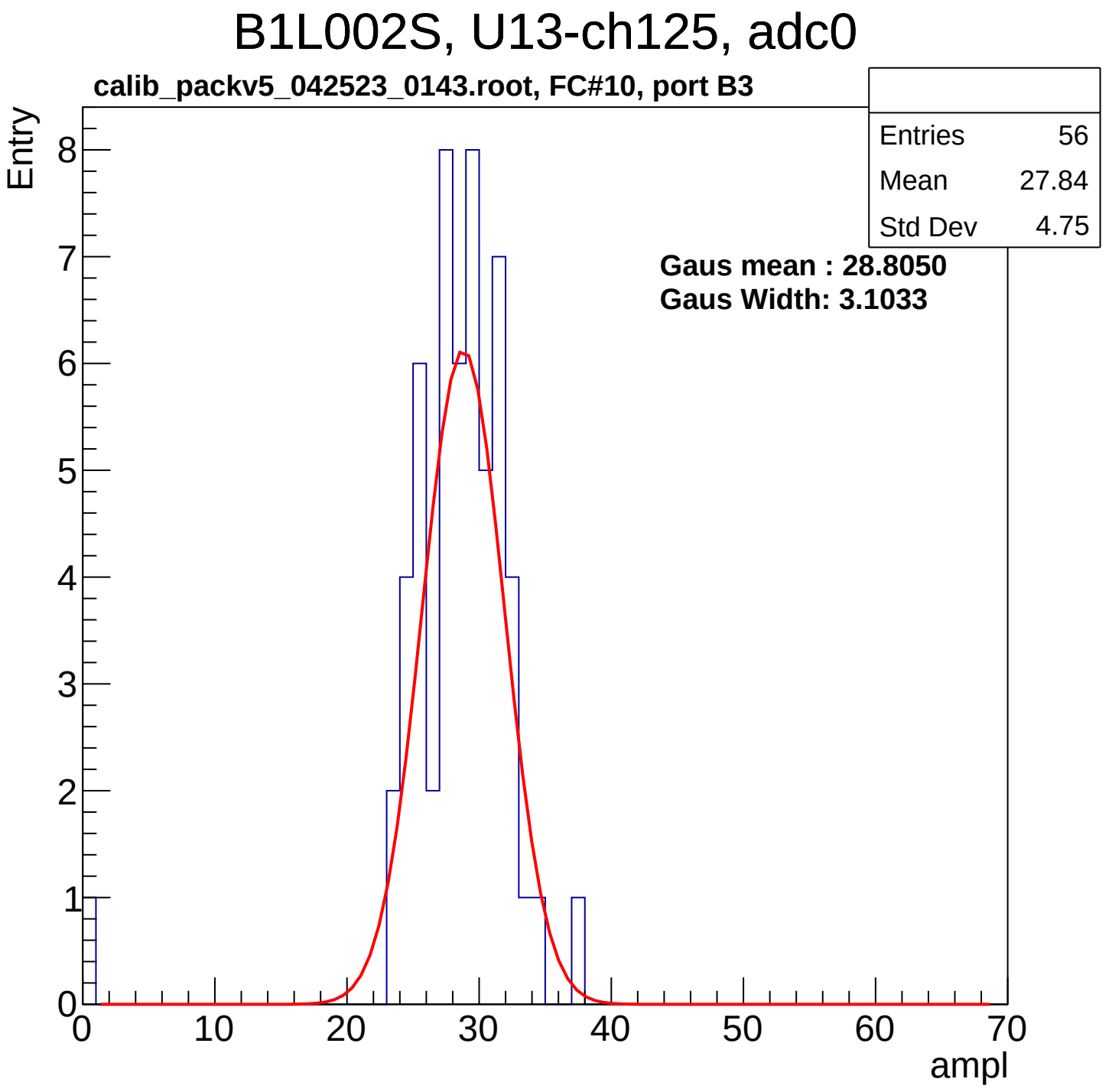
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.84
Std Dev 4.75
Gaus mean : 28.8050
Gaus Width: 3.1033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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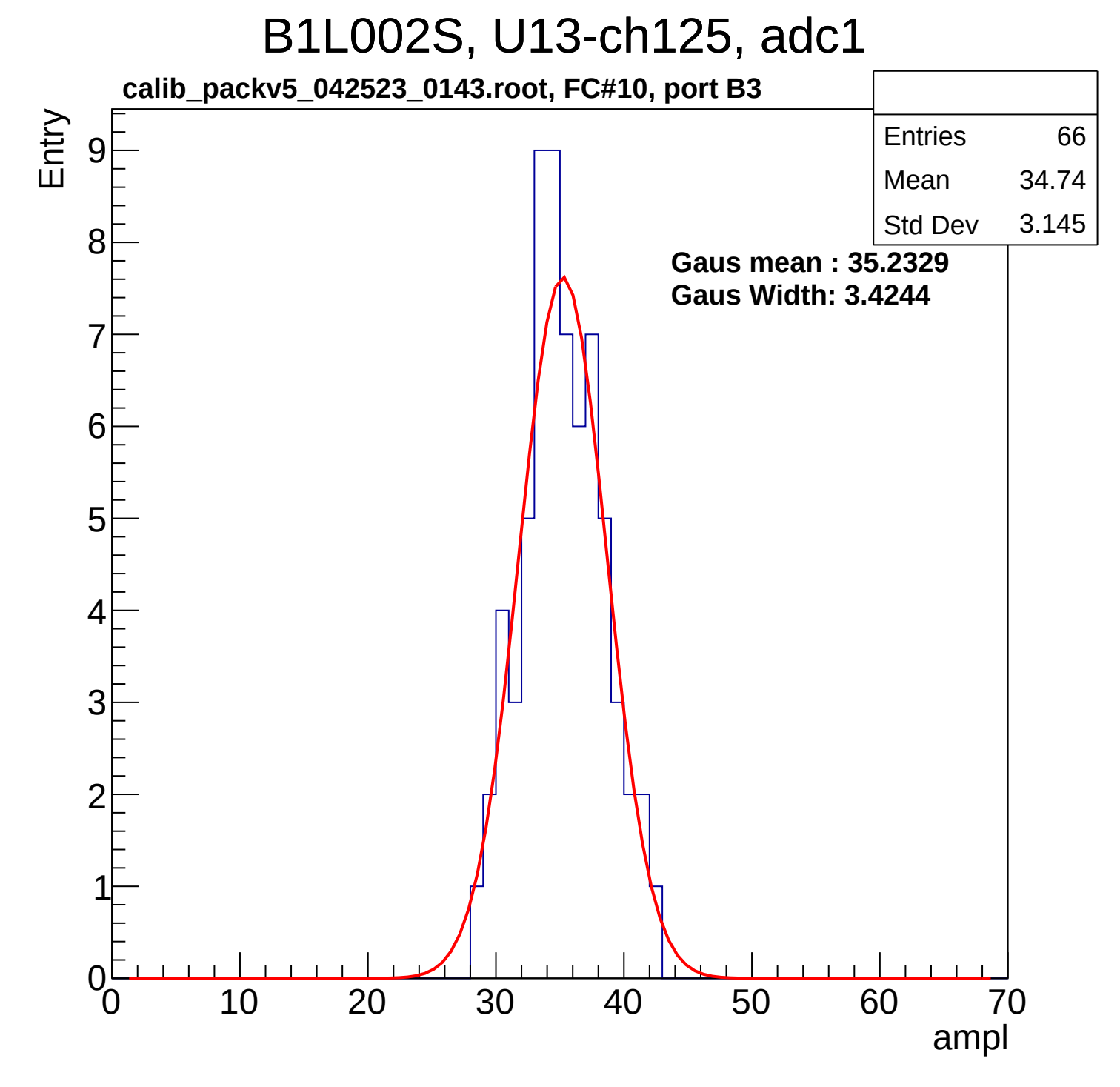
B1L002S, U13-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.74
Std Dev 3.145
Gaus mean : 35.2329
Gaus Width: 3.4244
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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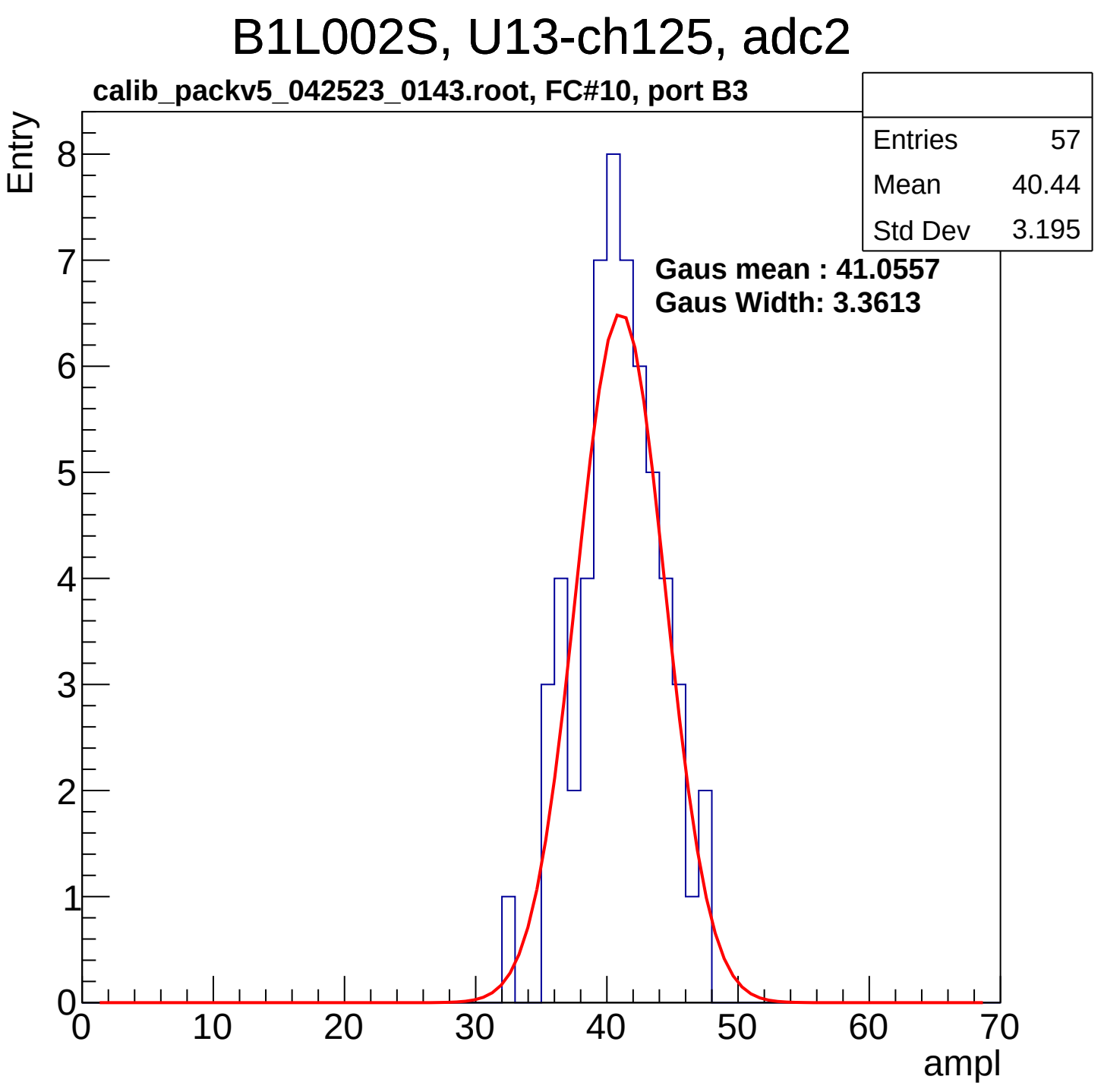
B1L002S, U13-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.44
Std Dev 3.195
Gaus mean : 41.0557
Gaus Width: 3.3613
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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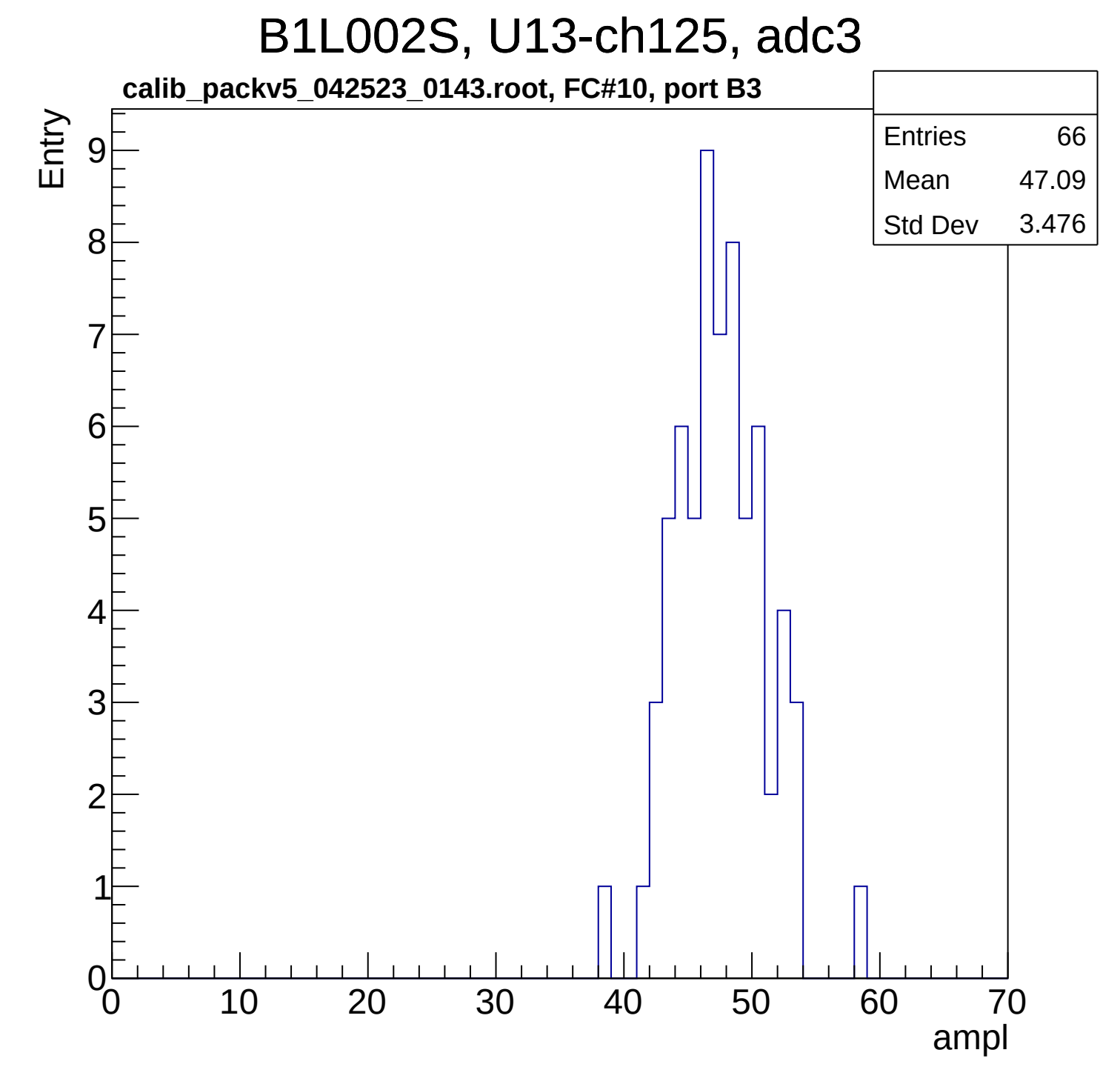
B1L002S, U13-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.09
Std Dev 3.476
Entry
ampl
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2
3
4
5
6
7
8
9
0
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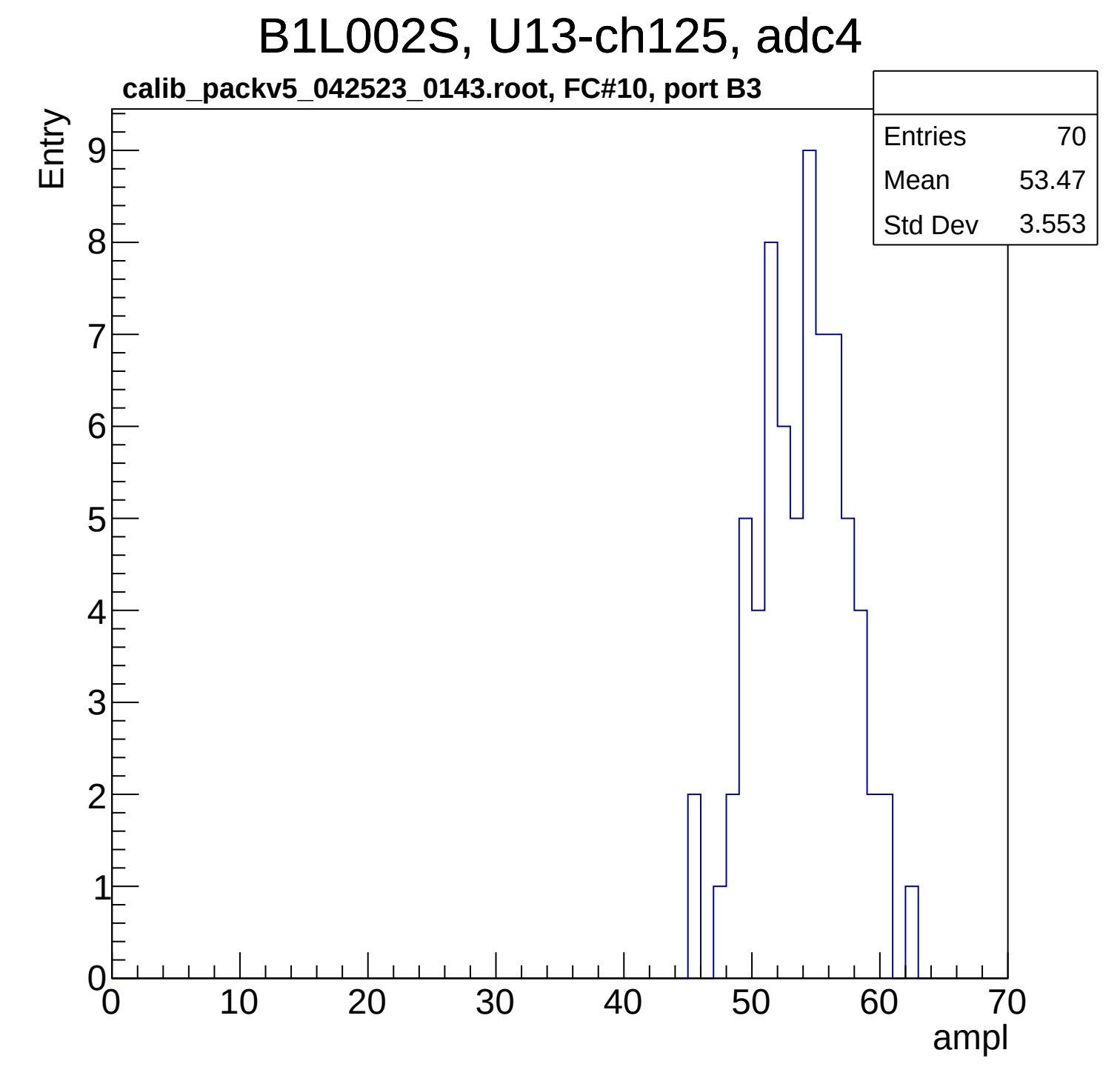
40
50
60
70


B1L002S, U13-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 53.47
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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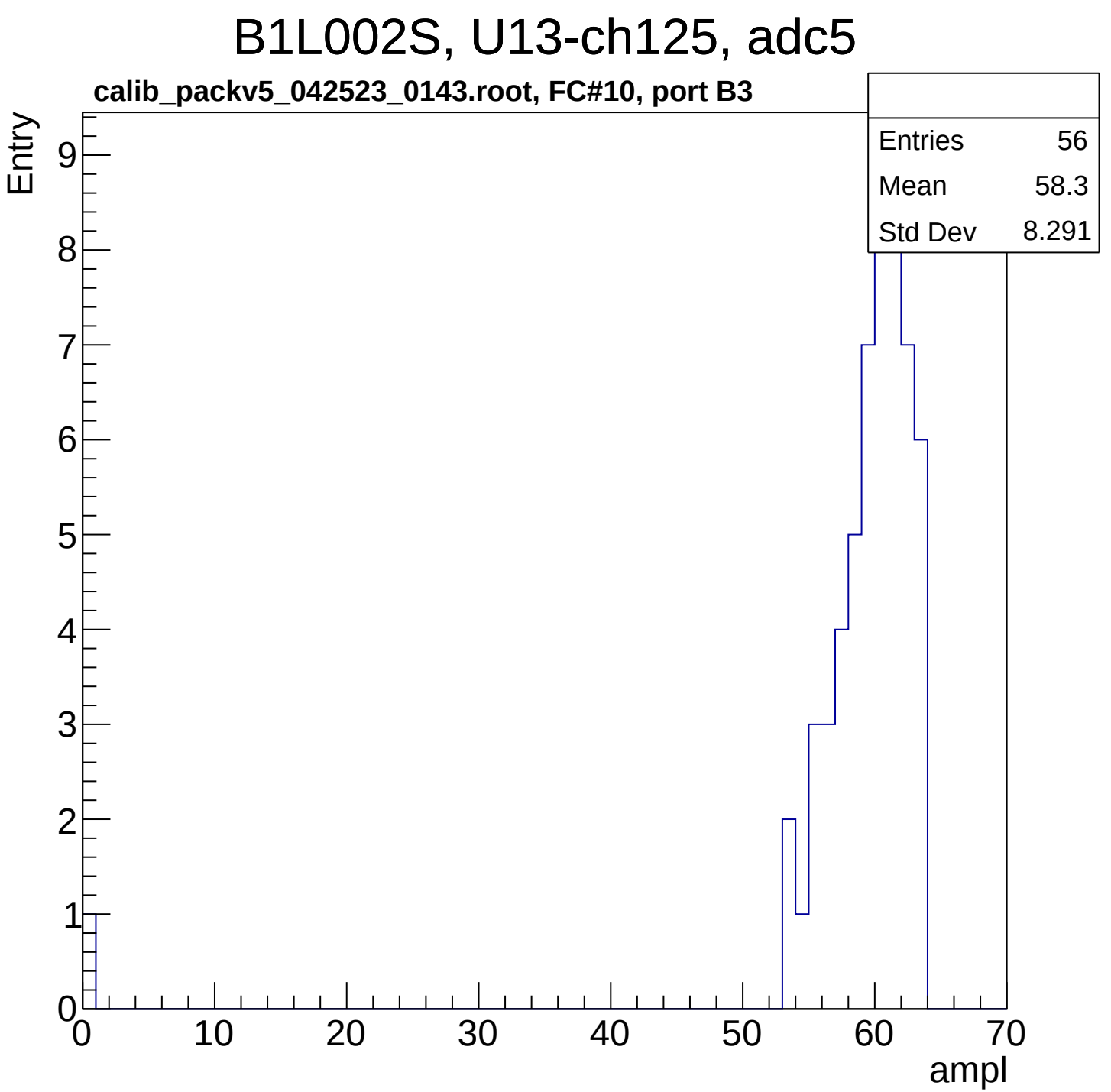
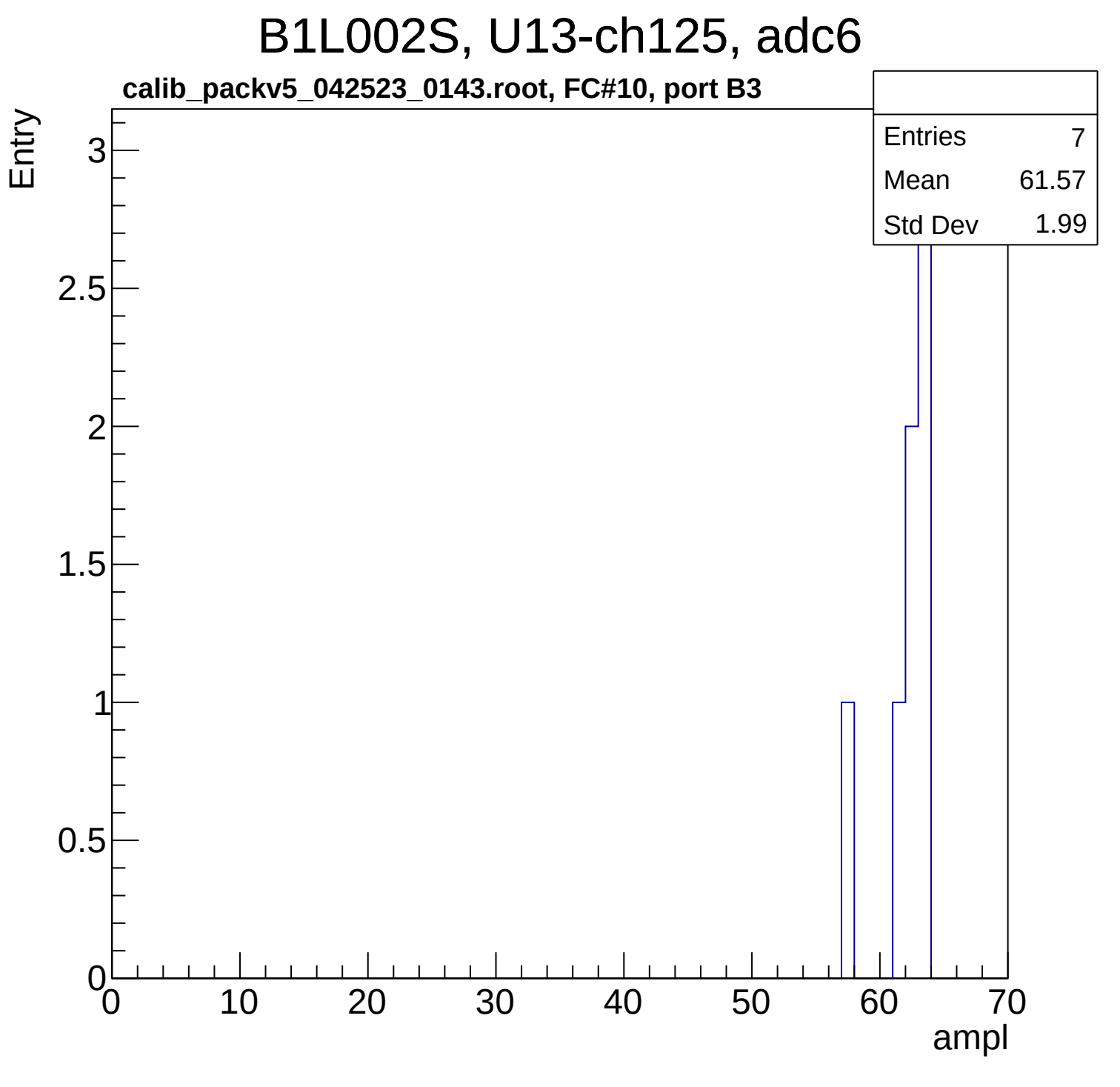

B1L002S, U13-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.3
Std Dev 8.291
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.57
Std Dev 1.99
Entry
ampl
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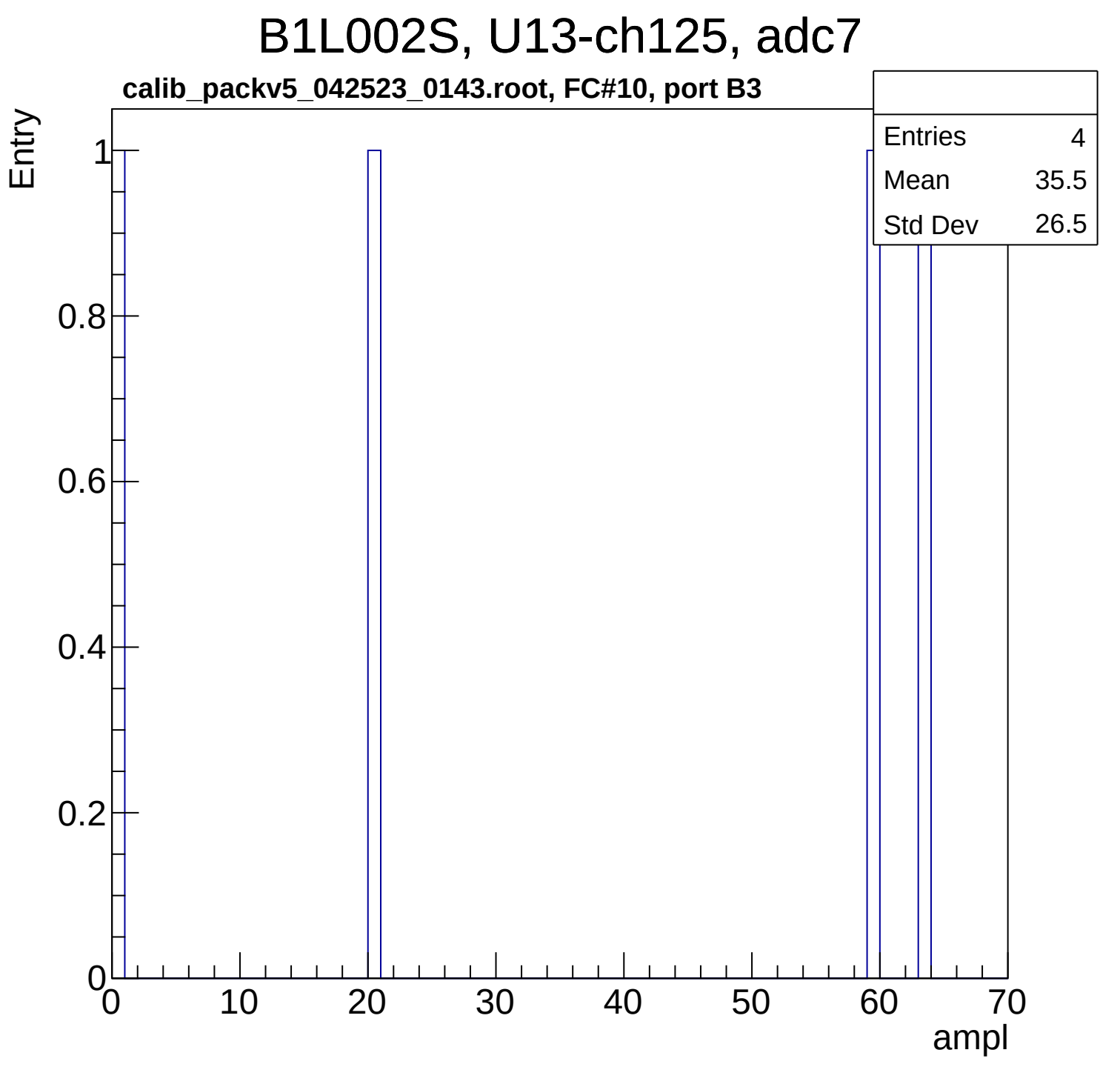
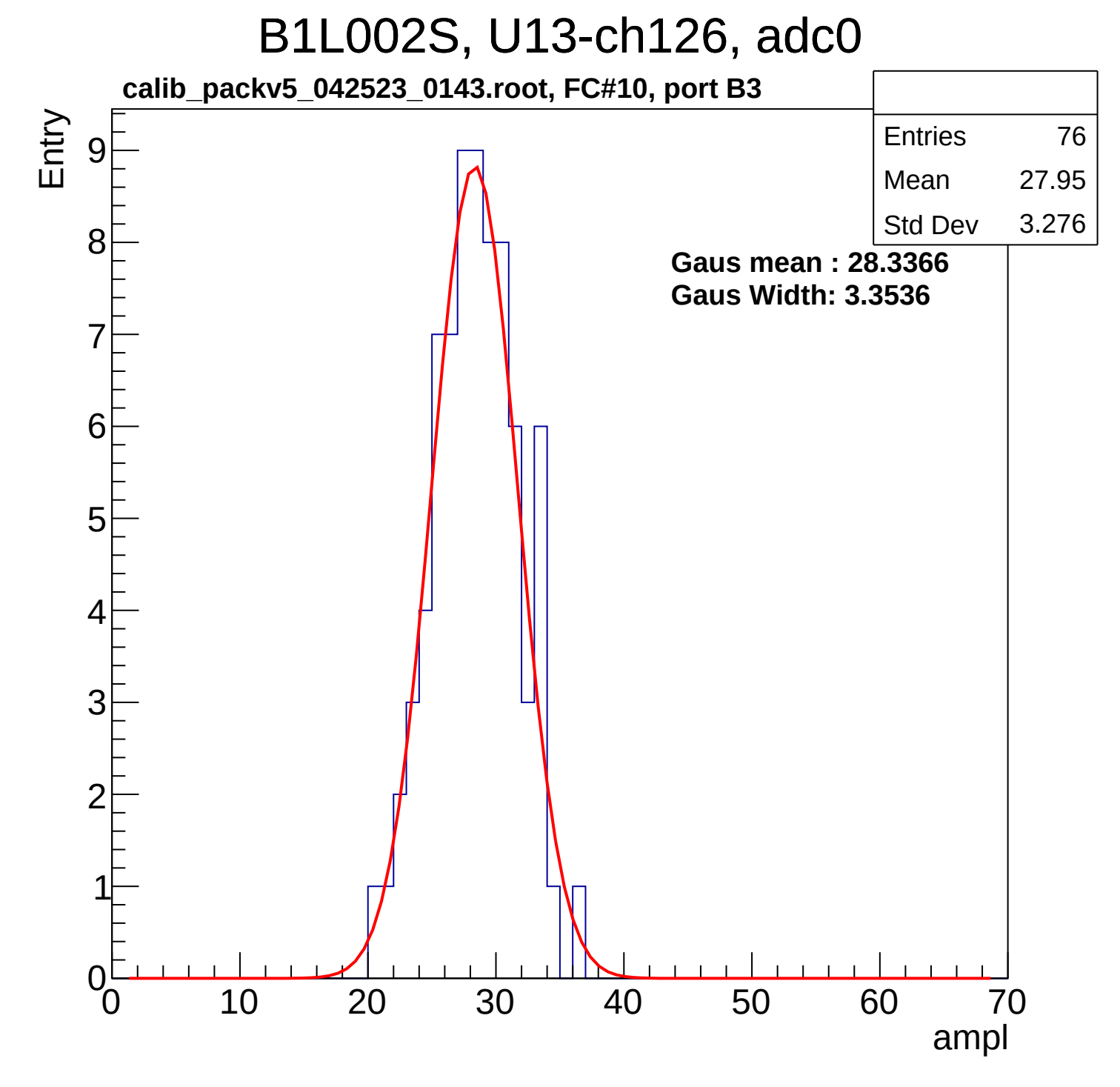
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U13-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 35.5
Std Dev 26.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.95
Std Dev 3.276
Gaus mean : 28.3366
Gaus Width: 3.3536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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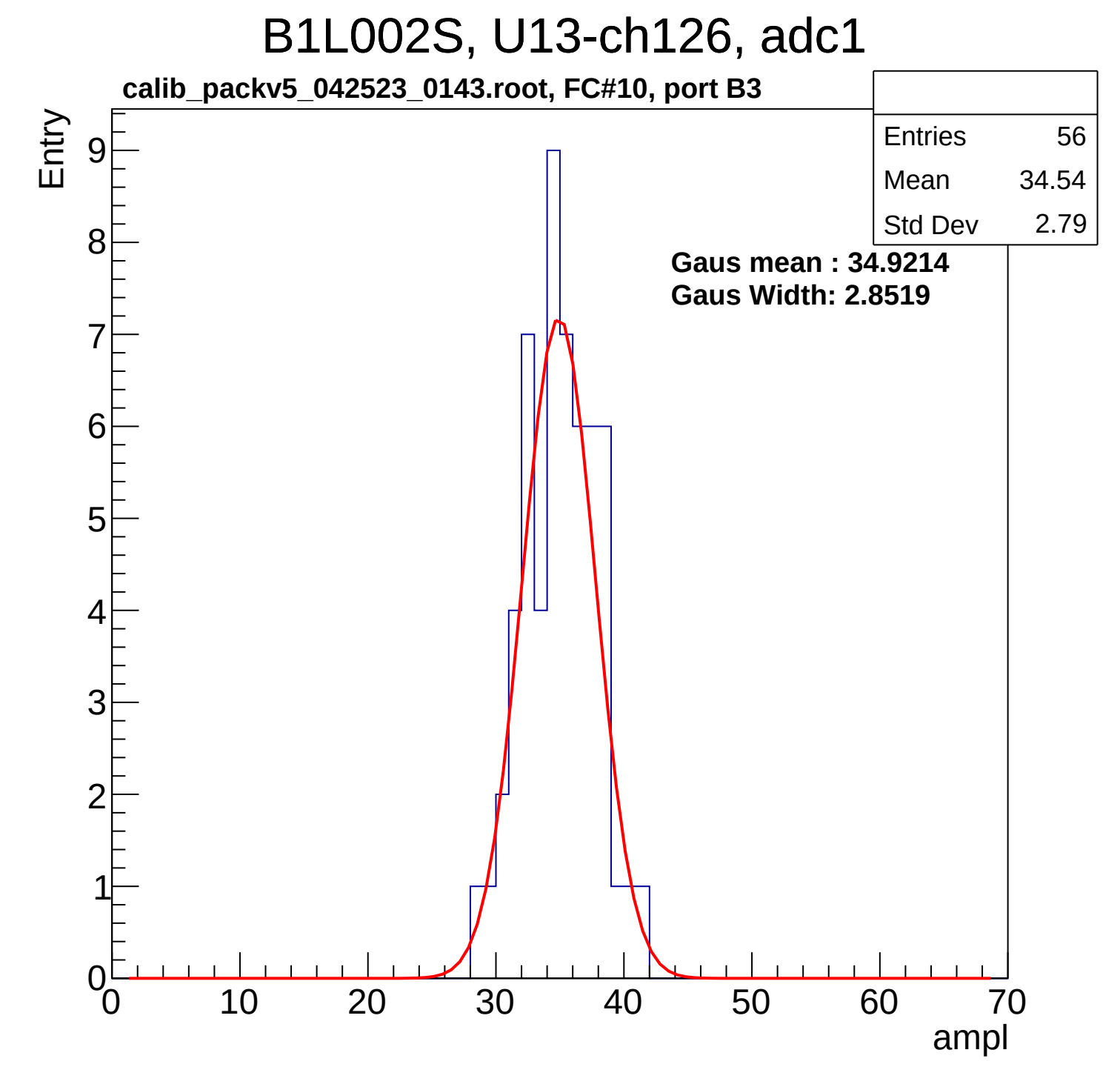
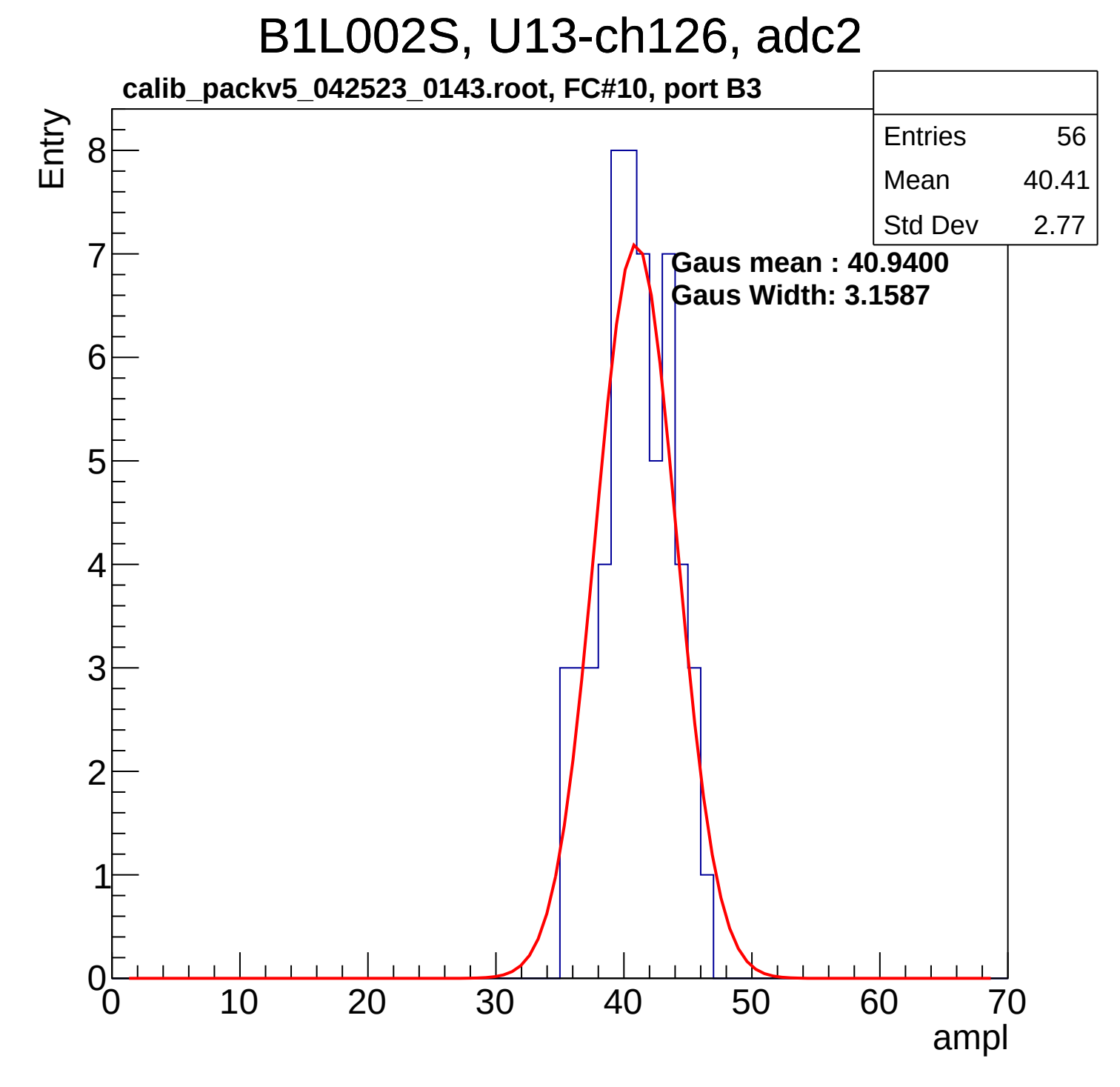
60
70


B1L002S, U13-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.54
Std Dev 2.79
Gaus mean : 34.9214
Gaus Width: 2.8519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.41
Std Dev 2.77
Gaus mean : 40.9400
Gaus Width: 3.1587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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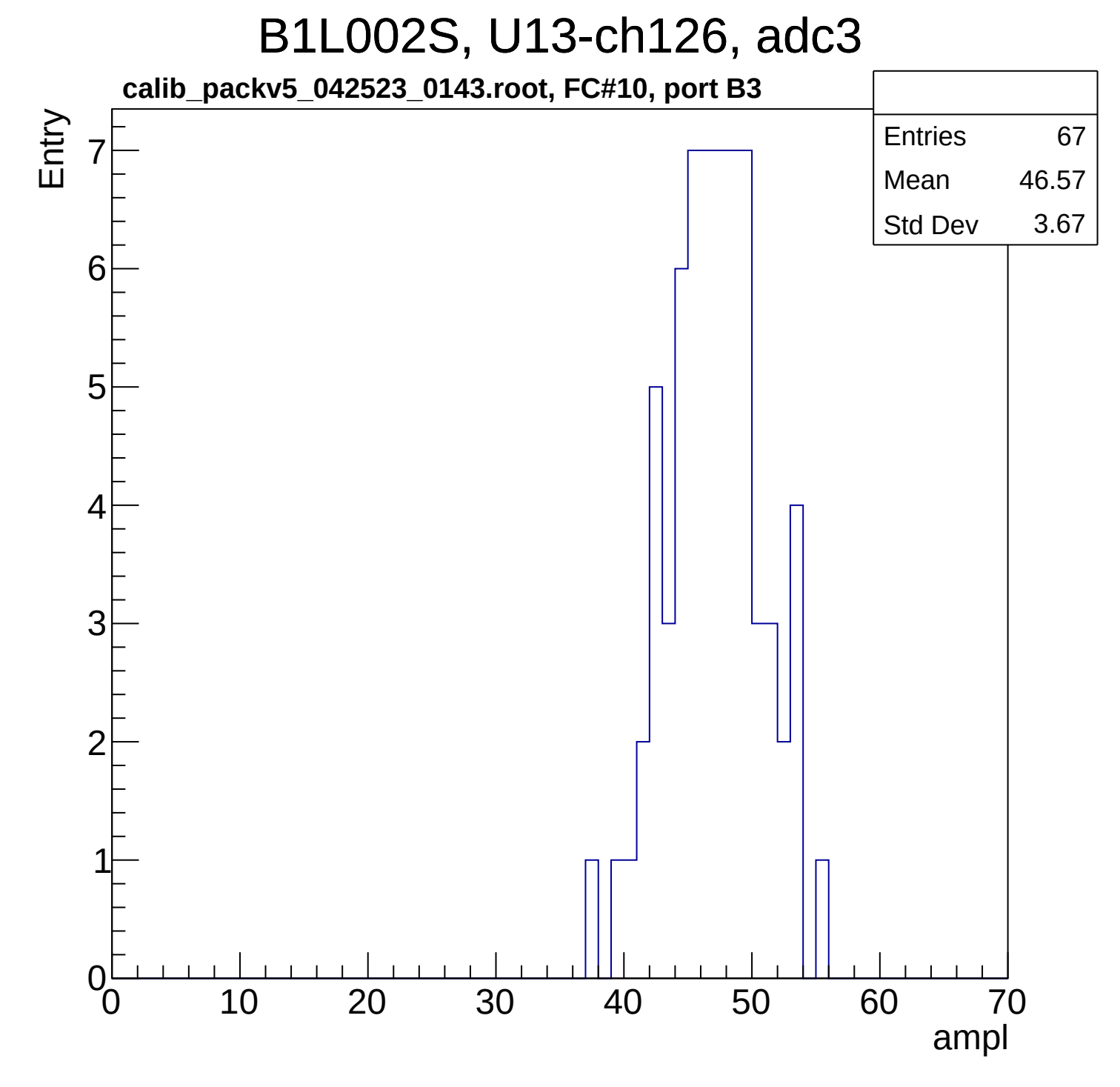
B1L002S, U13-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.57
Std Dev 3.67
Entry
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5
6
7
0
10
20
30
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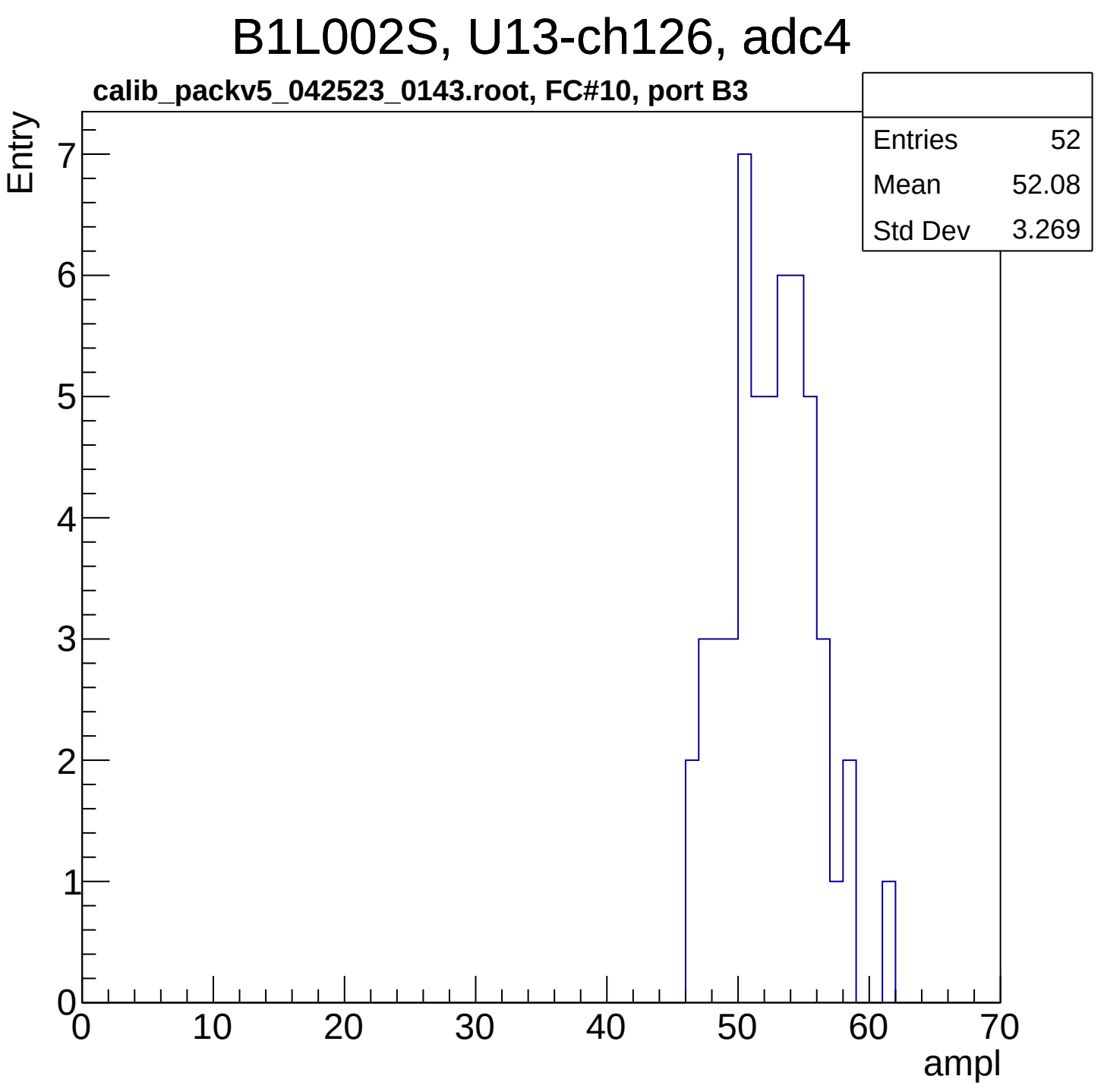
50
60
70
ampl


B1L002S, U13-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.08
Std Dev 3.269
Entry
ampl
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4
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40
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60
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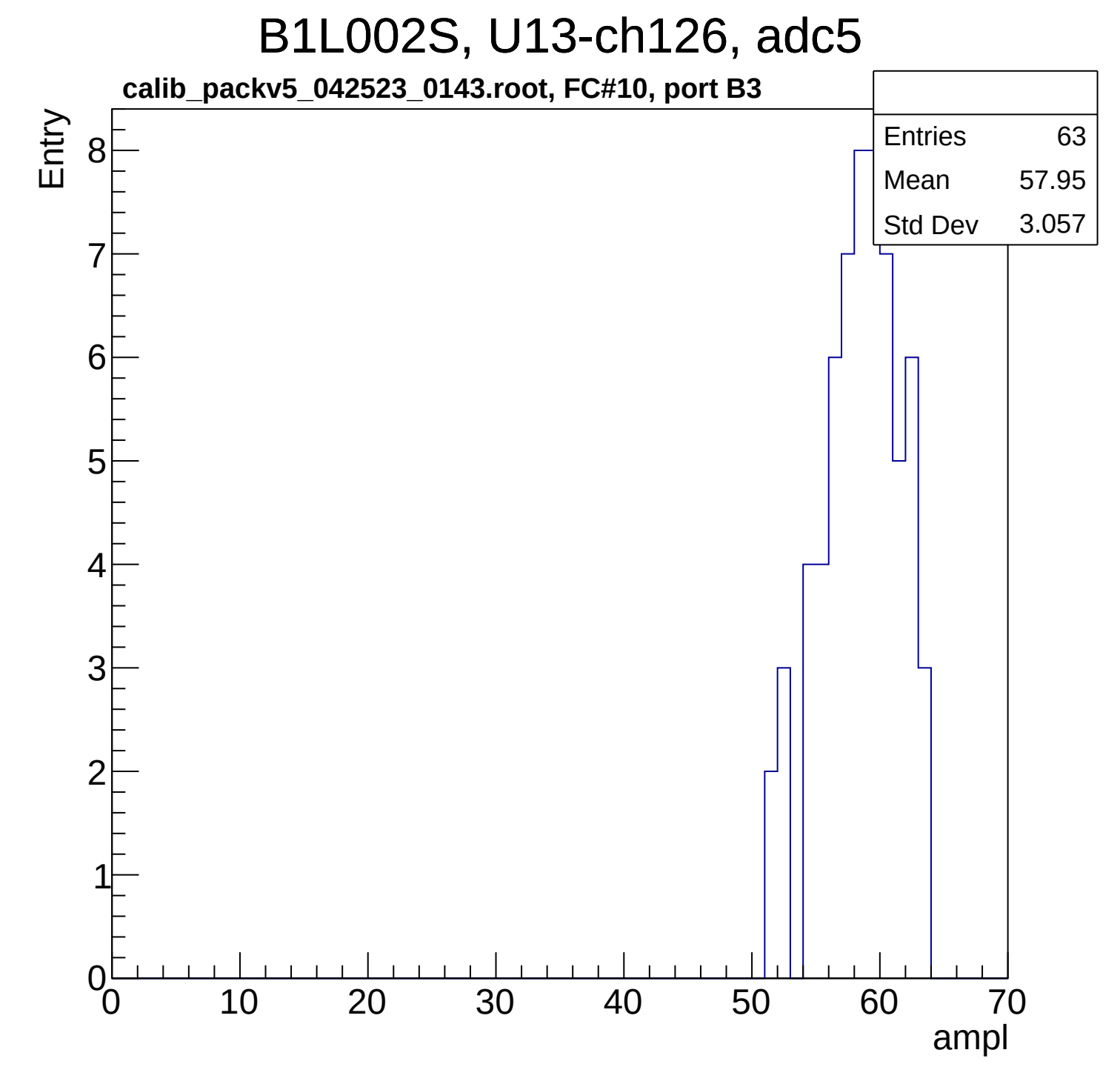
B1L002S, U13-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.95
Std Dev 3.057
Entry
ampl
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2
3
4
5
6
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20
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40
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60
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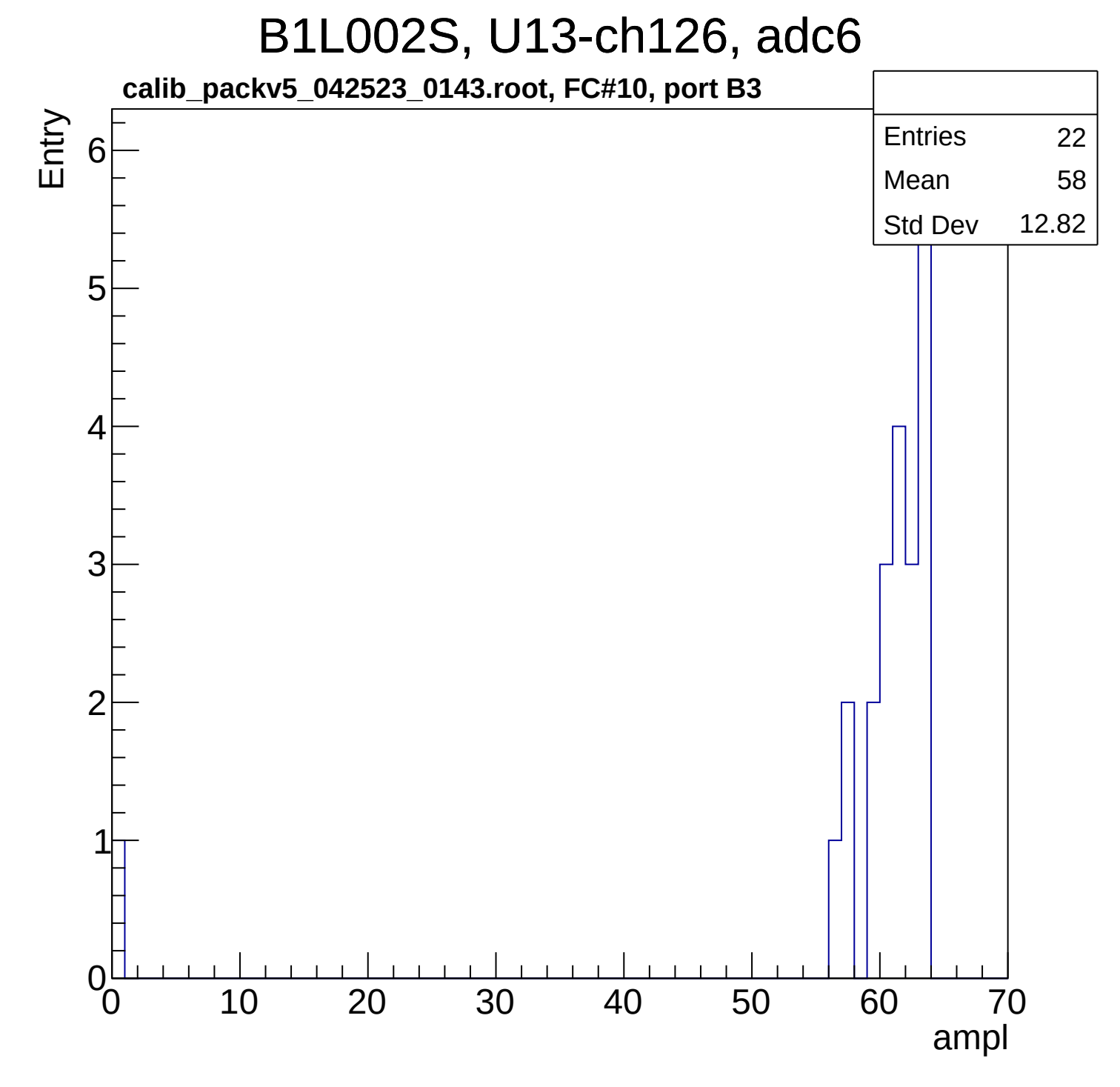
B1L002S, U13-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58
Std Dev 12.82
Entry
0
1
2
3
4
5
6
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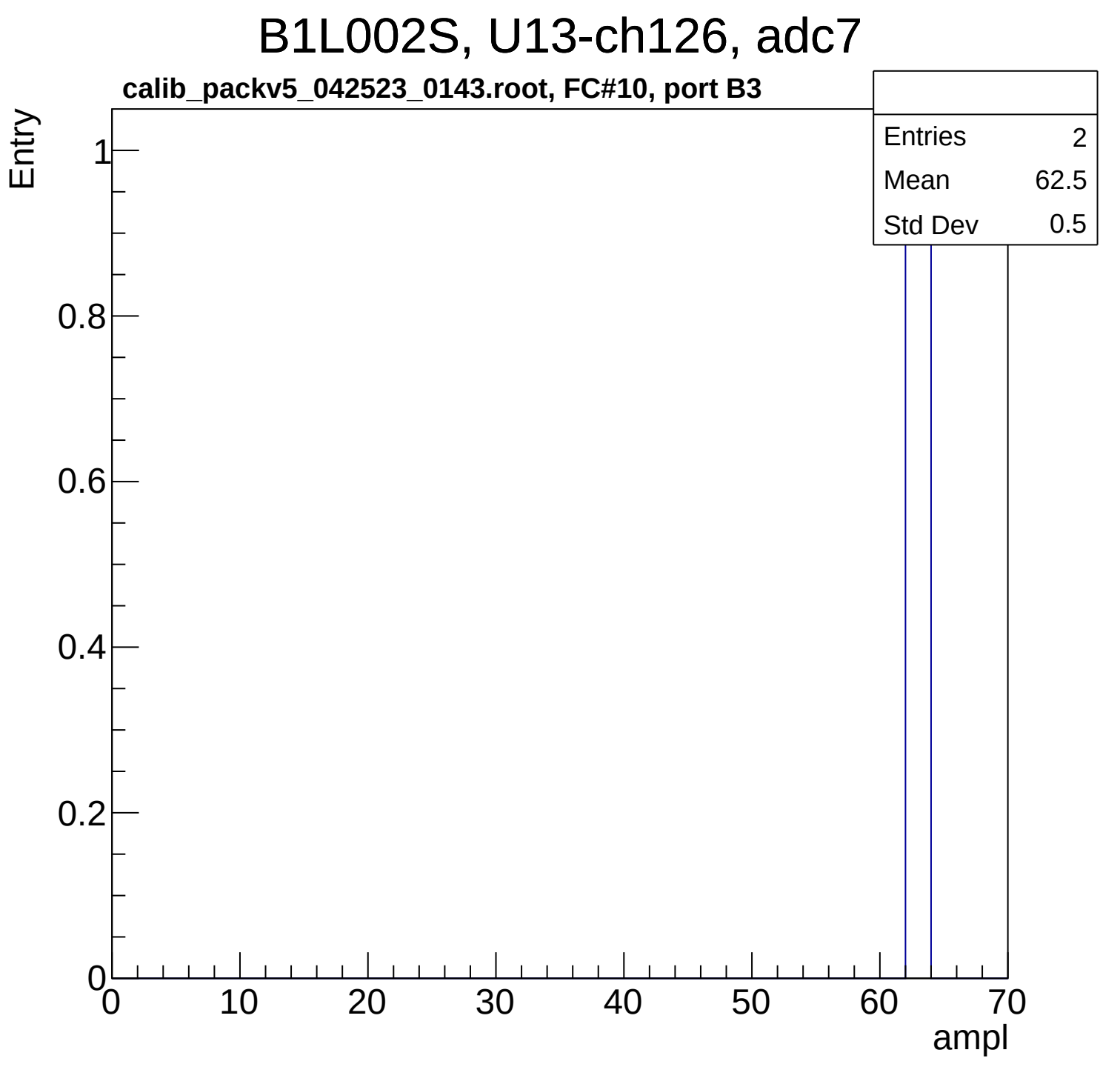
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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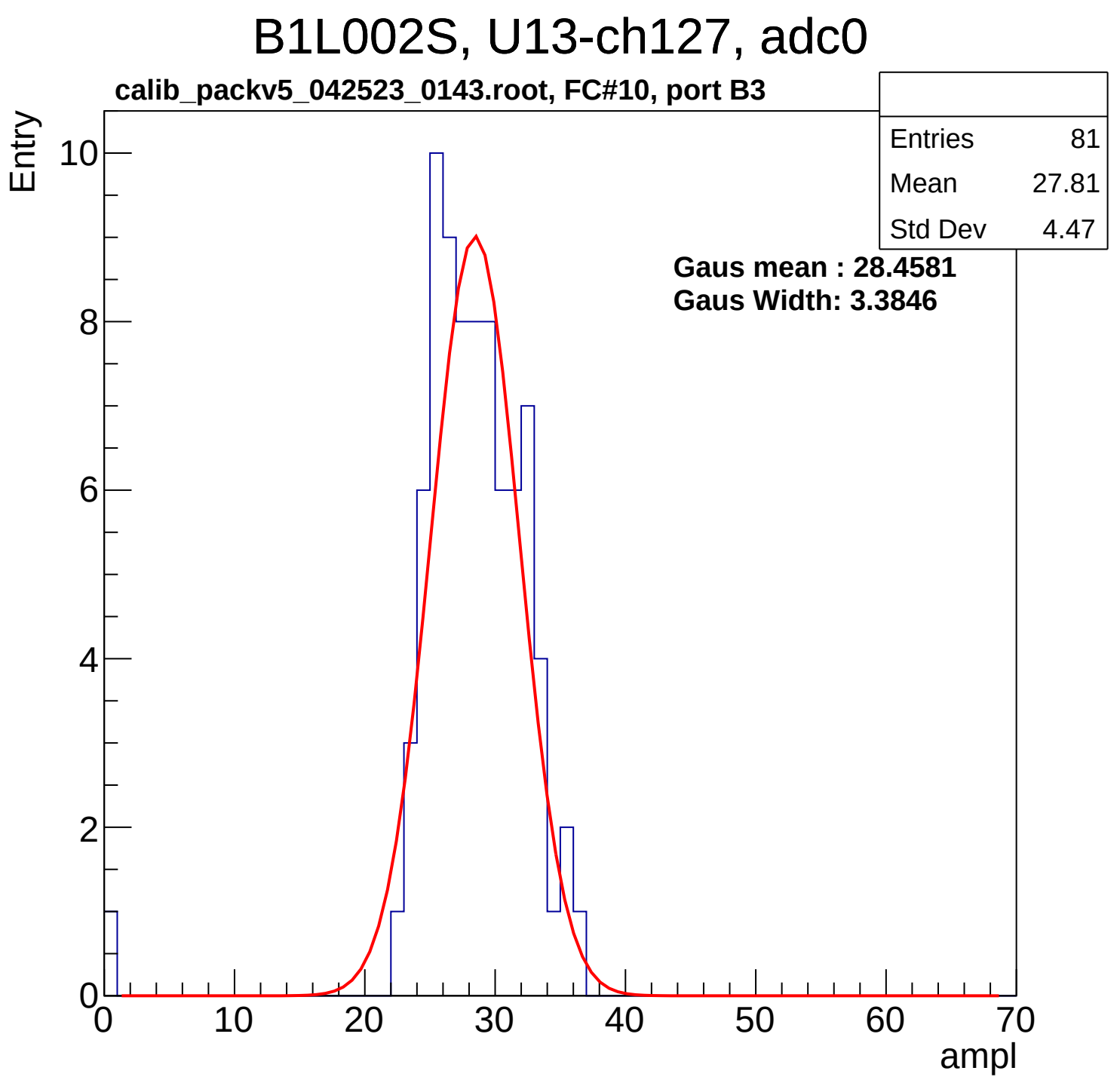
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U13-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 27.81
Std Dev 4.47
Gaus mean : 28.4581
Gaus Width: 3.3846
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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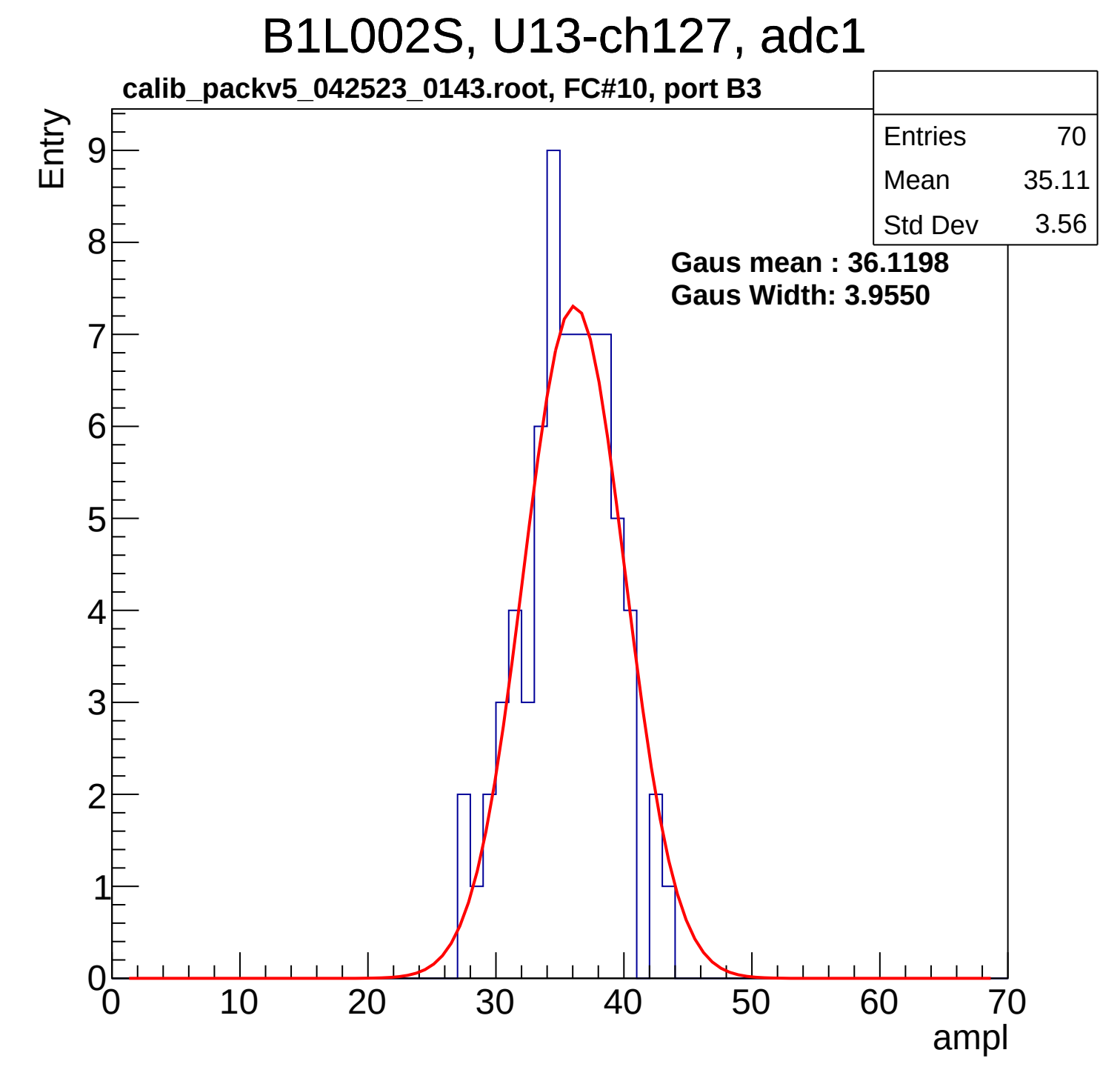
B1L002S, U13-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.11
Std Dev 3.56
Gaus mean : 36.1198
Gaus Width: 3.9550
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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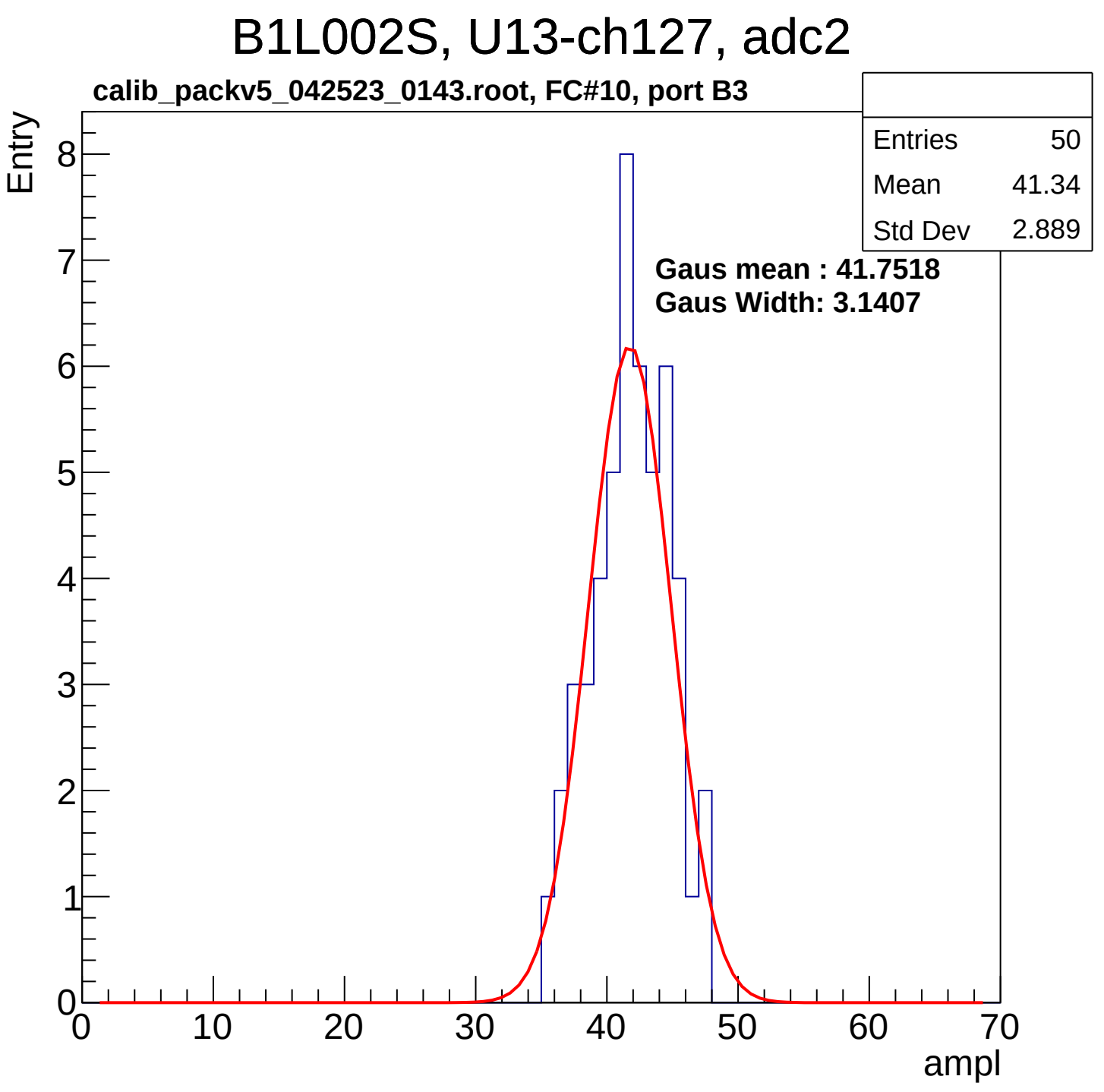
B1L002S, U13-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.34
Std Dev 2.889
Gaus mean : 41.7518
Gaus Width: 3.1407
Entry
ampl
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1
2
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4
5
6
7
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0
10
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60
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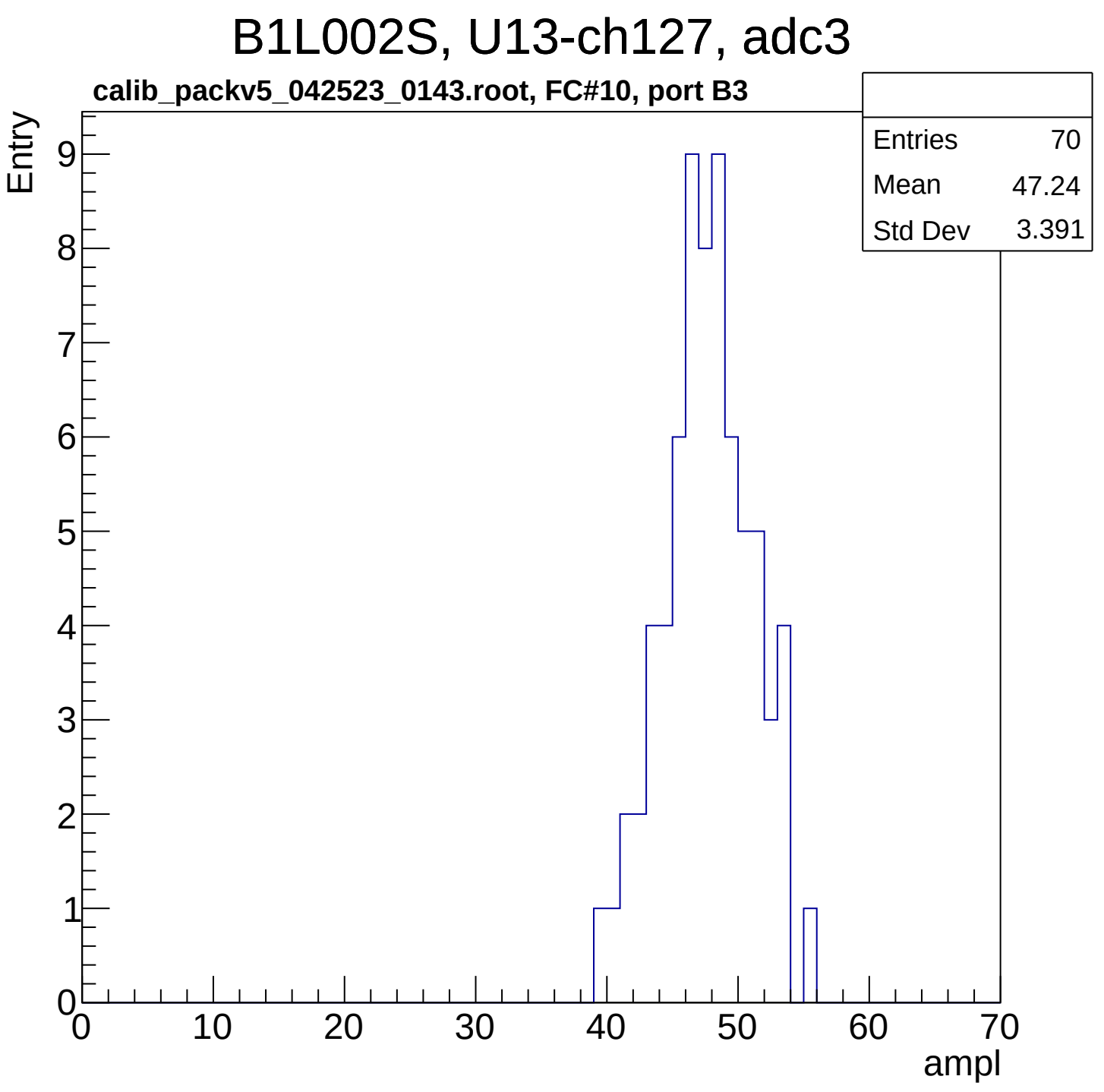
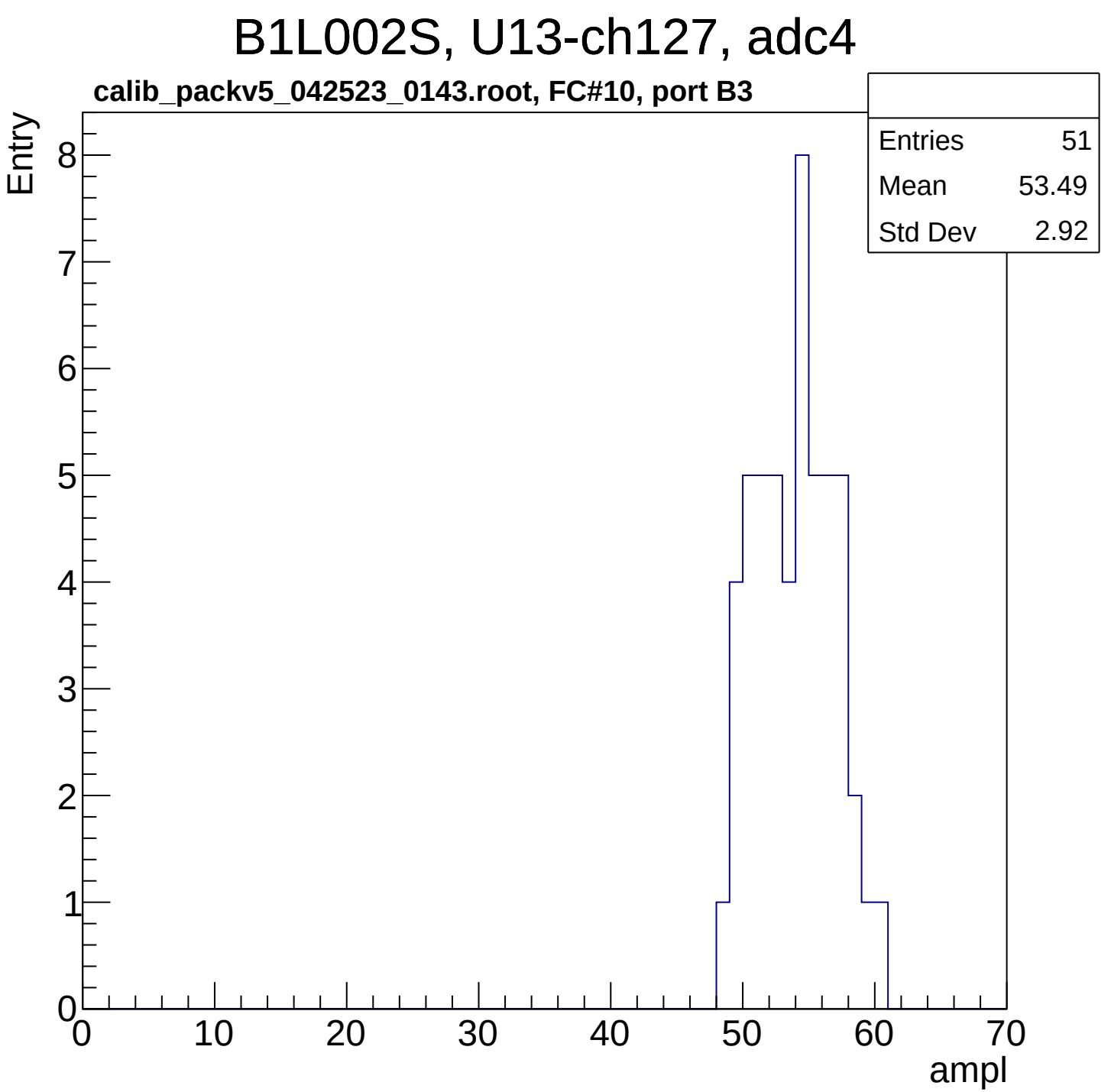
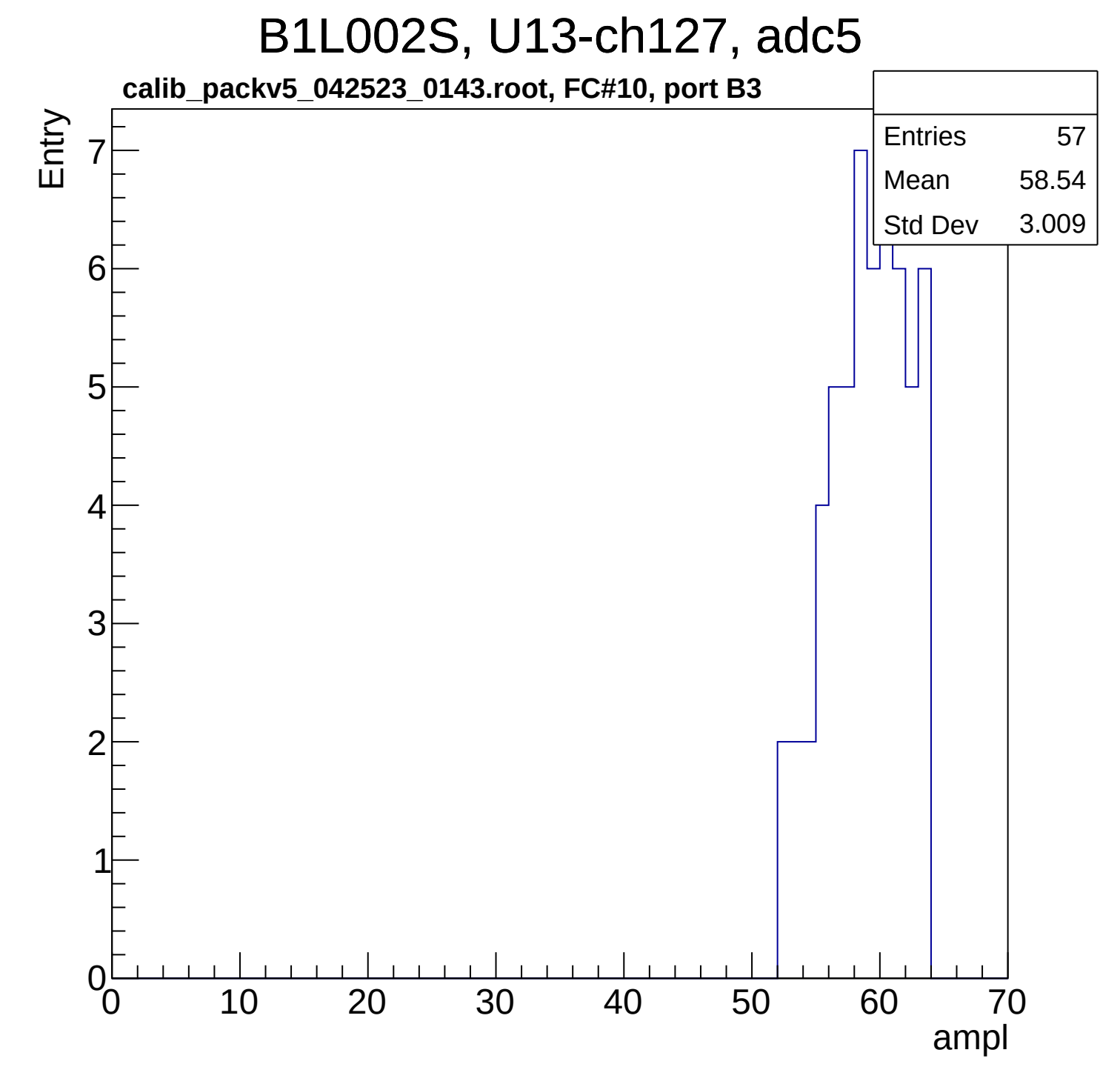
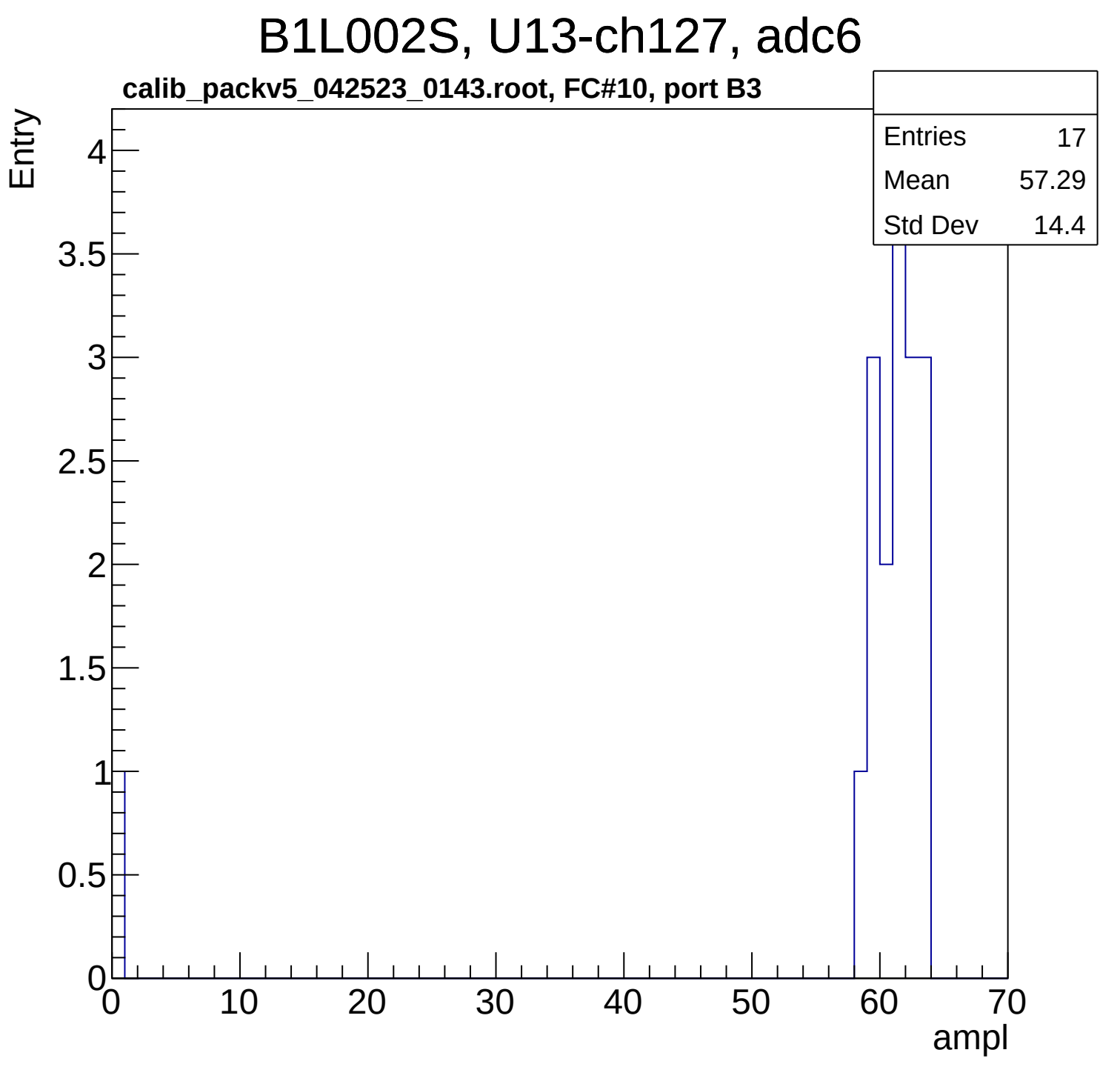
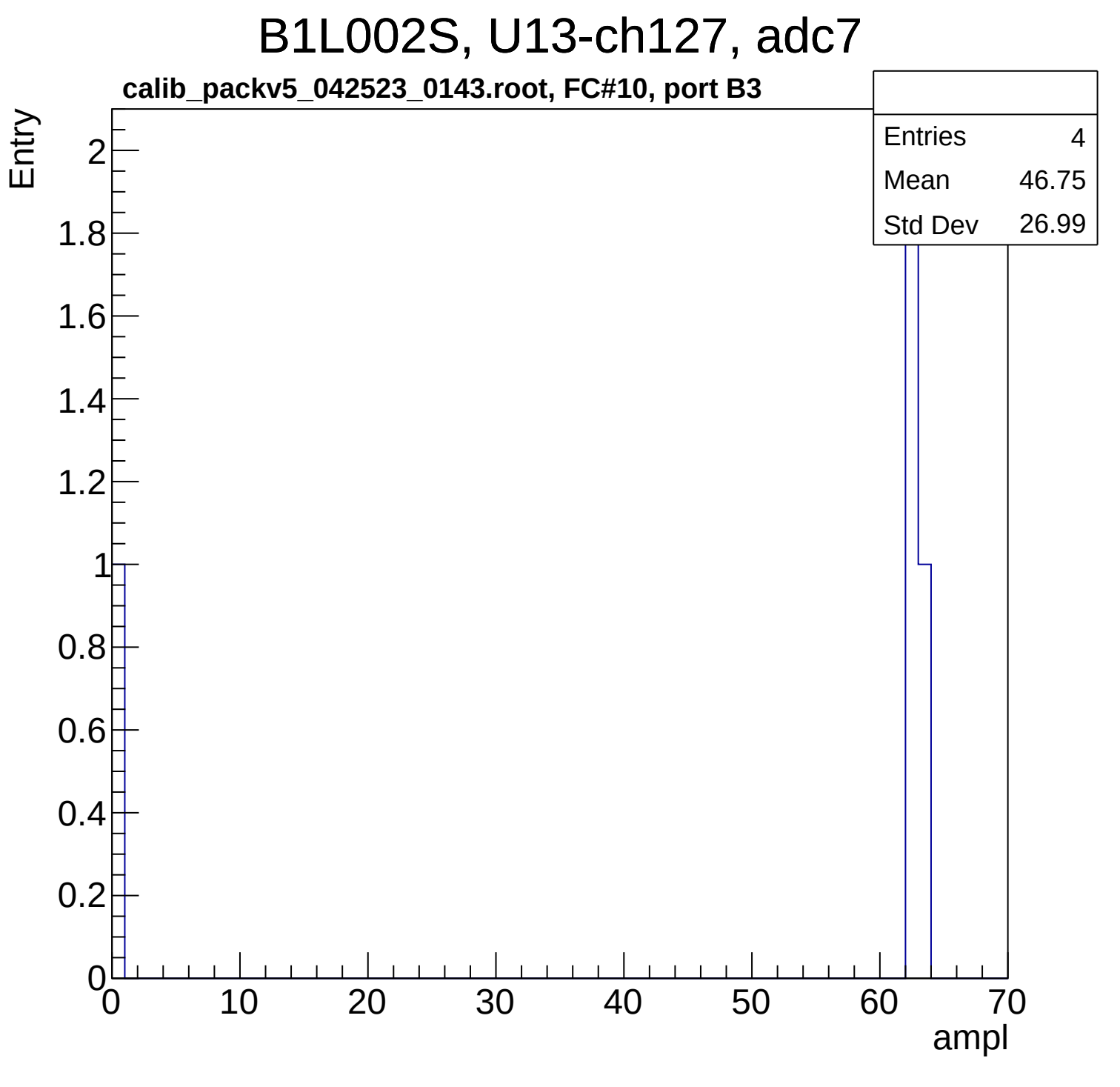
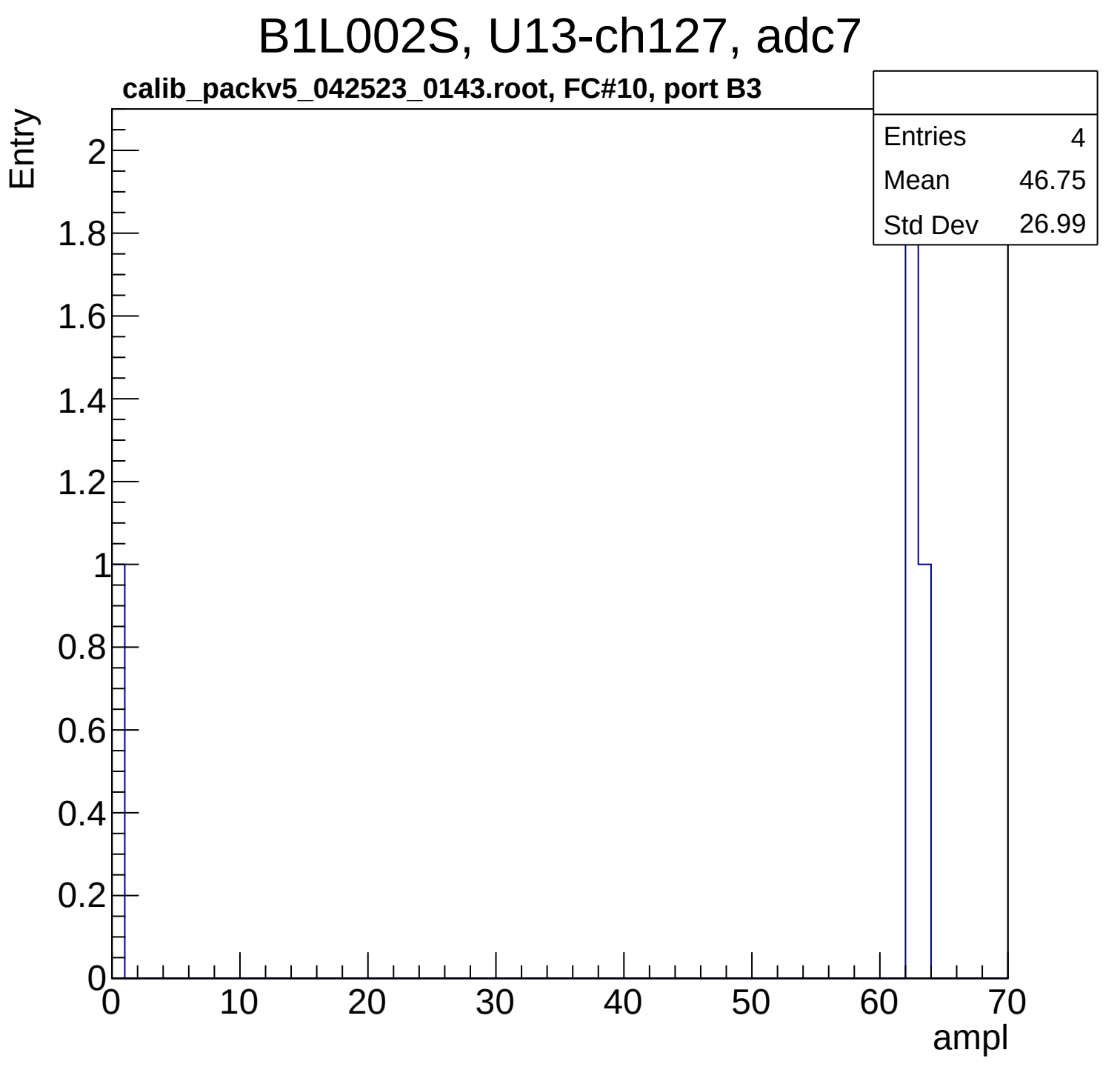

B1L002S, U13-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.24
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U13-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.49
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U13-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.54
Std Dev 3.009
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U13-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.29
Std Dev 14.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70