

B1L102S, U11-ch0, adc0

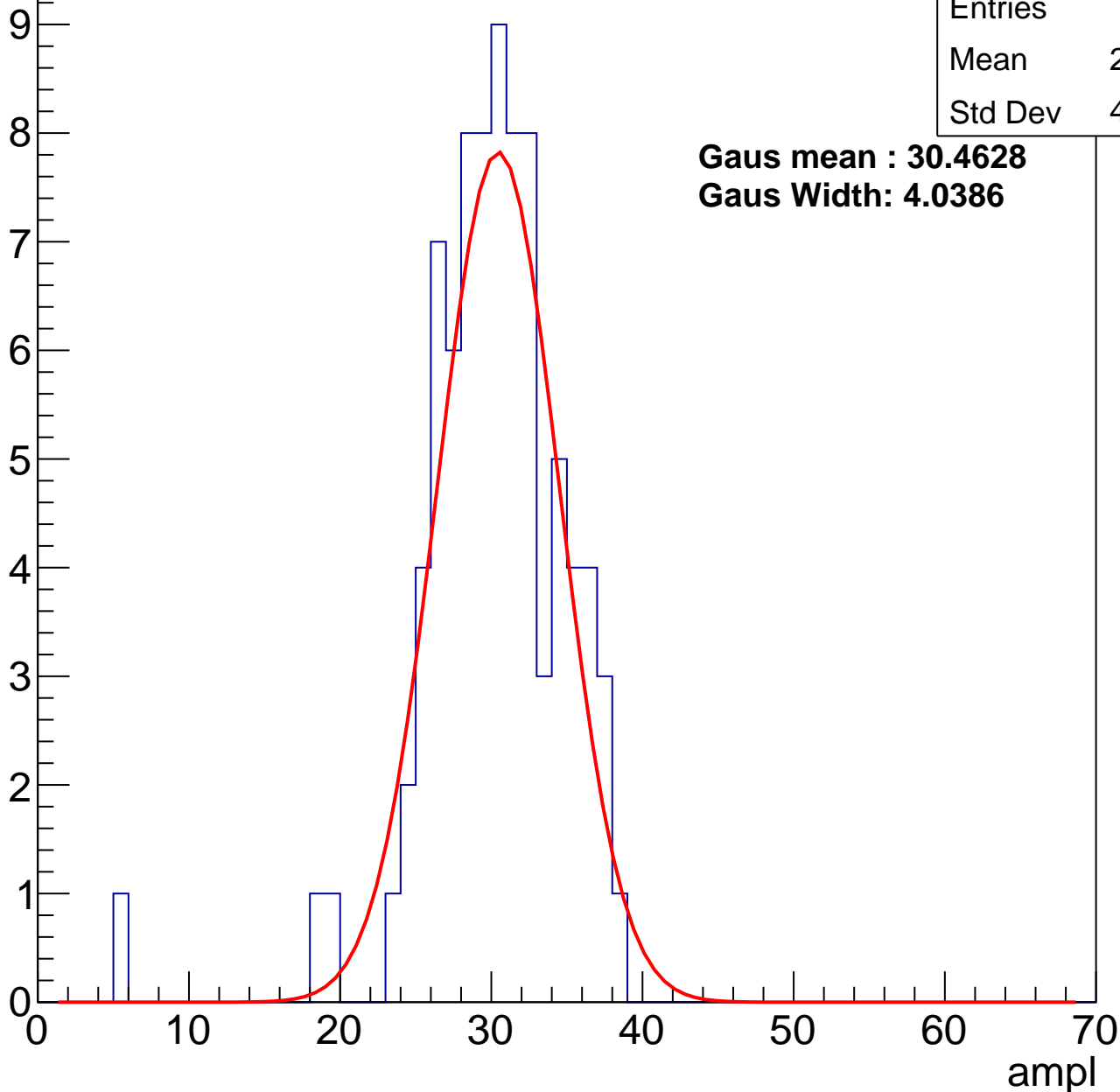
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	29.63
Std Dev	4.765

Gaus mean : 30.4628

Gaus Width: 4.0386



B1L102S, U11-ch0, adc1

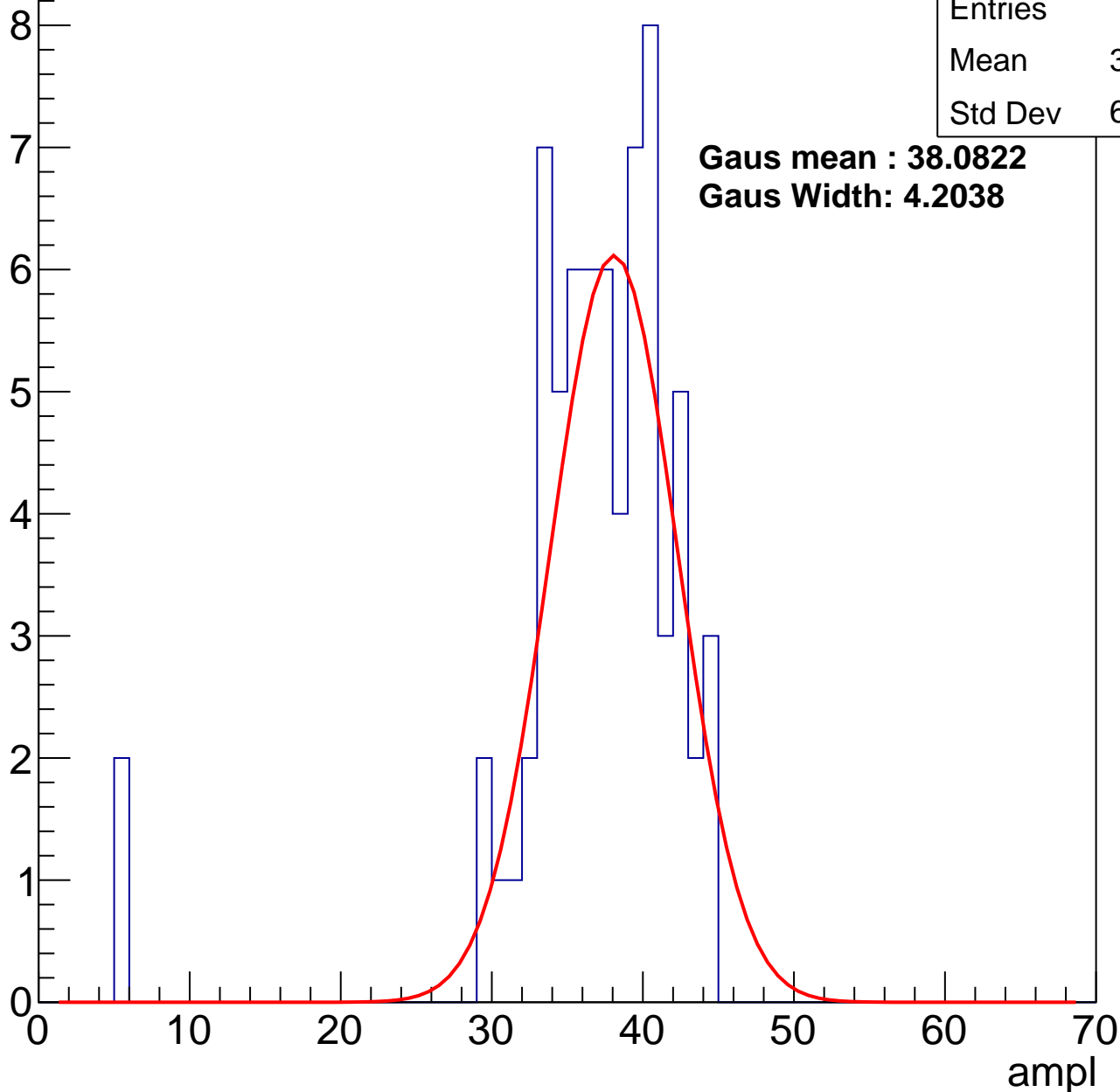
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.26
Std Dev	6.498

Gaus mean : 38.0822

Gaus Width: 4.2038



B1L102S, U11-ch0, adc2

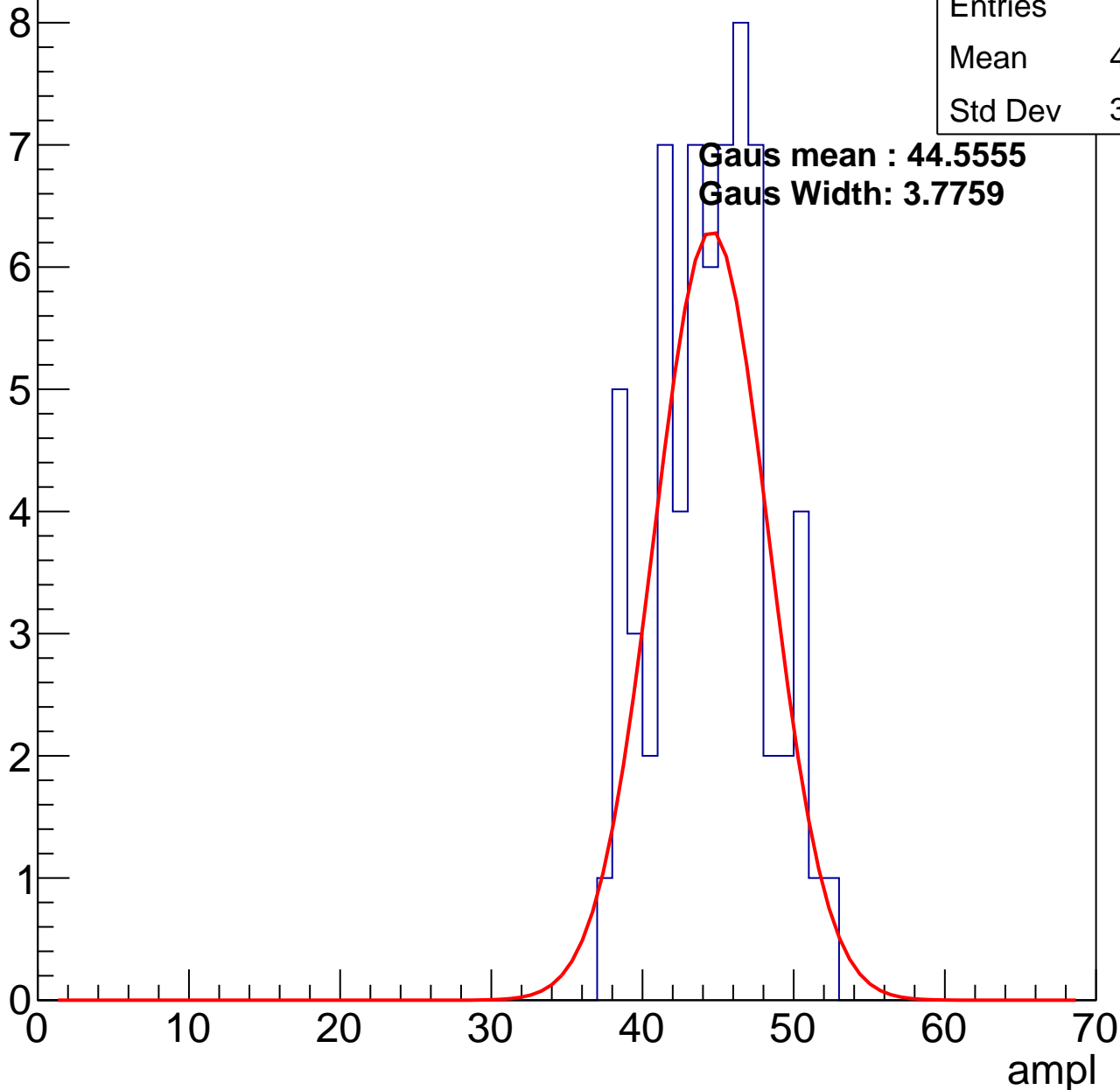
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.07
Std Dev	3.588

Gaus mean : 44.5555

Gaus Width: 3.7759

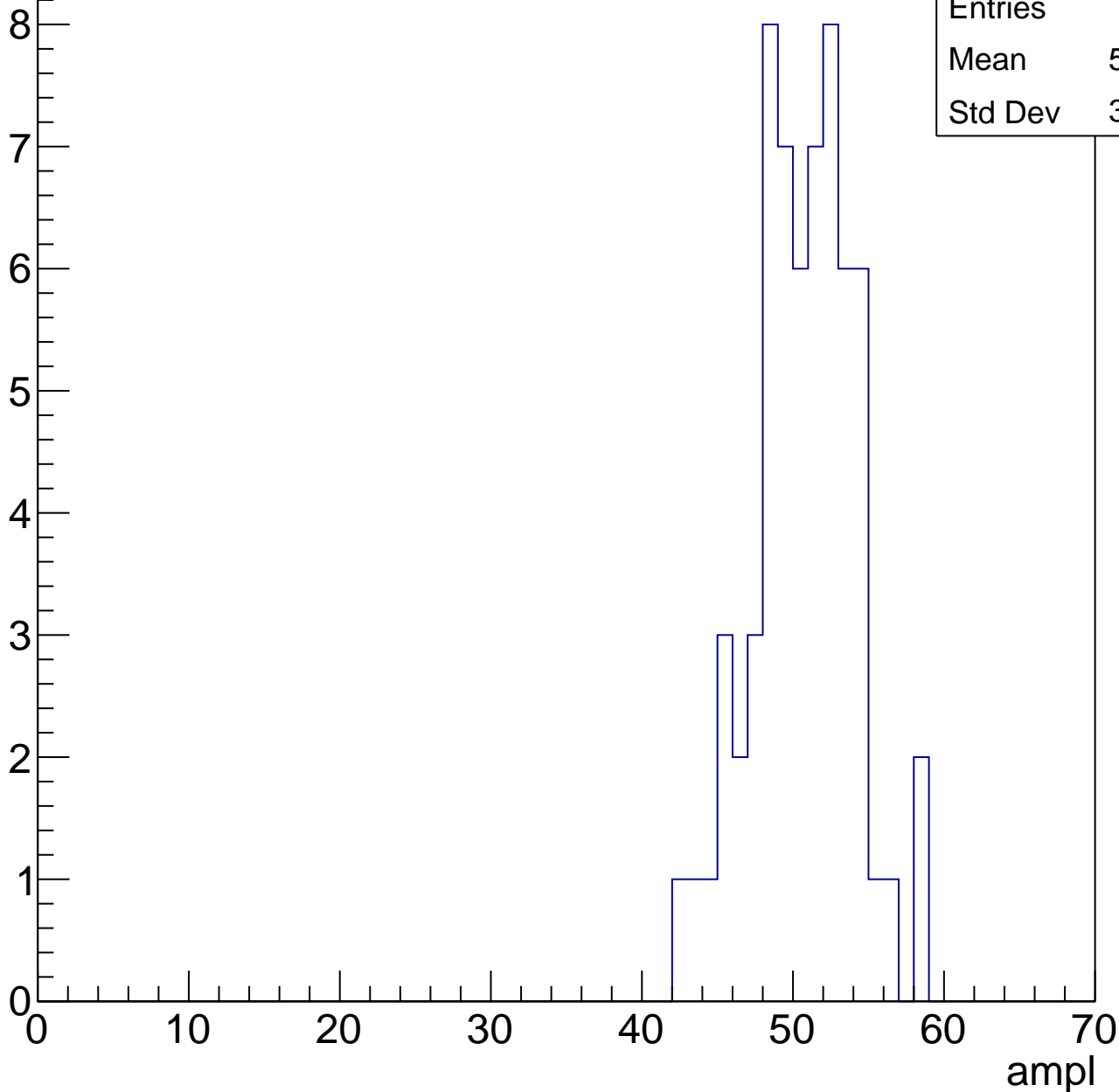


B1L102S, U11-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.25
Std Dev	3.324

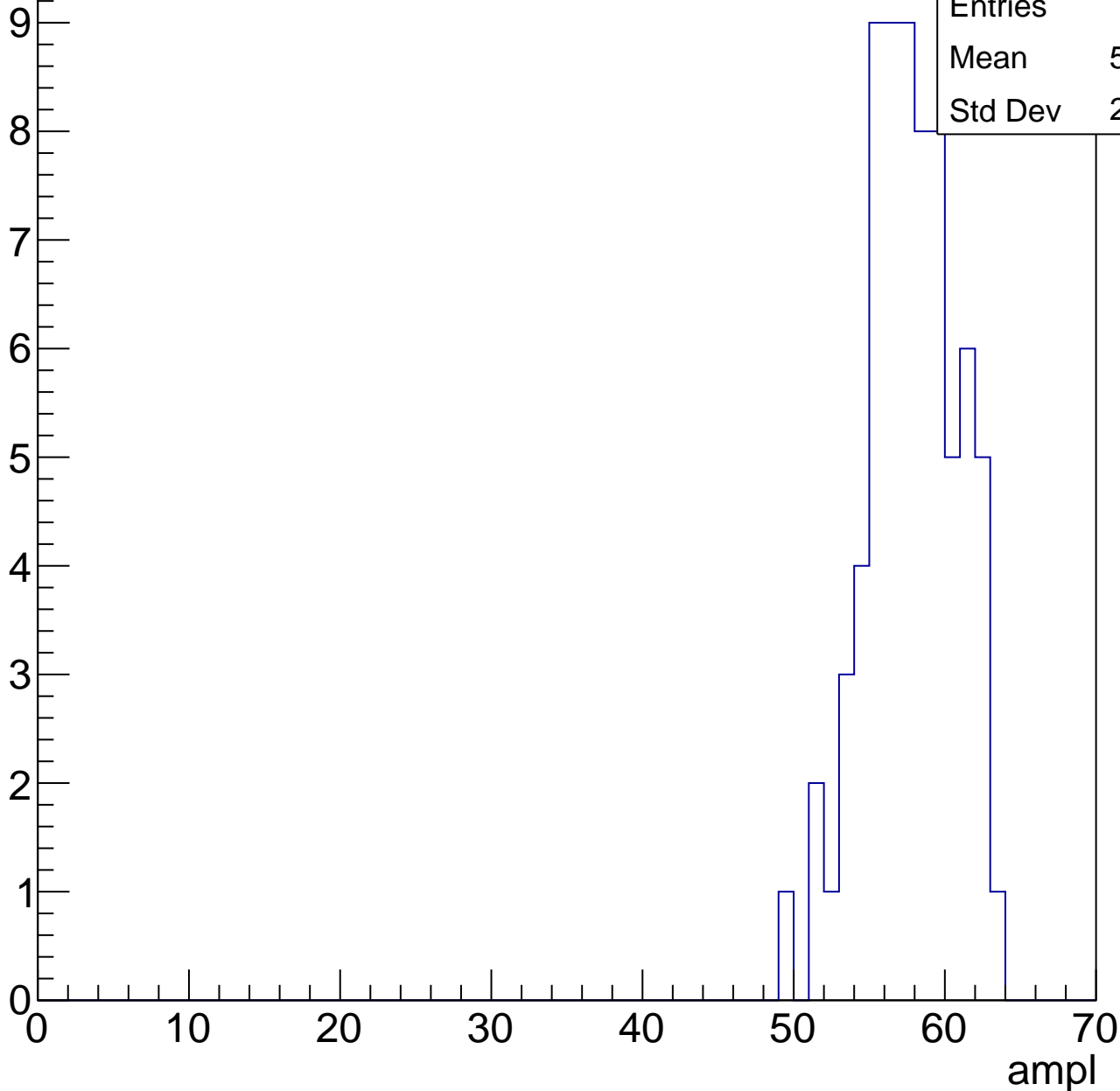


B1L102S, U11-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	57.25
Std Dev	2.973

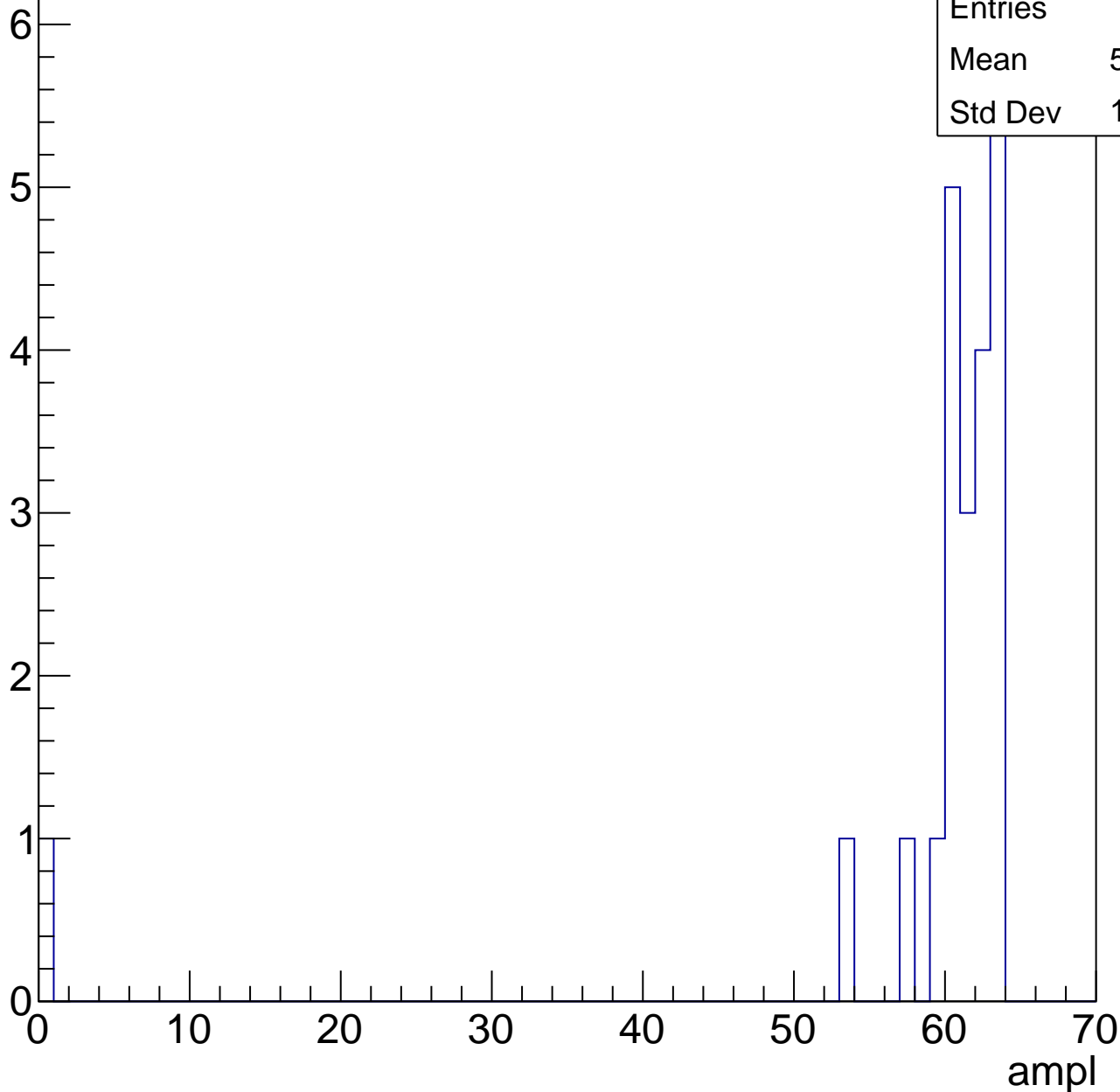


B1L102S, U11-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	58.09
Std Dev	12.88



B1L102S, U11-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

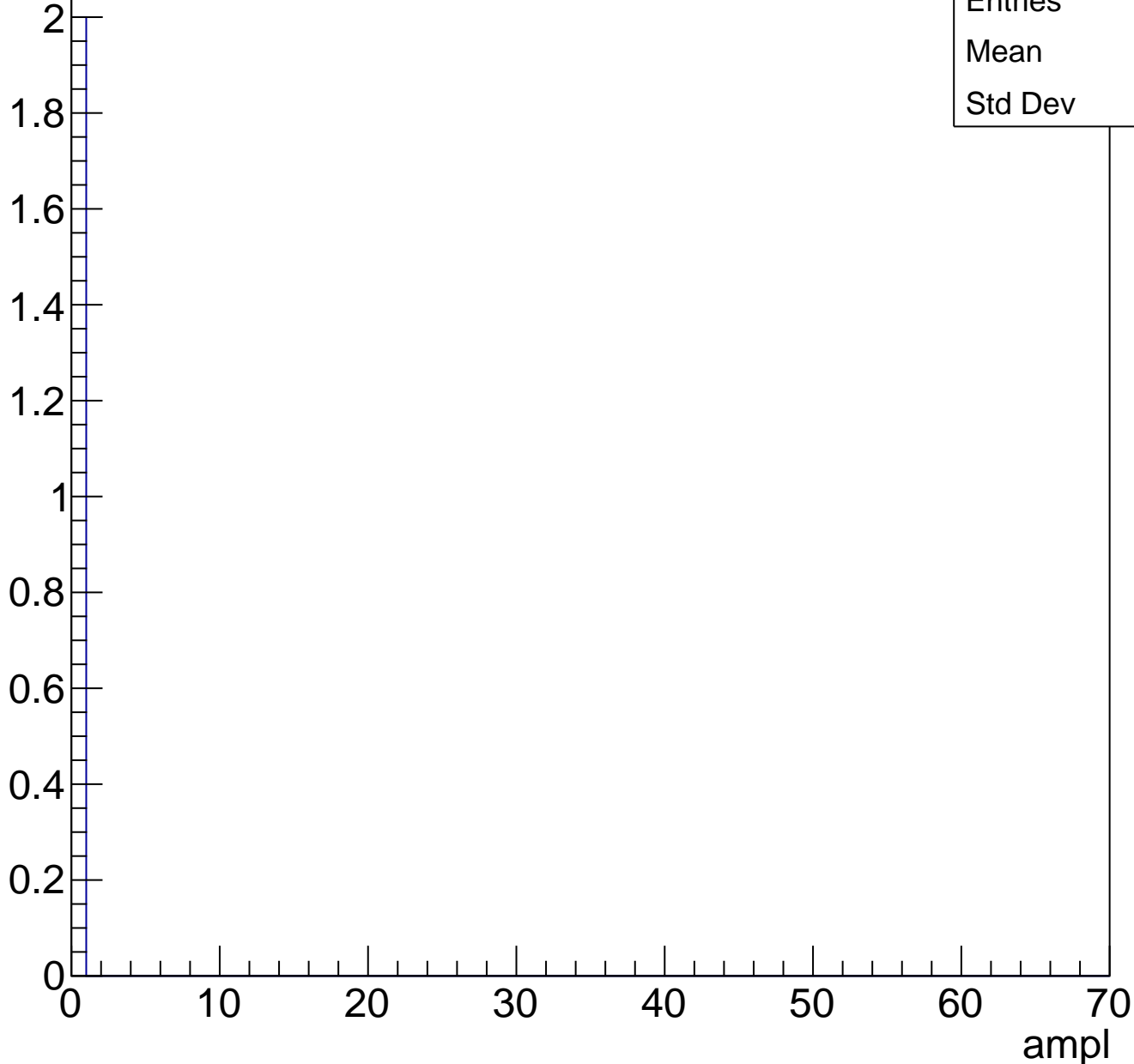
Entry



B1L102S, U11-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch1, adc0

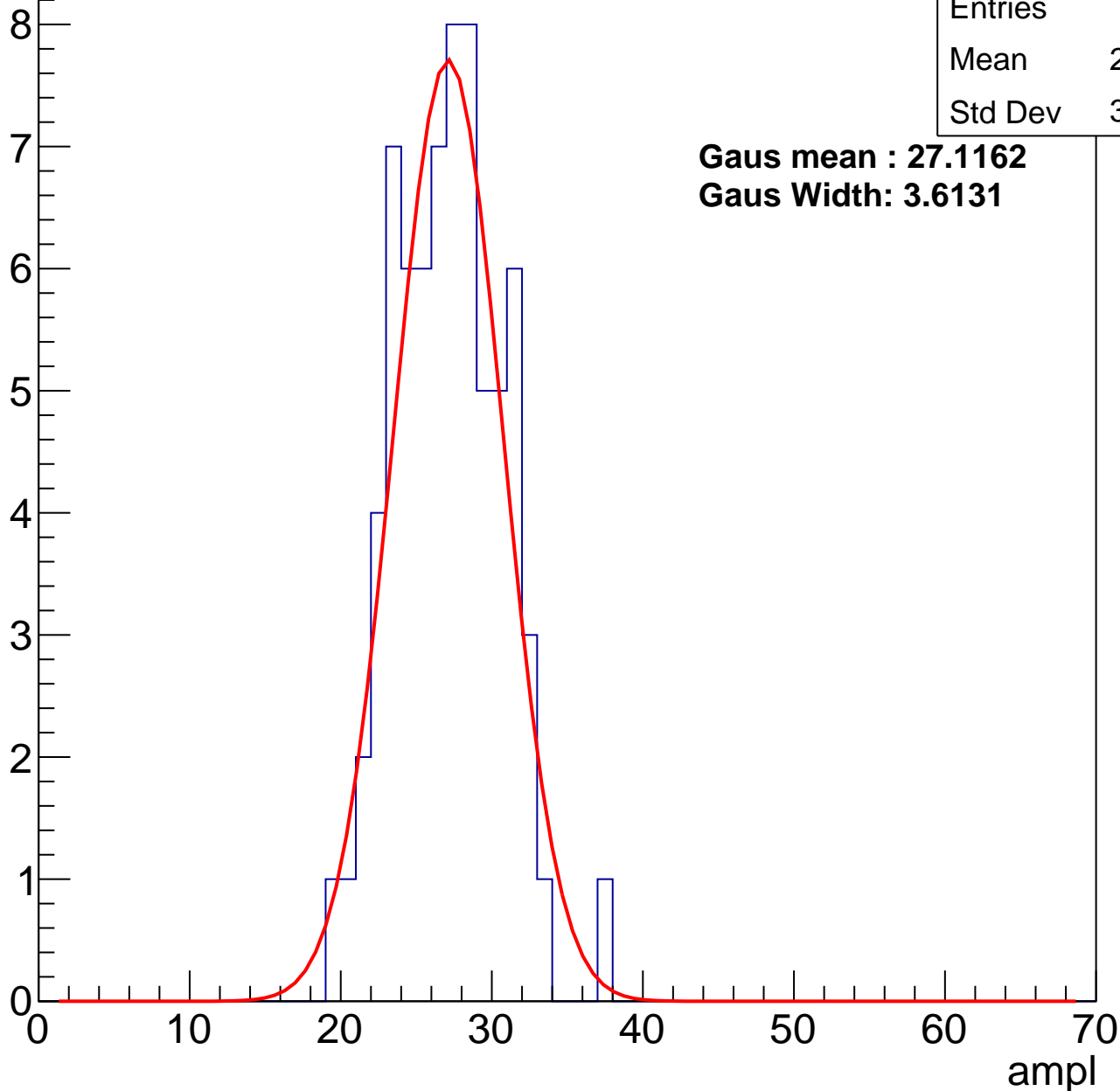
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.66
Std Dev	3.468

Gaus mean : 27.1162

Gaus Width: 3.6131



B1L102S, U11-ch1, adc1

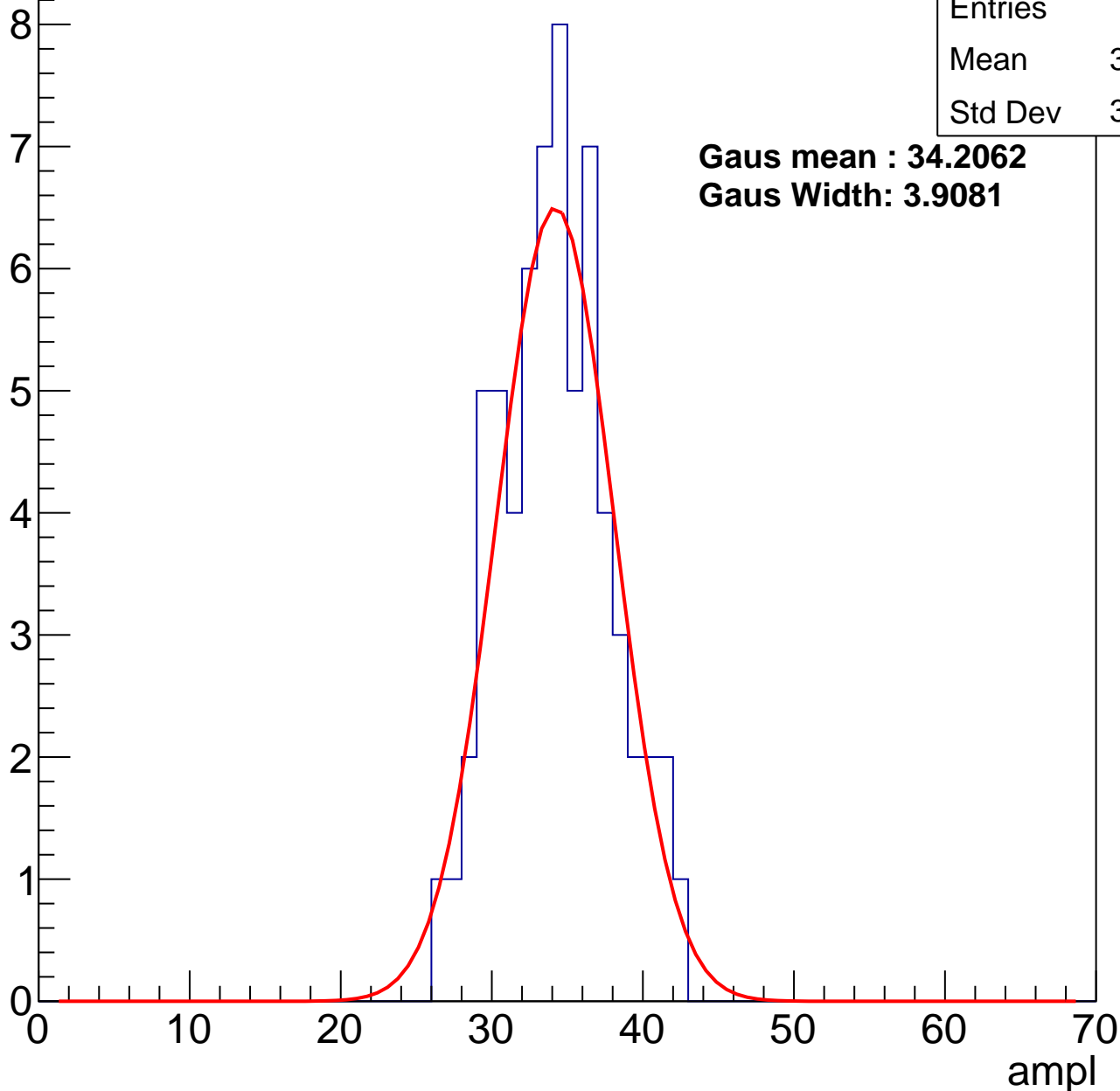
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	33.75
Std Dev	3.625

Gaus mean : 34.2062

Gaus Width: 3.9081



B1L102S, U11-ch1, adc2

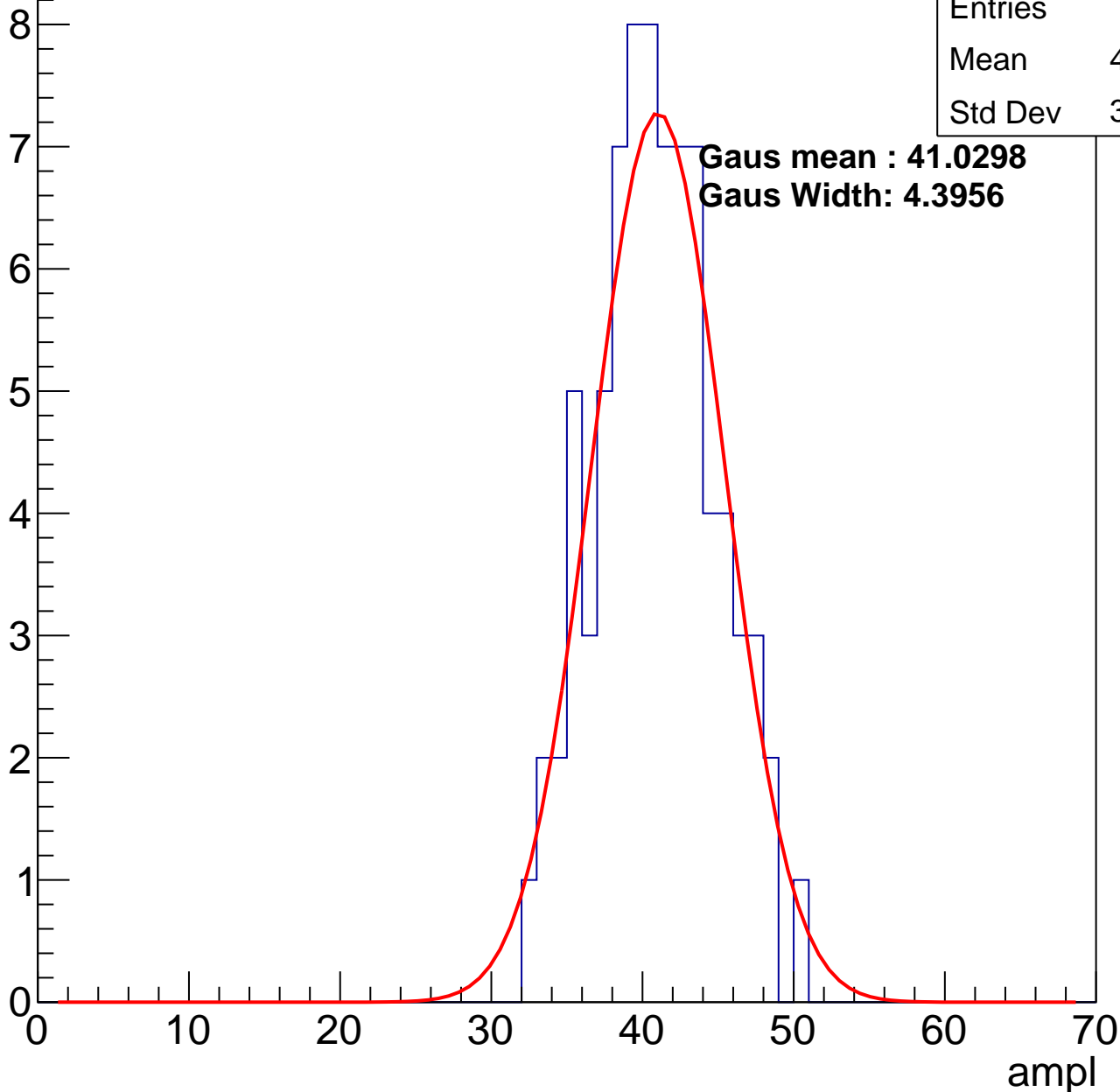
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	40.44
Std Dev	3.926

Gaus mean : 41.0298

Gaus Width: 4.3956

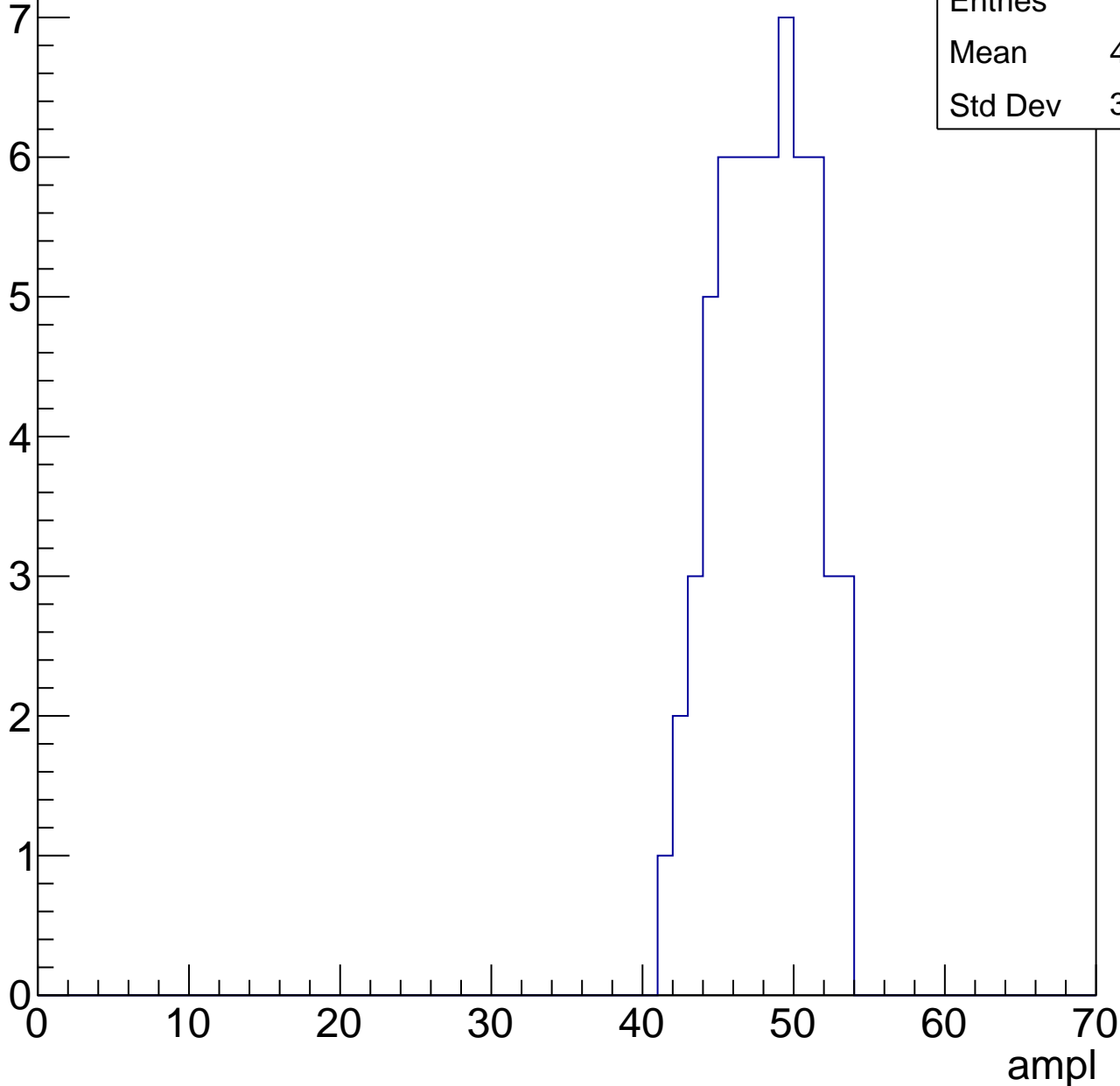


B1L102S, U11-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.57
Std Dev	3.046



B1L102S, U11-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	53.97
Std Dev	3.8

Entry

10

8

6

4

2

0

0

10

20

30

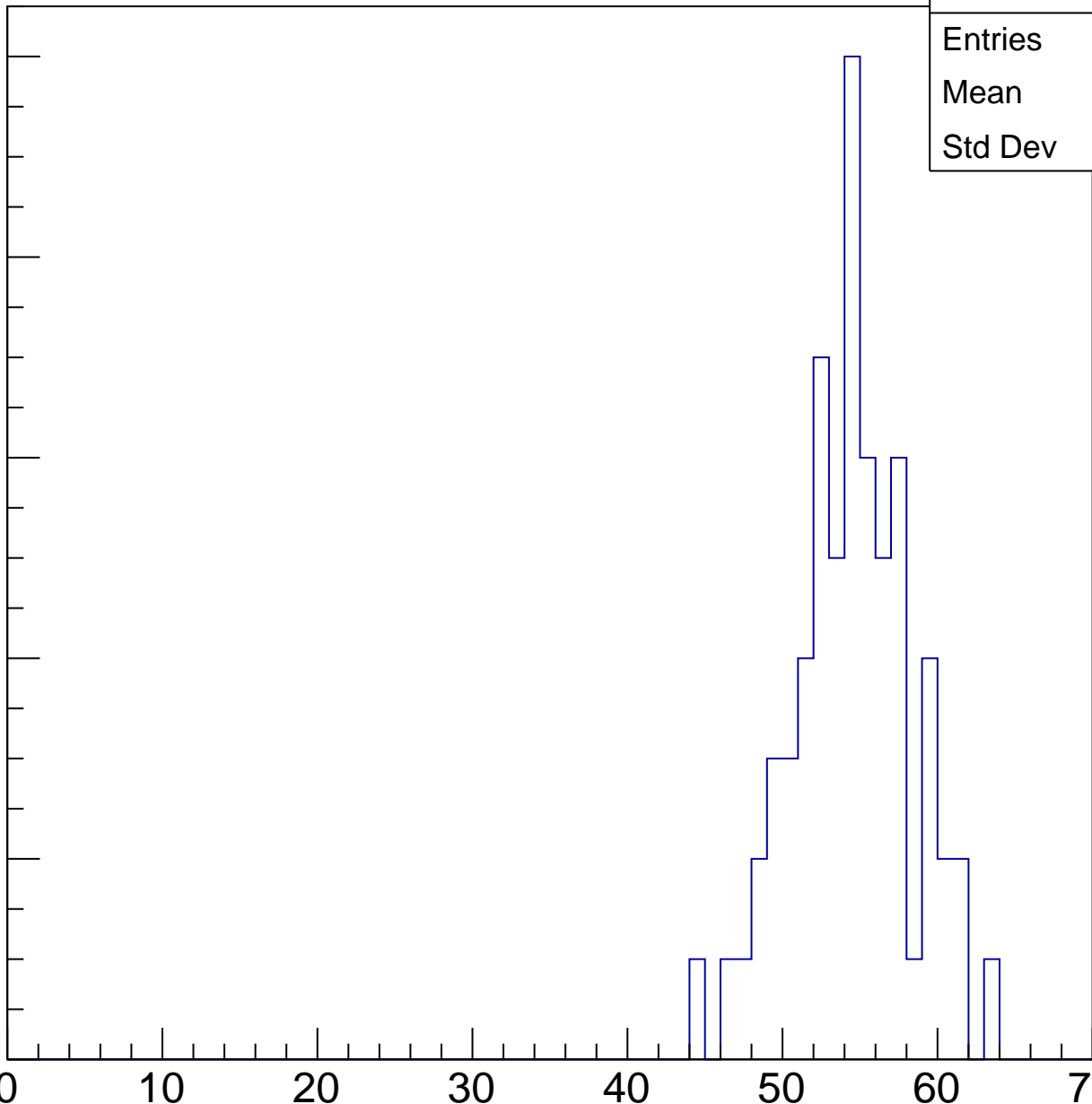
40

50

60

70

ampl

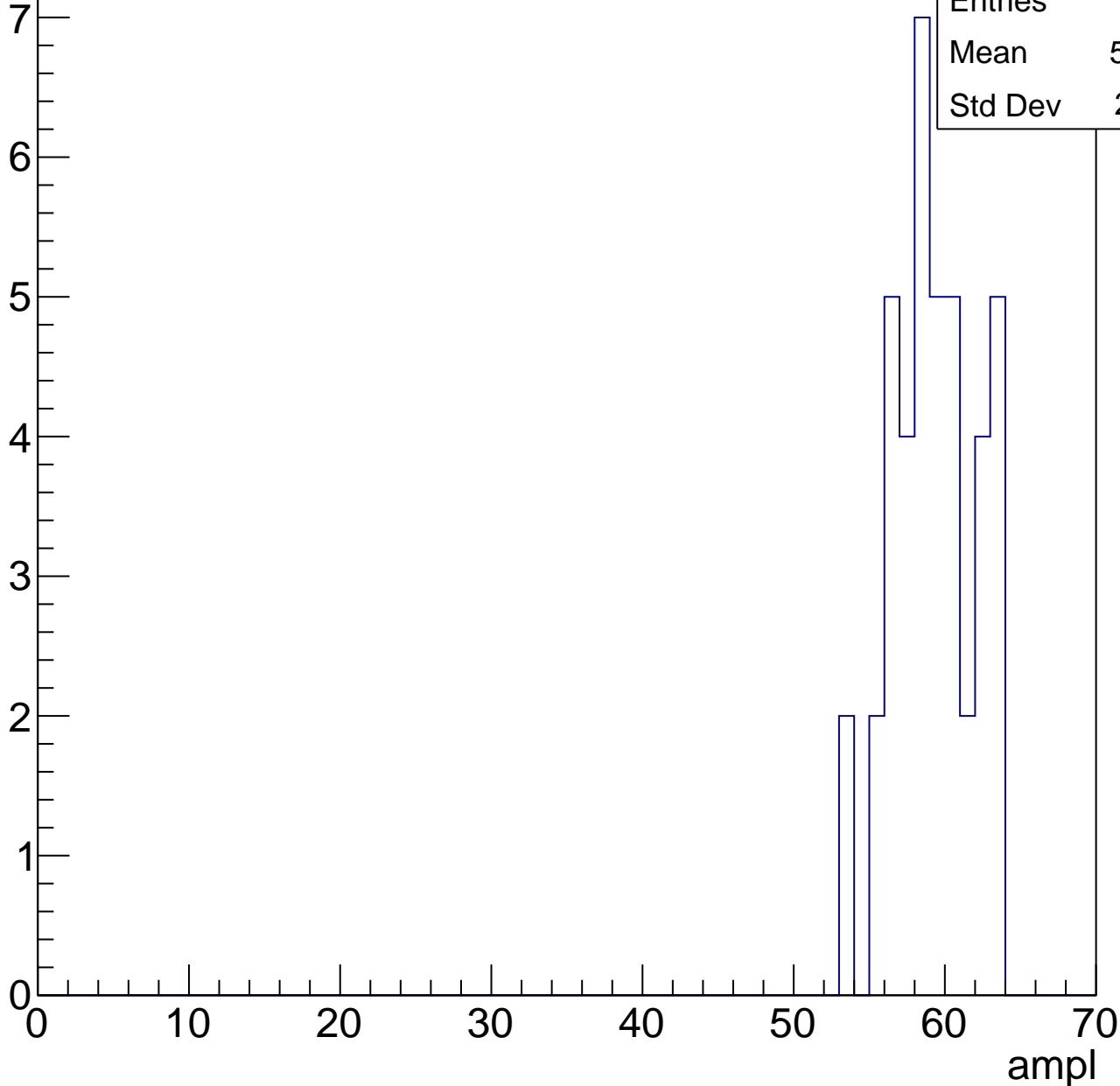


B1L102S, U11-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.78
Std Dev	2.701

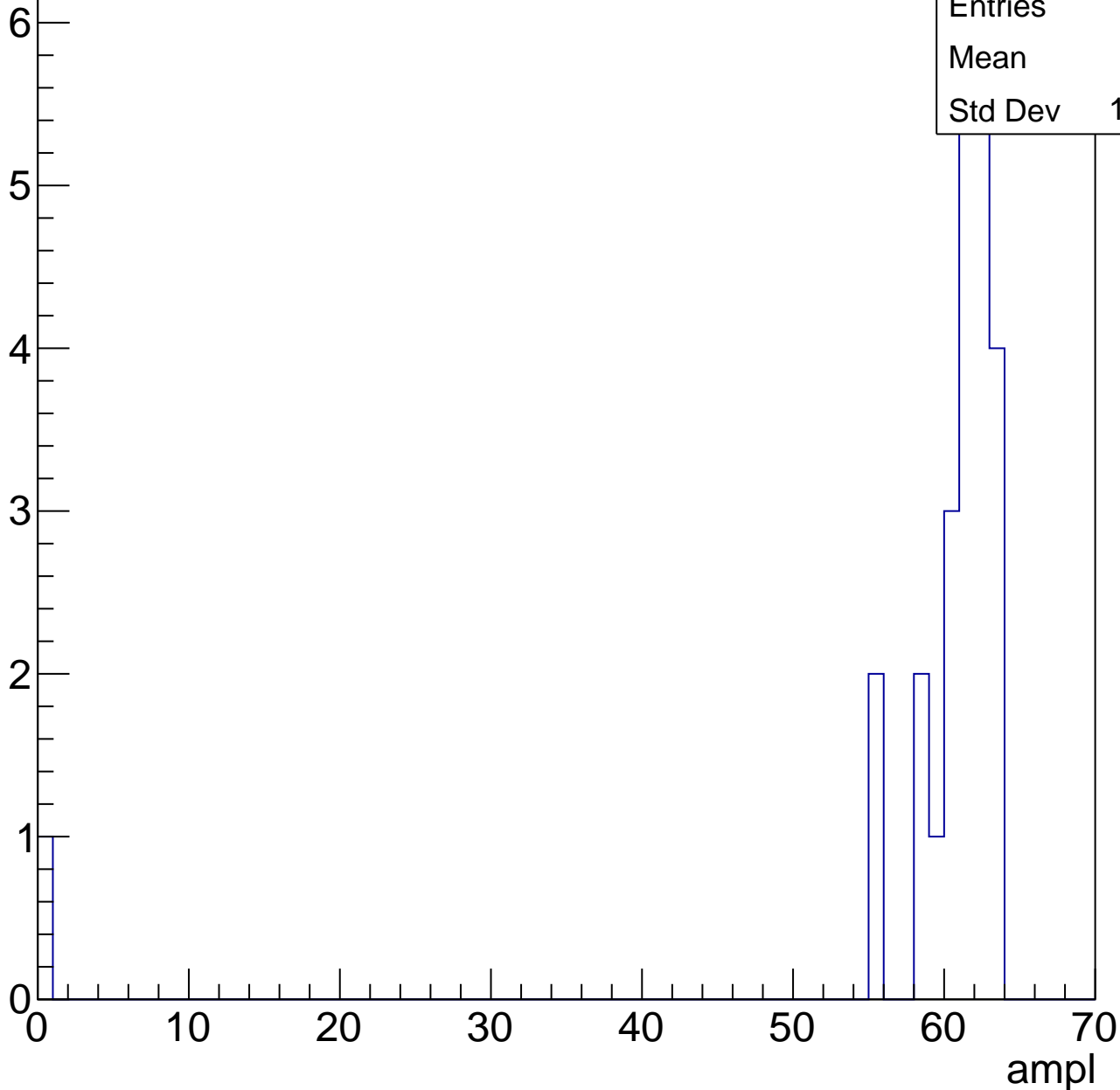


B1L102S, U11-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.2
Std Dev	12.07



B1L102S, U11-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	16
Std Dev	0

ampl

B1L102S, U11-ch2, adc0

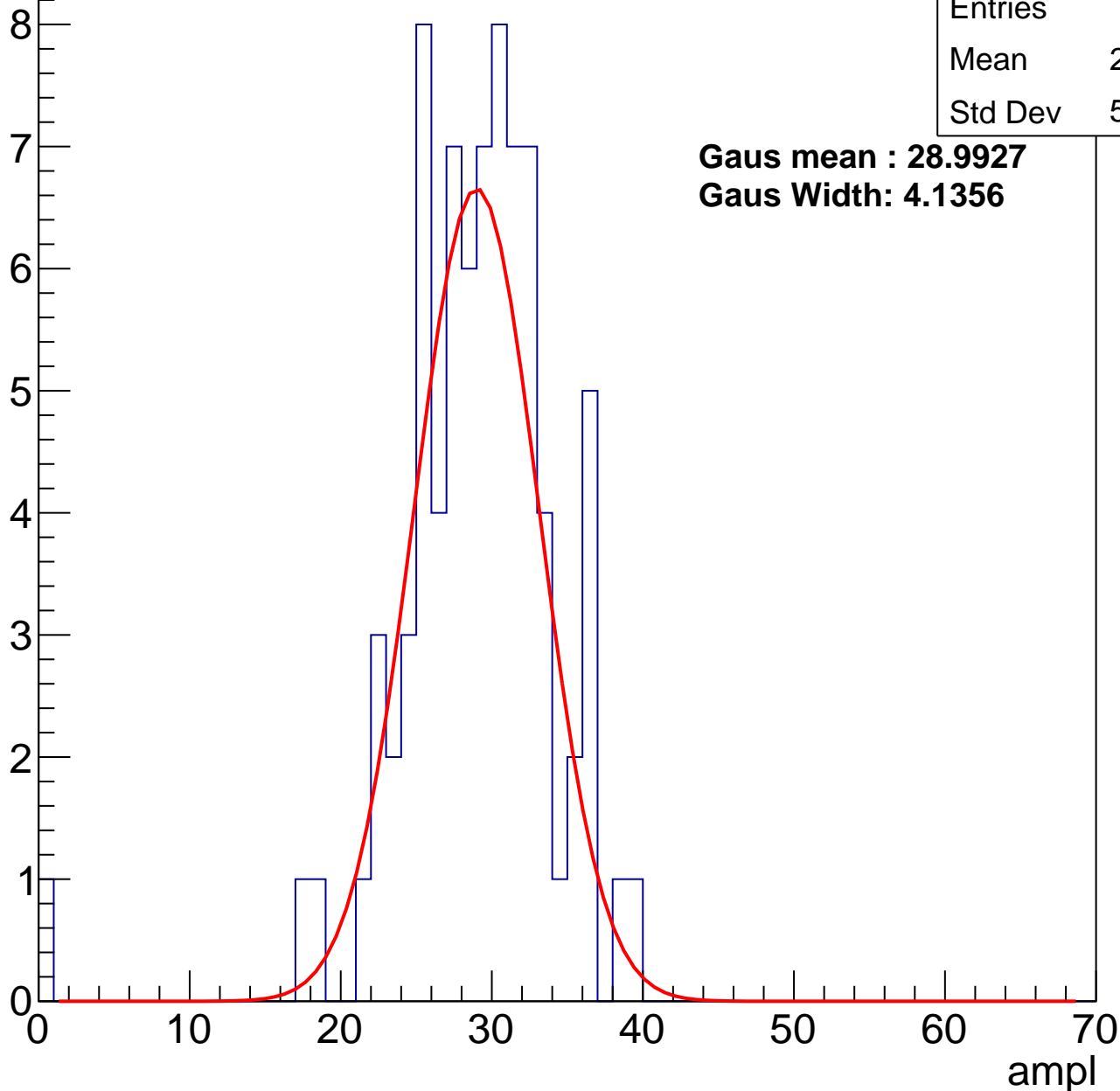
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.48
Std Dev	5.392

Gaus mean : 28.9927

Gaus Width: 4.1356



B1L102S, U11-ch2, adc1

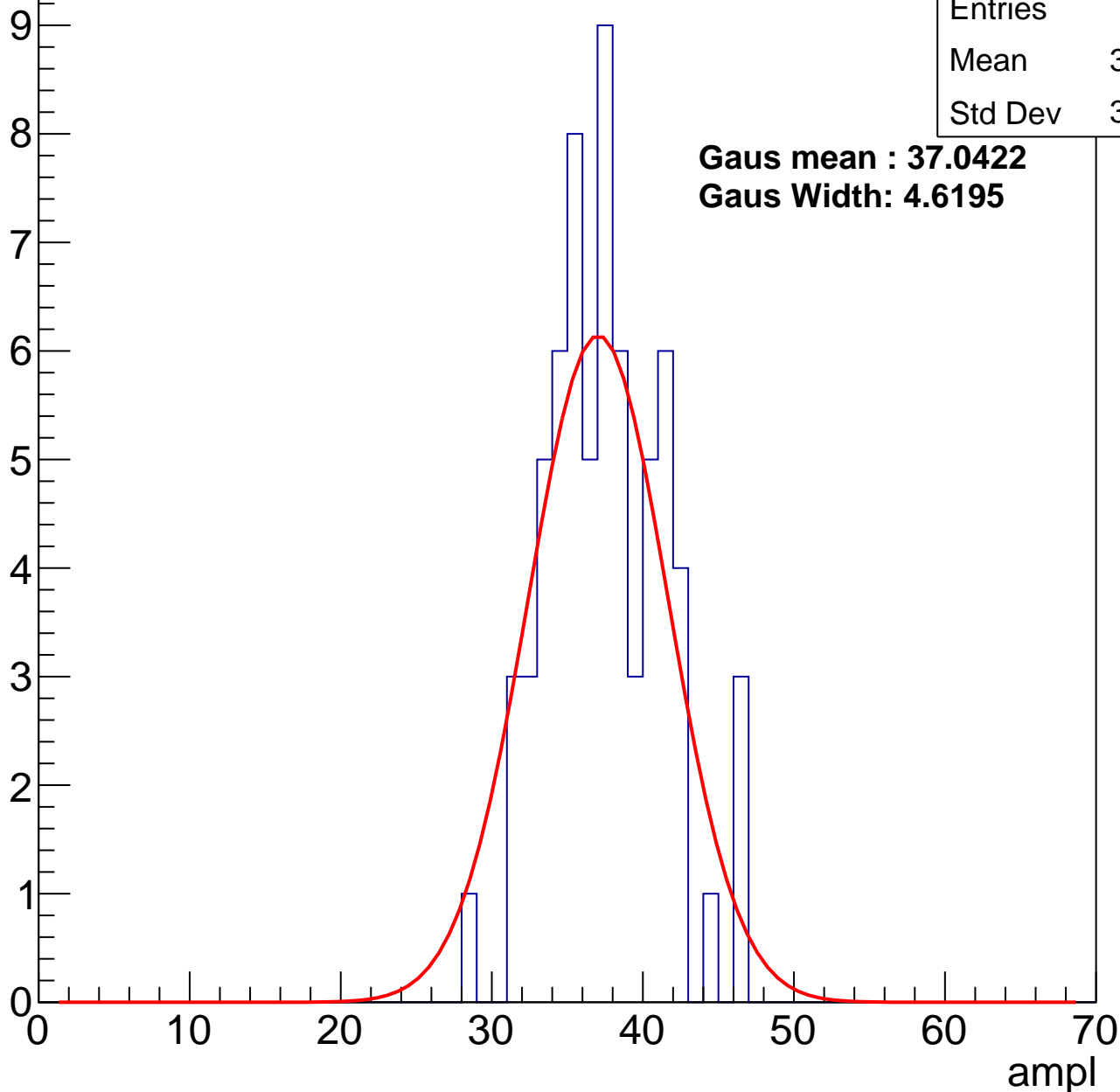
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.06
Std Dev	3.807

Gaus mean : 37.0422

Gaus Width: 4.6195



B1L102S, U11-ch2, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	50
Mean	42.02
Std Dev	3.614

Gaus mean : 42.6033

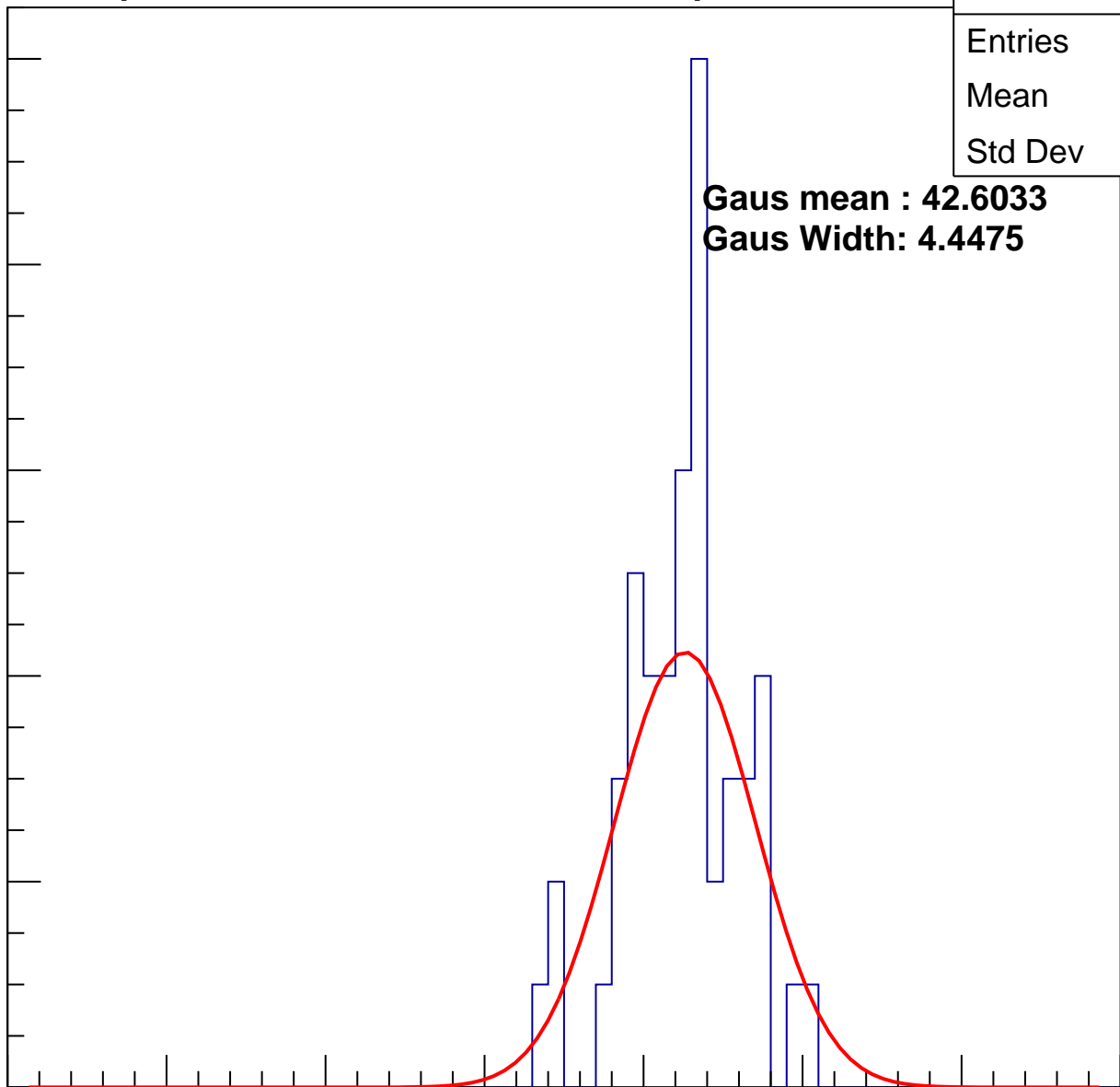
Gaus Width: 4.4475

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

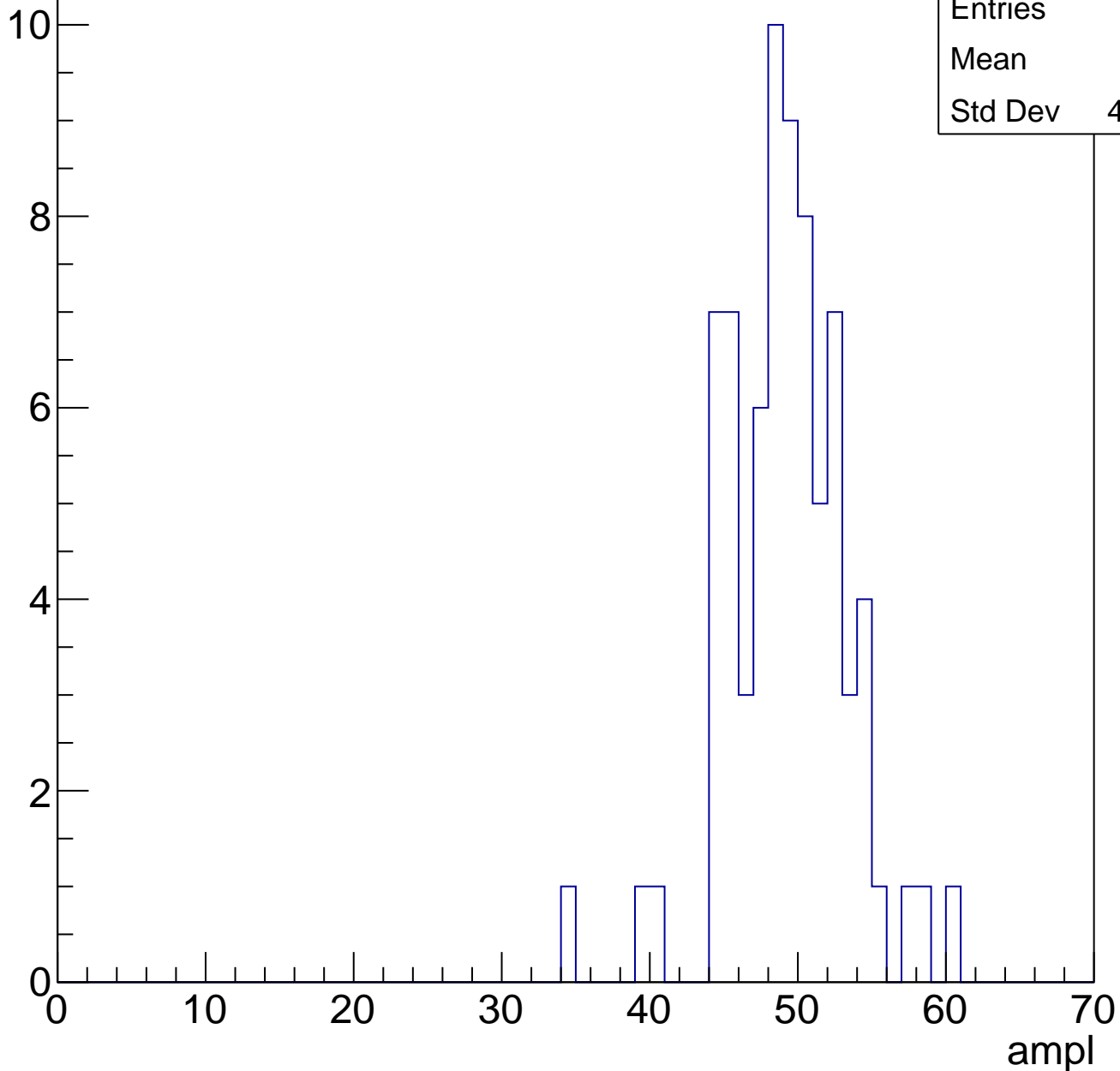


B1L102S, U11-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	48.7
Std Dev	4.117

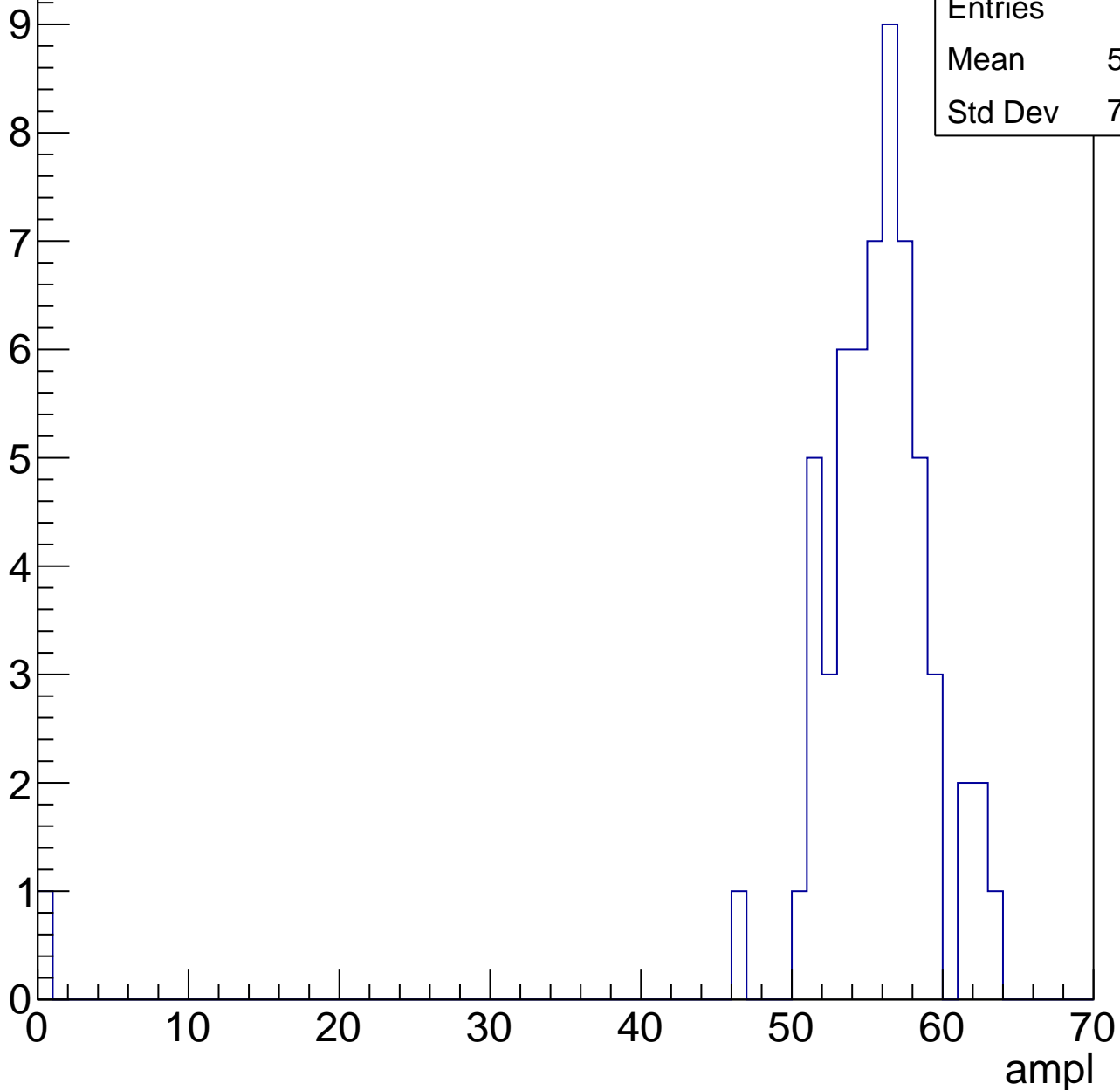


B1L102S, U11-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.46
Std Dev	7.827



B1L102S, U11-ch2, adc5

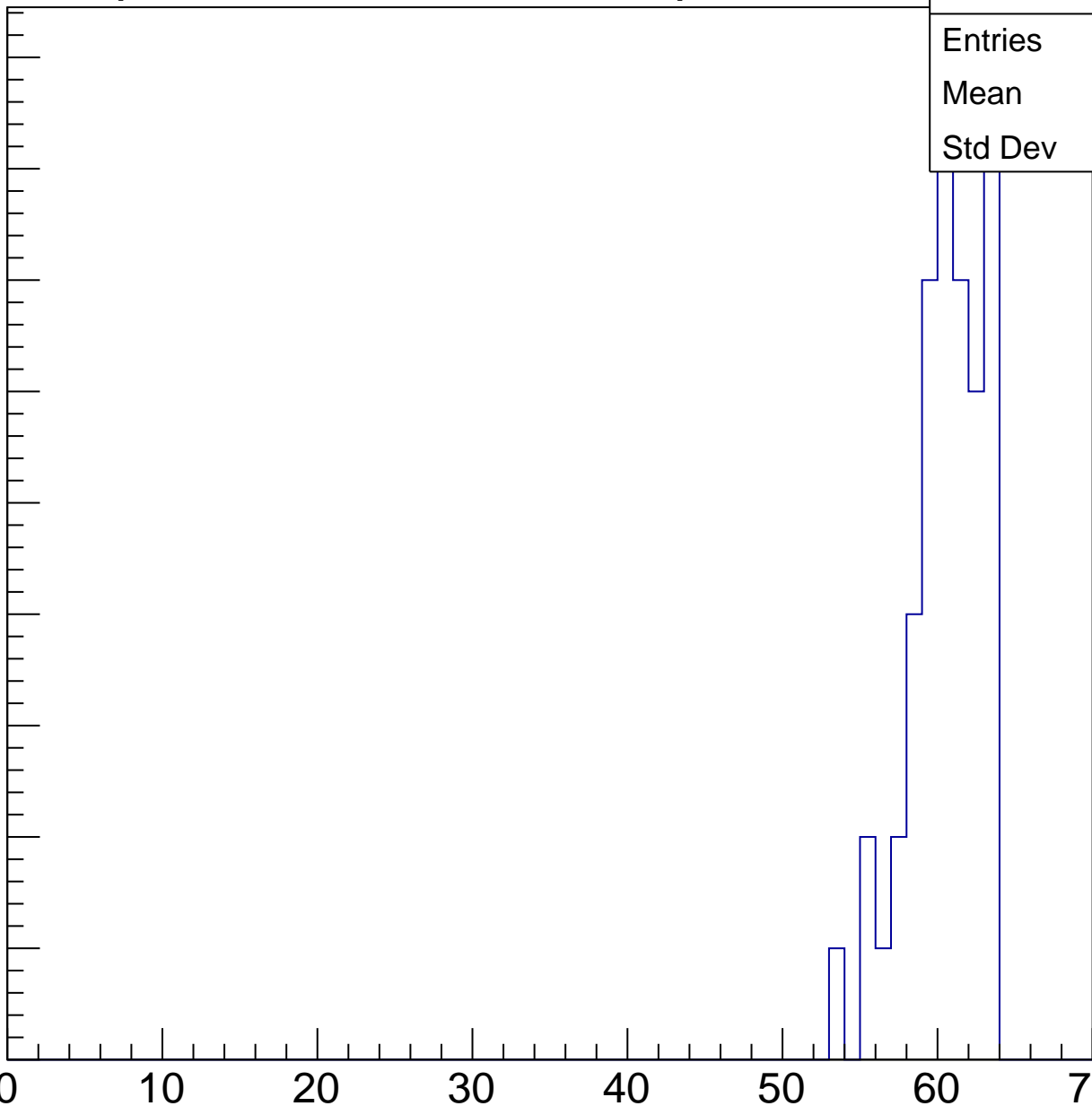
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.02
Std Dev	2.347

ampl



B1L102S, U11-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U11-ch3, adc0

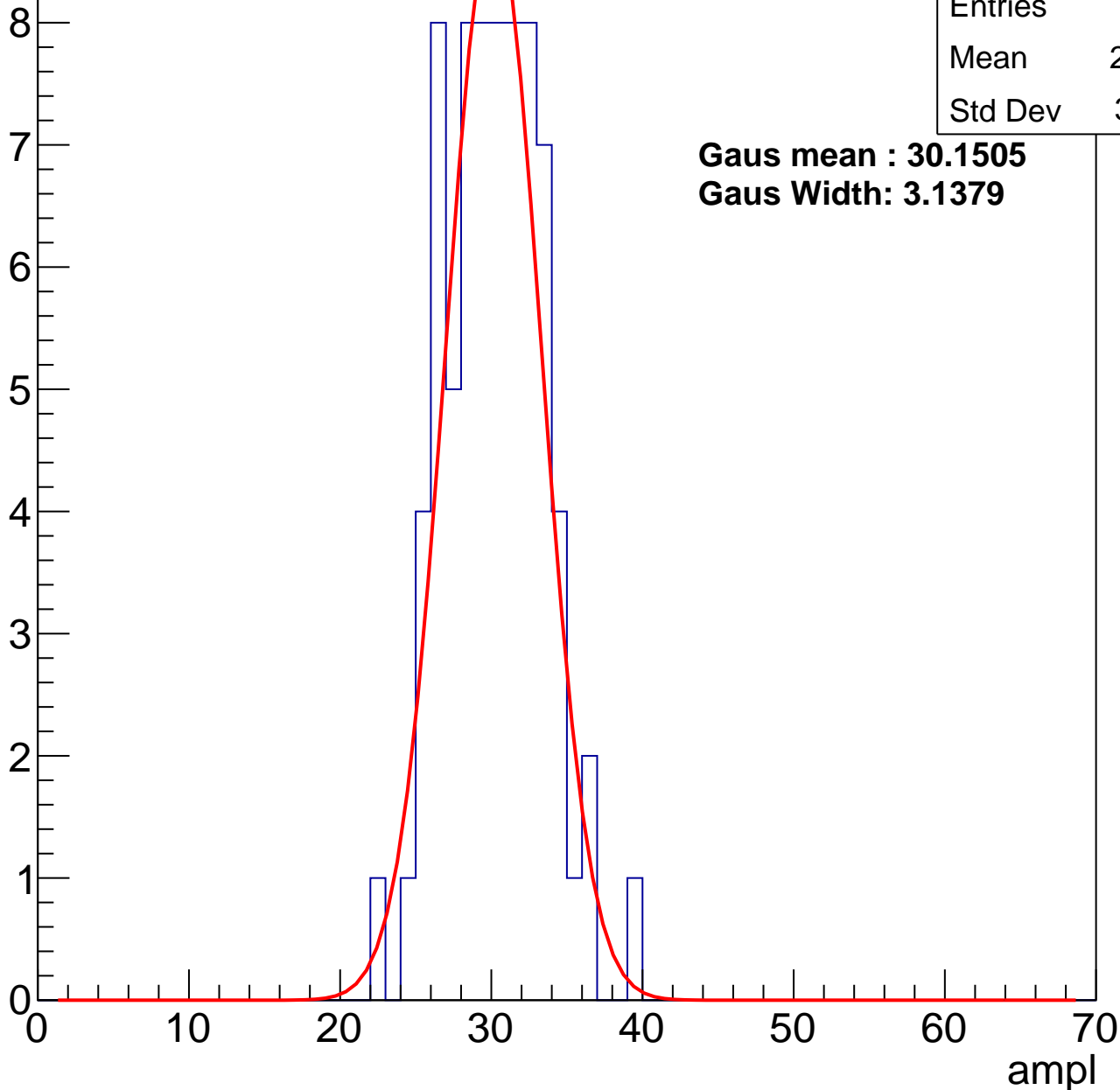
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.76
Std Dev	3.191

Gaus mean : 30.1505

Gaus Width: 3.1379



B1L102S, U11-ch3, adc1

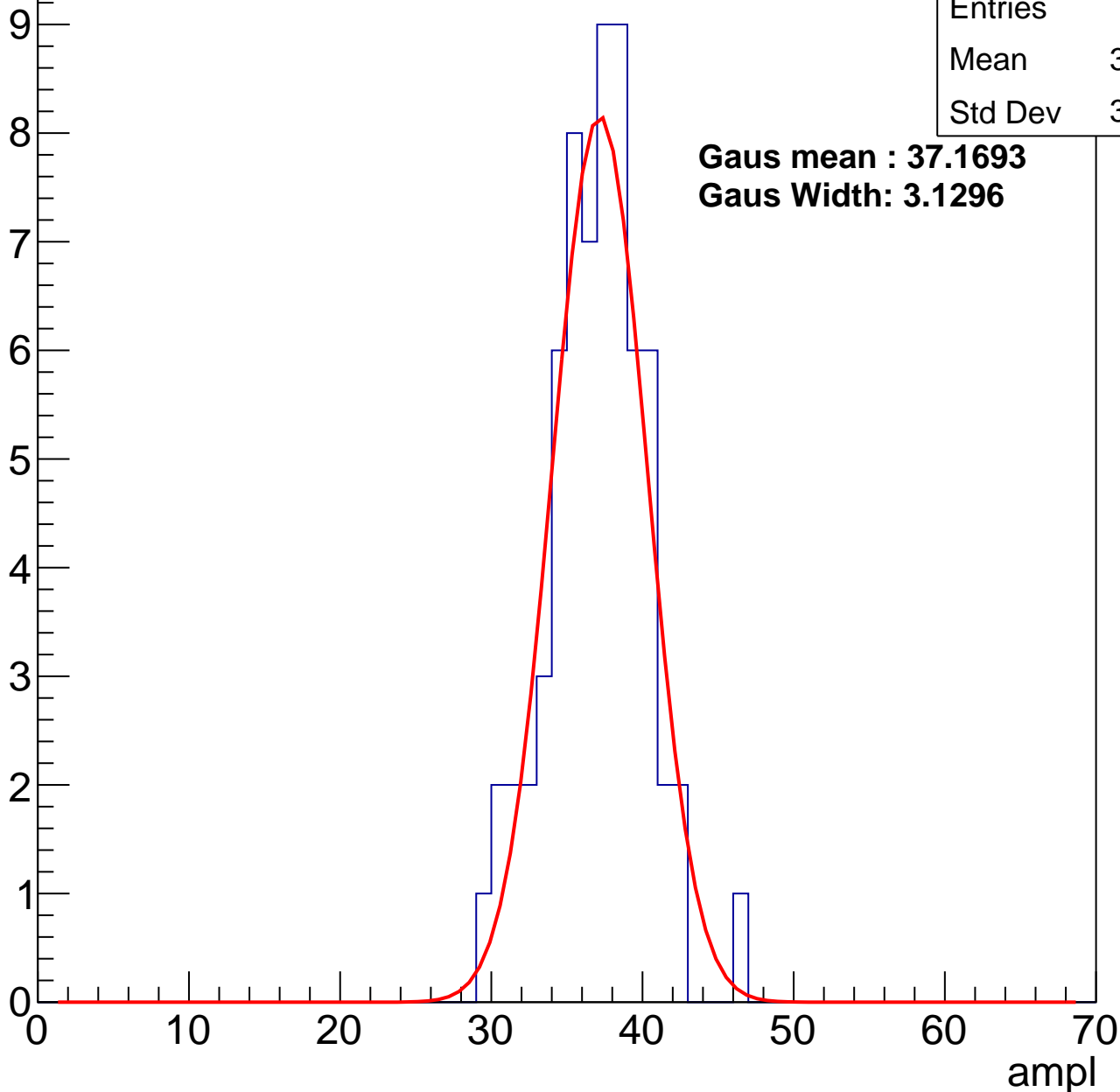
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.53
Std Dev	3.163

Gaus mean : 37.1693

Gaus Width: 3.1296



B1L102S, U11-ch3, adc2

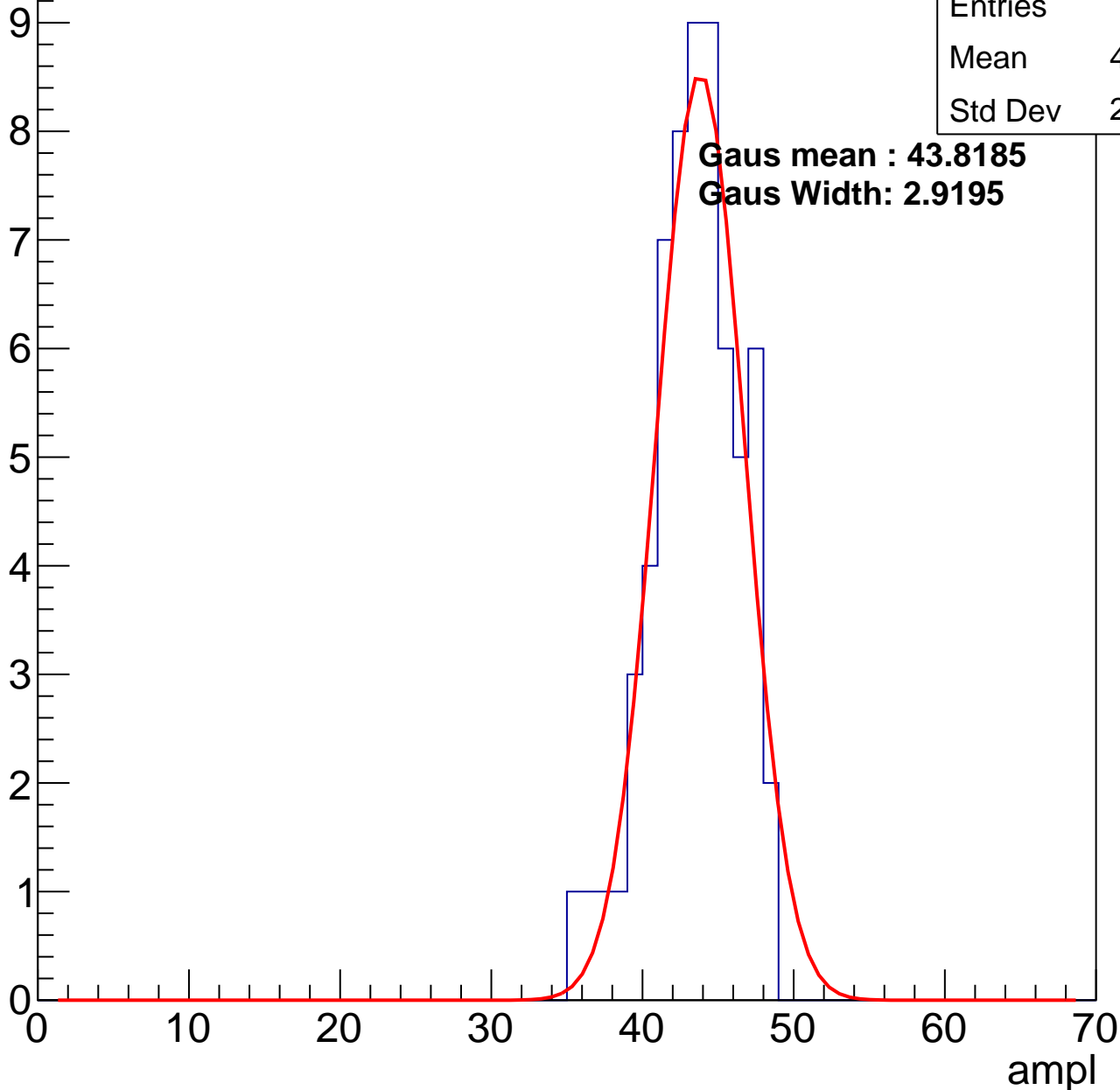
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.97
Std Dev	2.867

Gaus mean : 43.8185

Gaus Width: 2.9195

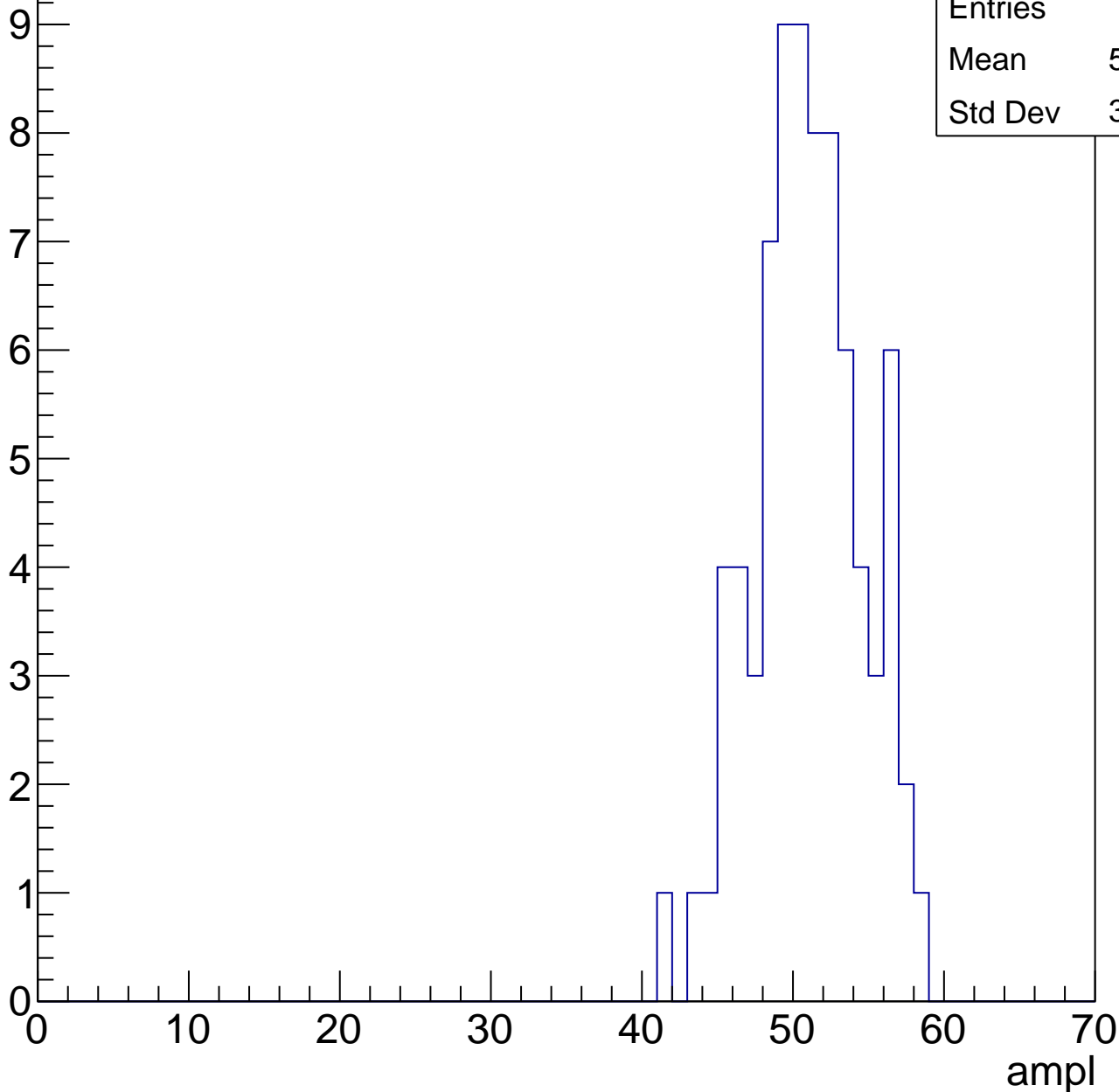


B1L102S, U11-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	50.53
Std Dev	3.573

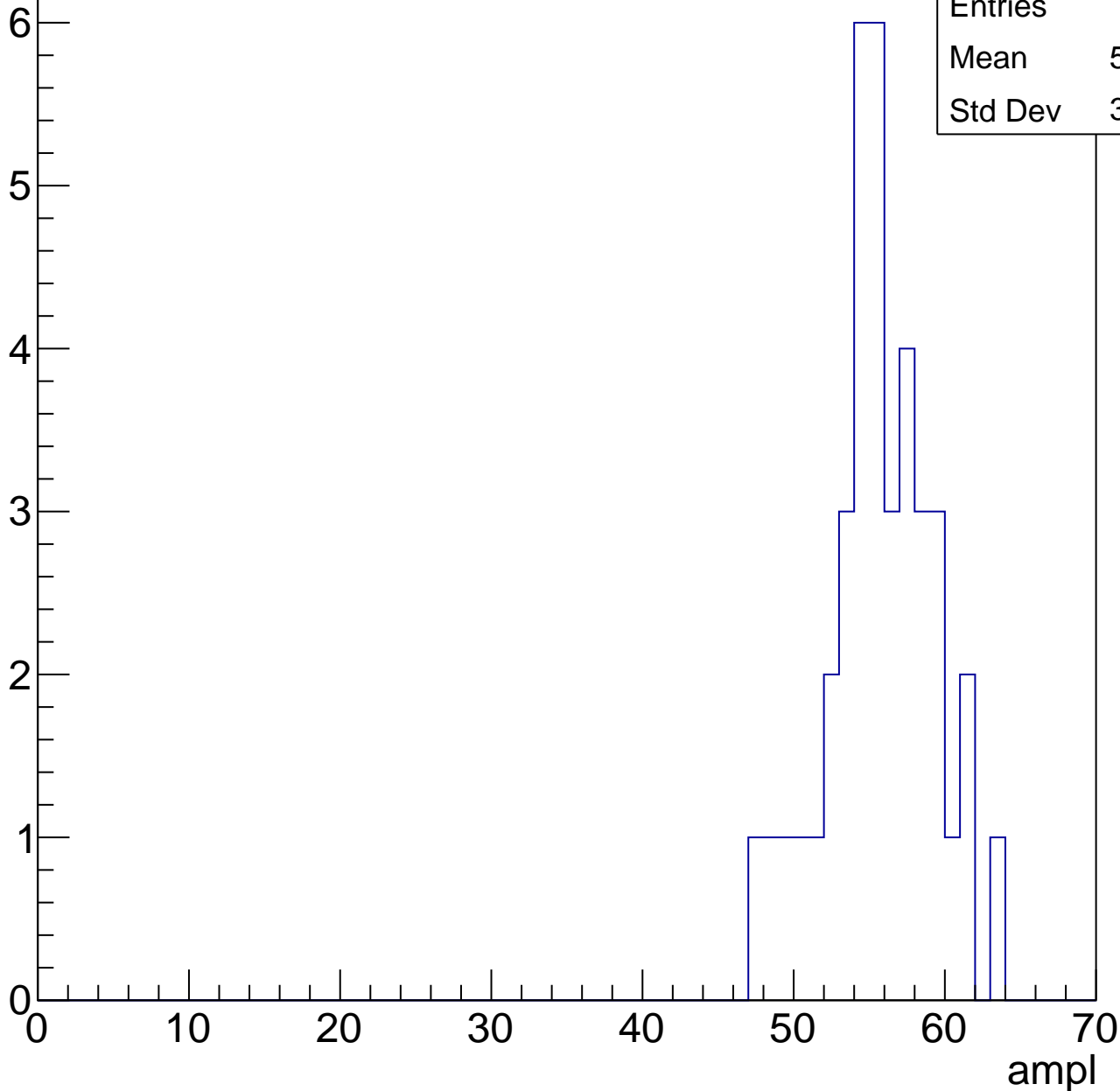


B1L102S, U11-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

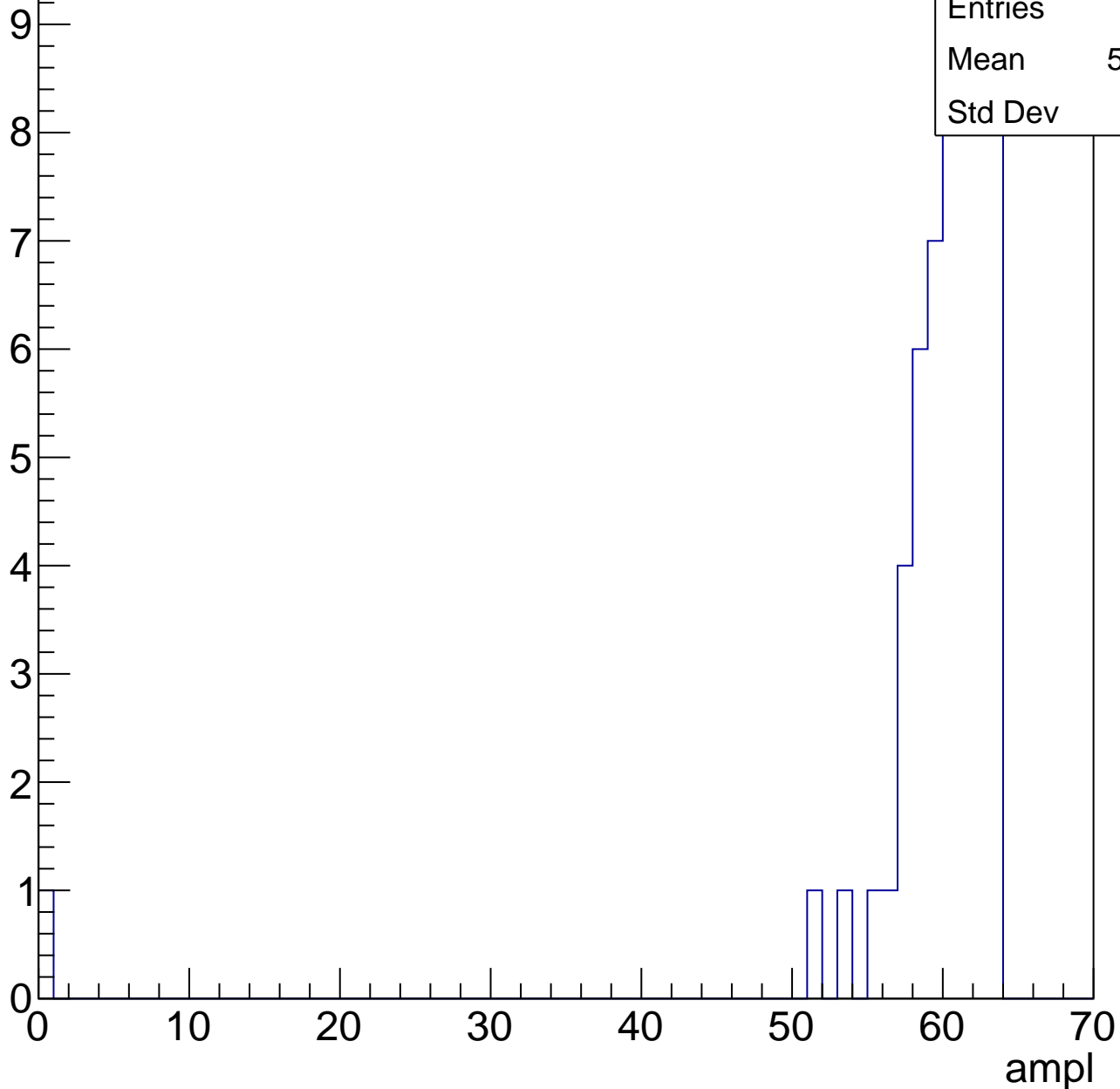
Entries	39
Mean	55.23
Std Dev	3.504



B1L102S, U11-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch4, adc0

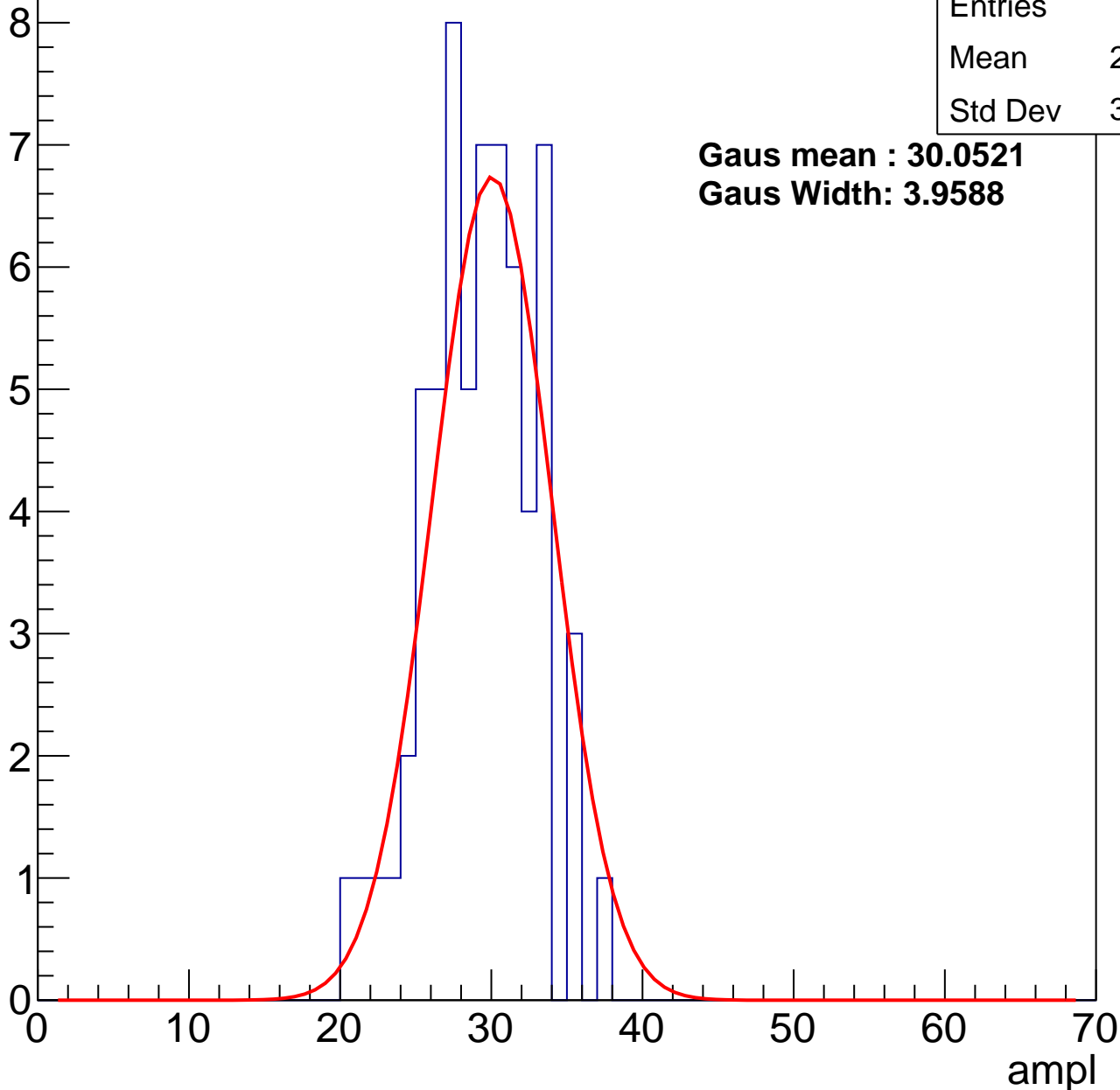
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.83
Std Dev	3.516

Gaus mean : 30.0521

Gaus Width: 3.9588

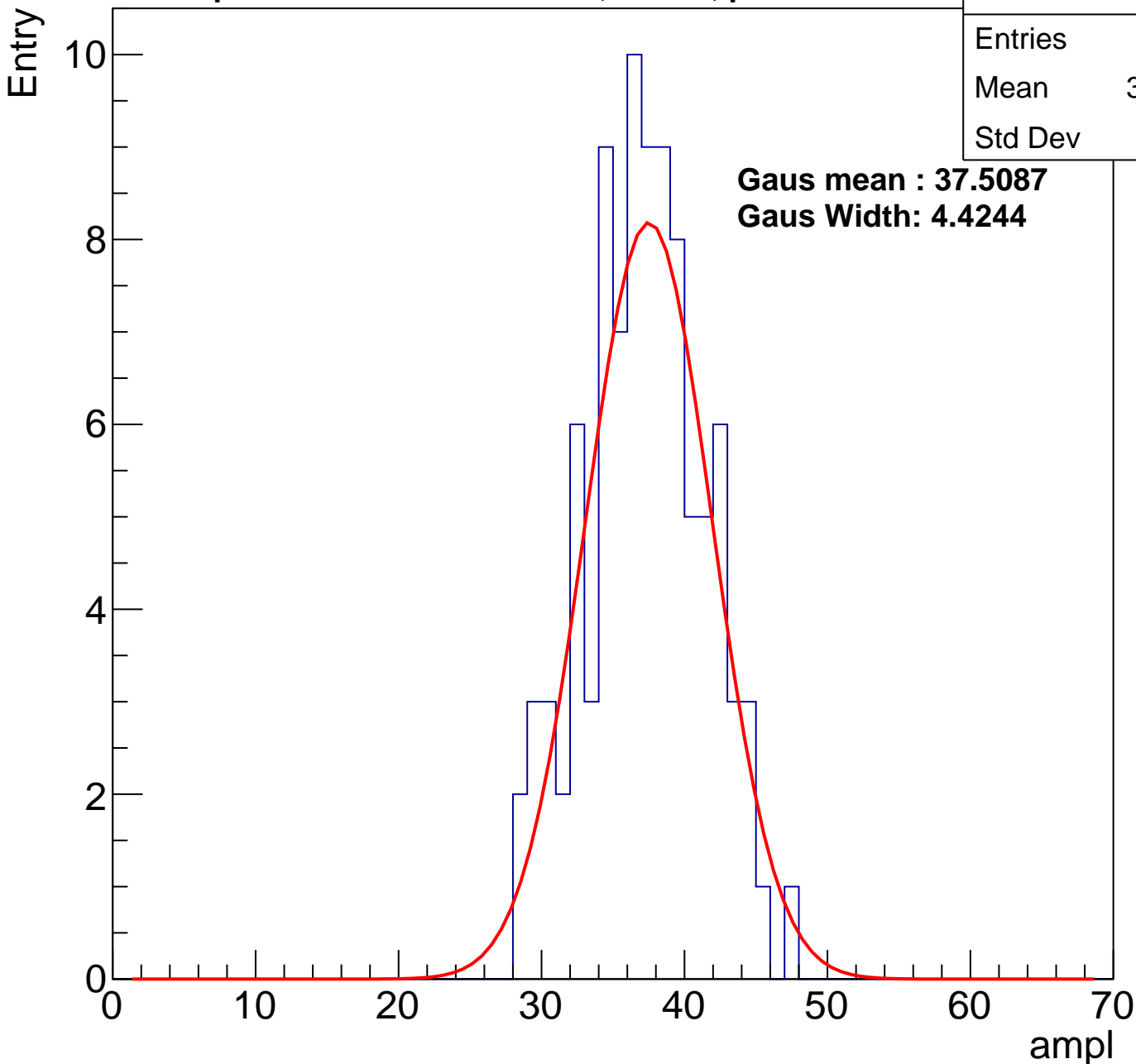


B1L102S, U11-ch4, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	95
Mean	36.78
Std Dev	4.13

Gaus mean : 37.5087
Gaus Width: 4.4244



B1L102S, U11-ch4, adc2

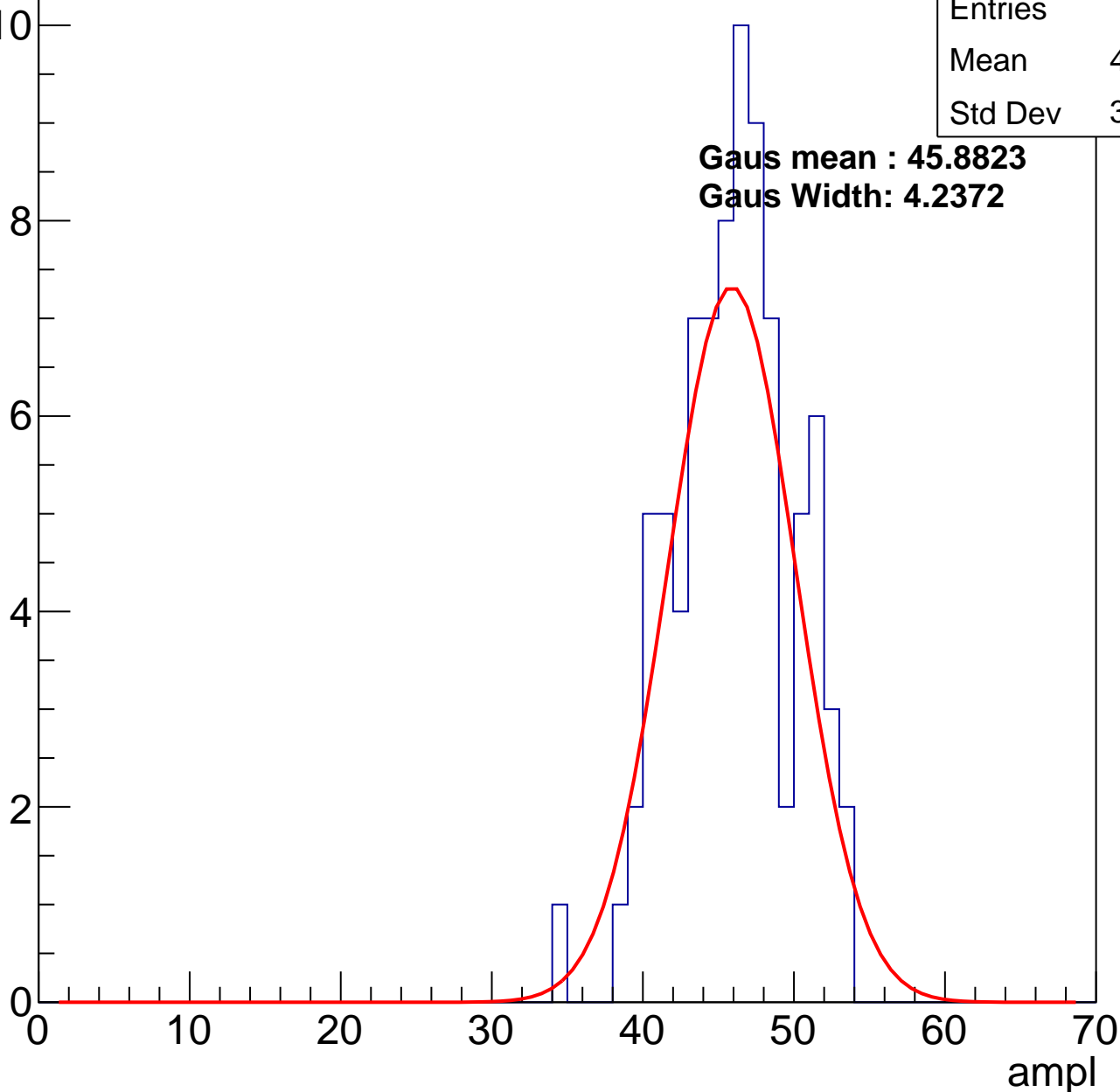
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	45.56
Std Dev	3.856

Gaus mean : 45.8823

Gaus Width: 4.2372

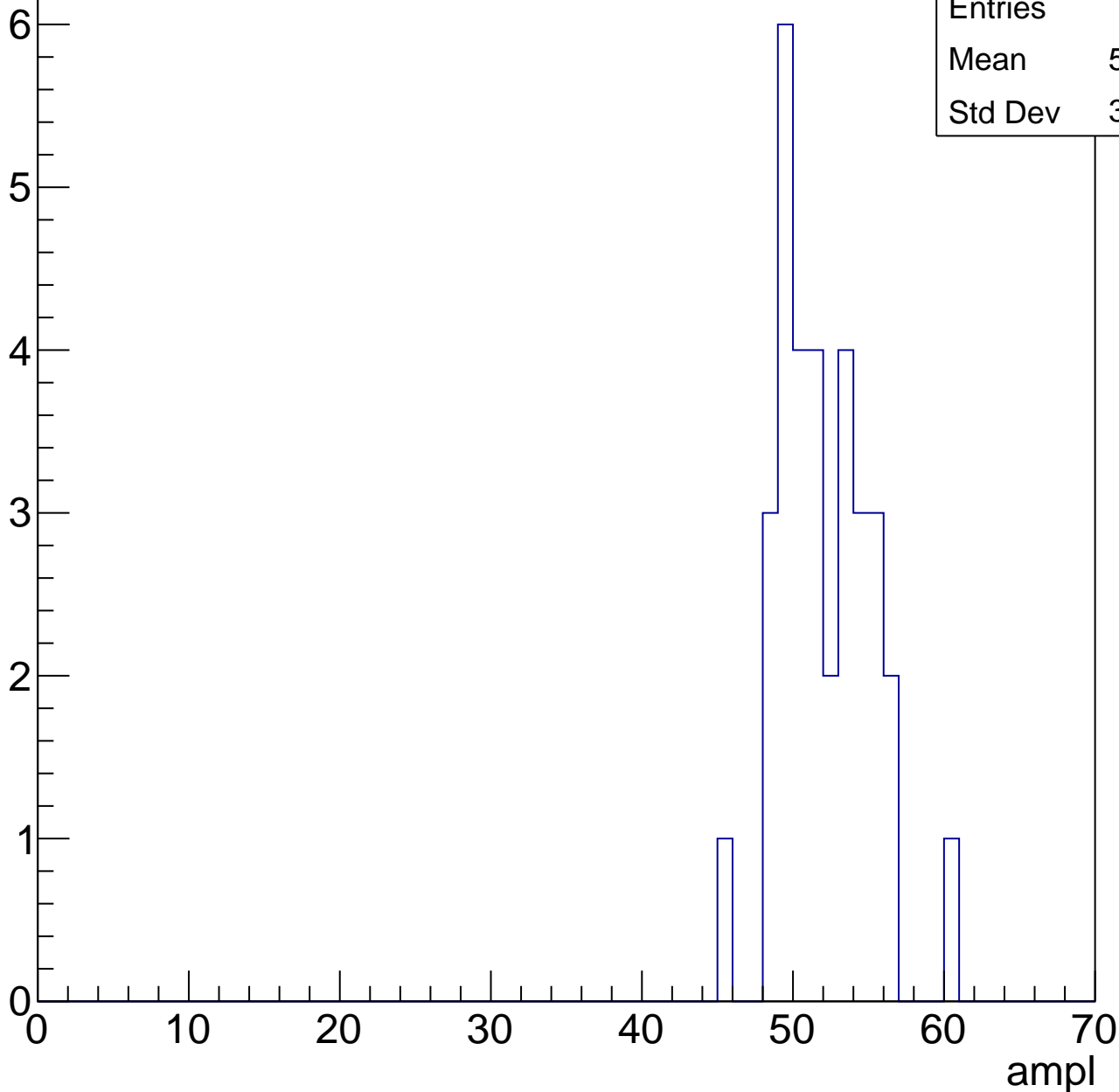


B1L102S, U11-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	51.58
Std Dev	3.035

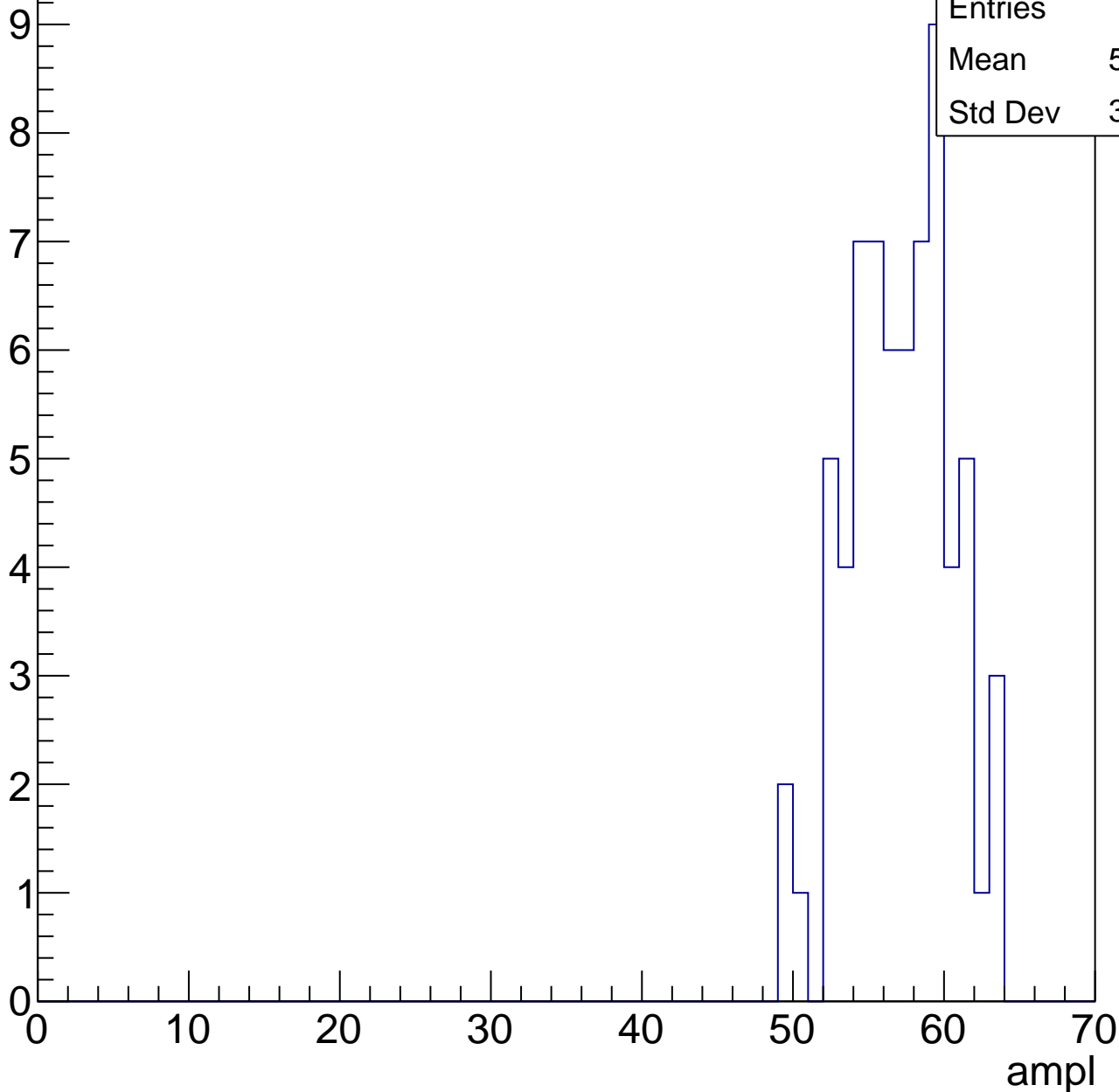


B1L102S, U11-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	56.63
Std Dev	3.336

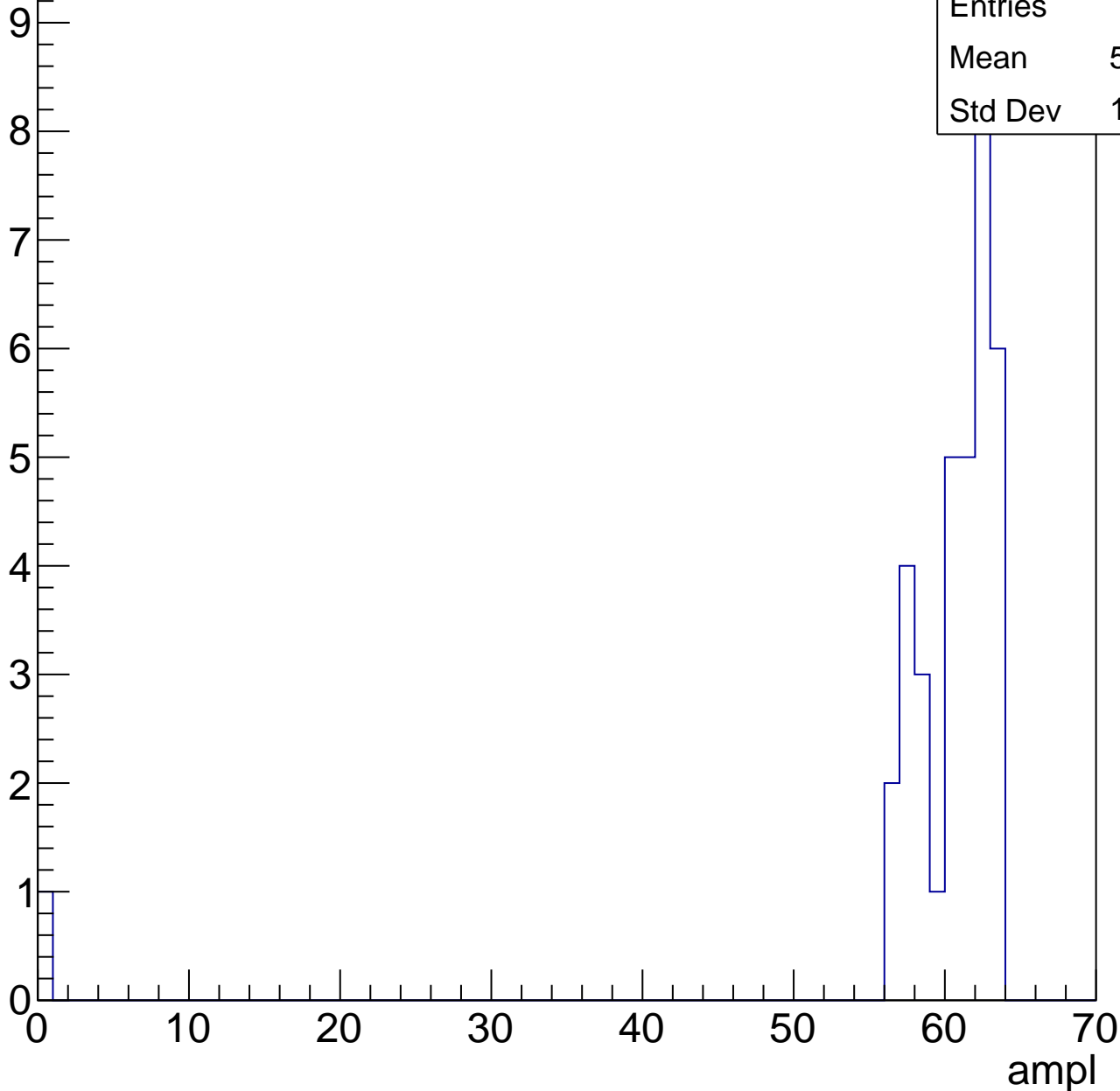


B1L102S, U11-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.72
Std Dev	10.16



B1L102S, U11-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

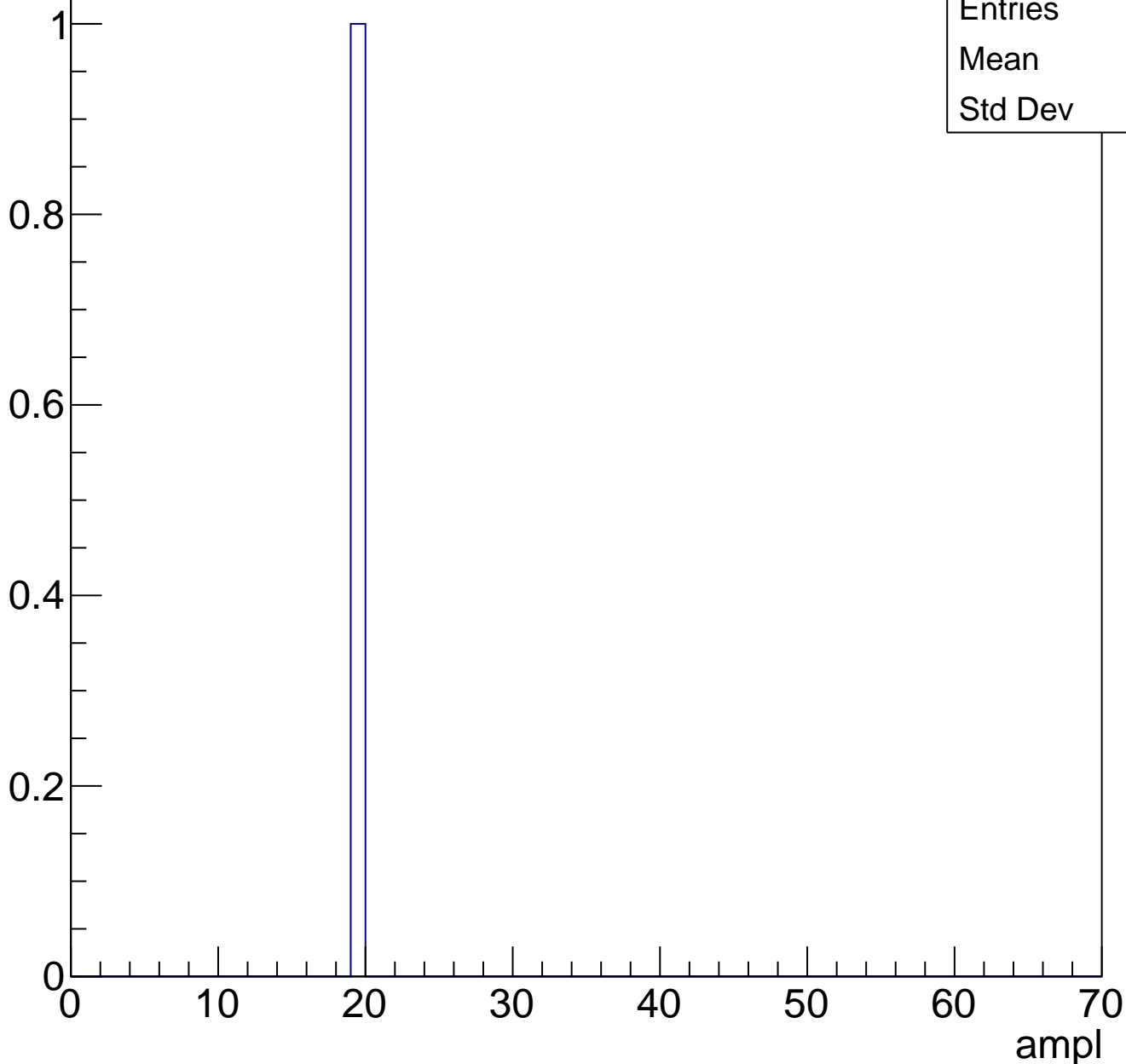
Entries	1
Mean	63
Std Dev	0

ampl

B1L102S, U11-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch5, adc0

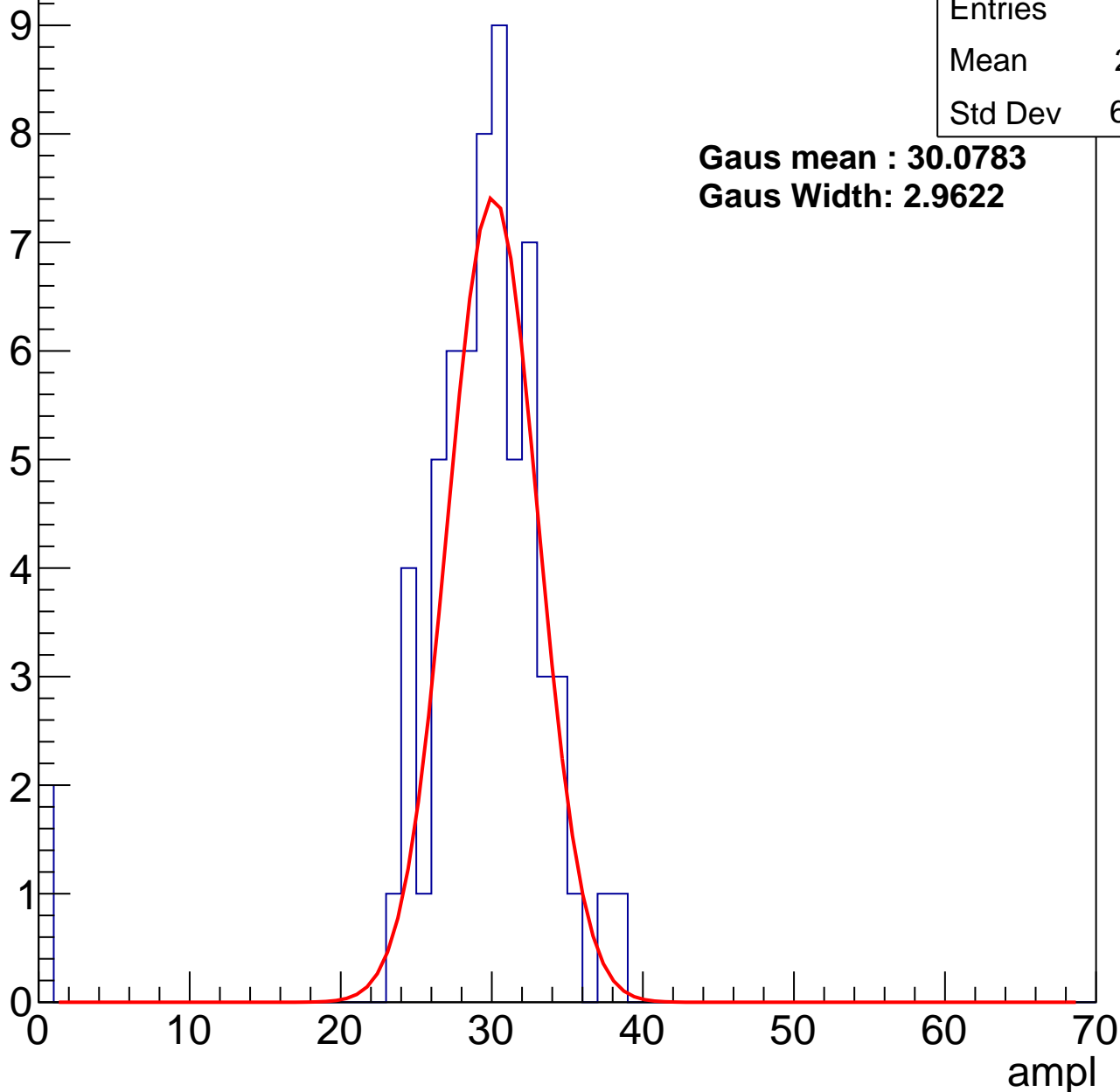
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.51
Std Dev	6.029

Gaus mean : 30.0783

Gaus Width: 2.9622



B1L102S, U11-ch5, adc1

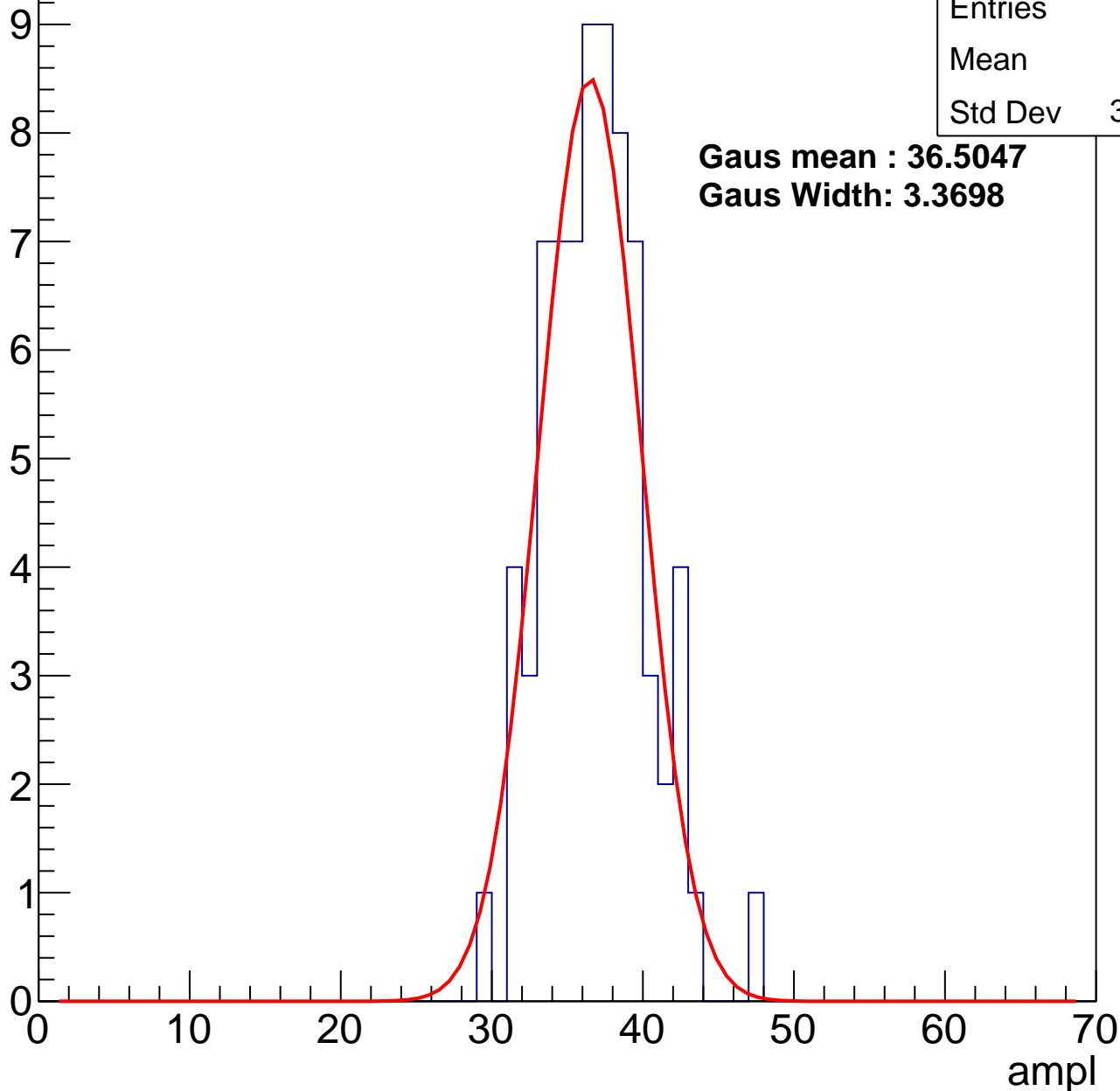
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.4
Std Dev	3.309

Gaus mean : 36.5047

Gaus Width: 3.3698



B1L102S, U11-ch5, adc2

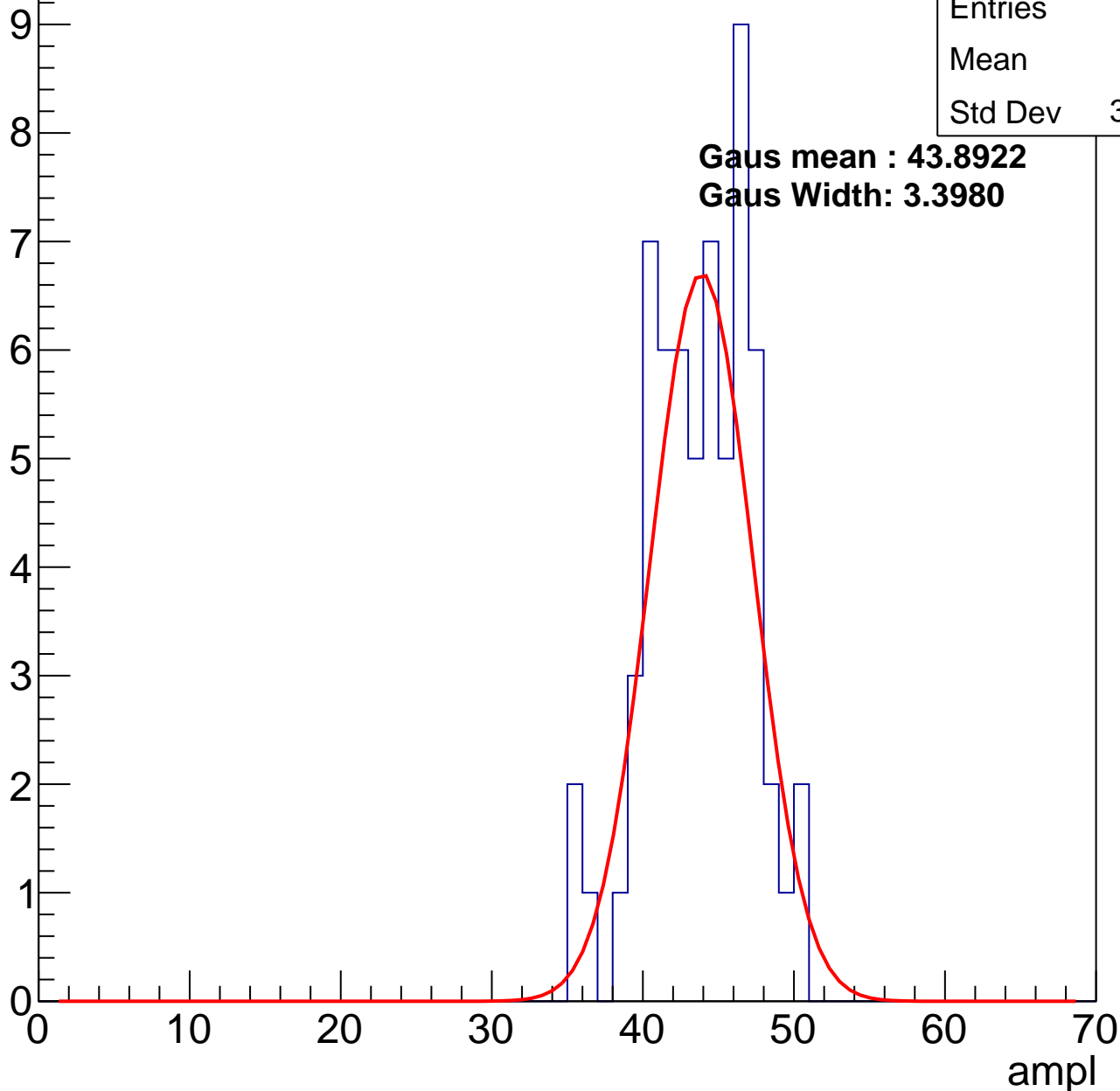
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.3
Std Dev	3.393

Gaus mean : 43.8922

Gaus Width: 3.3980



B1L102S, U11-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

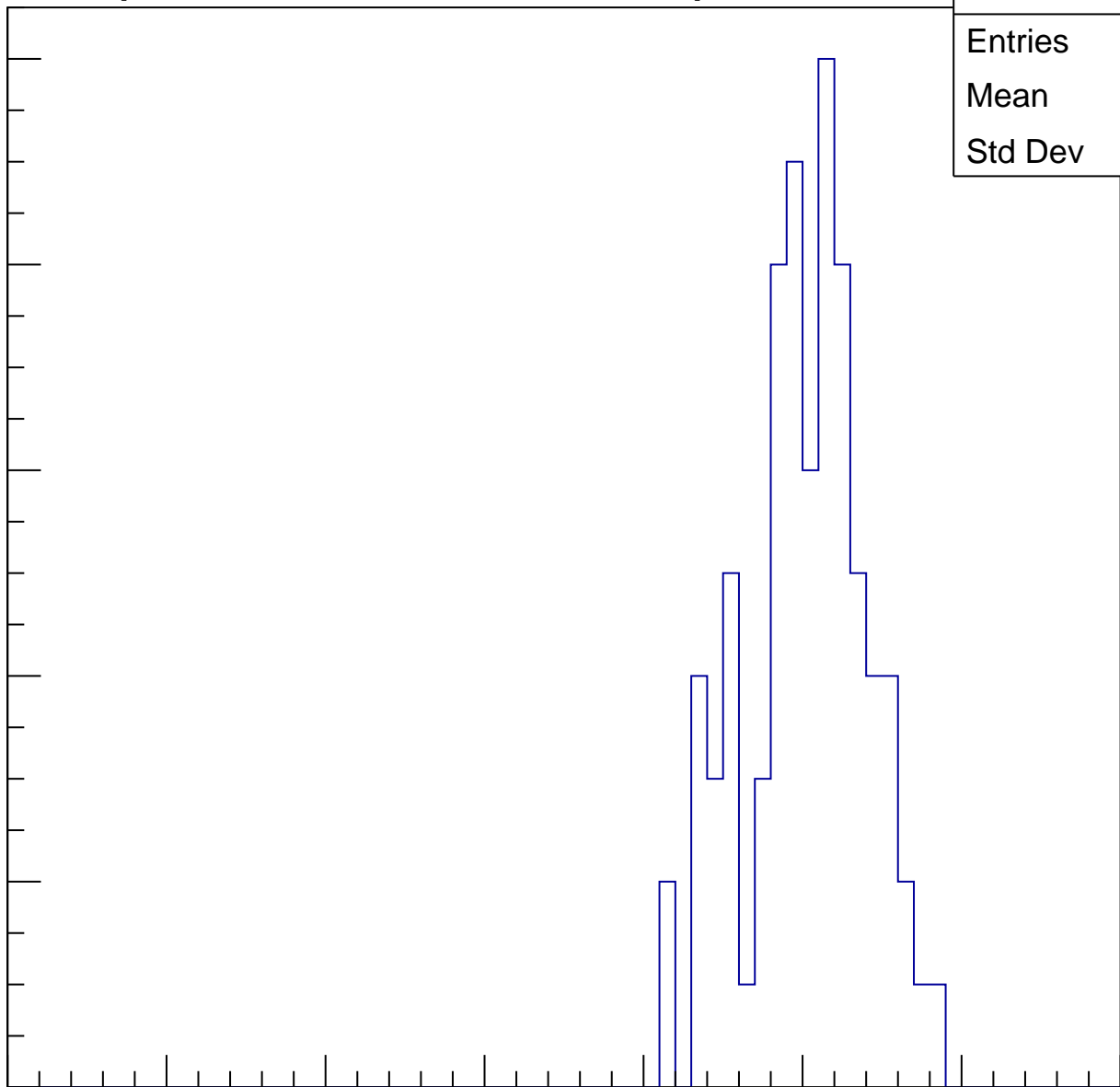
Entries	76
Mean	49.7
Std Dev	3.811

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

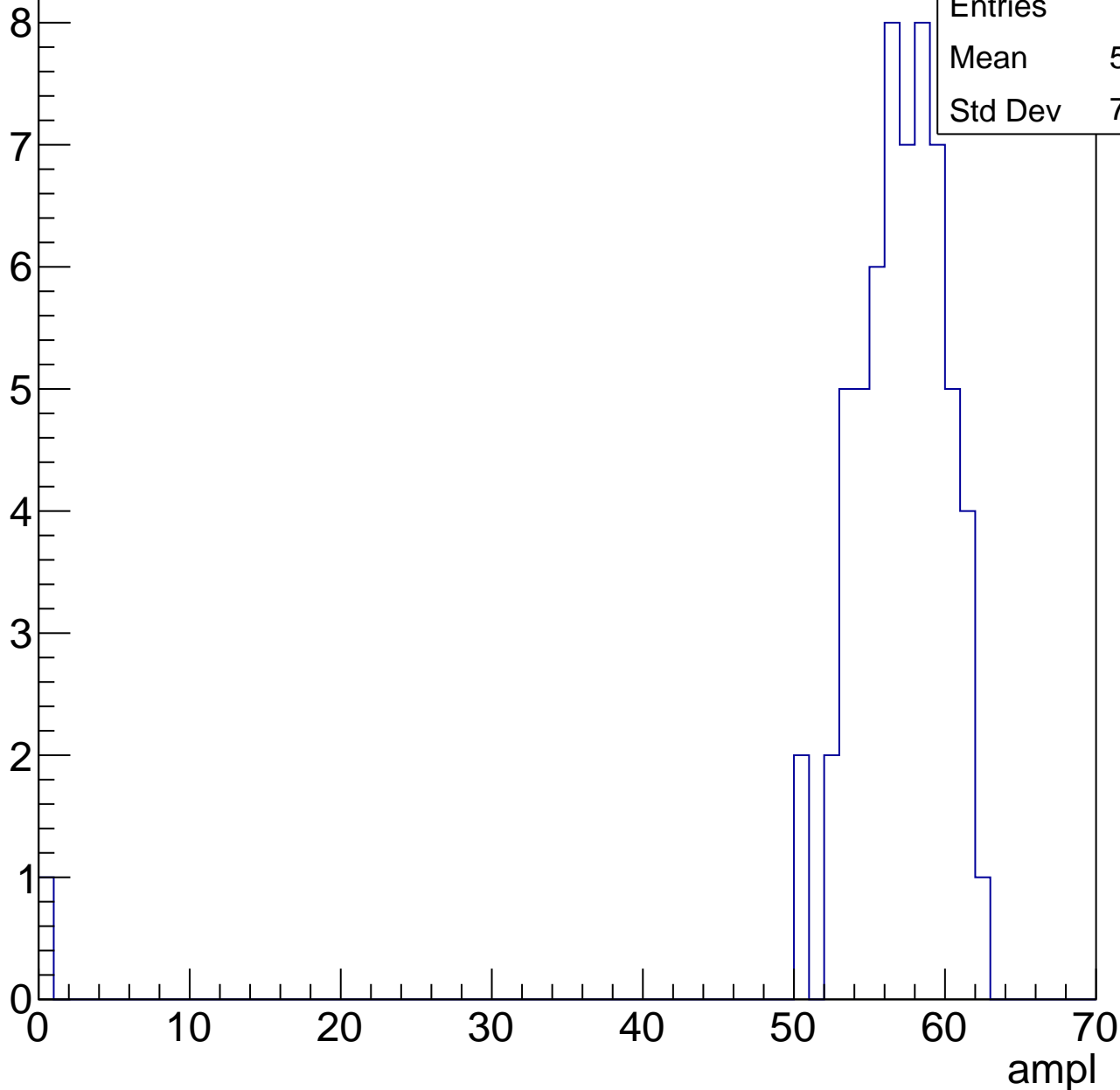
ampl



B1L102S, U11-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

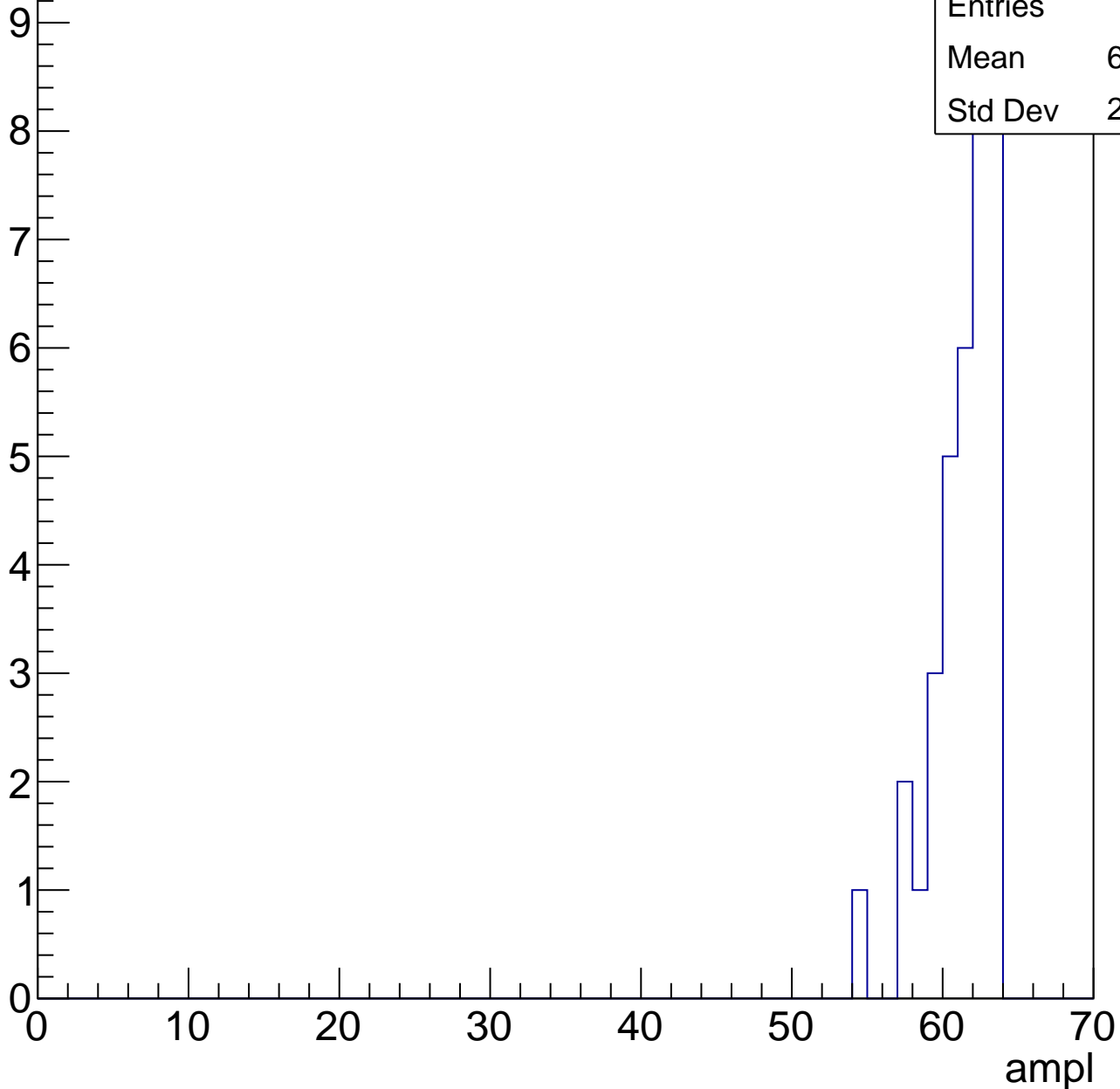


B1L102S, U11-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	60.94
Std Dev	2.054



B1L102S, U11-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch6, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	30.14
Std Dev	5.921

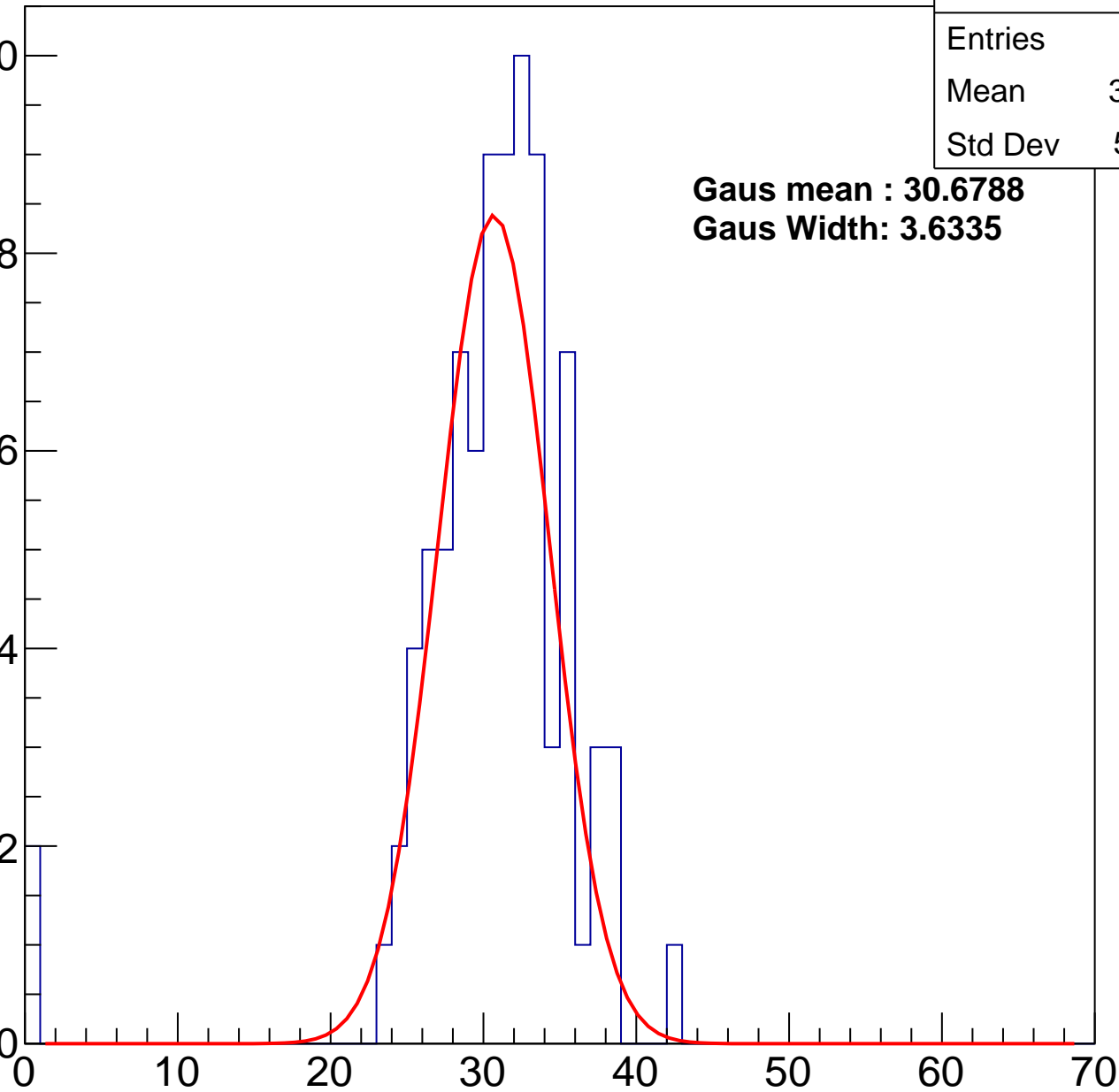
Gaus mean : 30.6788

Gaus Width: 3.6335

Entry

10
8
6
4
2
0

ampl



B1L102S, U11-ch6, adc1

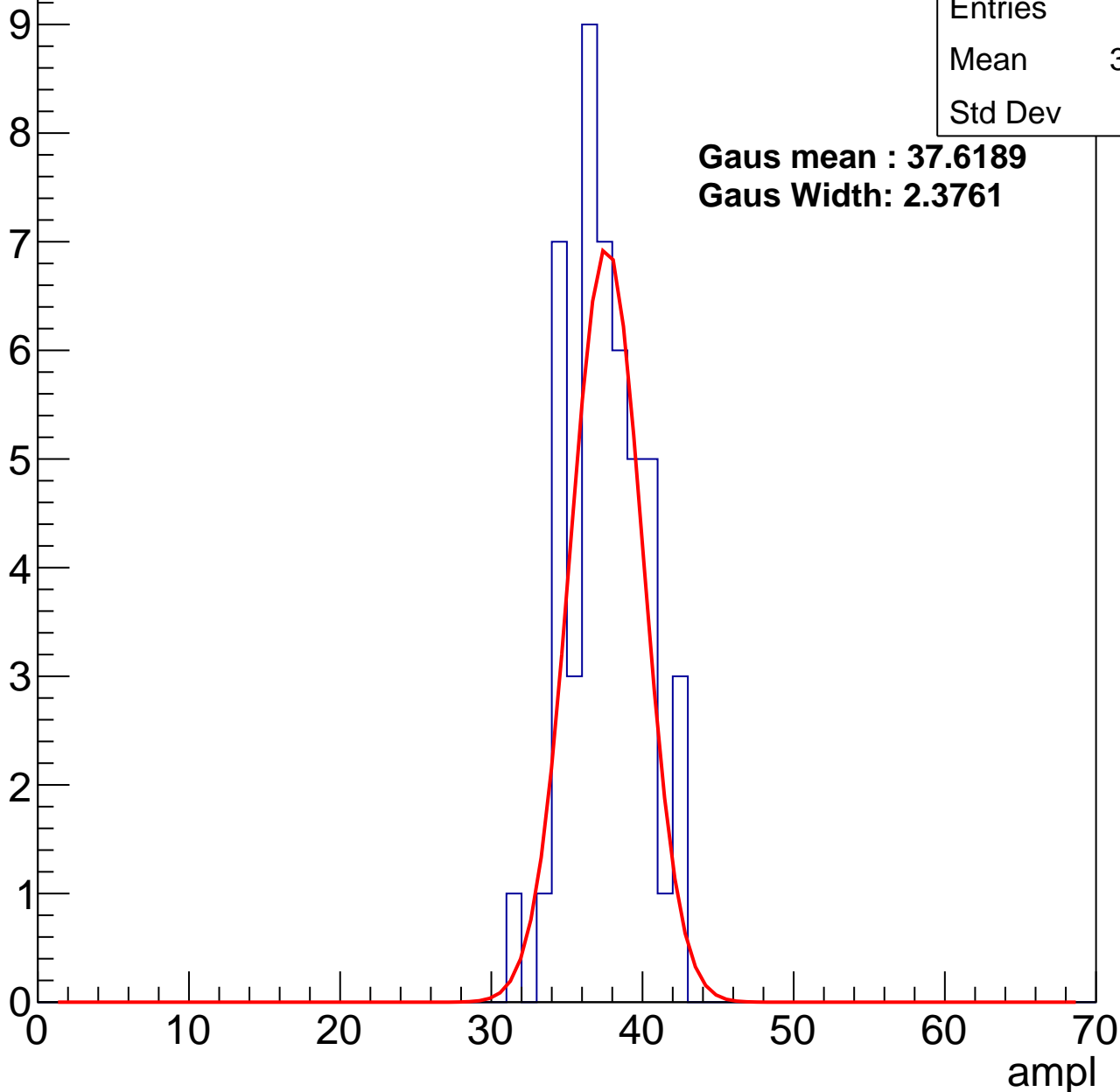
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	37.08
Std Dev	2.49

Gaus mean : 37.6189

Gaus Width: 2.3761



B1L102S, U11-ch6, adc2

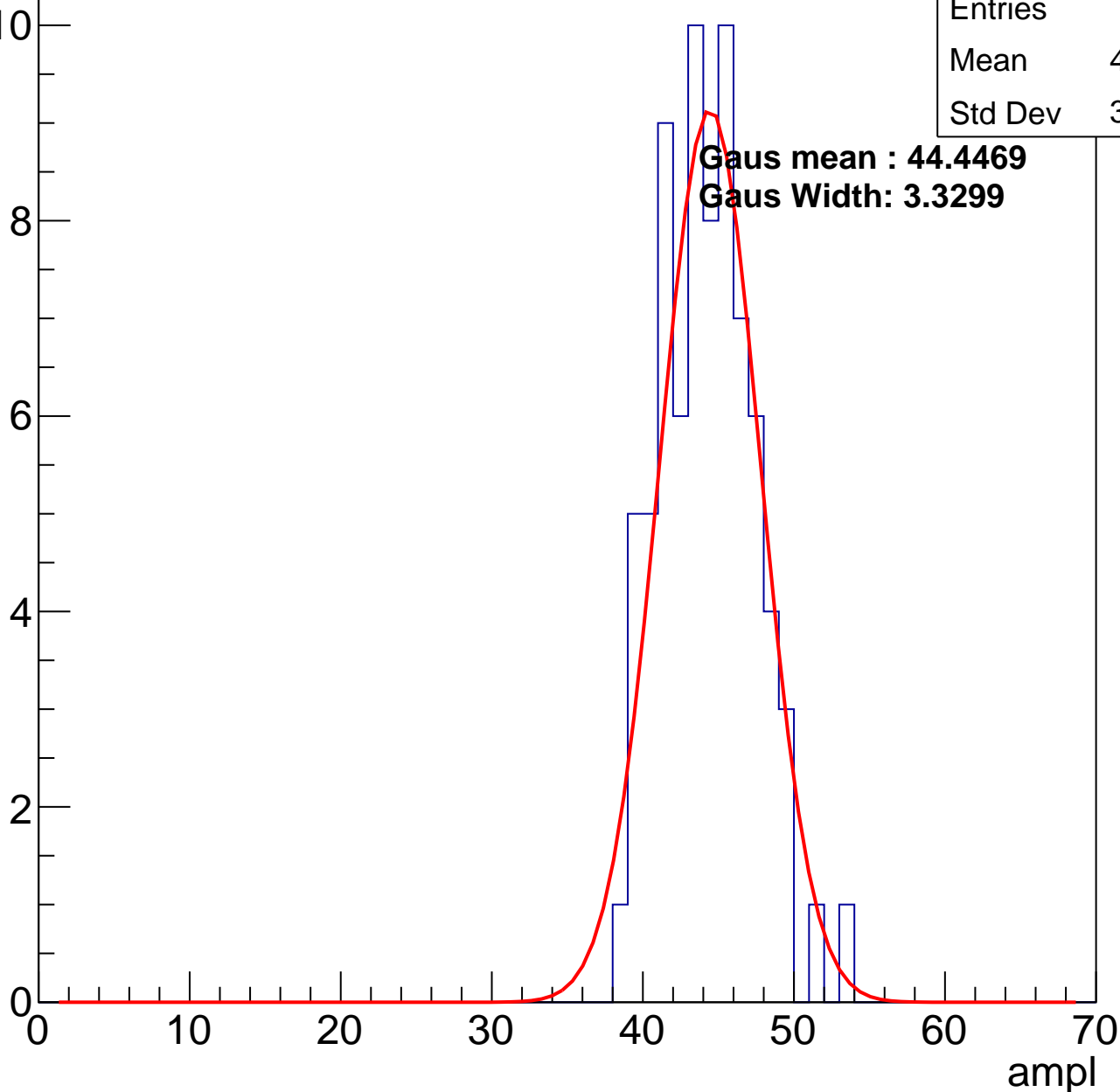
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.86
Std Dev	3.068

Gaus mean : 44.4469

Gaus Width: 3.3299

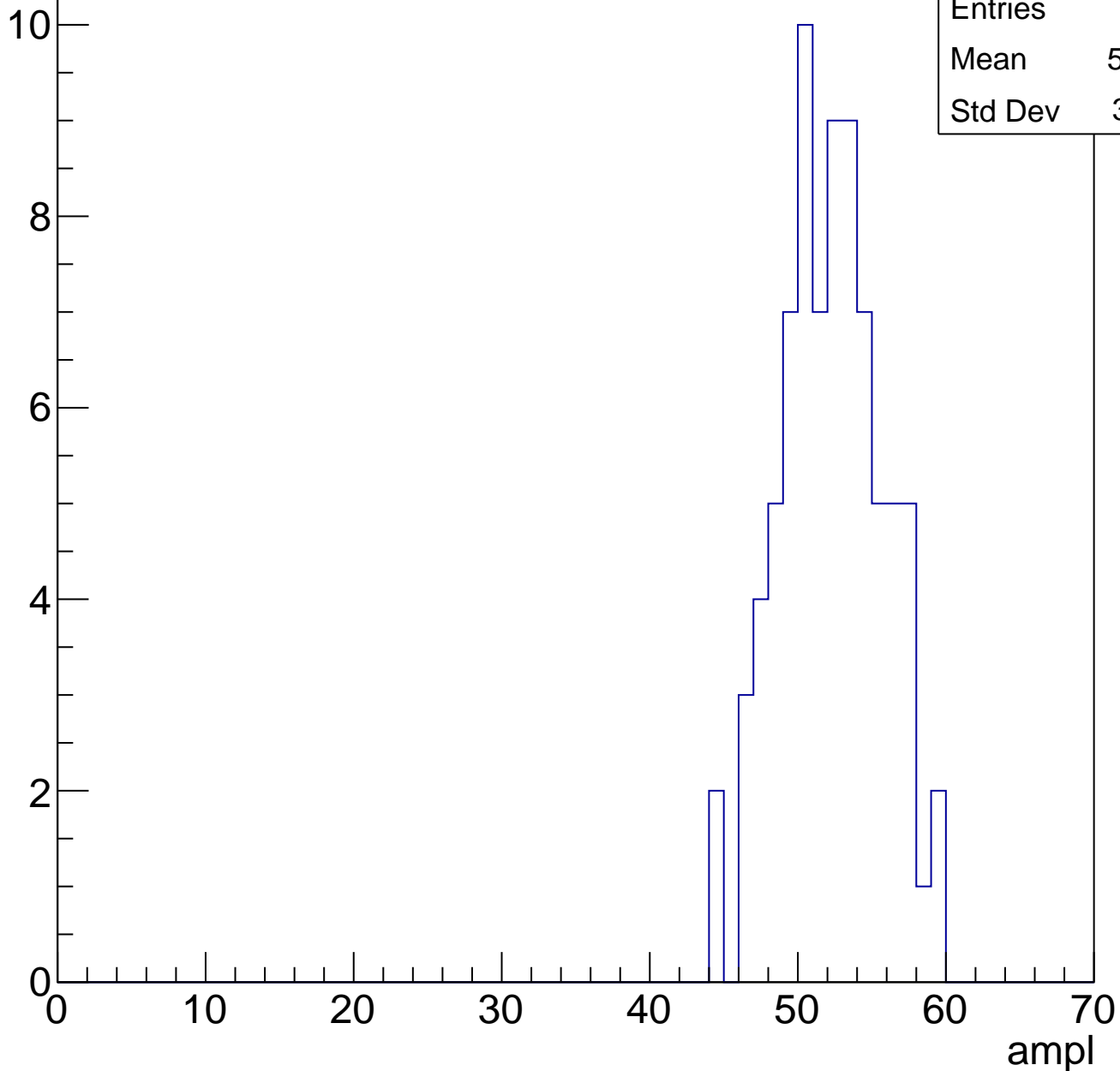


B1L102S, U11-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	51.77
Std Dev	3.411

Entry

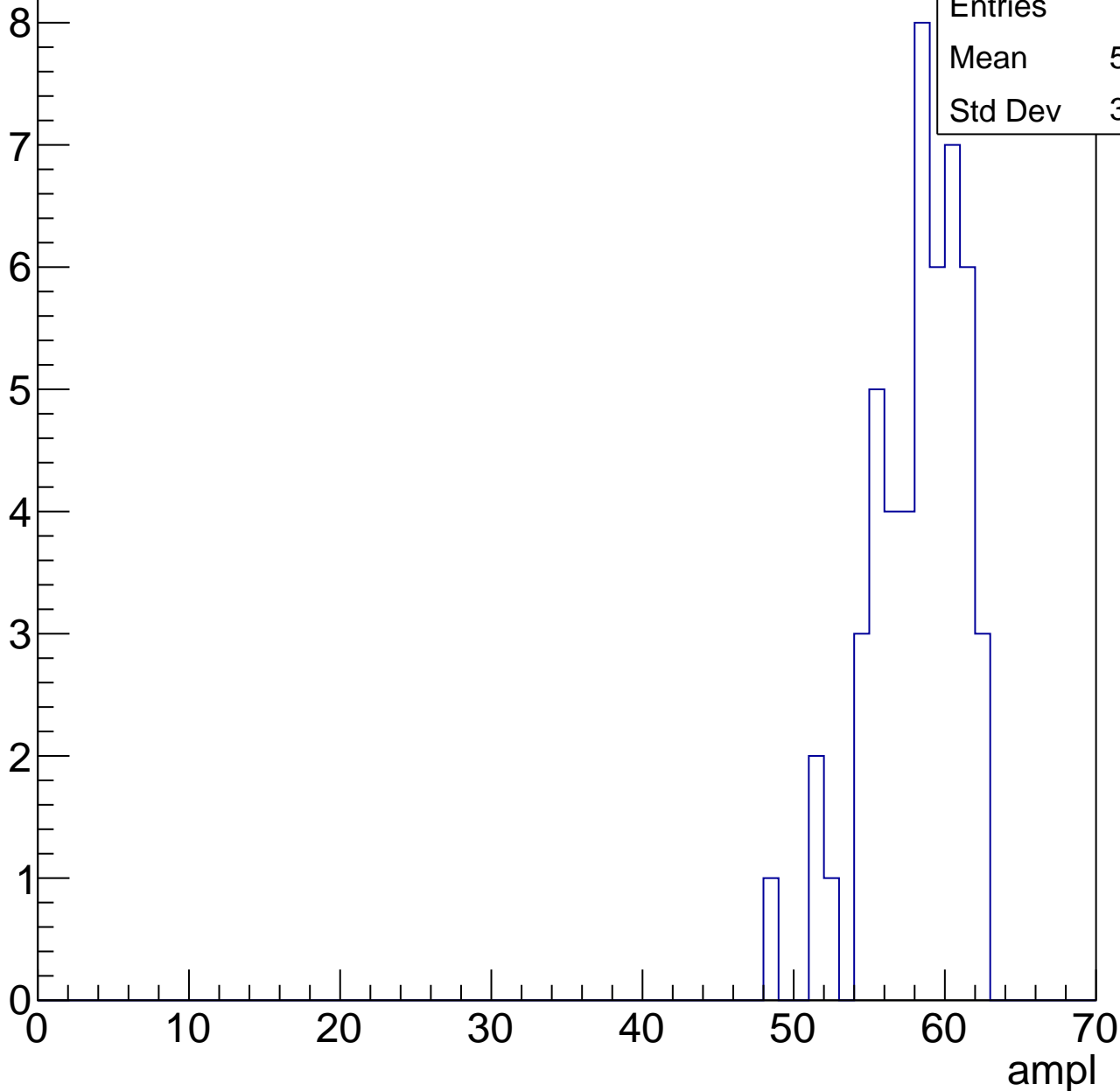


B1L102S, U11-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.62
Std Dev	3.085

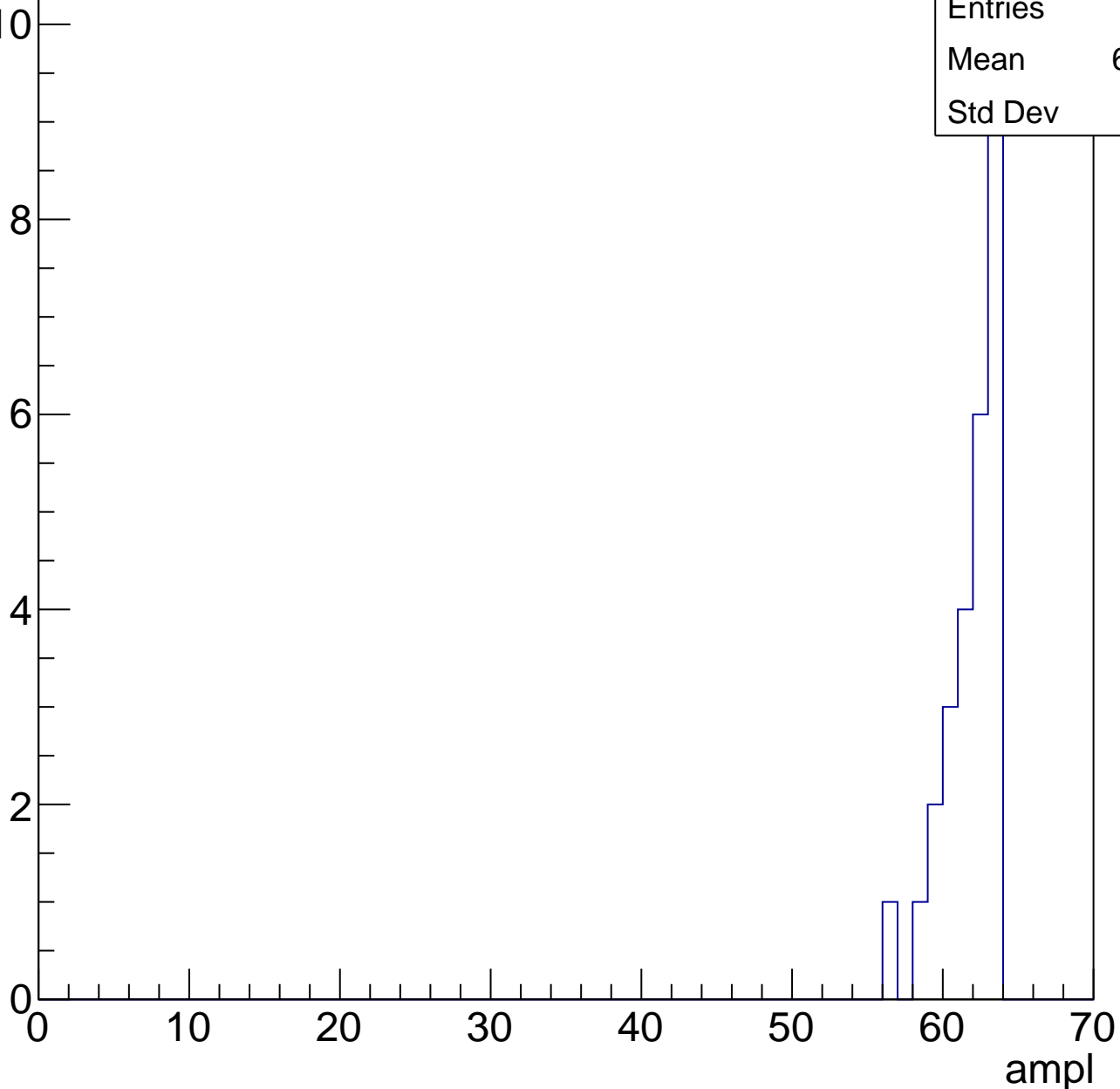


B1L102S, U11-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

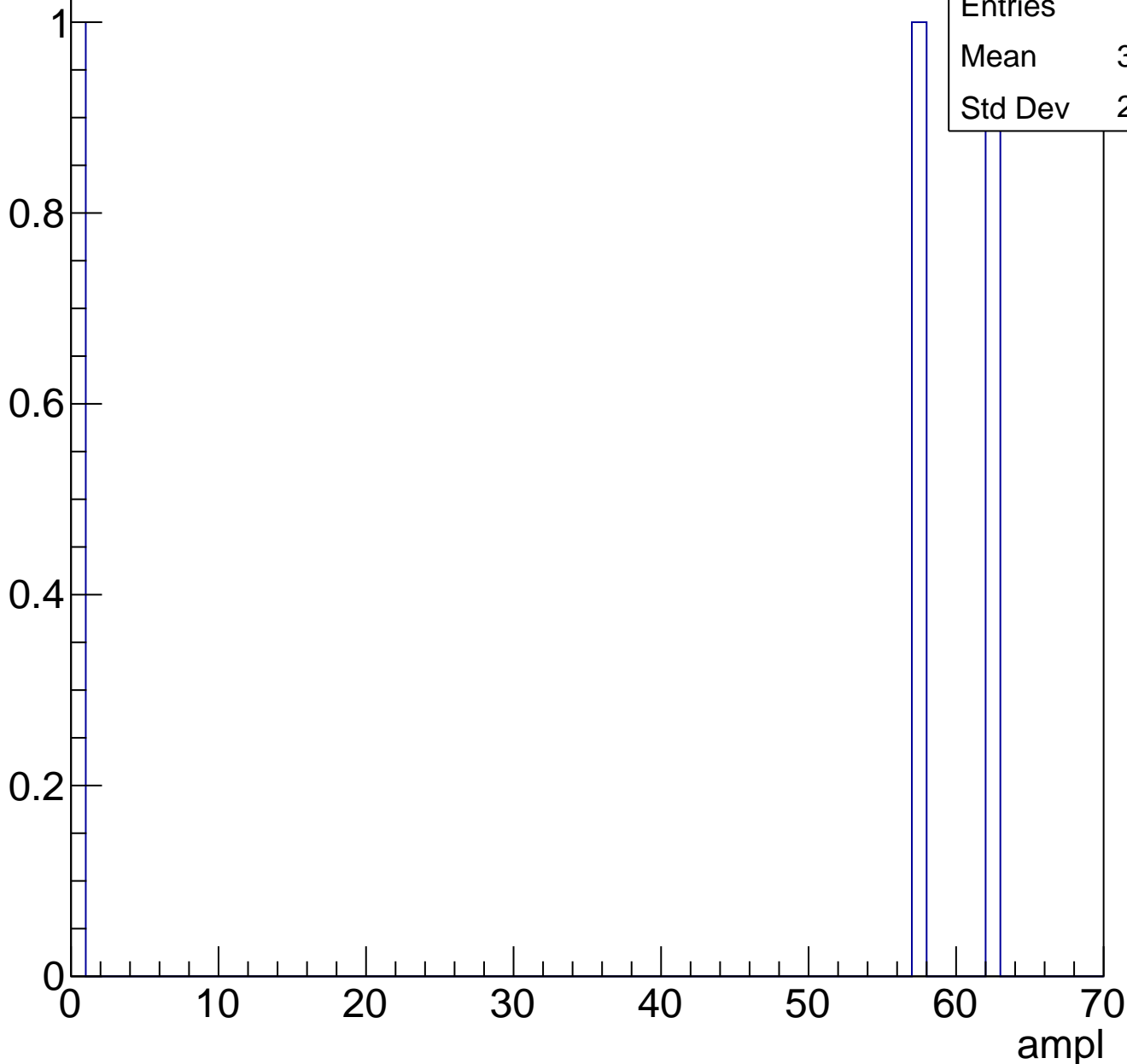
Entries	27
Mean	61.41
Std Dev	1.79



B1L102S, U11-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch7, adc0

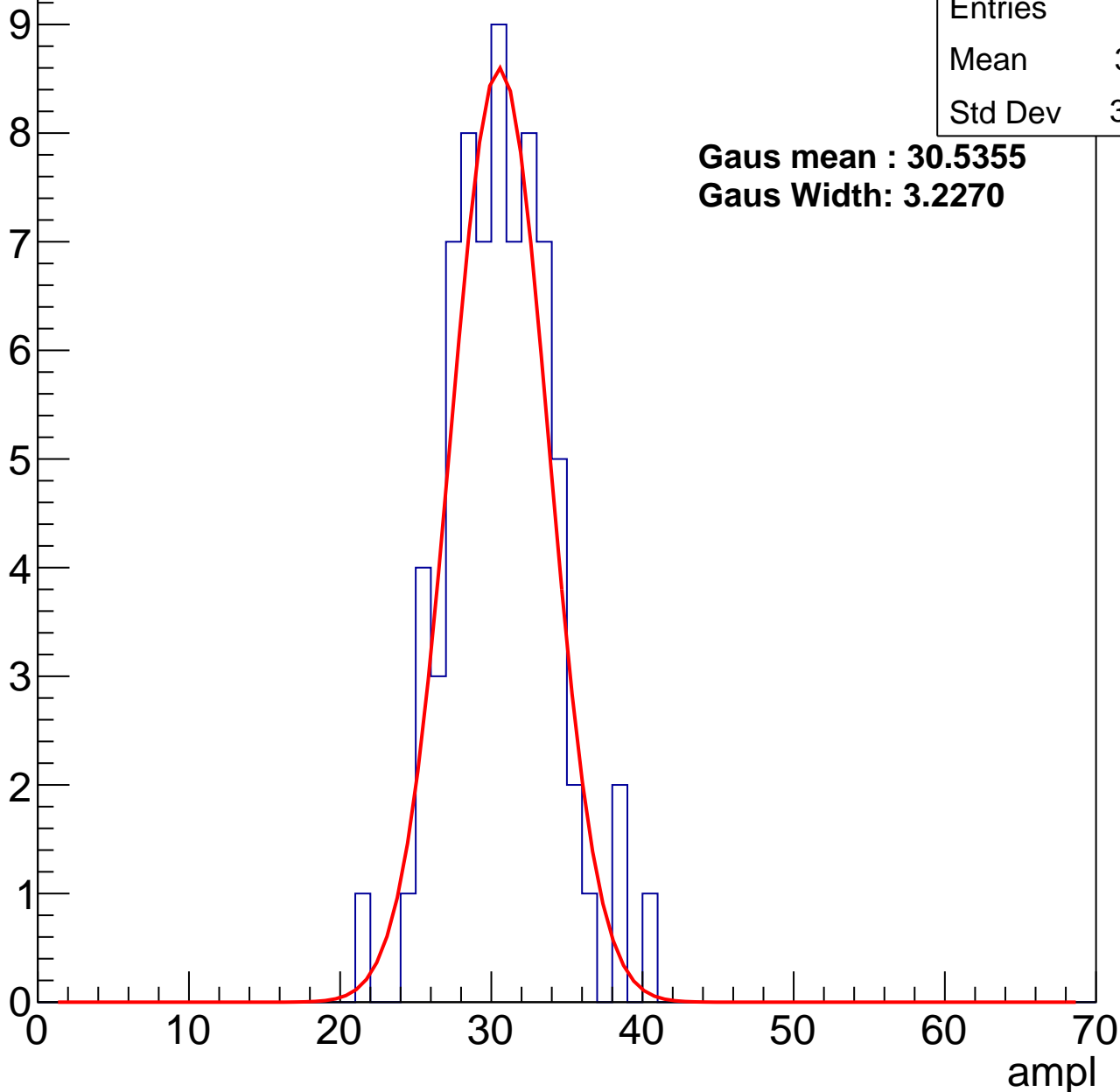
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.21
Std Dev	3.432

Gaus mean : 30.5355

Gaus Width: 3.2270



B1L102S, U11-ch7, adc1

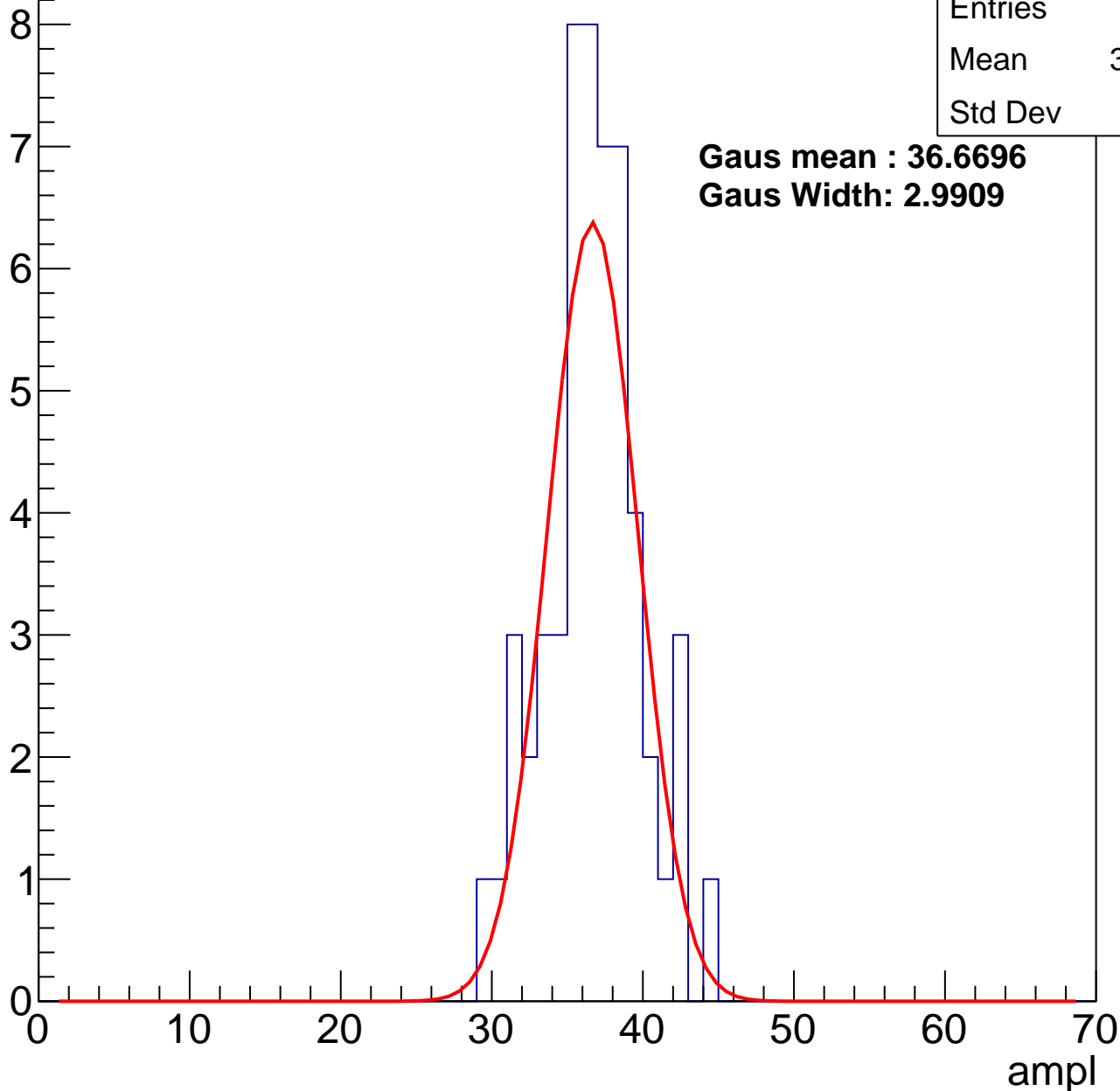
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	36.24
Std Dev	3.15

Gaus mean : 36.6696

Gaus Width: 2.9909



B1L102S, U11-ch7, adc2

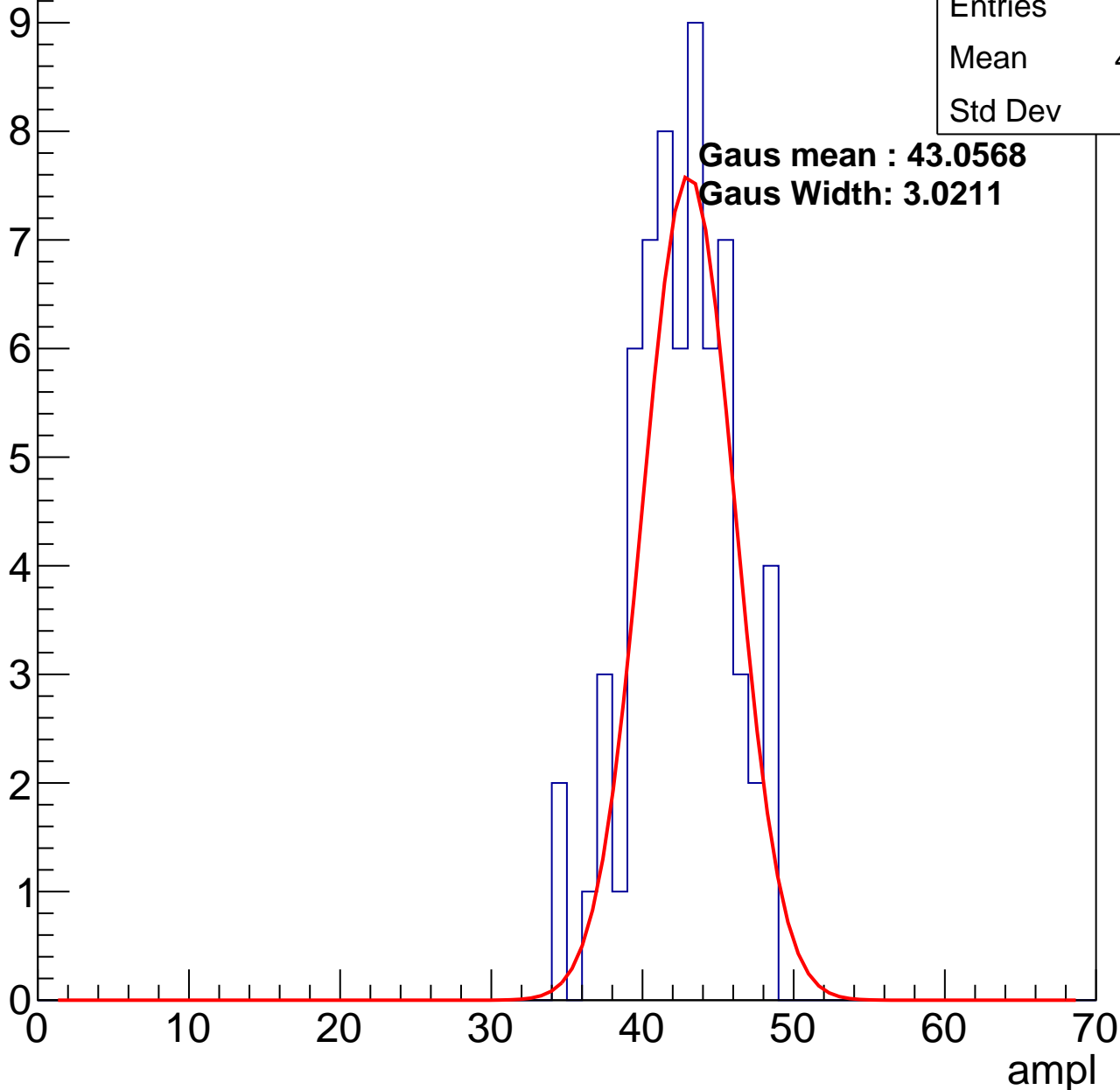
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.11
Std Dev	3.24

Gaus mean : 43.0568

Gaus Width: 3.0211

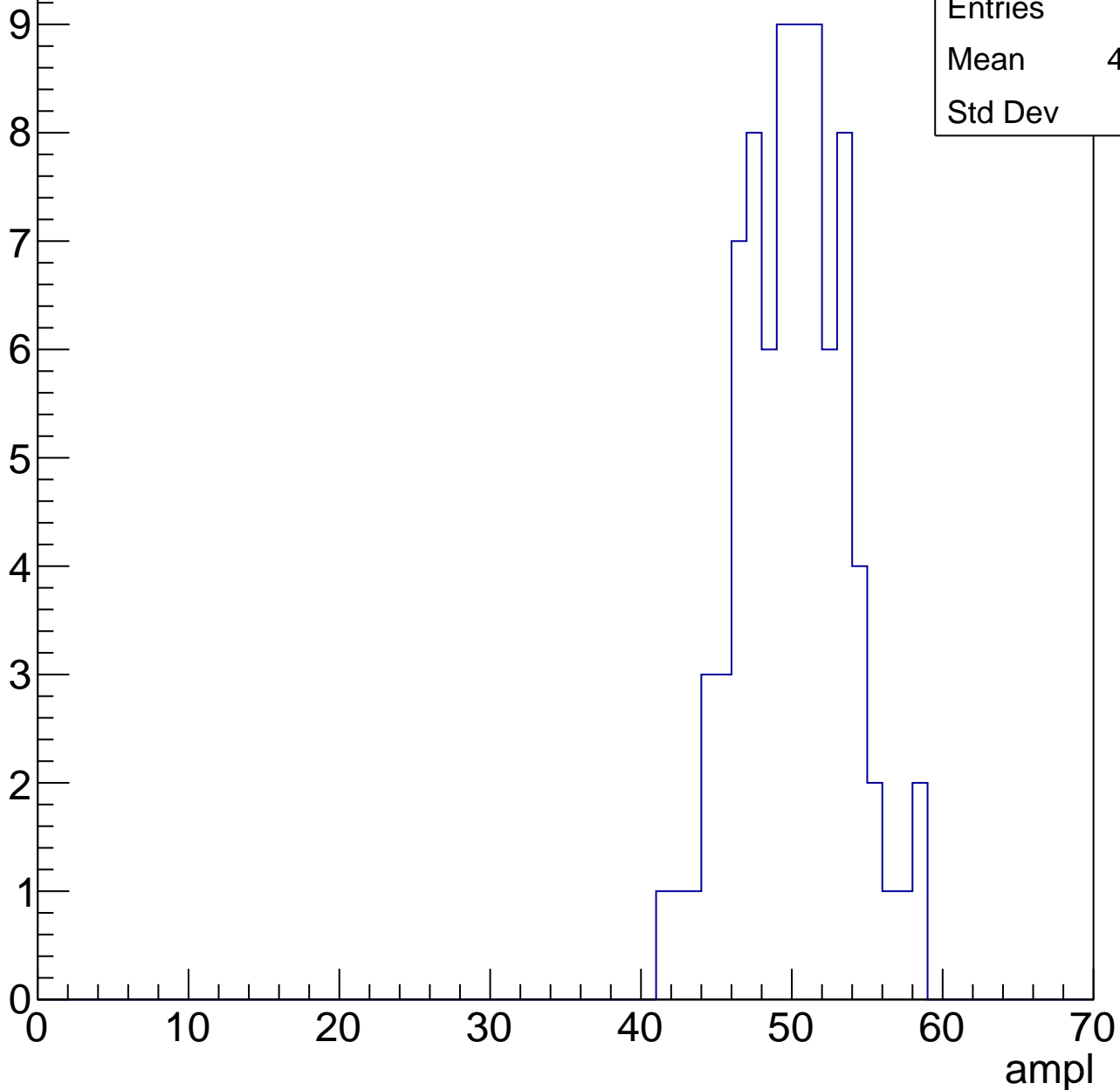


B1L102S, U11-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	49.63
Std Dev	3.54

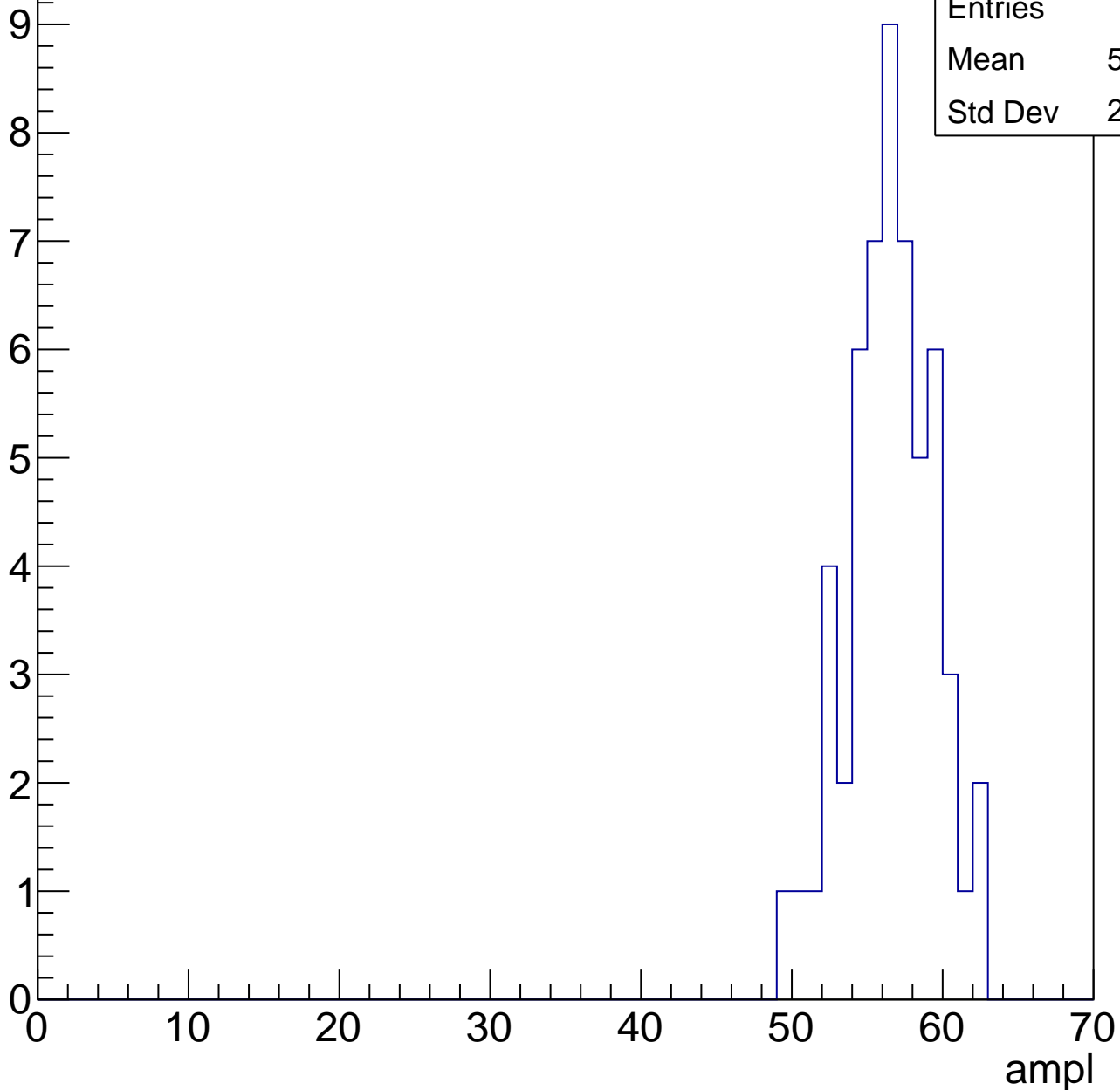


B1L102S, U11-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.09
Std Dev	2.856

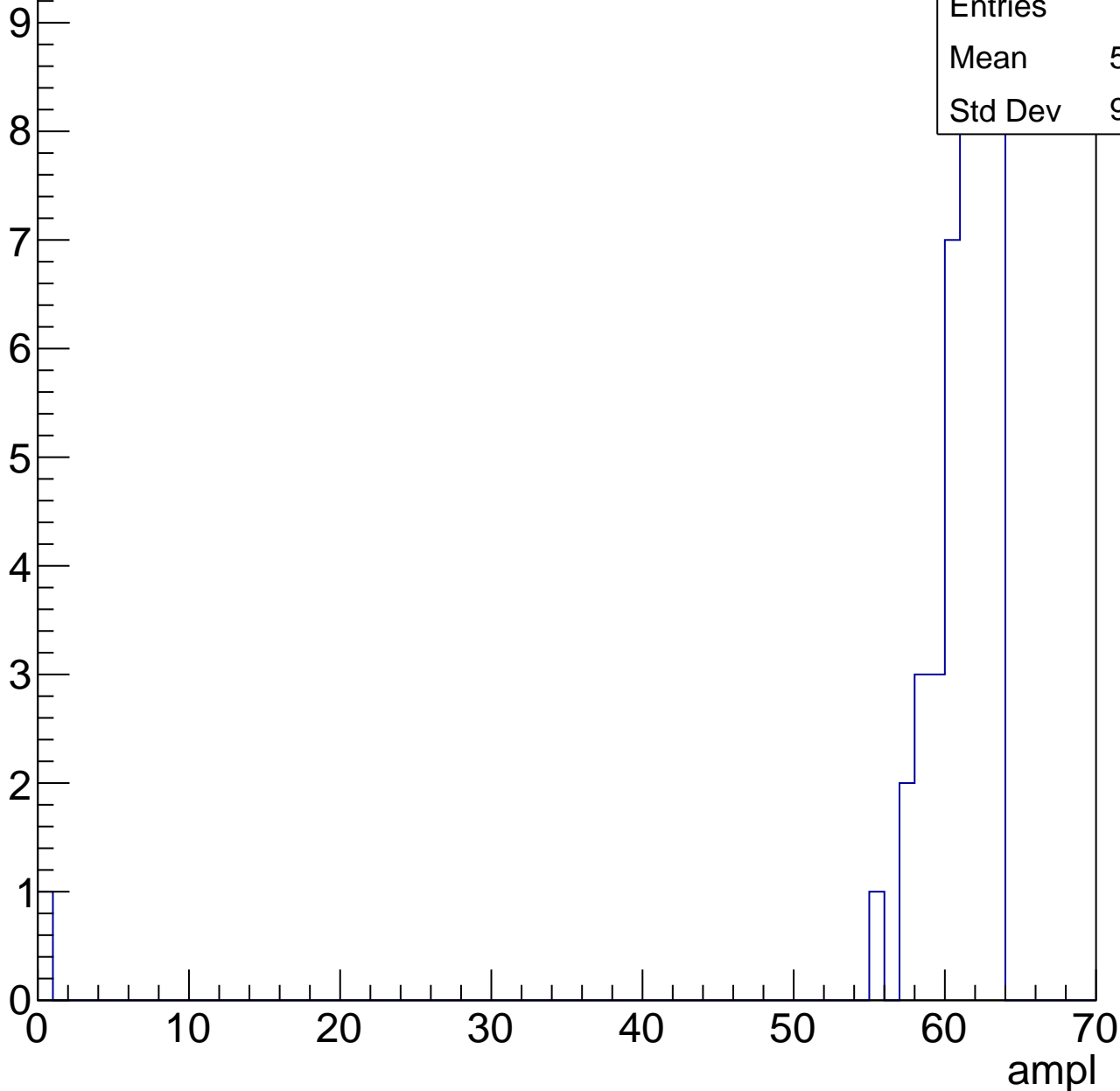


B1L102S, U11-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.35
Std Dev	9.353



B1L102S, U11-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

61

Std Dev

2

B1L102S, U11-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch8, adc0

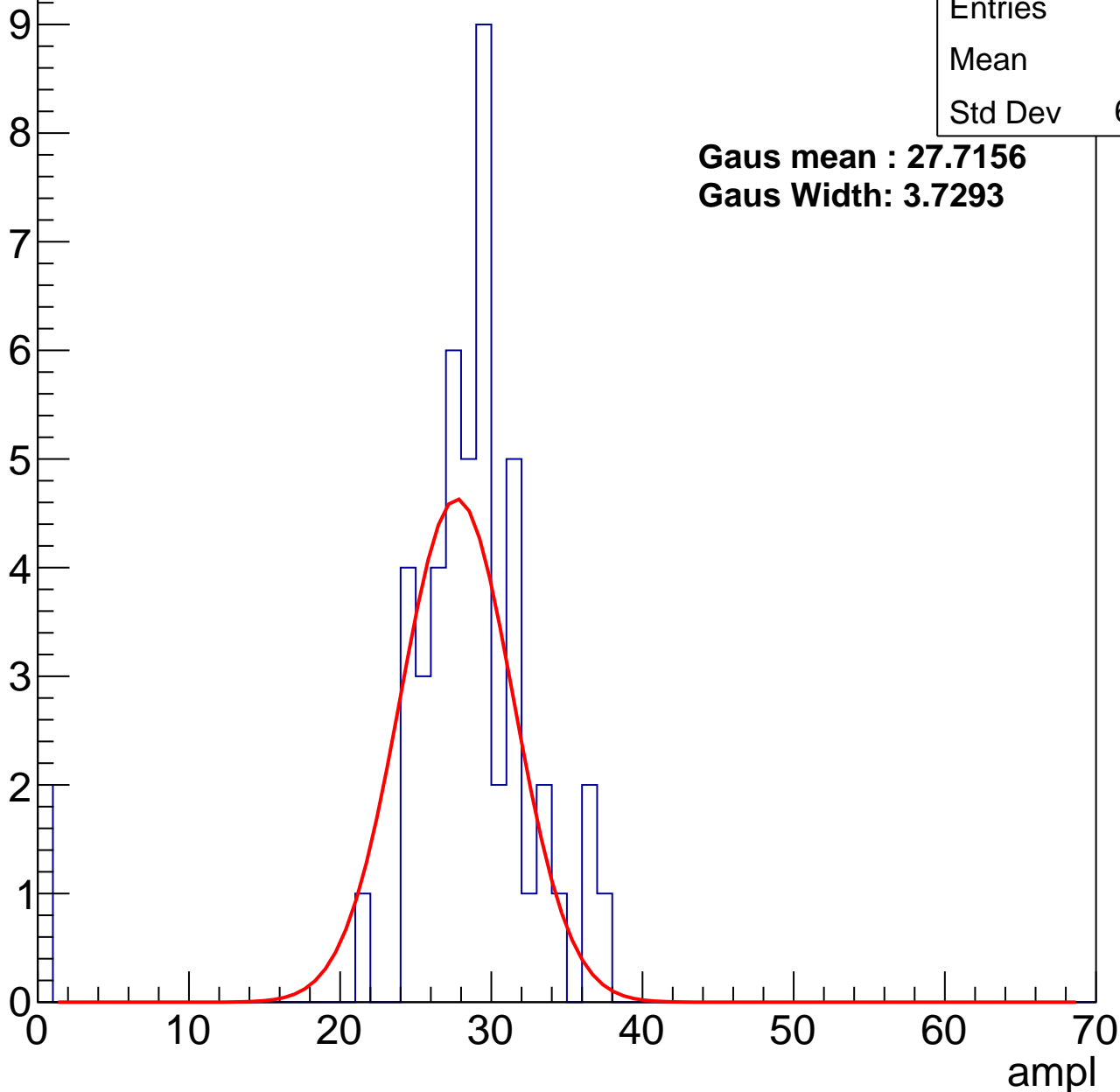
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	27.4
Std Dev	6.591

Gaus mean : 27.7156

Gaus Width: 3.7293



B1L102S, U11-ch8, adc1

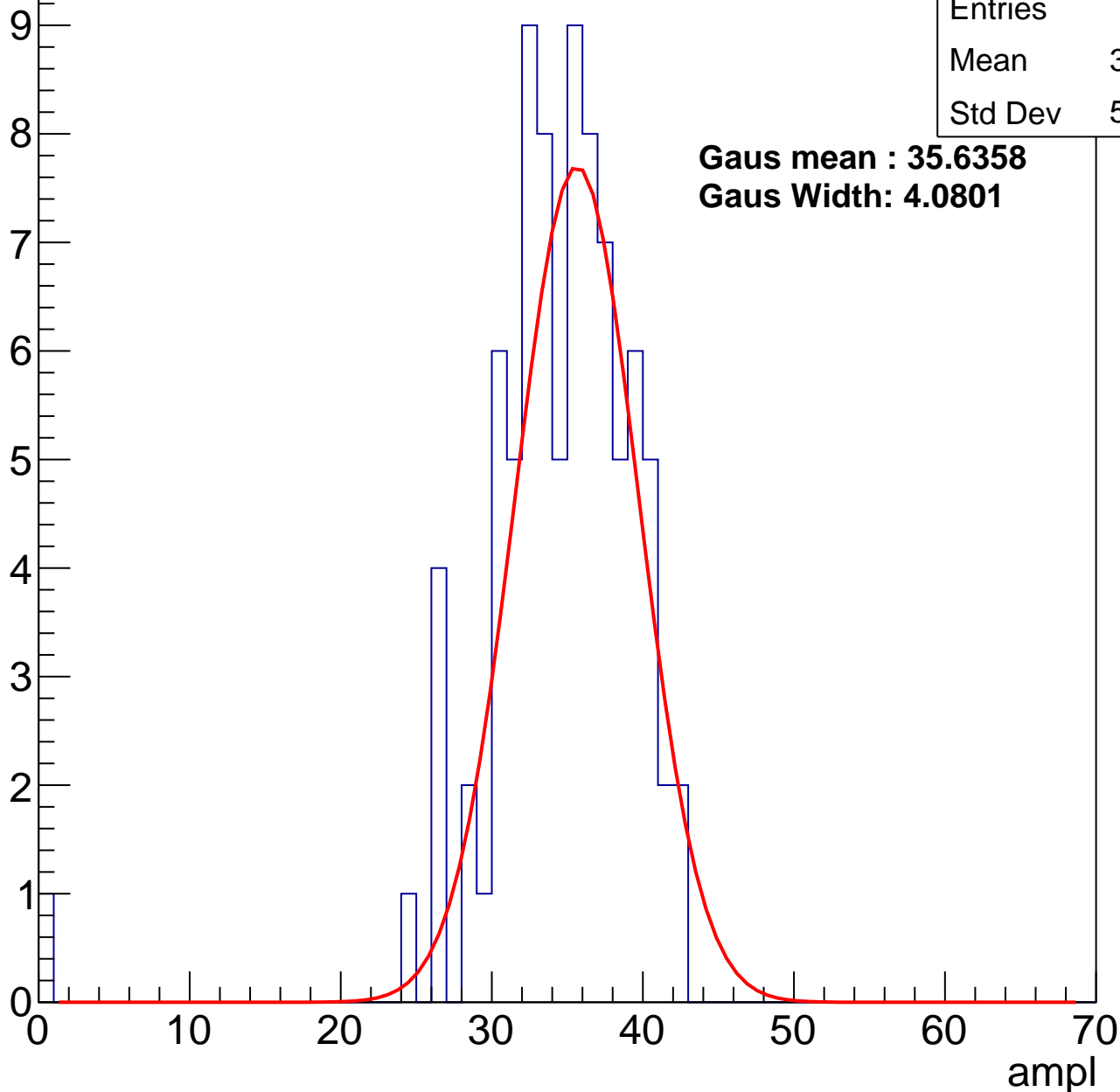
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	33.98
Std Dev	5.428

Gaus mean : 35.6358

Gaus Width: 4.0801



B1L102S, U11-ch8, adc2

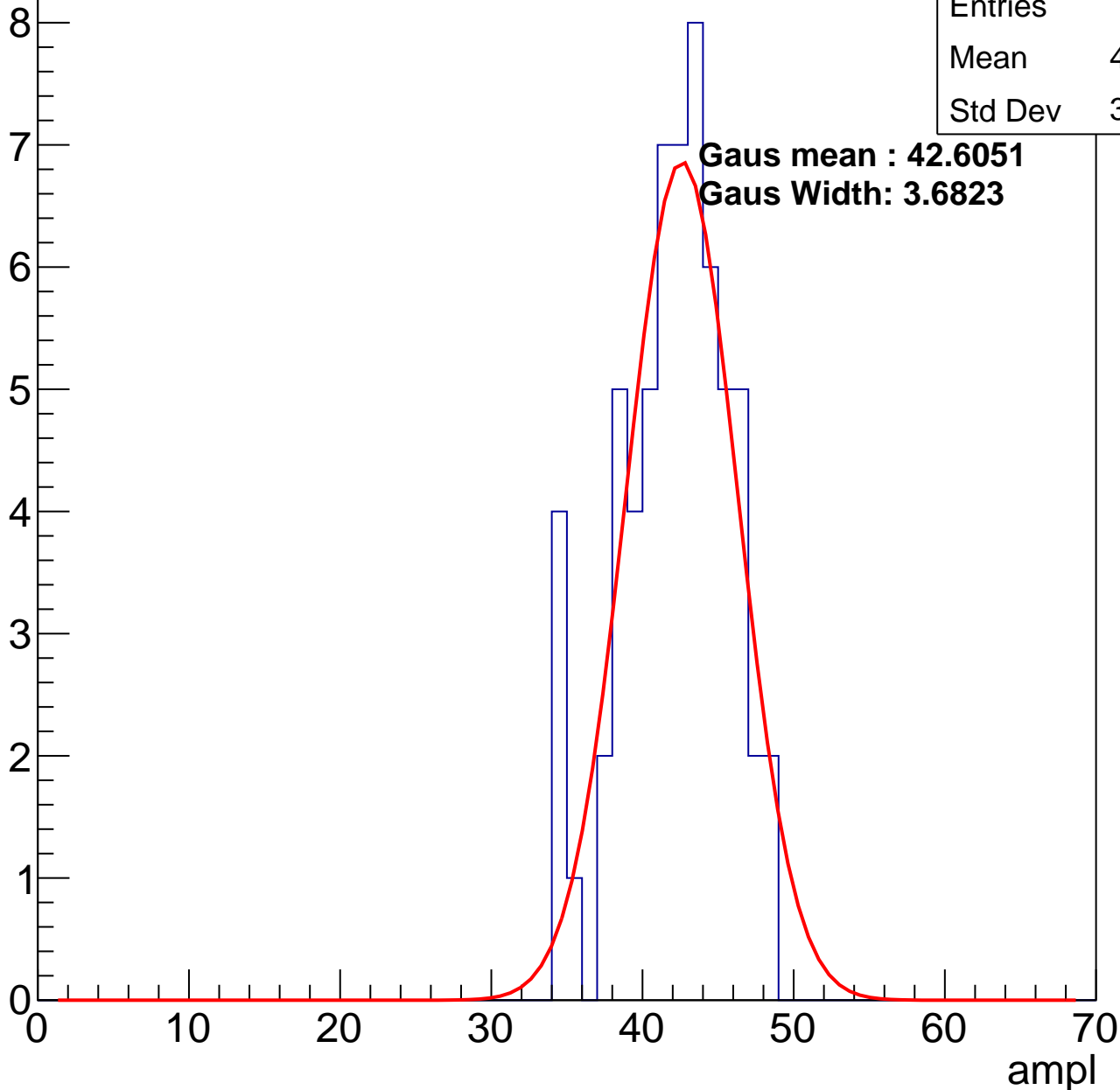
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.67
Std Dev	3.496

Gaus mean : 42.6051

Gaus Width: 3.6823

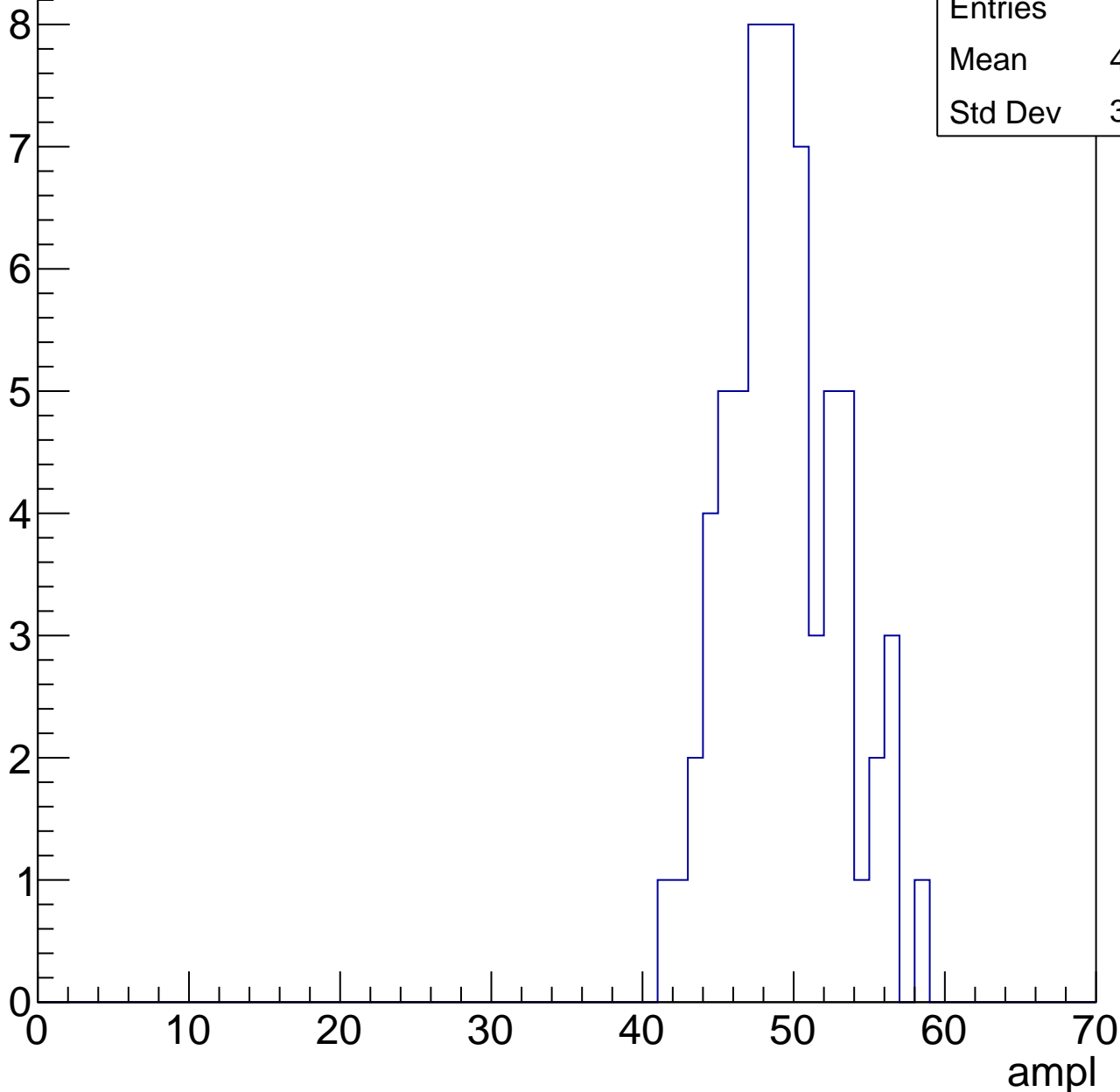


B1L102S, U11-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.84
Std Dev	3.646

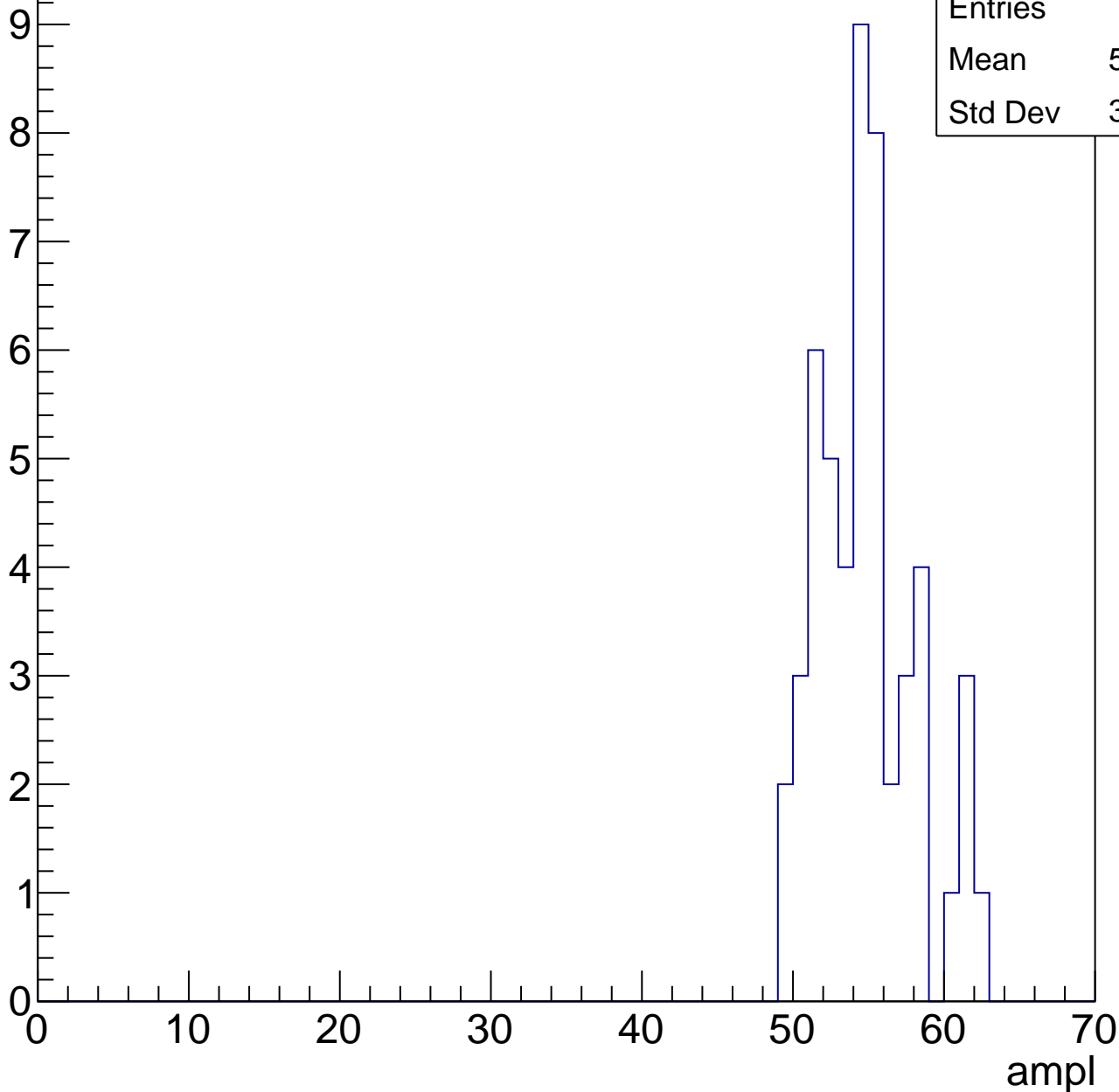


B1L102S, U11-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.35
Std Dev	3.204

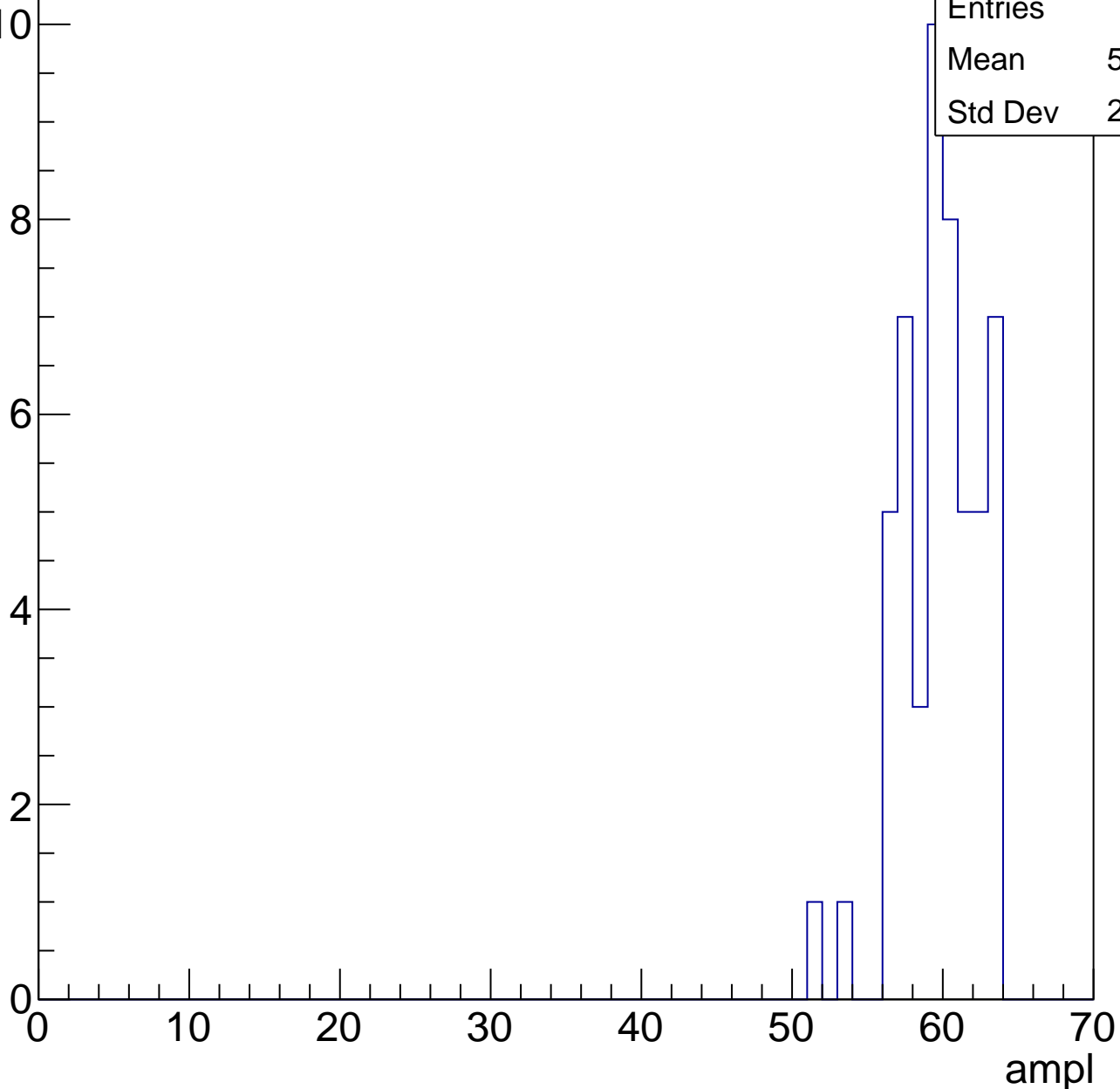


B1L102S, U11-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

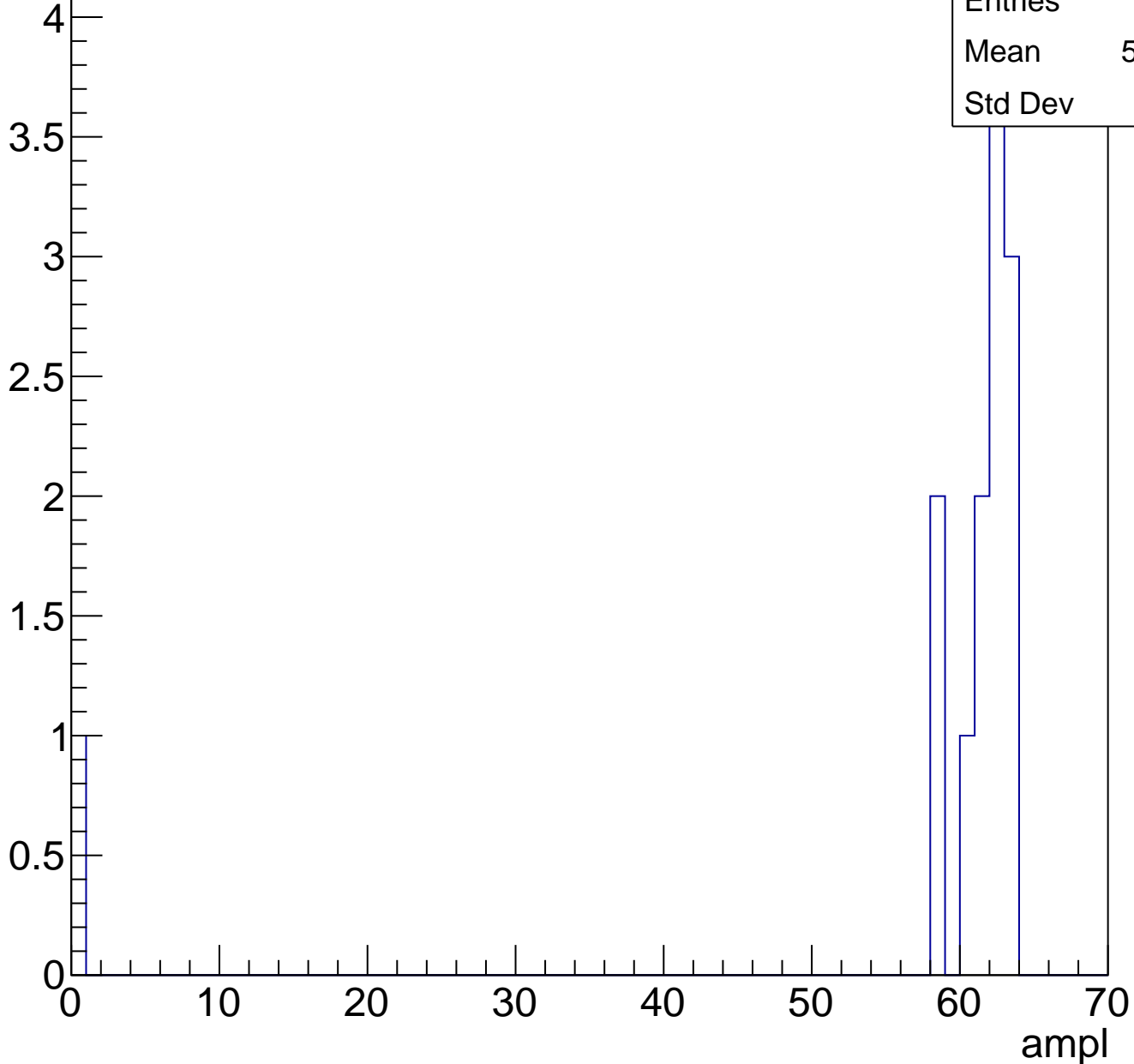
Entries	52
Mean	59.29
Std Dev	2.619



B1L102S, U11-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch9, adc0

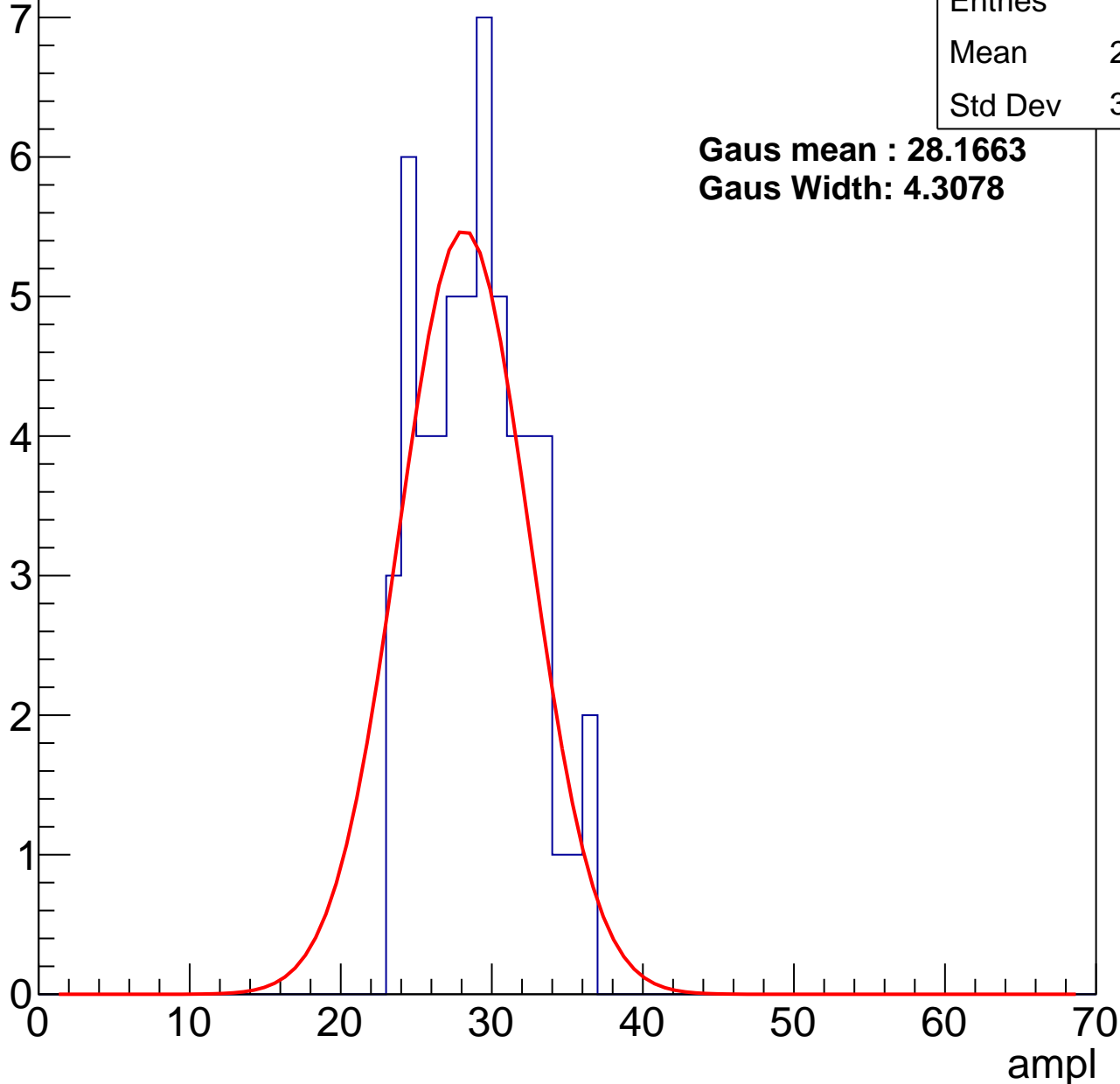
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.55
Std Dev	3.442

Gaus mean : 28.1663

Gaus Width: 4.3078



B1L102S, U11-ch9, adc1

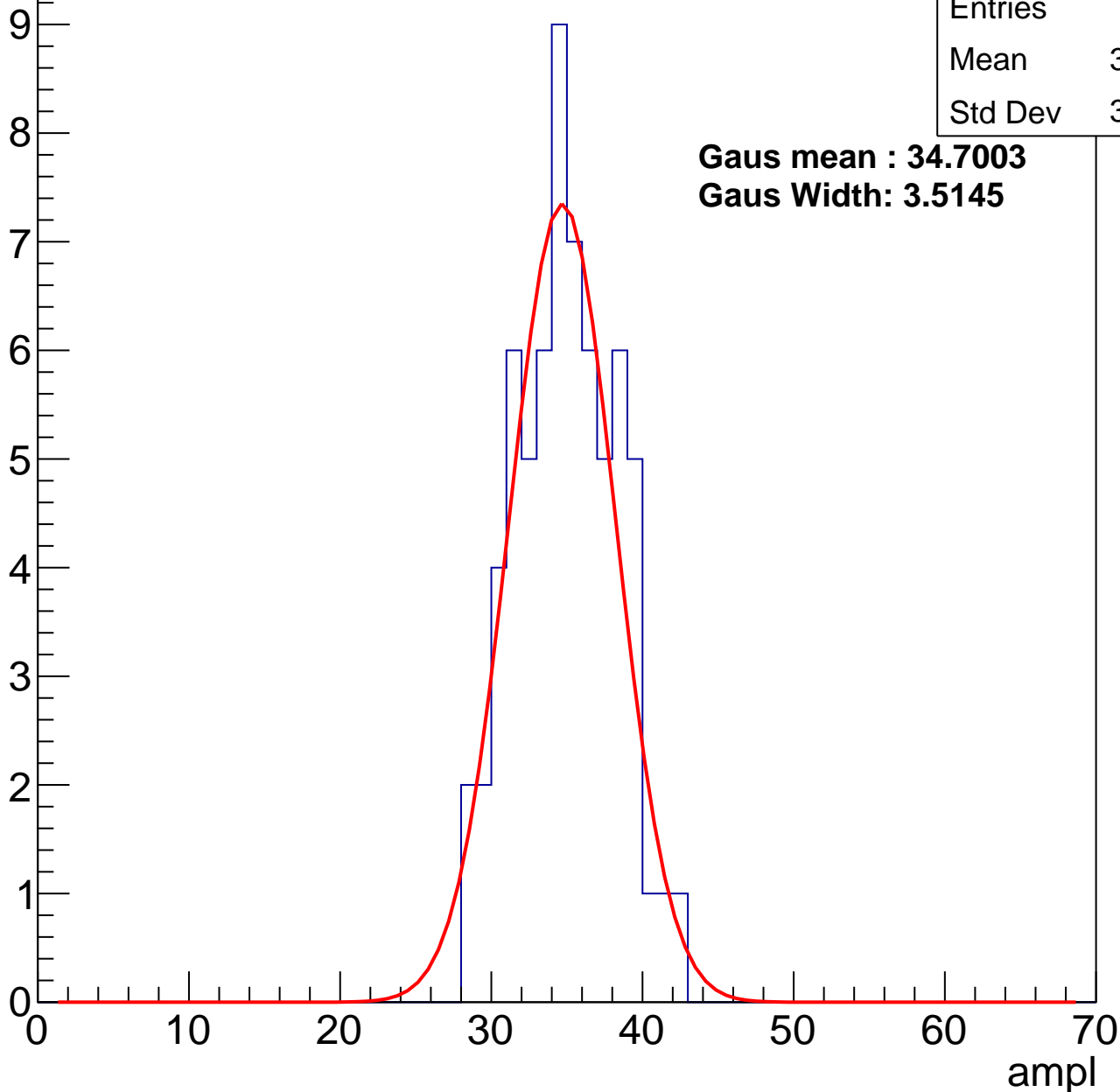
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.48
Std Dev	3.249

Gaus mean : 34.7003

Gaus Width: 3.5145



B1L102S, U11-ch9, adc2

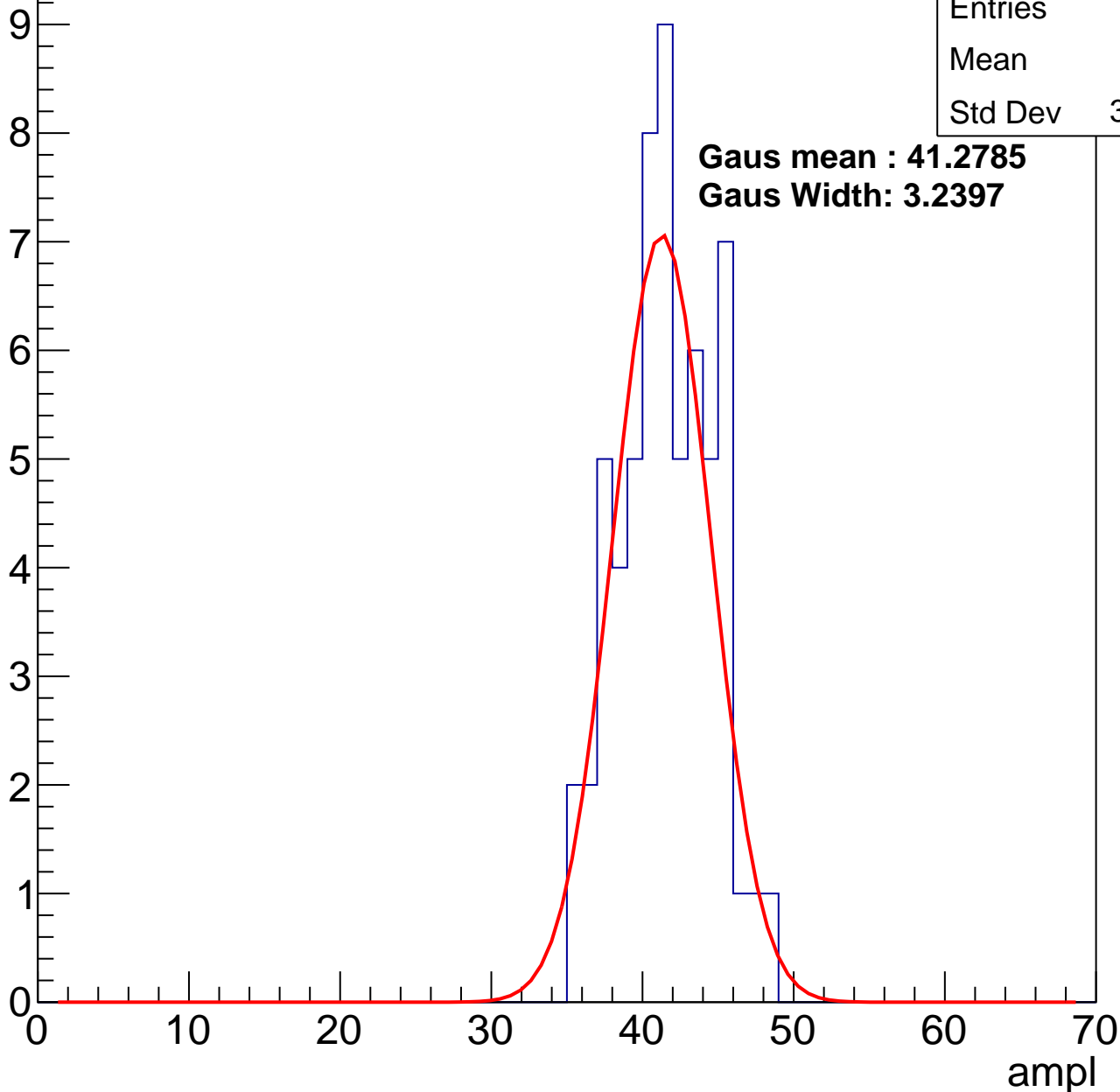
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.1
Std Dev	3.034

Gaus mean : 41.2785

Gaus Width: 3.2397

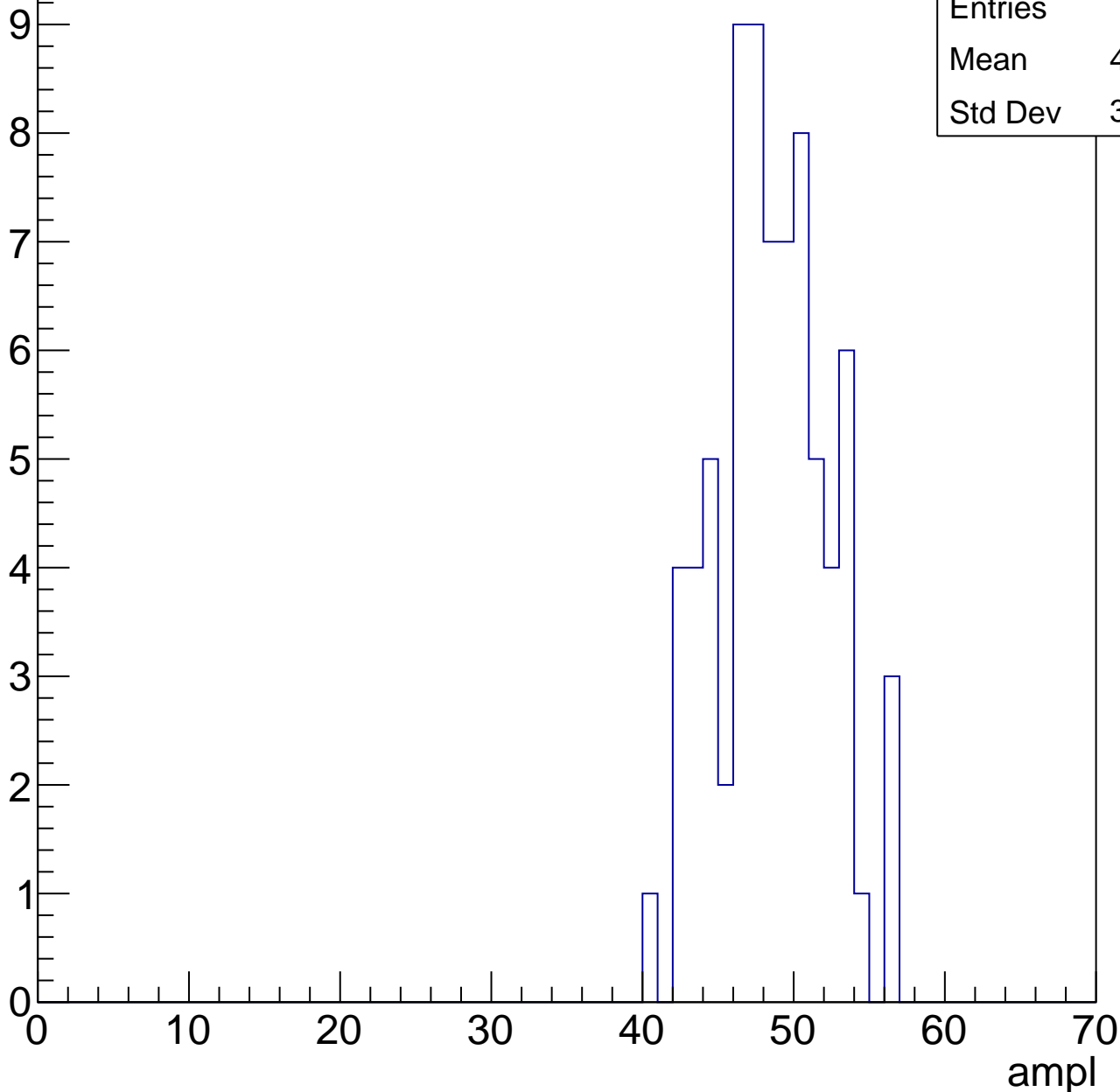


B1L102S, U11-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.12
Std Dev	3.607

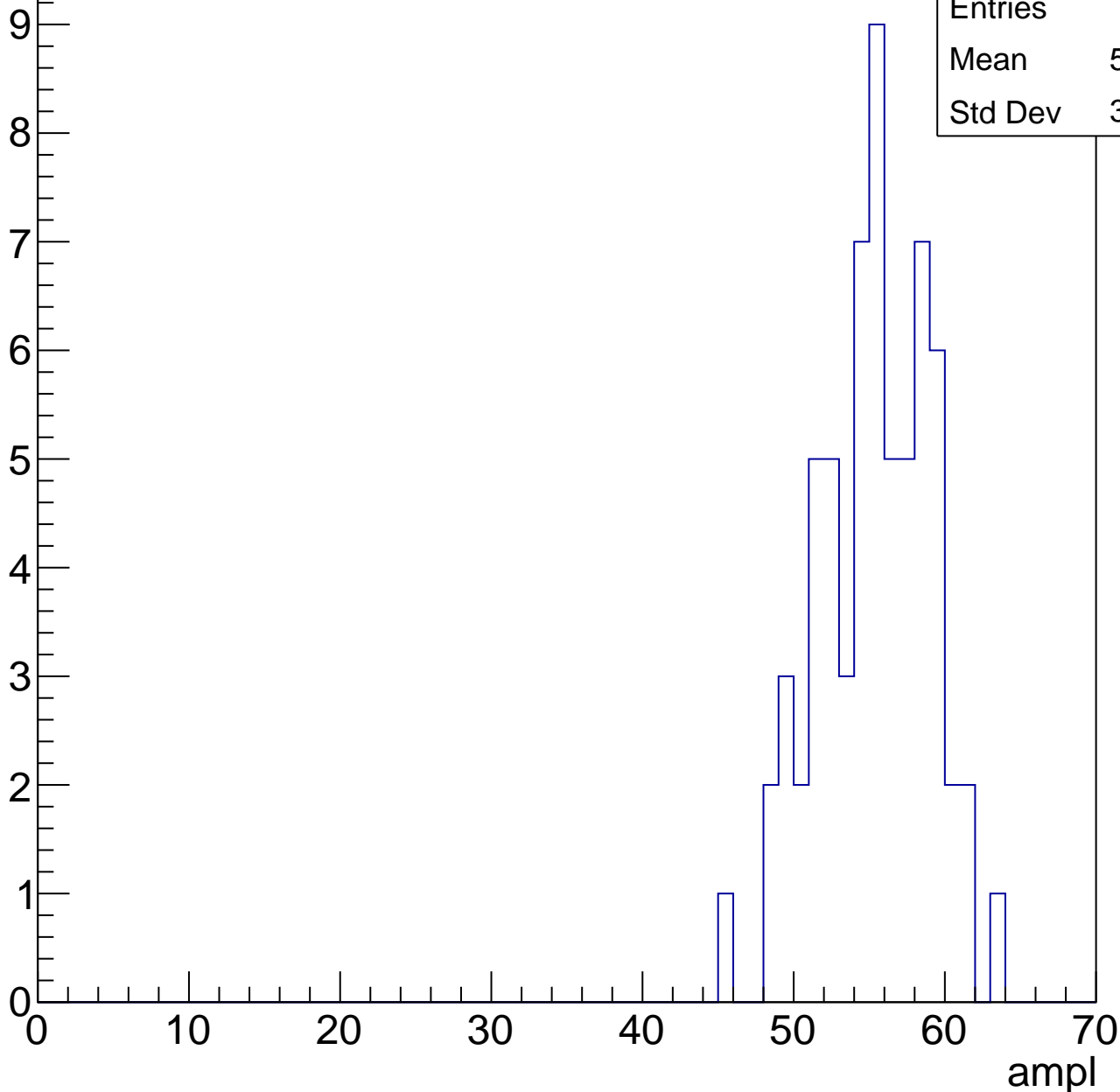


B1L102S, U11-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.85
Std Dev	3.655



B1L102S, U11-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

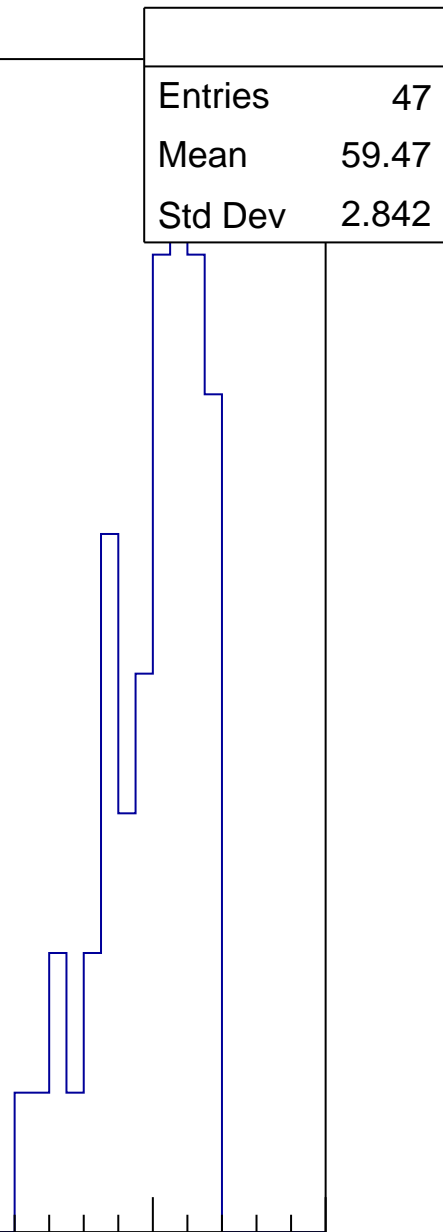
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.47
Std Dev	2.842

ampl

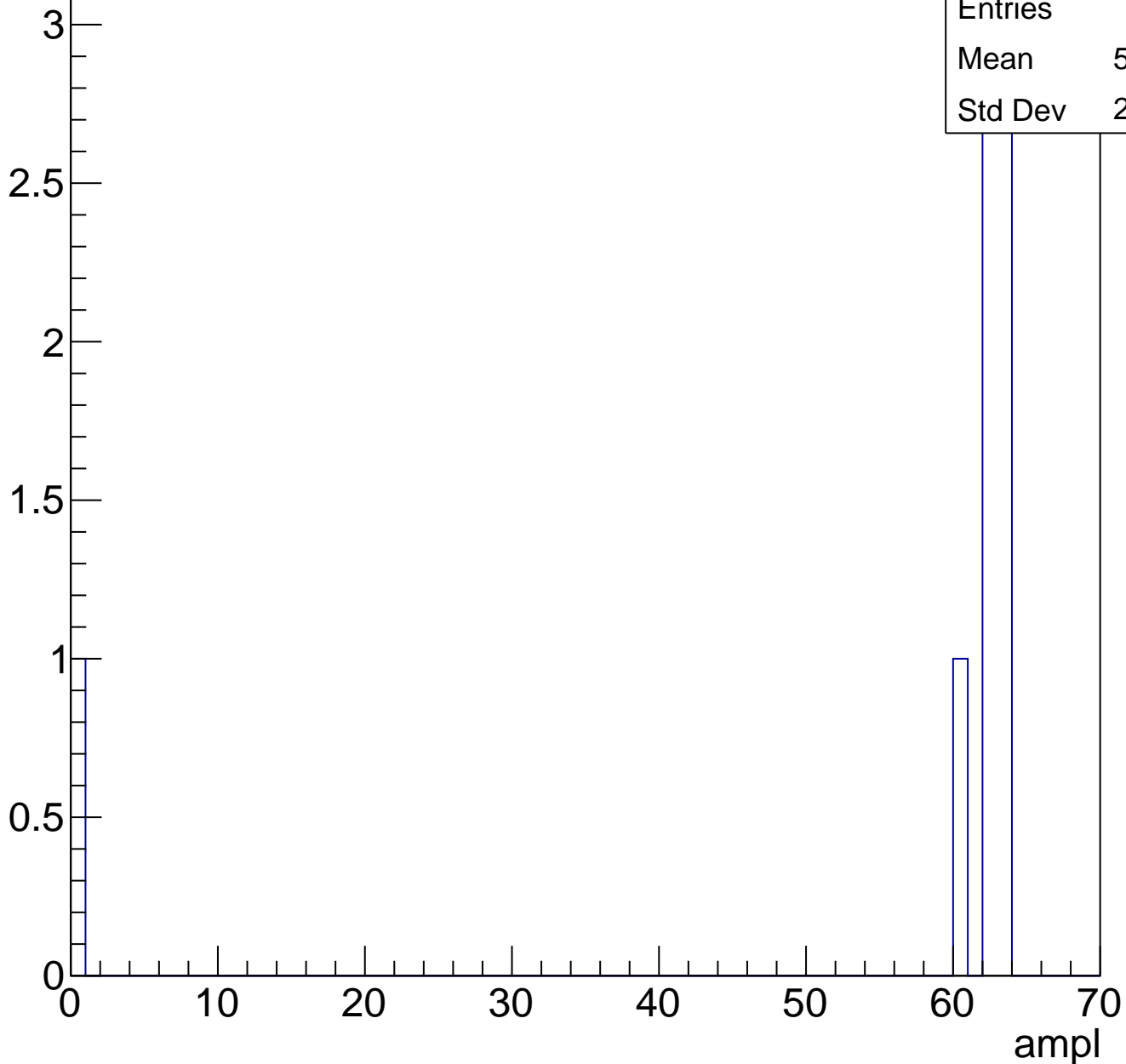
0 10 20 30 40 50 60 70



B1L102S, U11-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U11-ch10, adc0

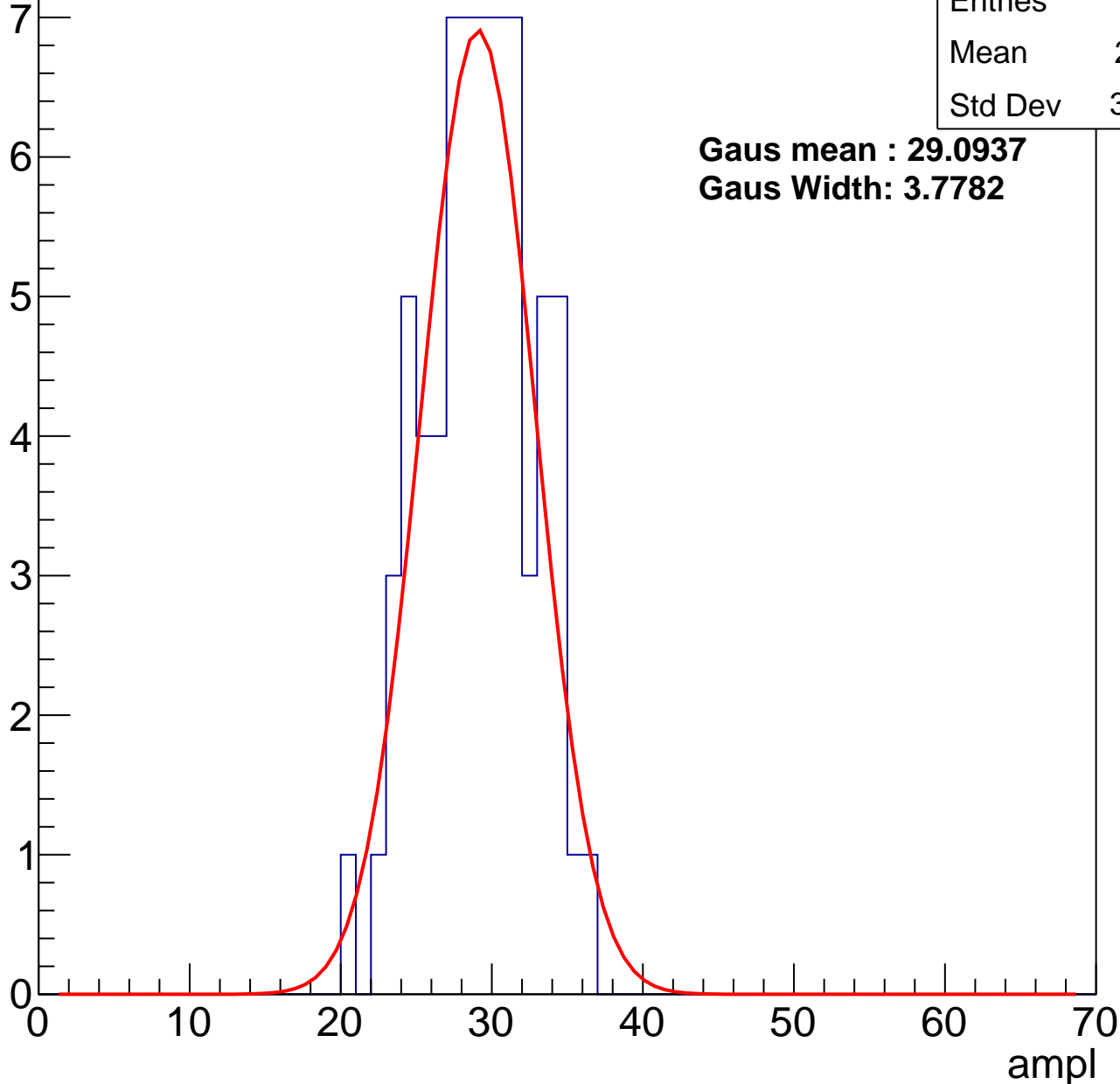
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.71
Std Dev	3.523

Gaus mean : 29.0937

Gaus Width: 3.7782



B1L102S, U11-ch10, adc1

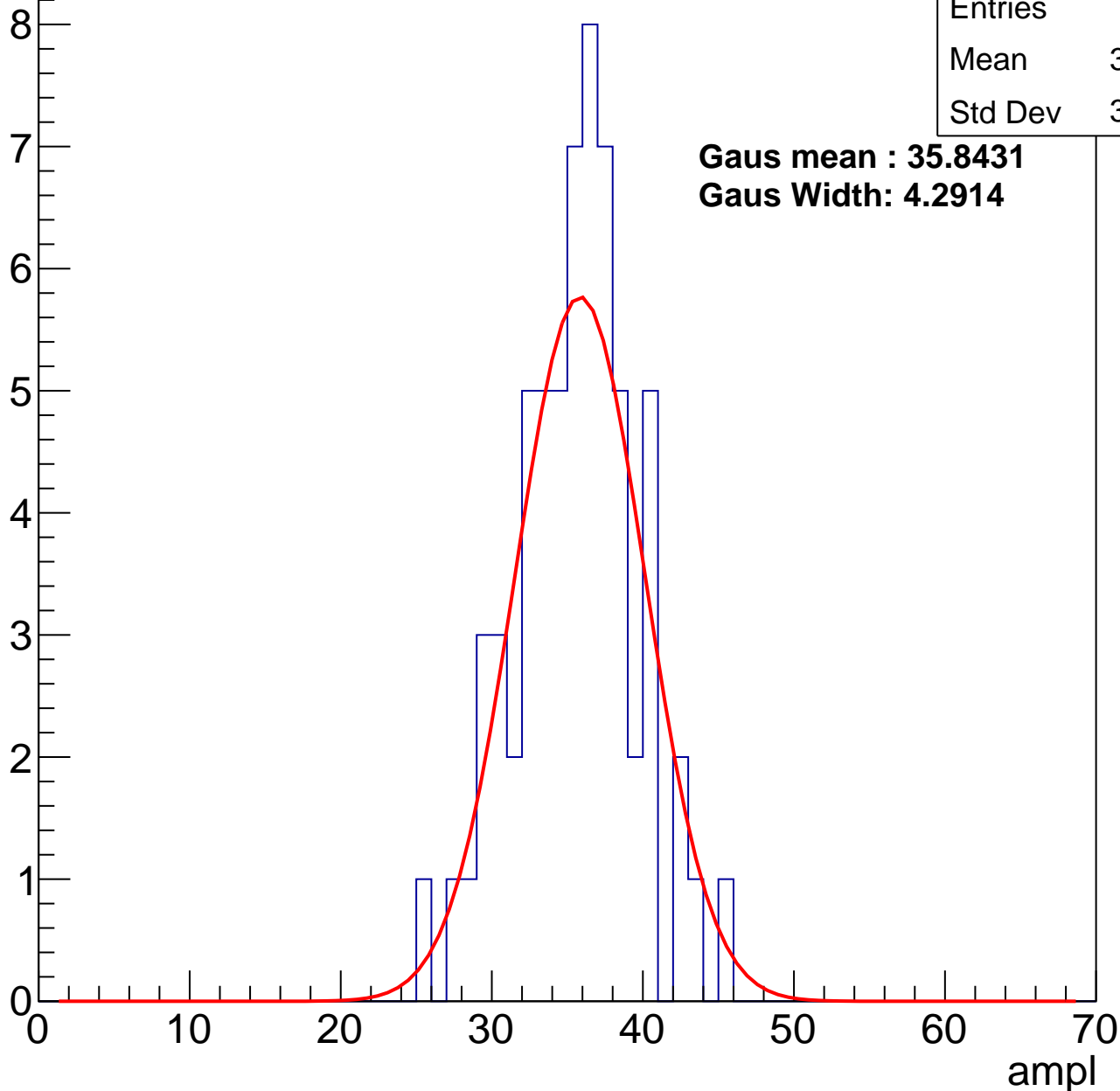
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.09
Std Dev	3.948

Gaus mean : 35.8431

Gaus Width: 4.2914



B1L102S, U11-ch10, adc2

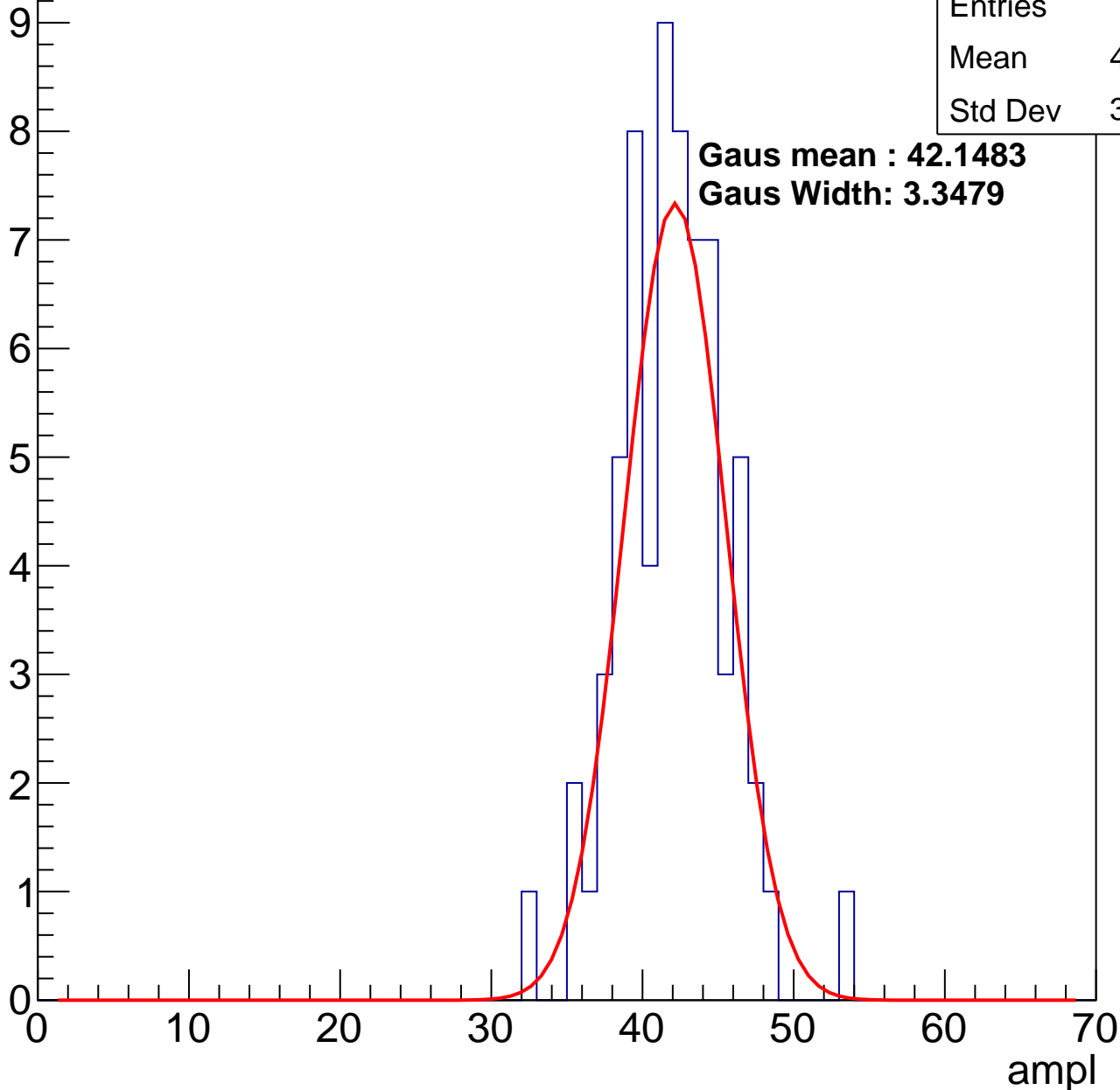
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.57
Std Dev	3.504

Gaus mean : 42.1483

Gaus Width: 3.3479



B1L102S, U11-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	49.05
Std Dev	3.533

Entry

10

8

6

4

2

0

0

10

20

30

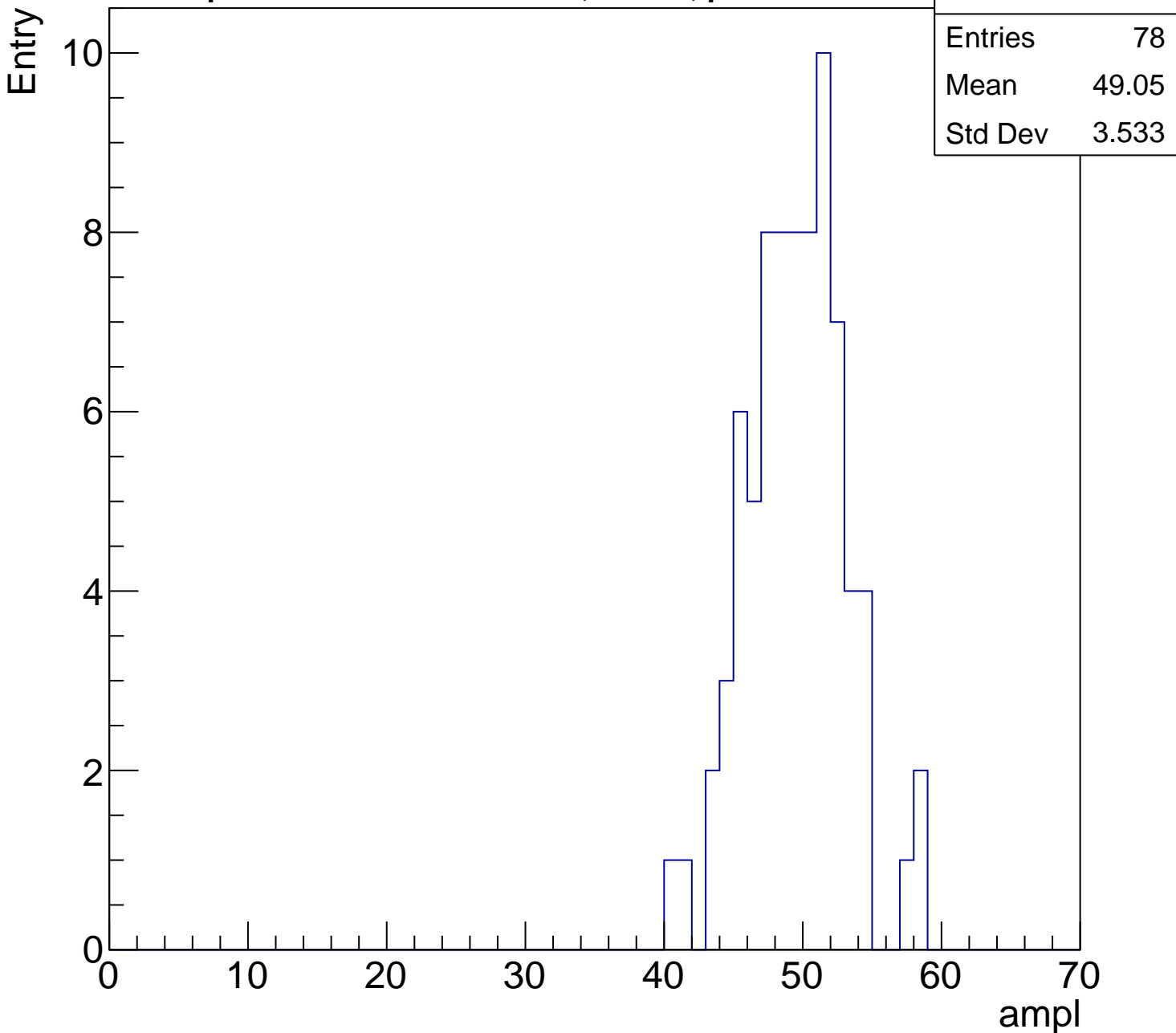
40

50

60

70

ampl

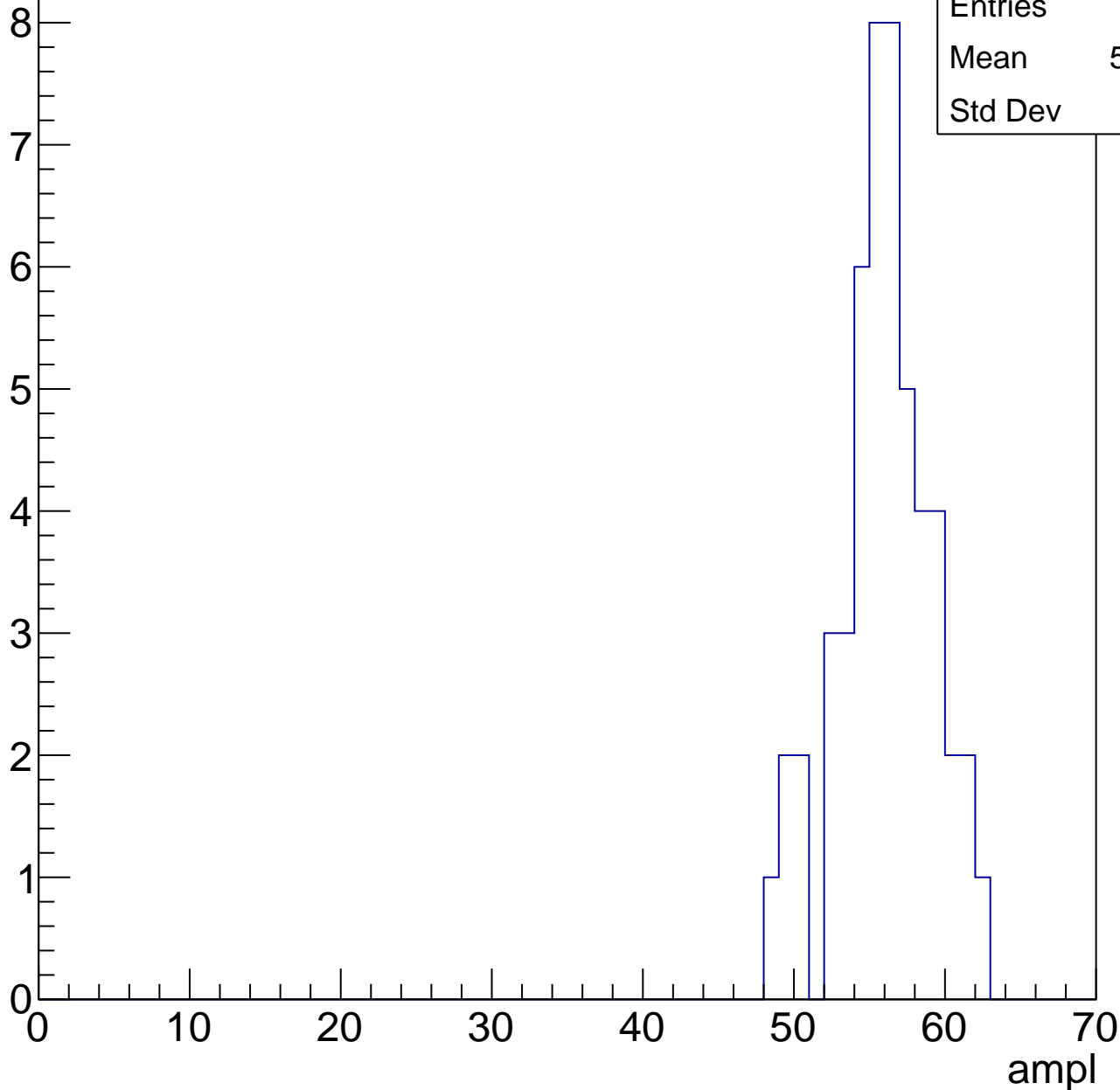


B1L102S, U11-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.49
Std Dev	3.14



B1L102S, U11-ch10, adc5

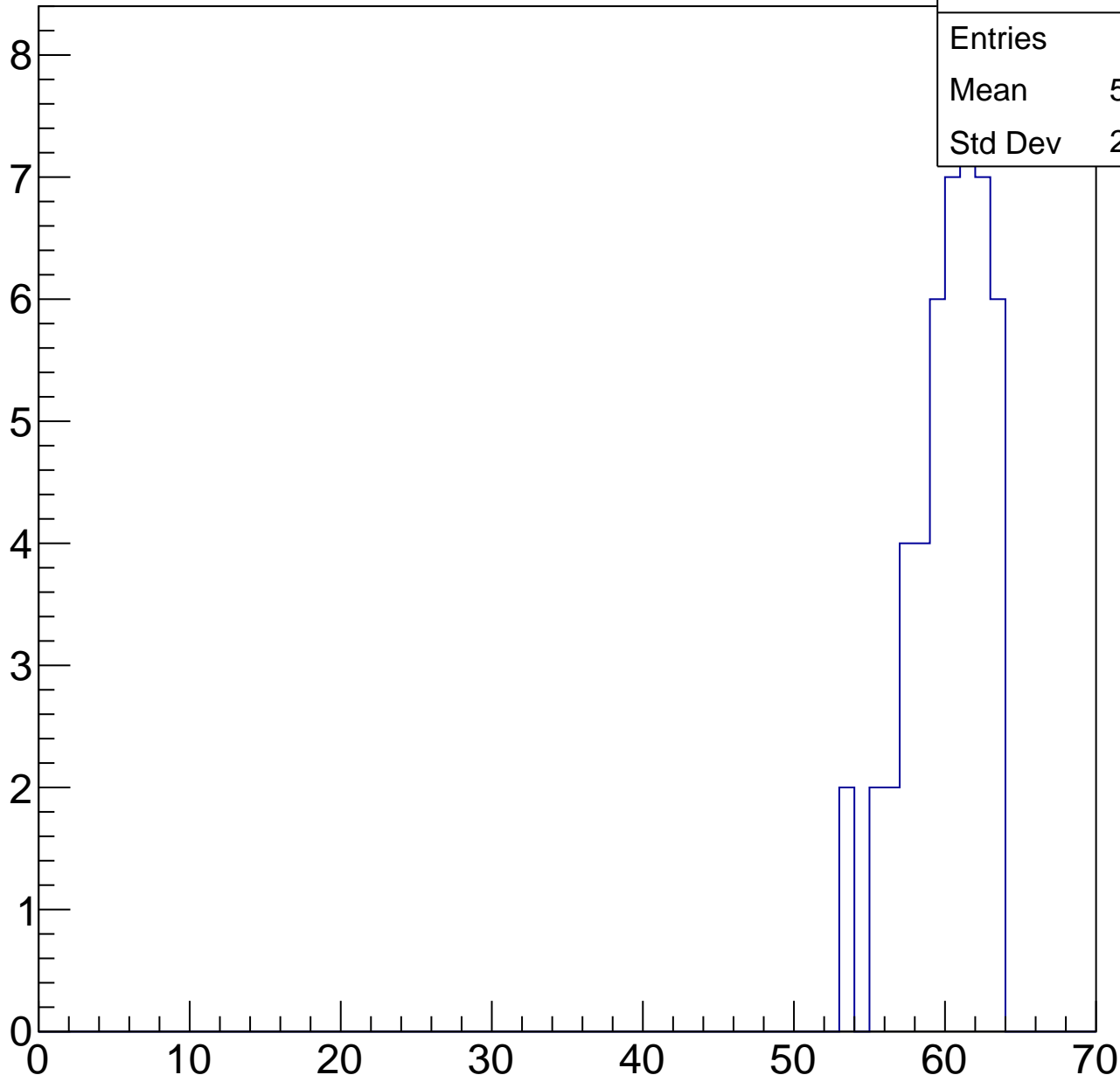
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.62
Std Dev	2.587

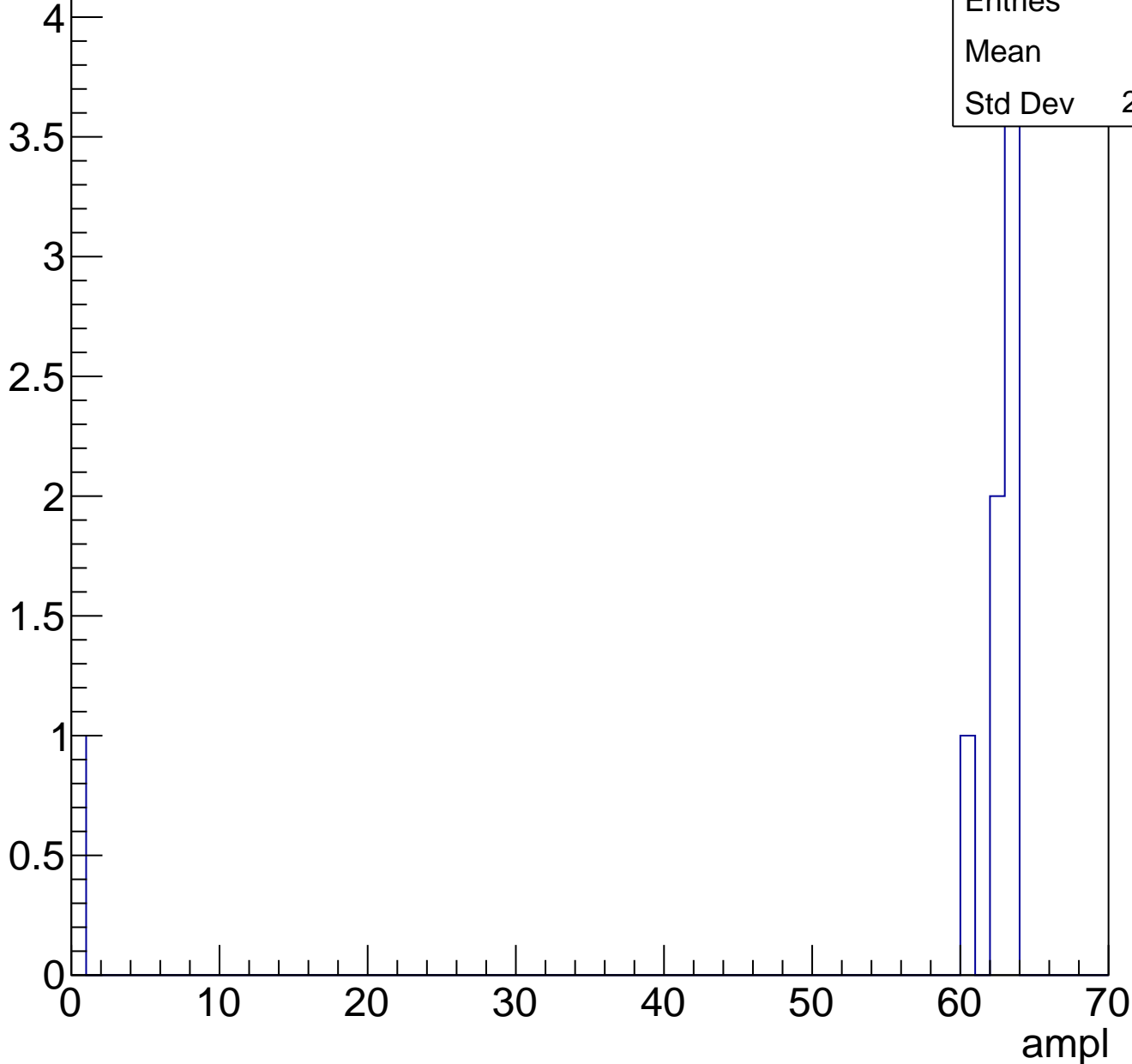
ampl



B1L102S, U11-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch11, adc0

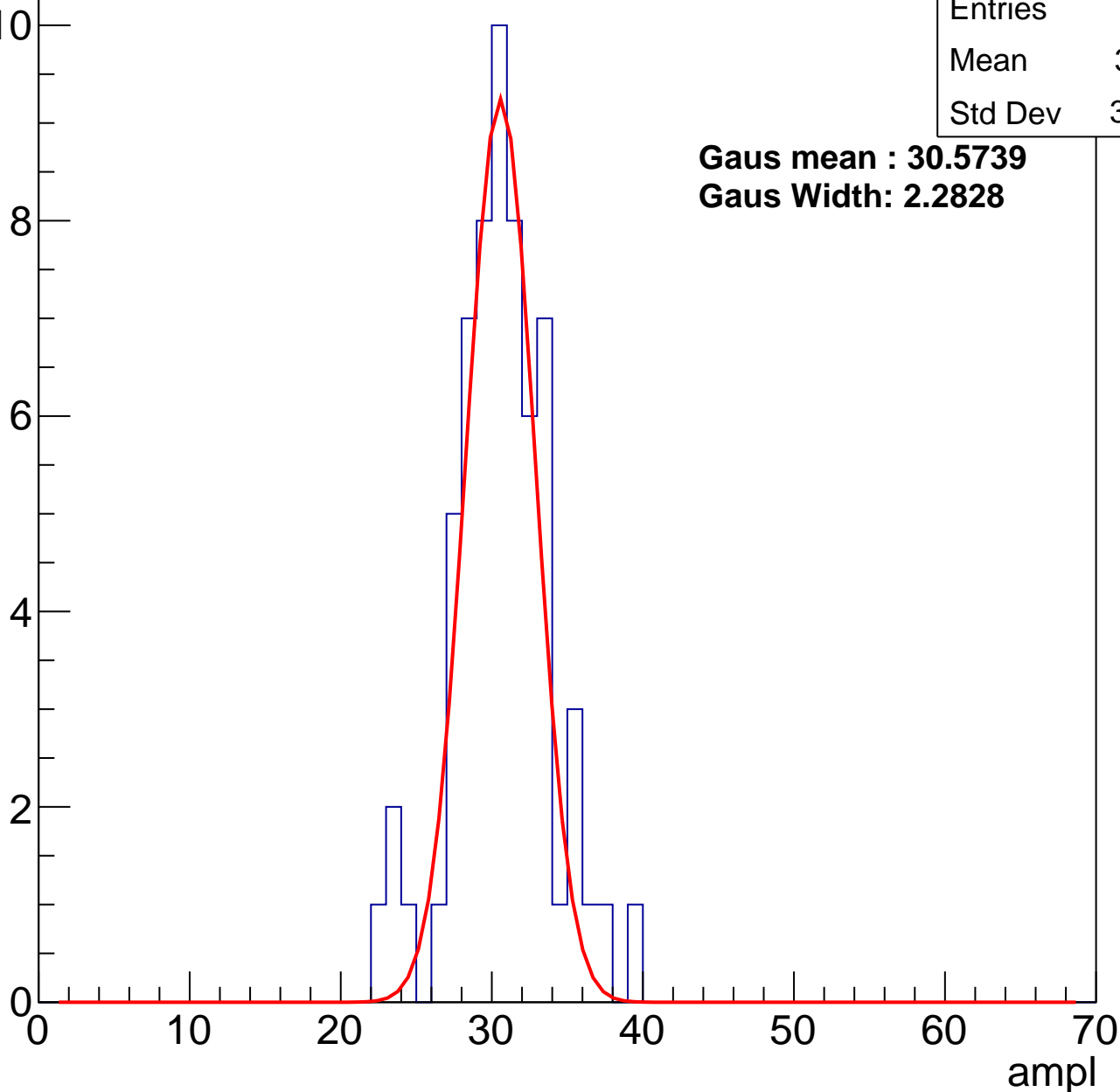
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.21
Std Dev	3.198

Gaus mean : 30.5739

Gaus Width: 2.2828



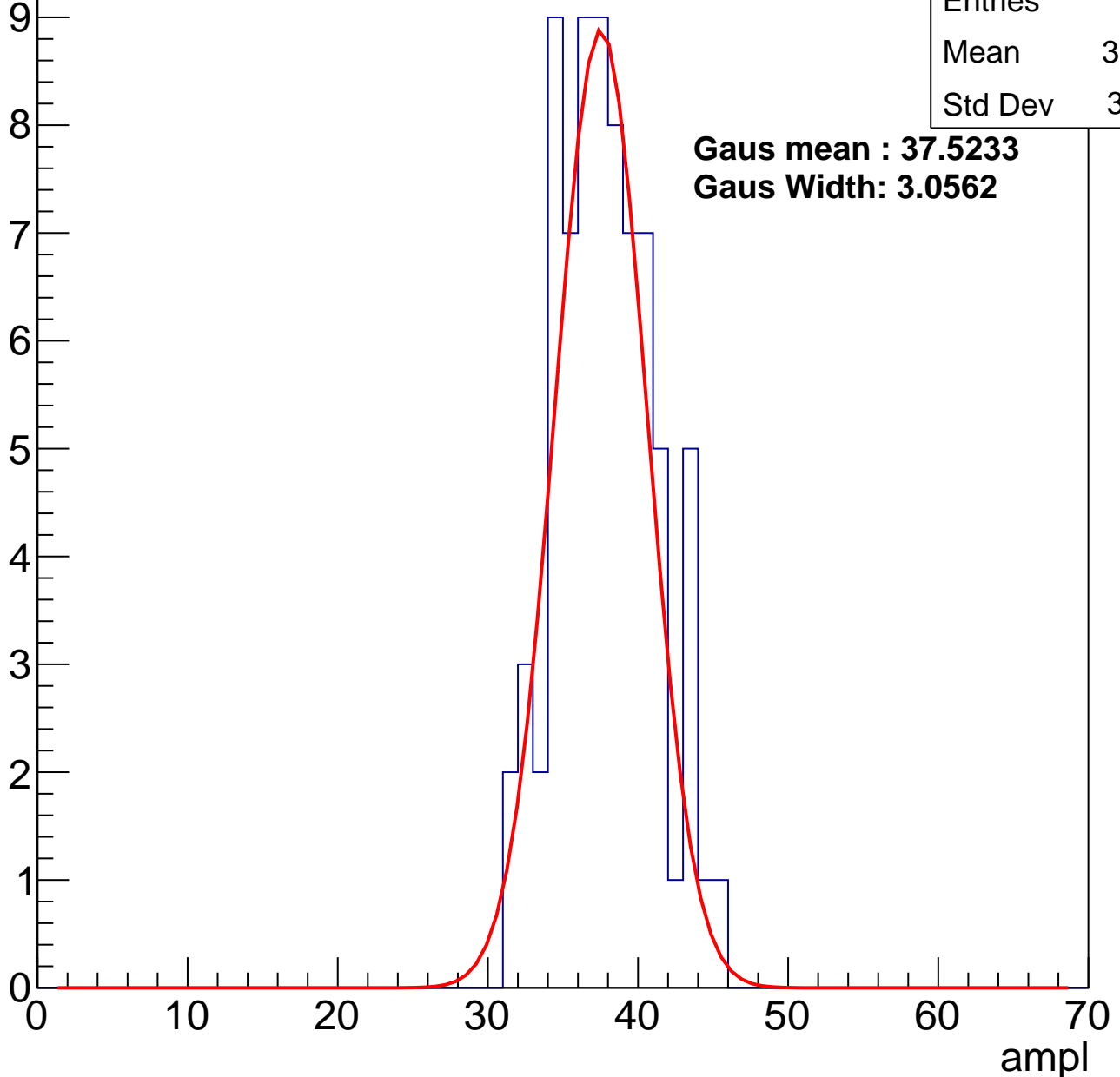
B1L102S, U11-ch11, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.37
Std Dev	3.211

Gaus mean : 37.5233
Gaus Width: 3.0562



B1L102S, U11-ch11, adc2

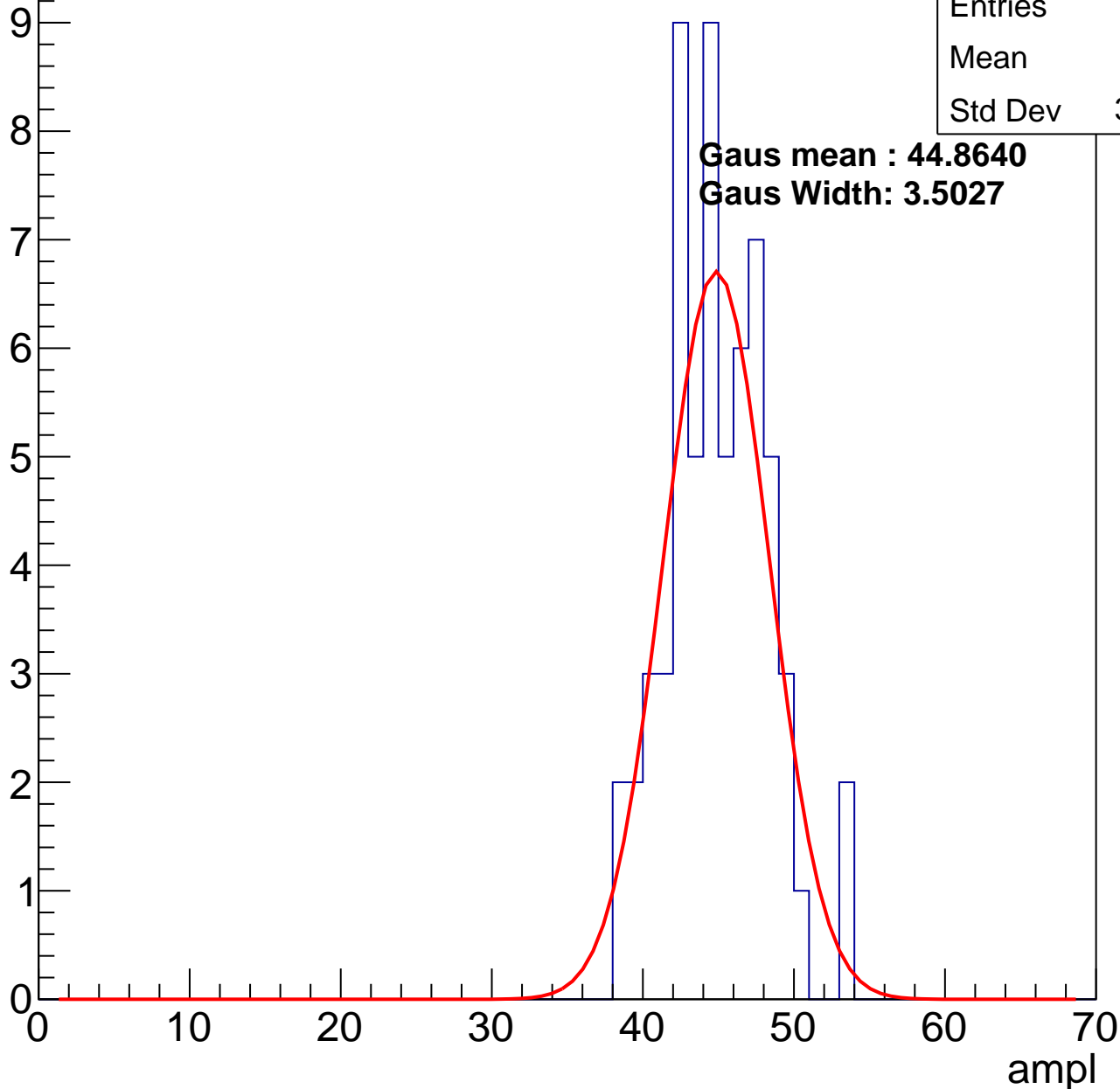
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.5
Std Dev	3.281

Gaus mean : 44.8640

Gaus Width: 3.5027

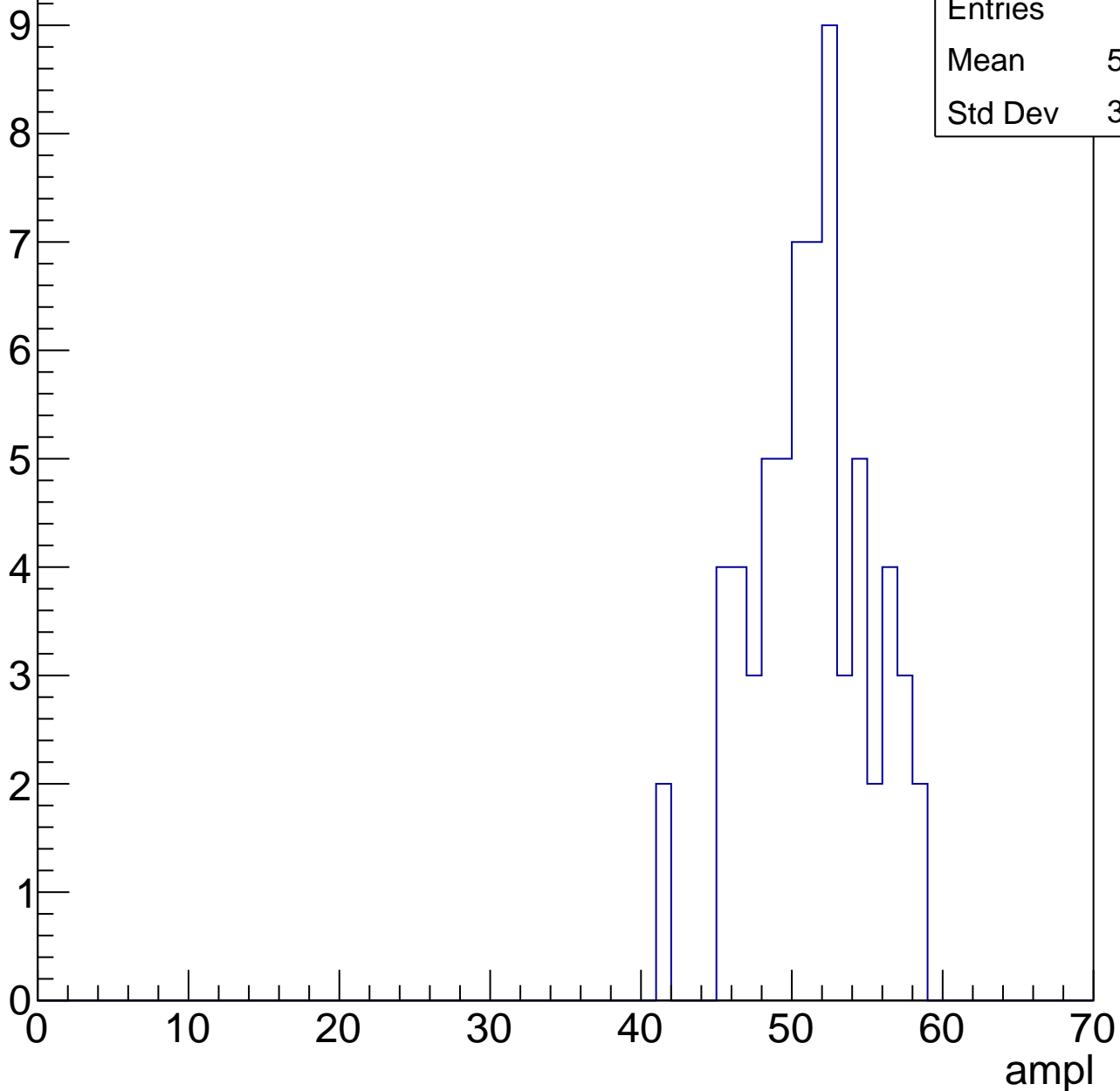


B1L102S, U11-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	50.72
Std Dev	3.849

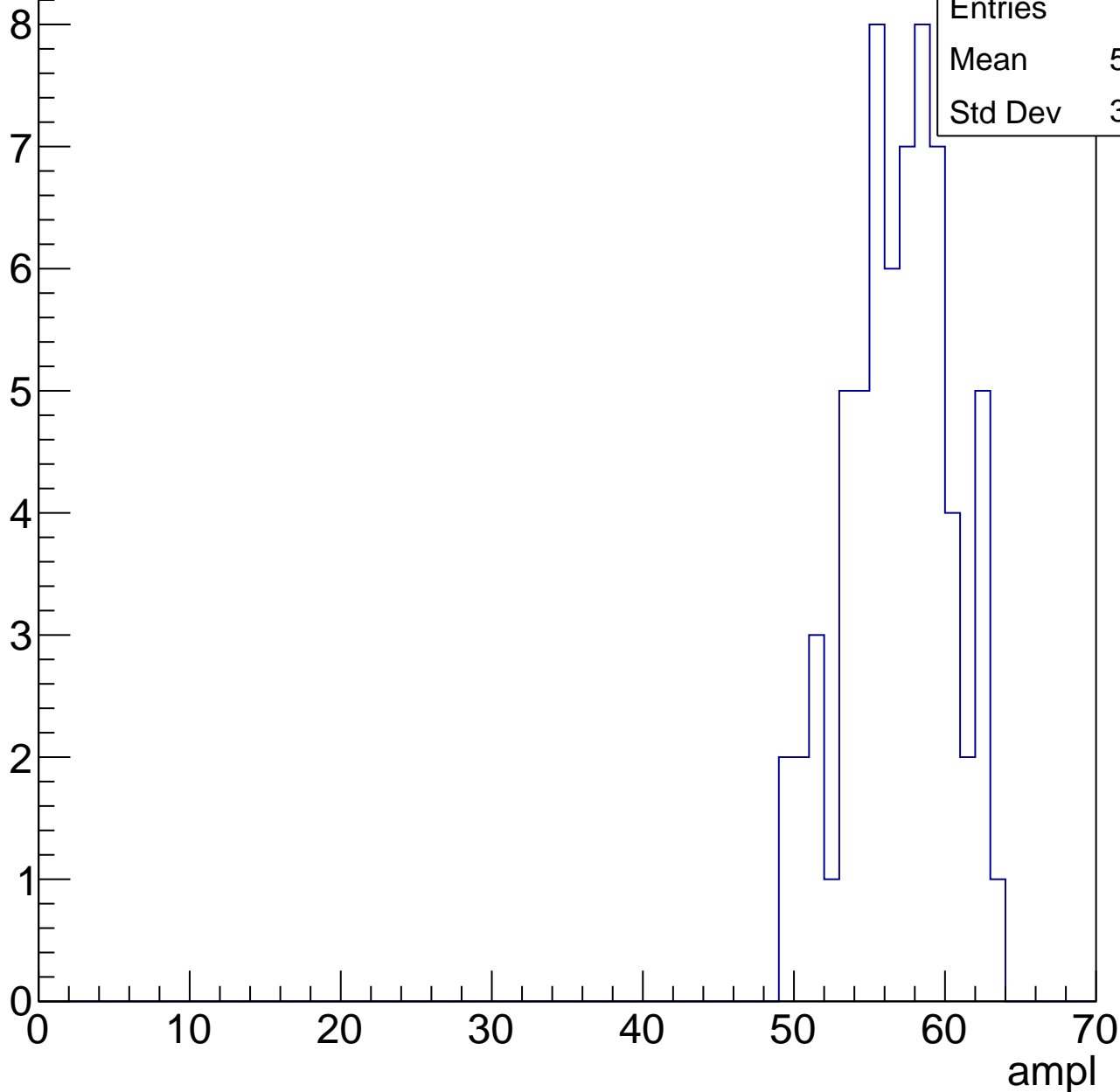


B1L102S, U11-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

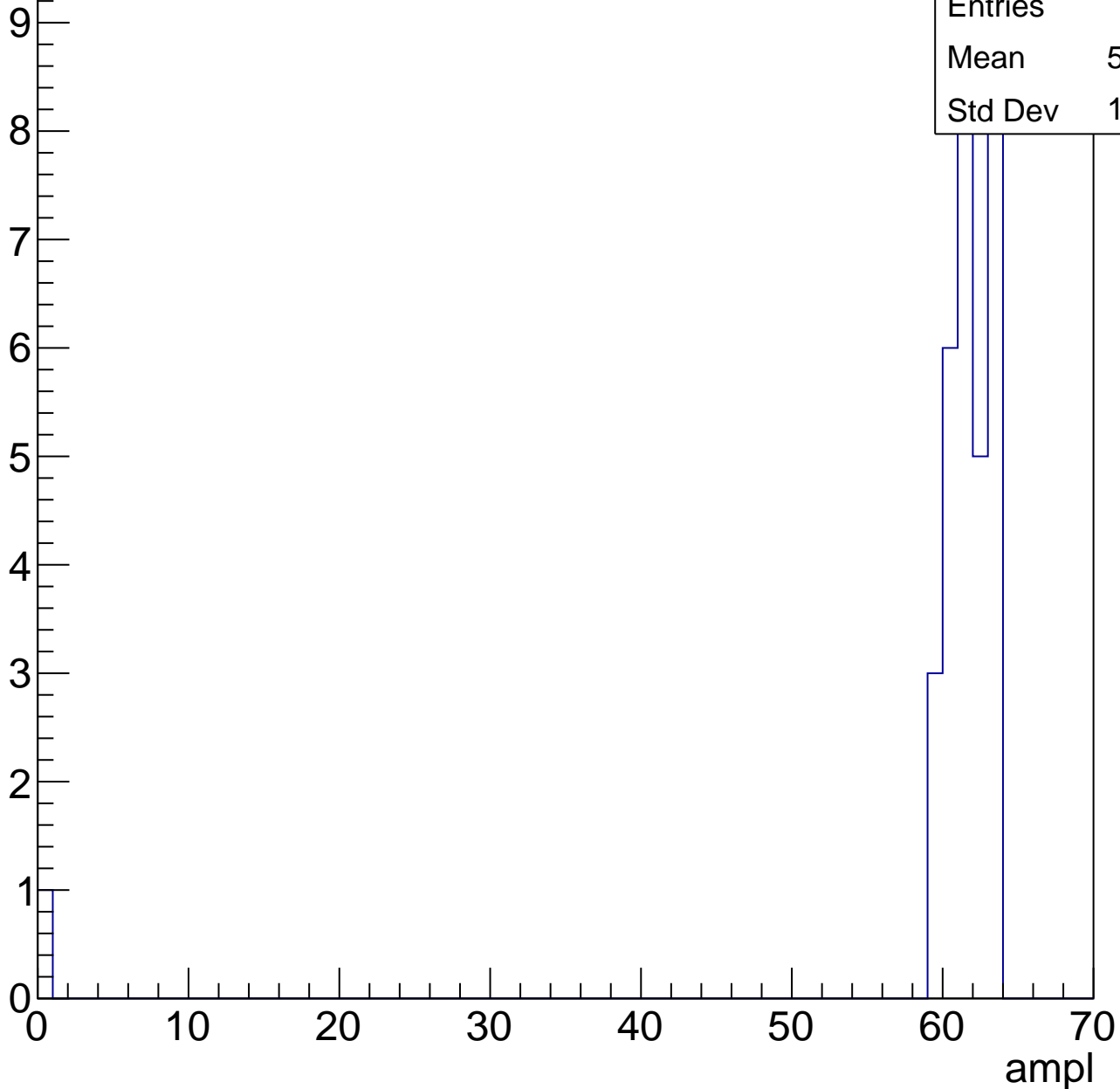
Entries	66
Mean	56.44
Std Dev	3.407



B1L102S, U11-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch12, adc0

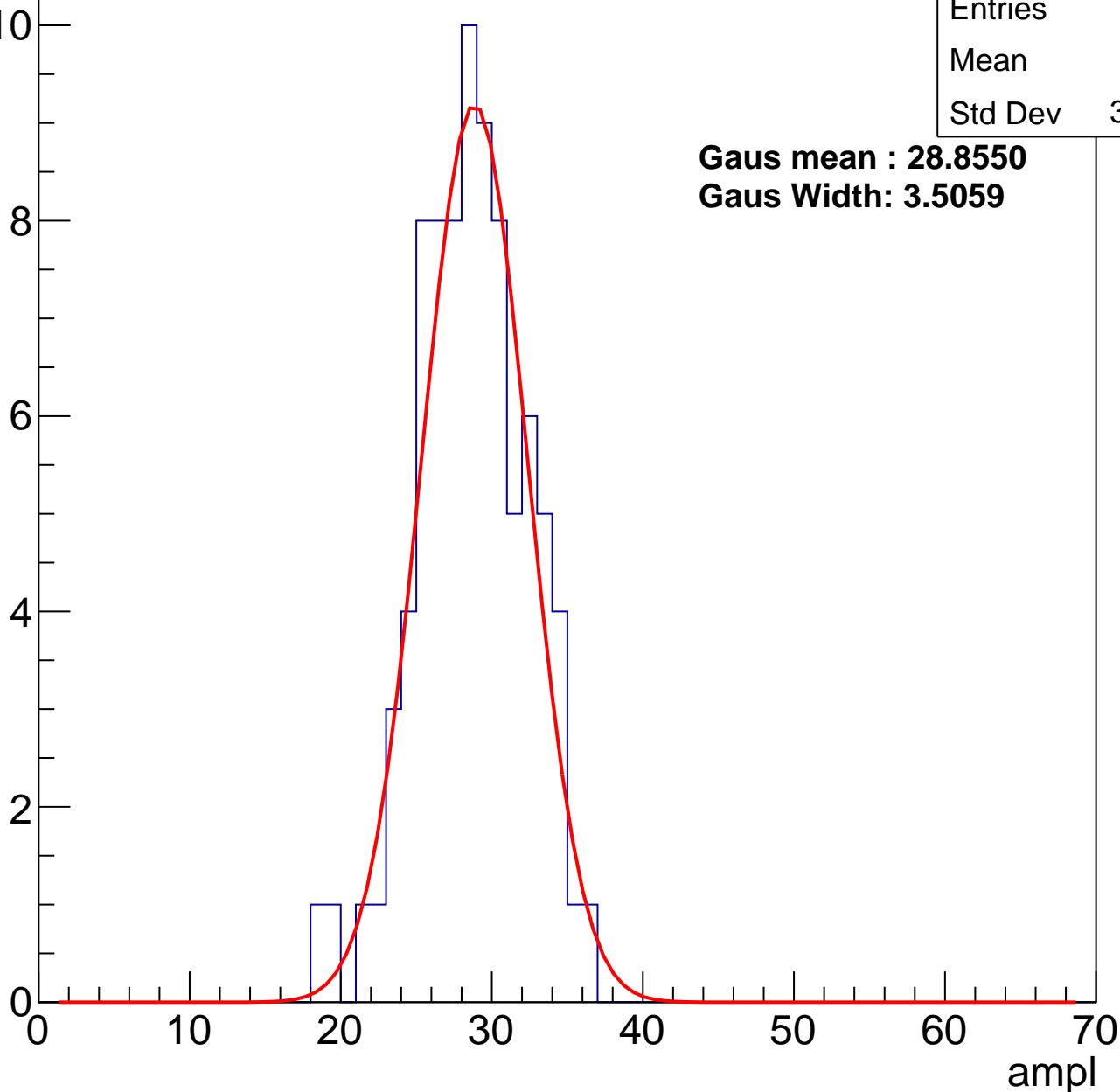
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.2
Std Dev	3.582

Gaus mean : 28.8550

Gaus Width: 3.5059



B1L102S, U11-ch12, adc1

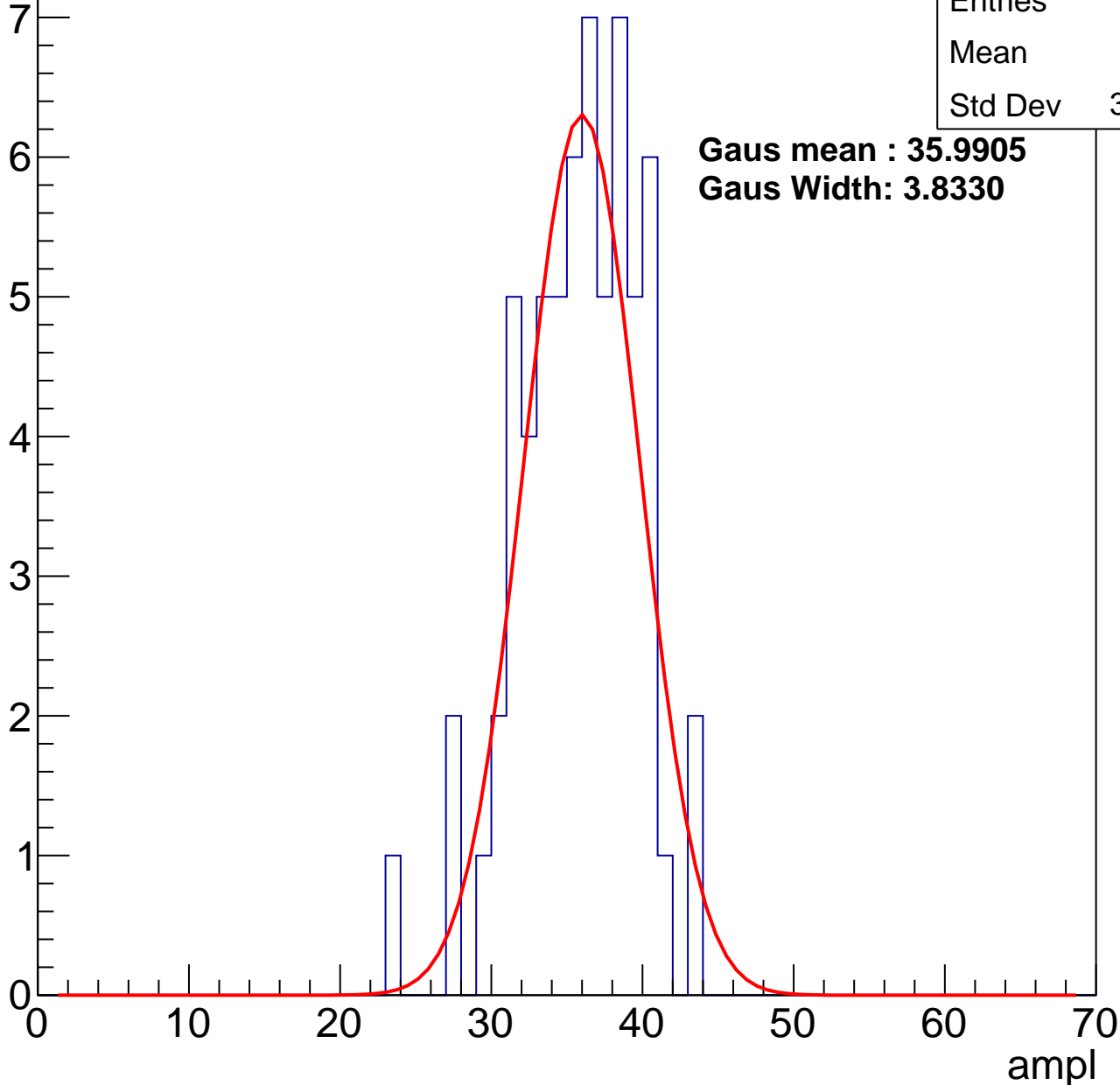
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.3
Std Dev	3.908

Gaus mean : 35.9905

Gaus Width: 3.8330



B1L102S, U11-ch12, adc2

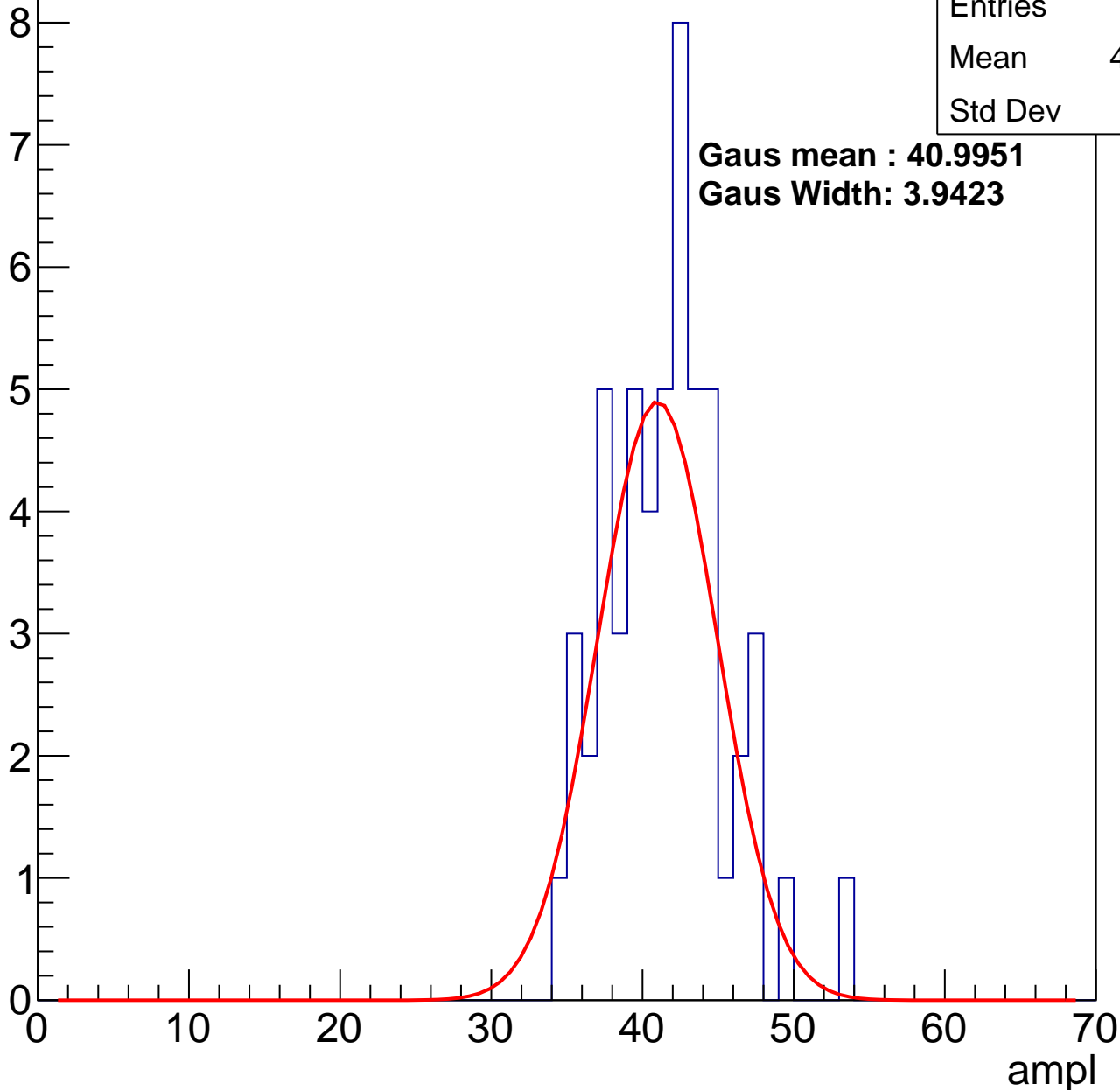
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.13
Std Dev	3.83

Gaus mean : 40.9951

Gaus Width: 3.9423

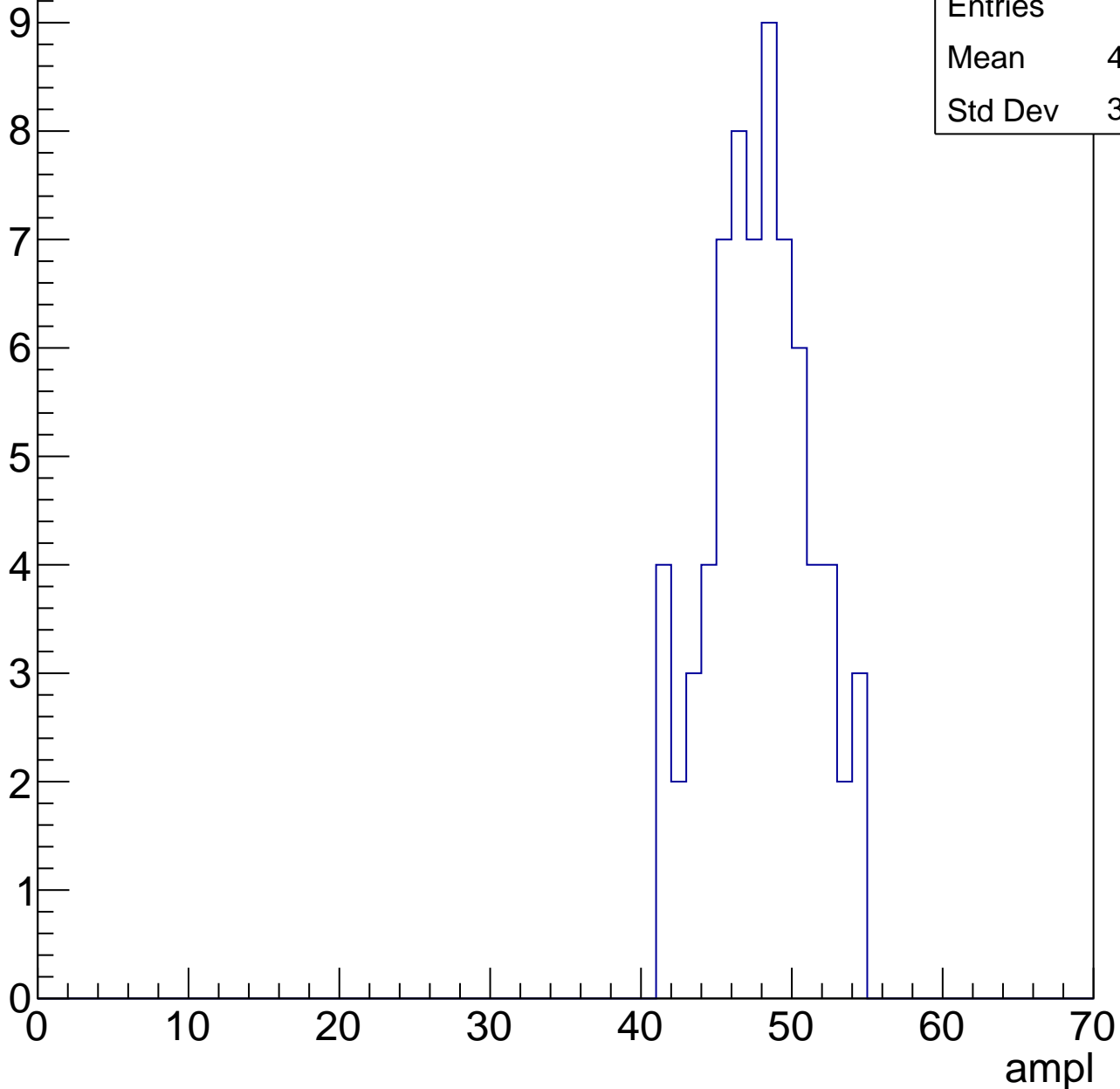


B1L102S, U11-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

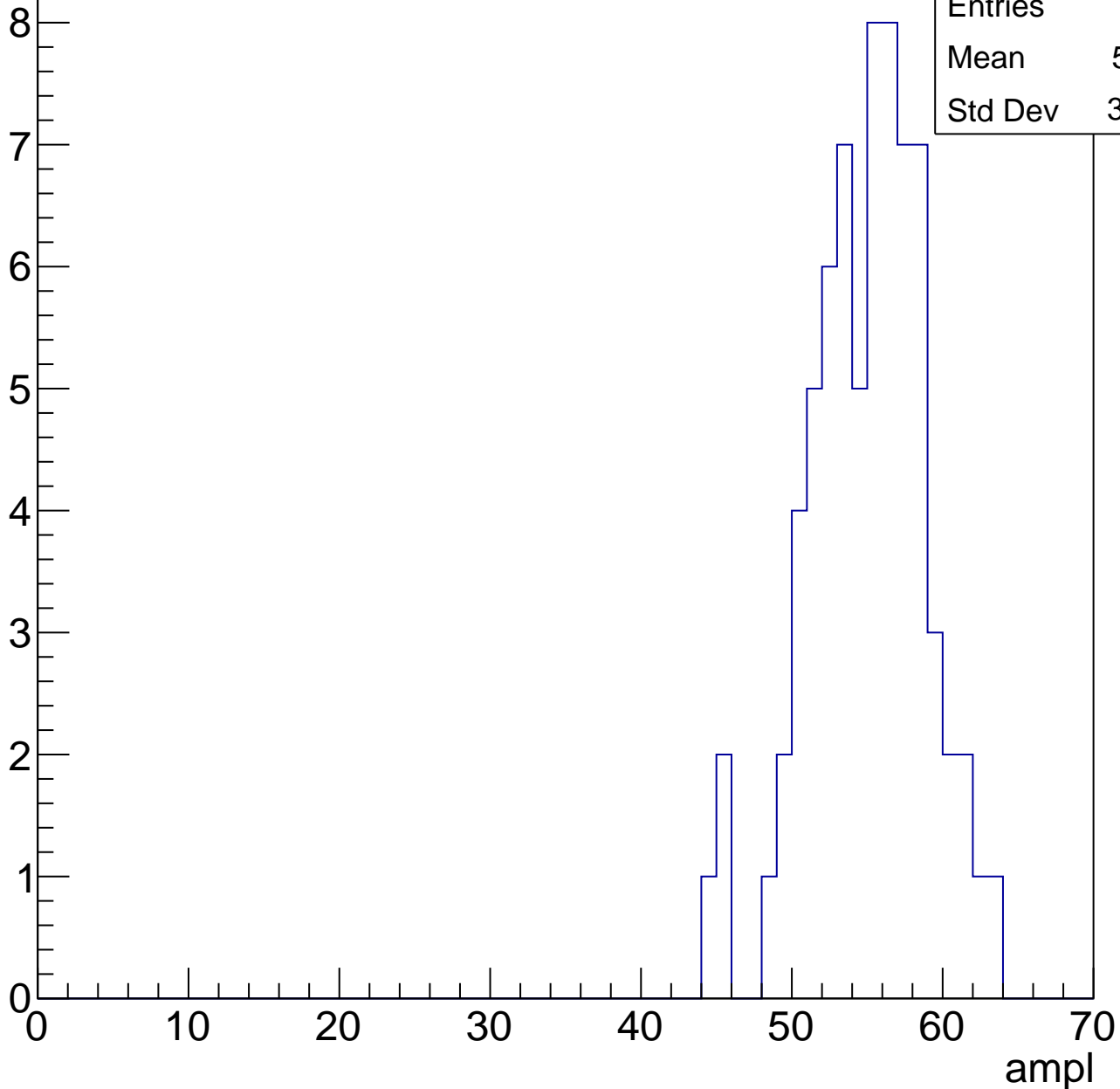
Entries	70
Mean	47.43
Std Dev	3.328



B1L102S, U11-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

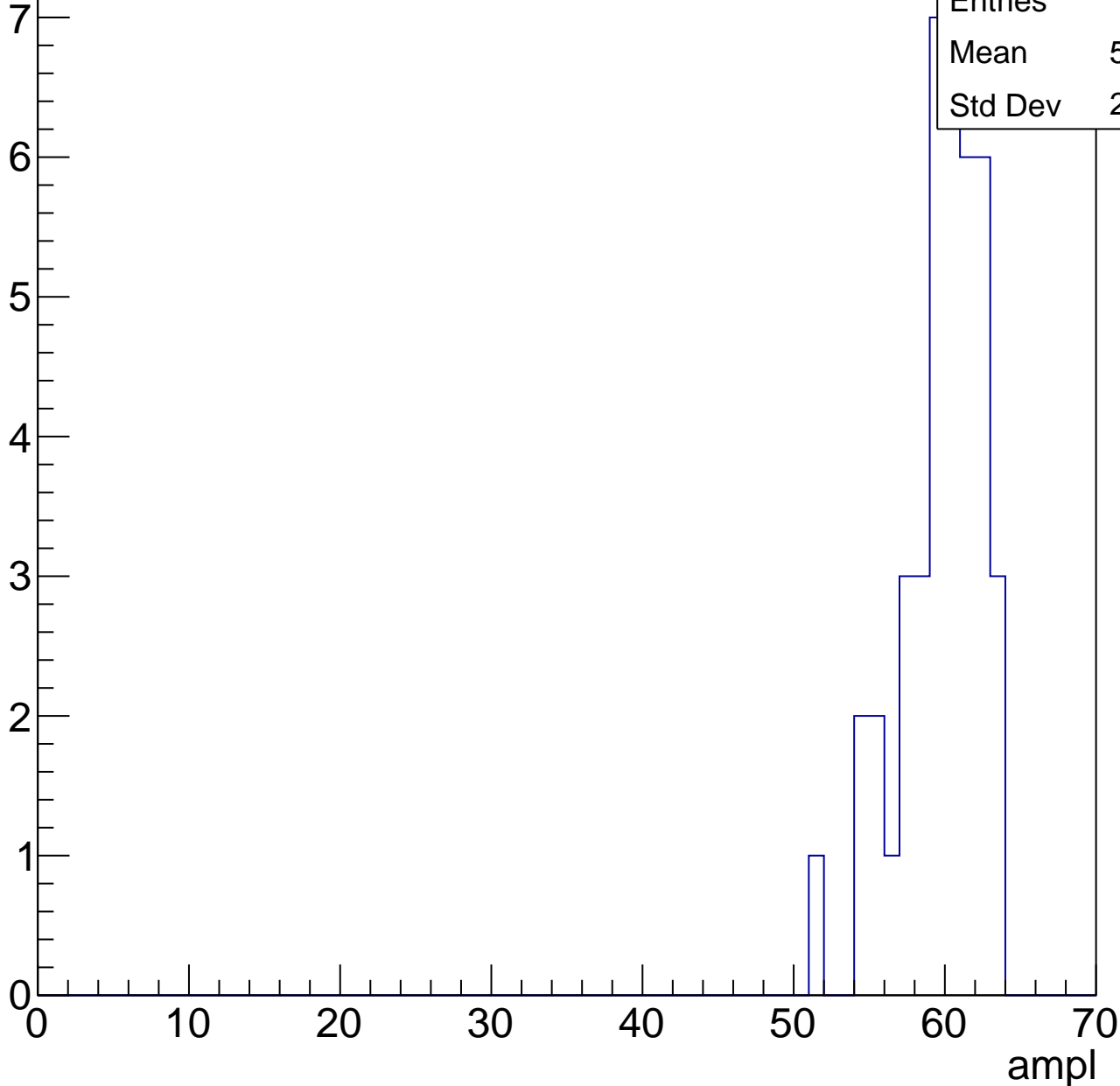


B1L102S, U11-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	59.27
Std Dev	2.705

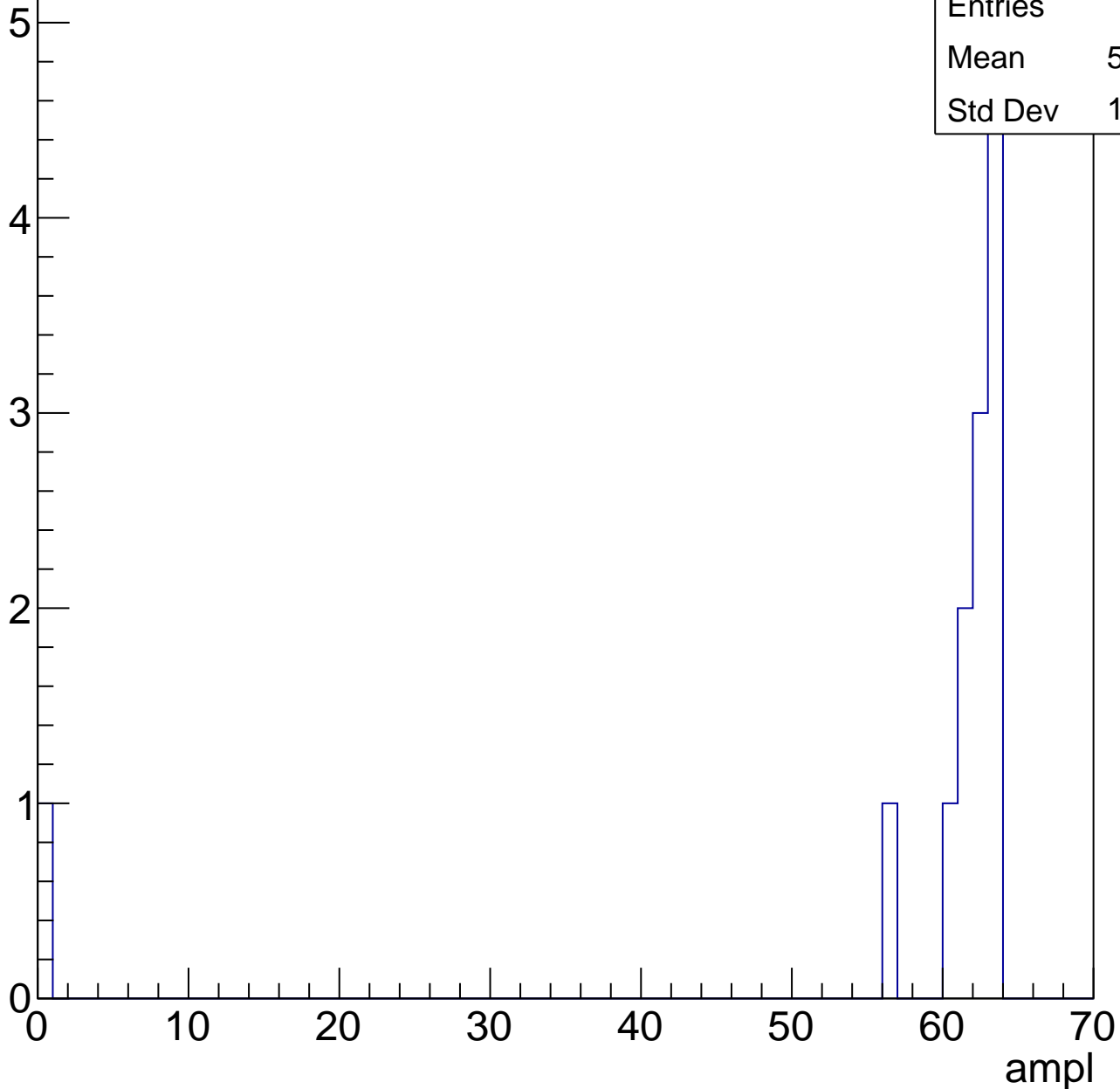


B1L102S, U11-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

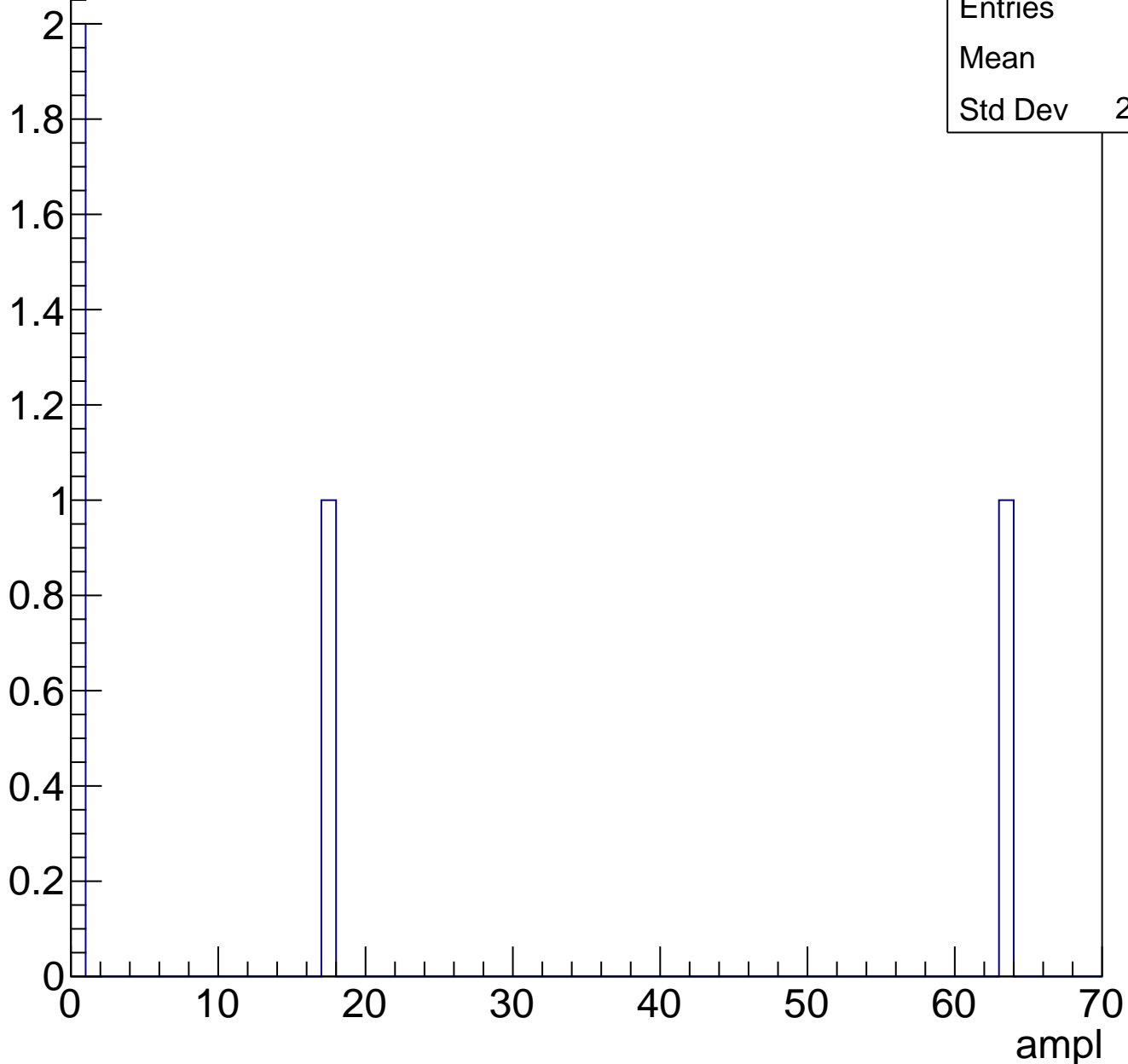
Entries	13
Mean	56.85
Std Dev	16.52



B1L102S, U11-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20
Std Dev	25.78

B1L102S, U11-ch13, adc0

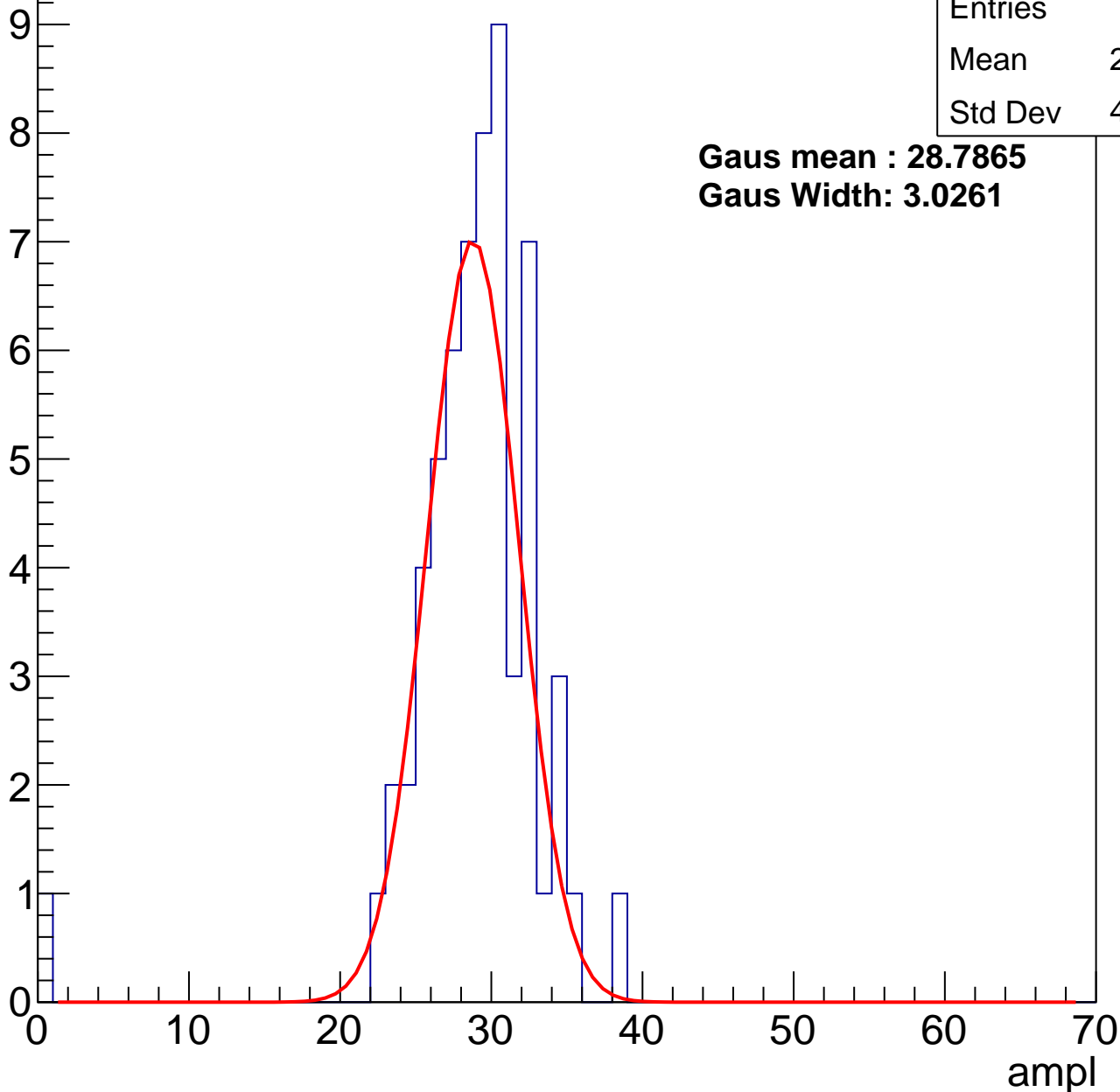
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.38
Std Dev	4.825

Gaus mean : 28.7865

Gaus Width: 3.0261



B1L102S, U11-ch13, adc1

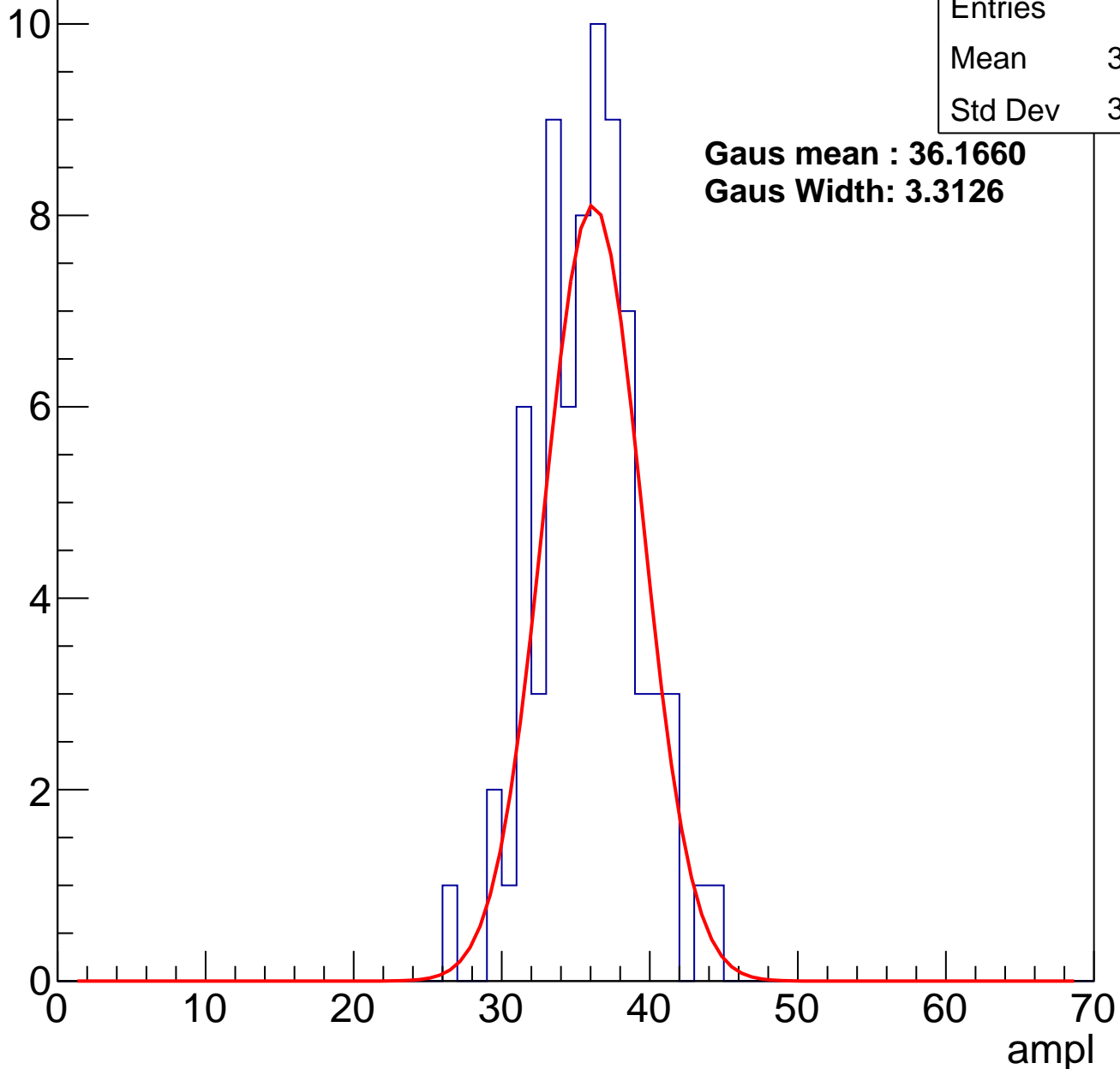
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	35.38
Std Dev	3.346

Gaus mean : 36.1660

Gaus Width: 3.3126

Entry



B1L102S, U11-ch13, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	42.51
Std Dev	3.451

Gaus mean : 43.0595

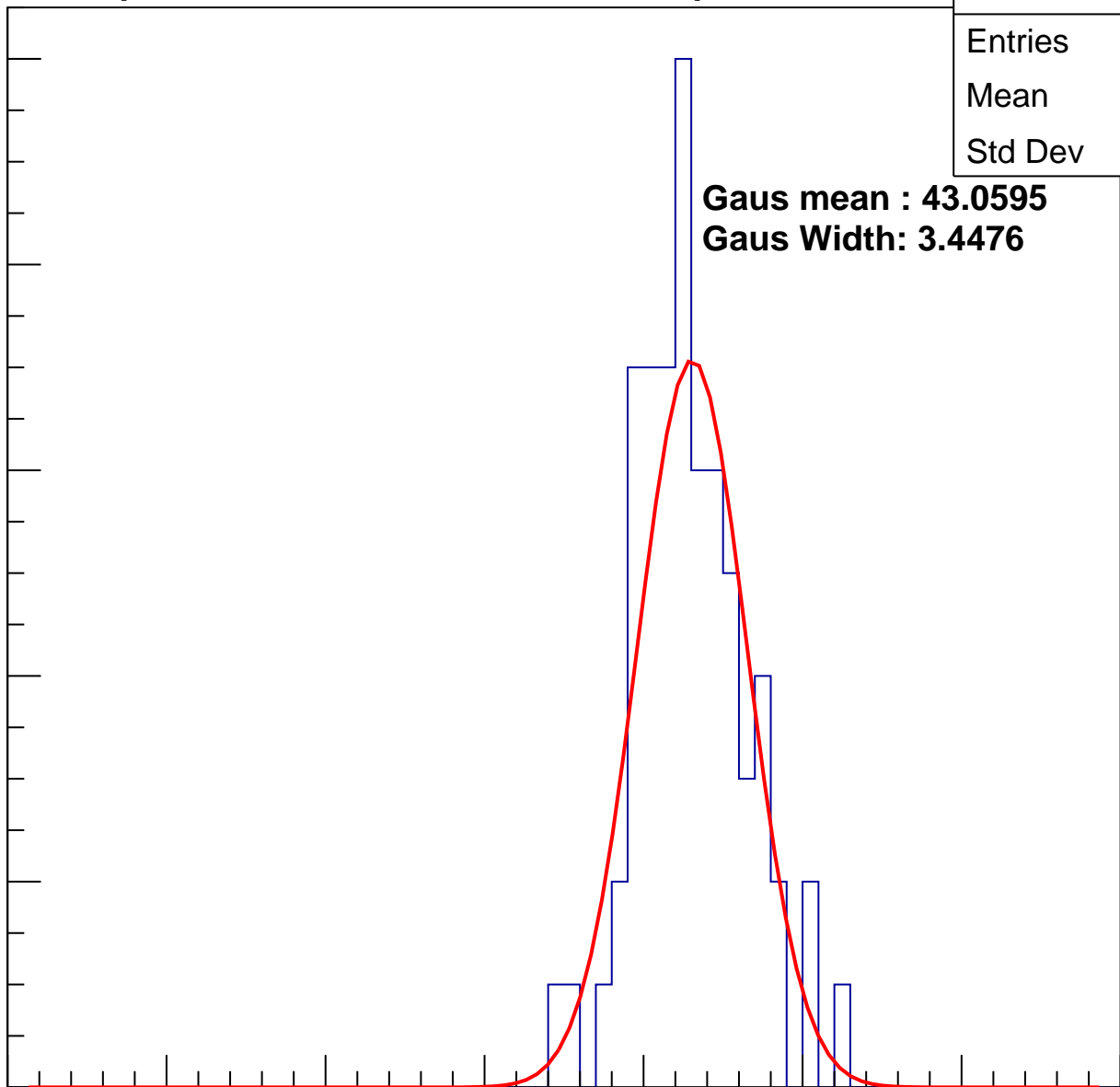
Gaus Width: 3.4476

Entry

10
8
6
4
2
0

ampl

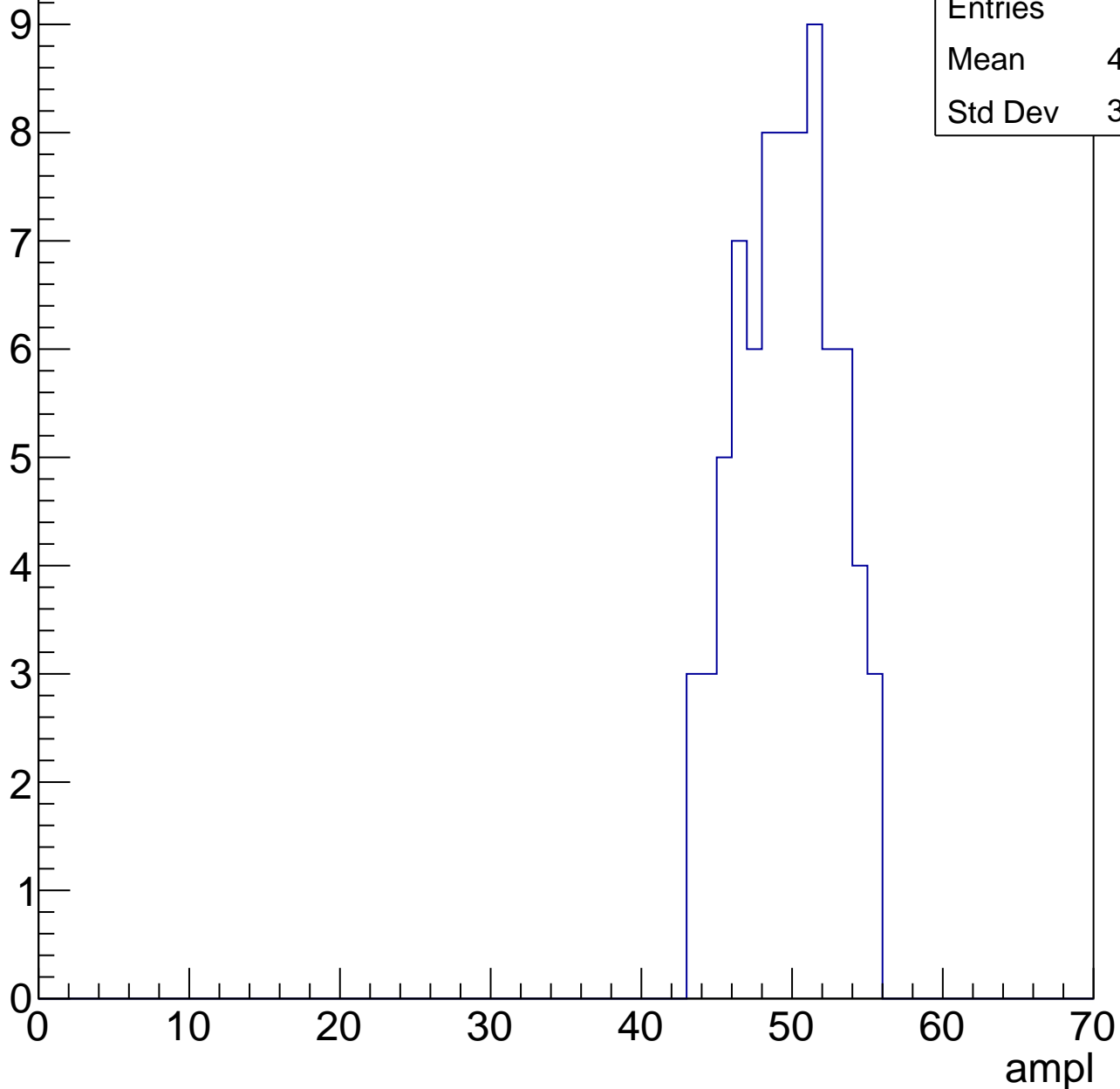
0 10 20 30 40 50 60 70



B1L102S, U11-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



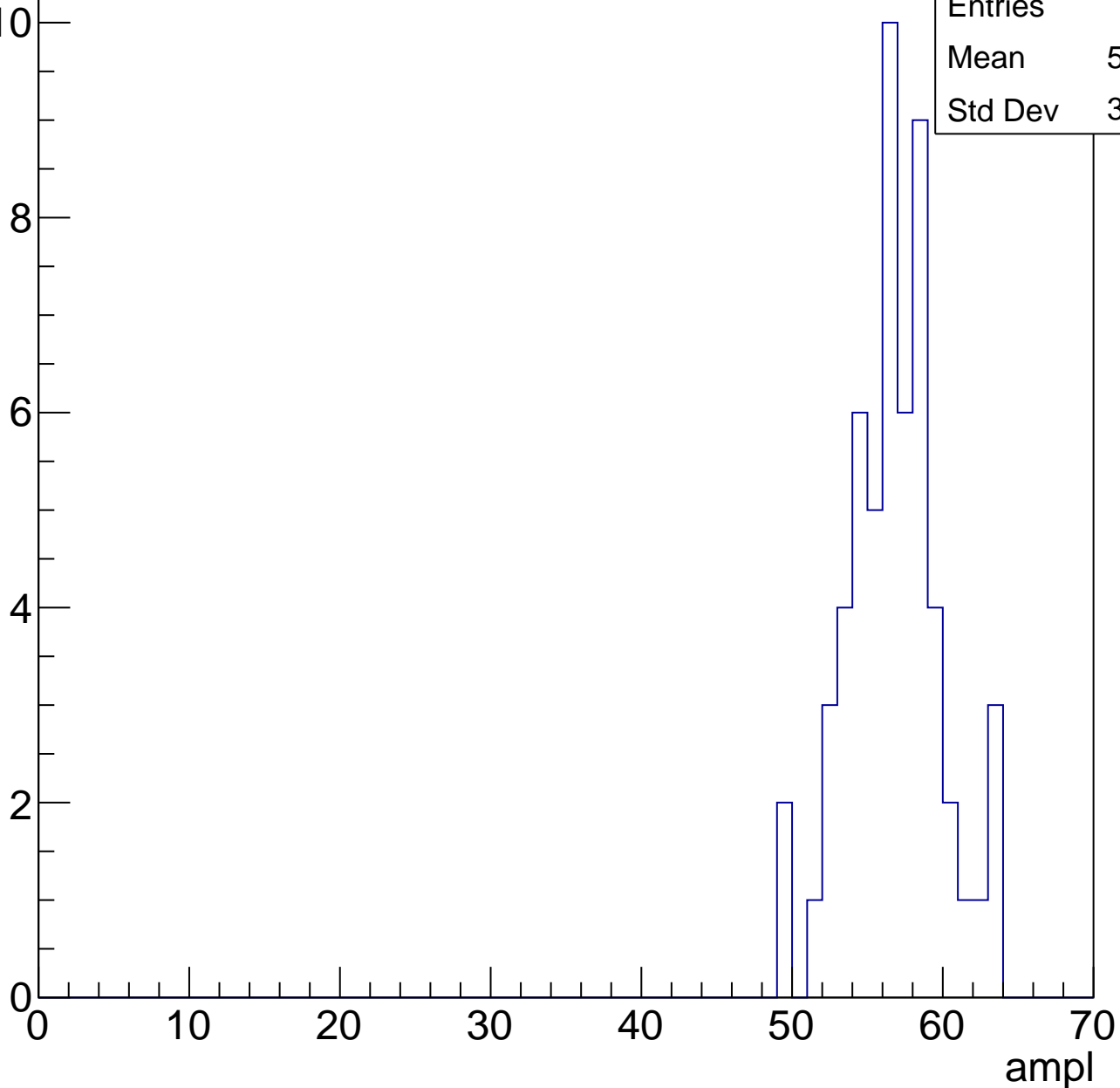
Entries	76
Mean	49.16
Std Dev	3.158

B1L102S, U11-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.28
Std Dev	3.105

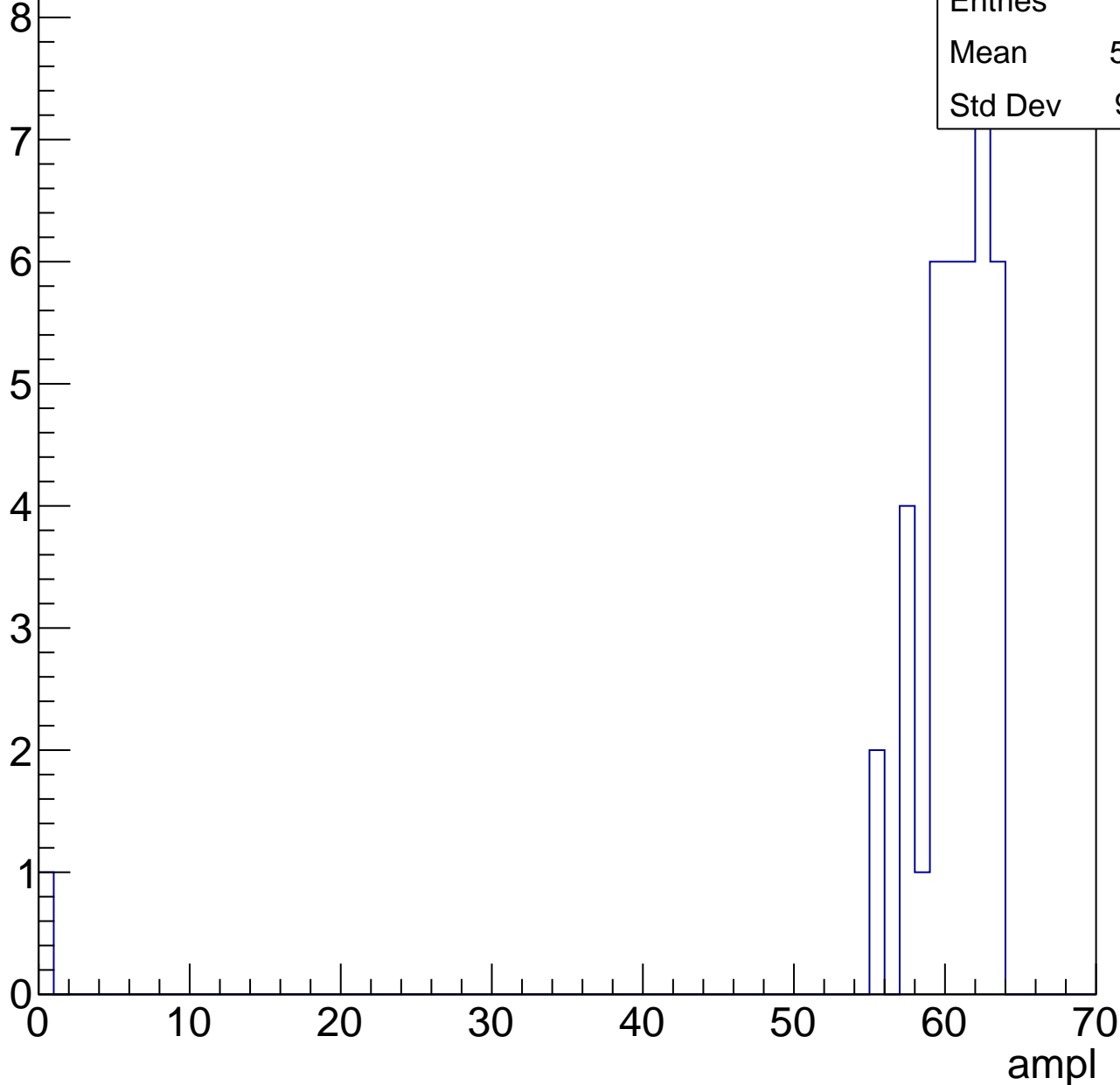


B1L102S, U11-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

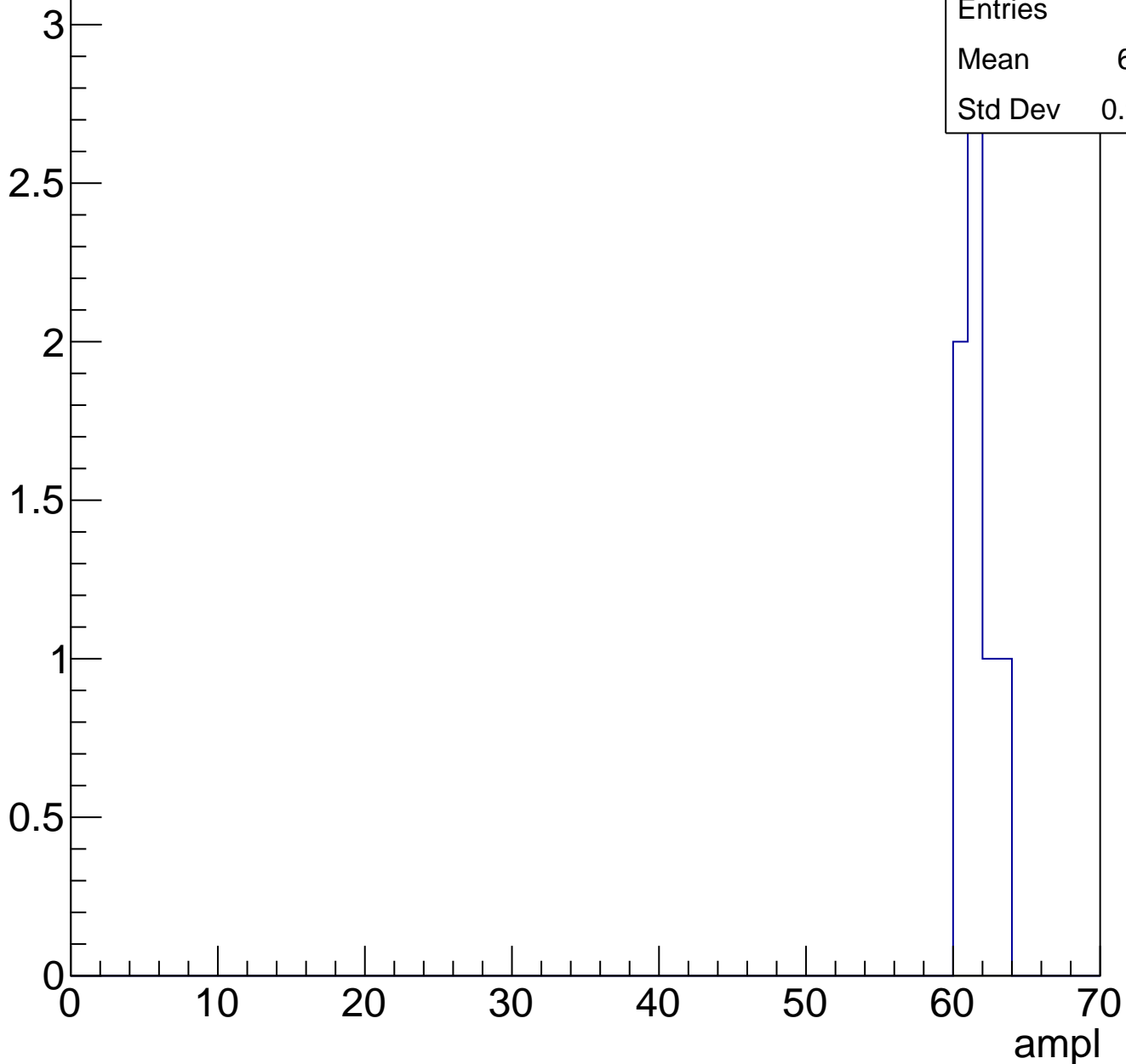
Entries	40
Mean	58.75
Std Dev	9.651



B1L102S, U11-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch14, adc0

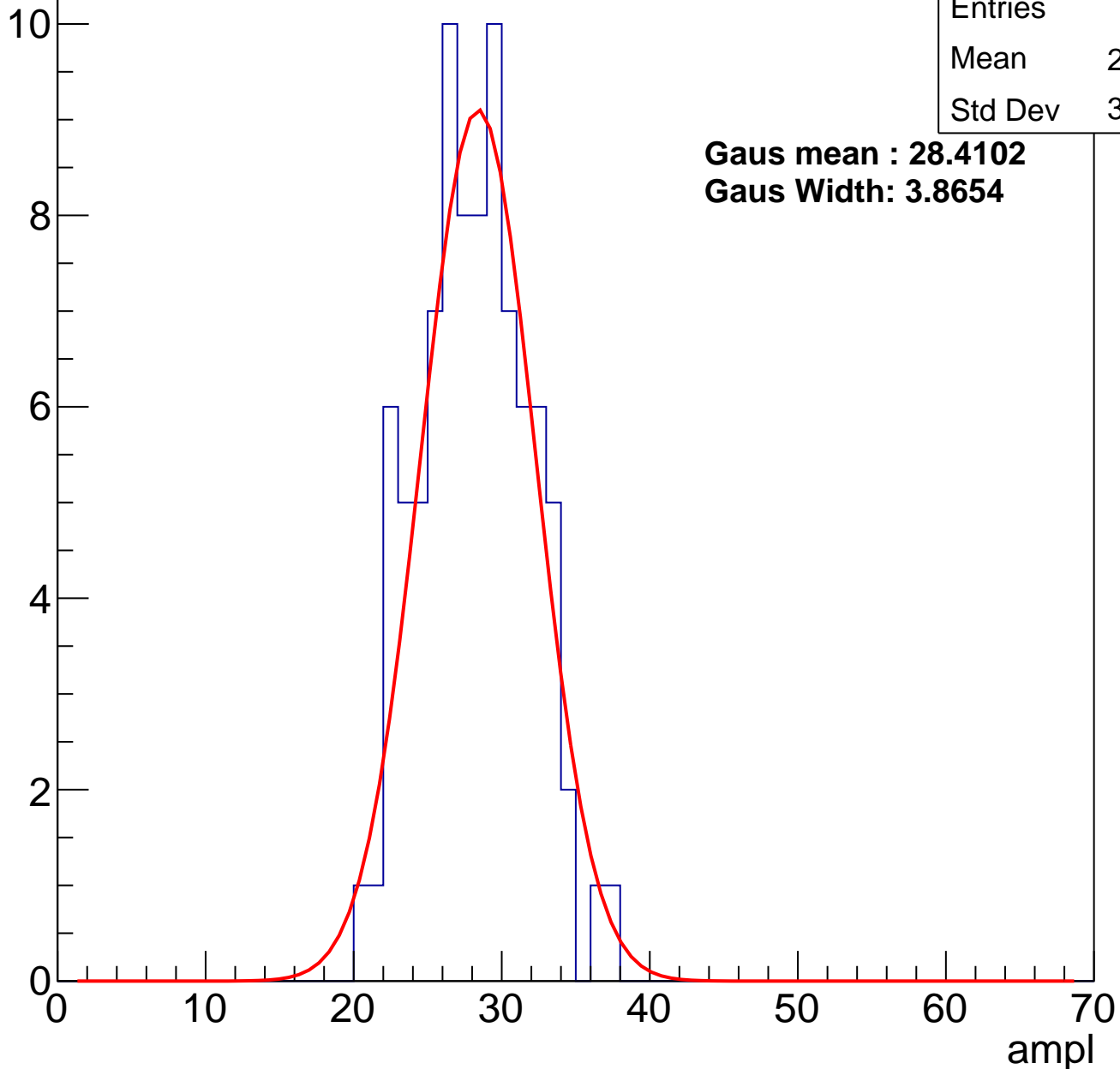
calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	27.72
Std Dev	3.623

Gaus mean : 28.4102

Gaus Width: 3.8654

Entry



B1L102S, U11-ch14, adc1

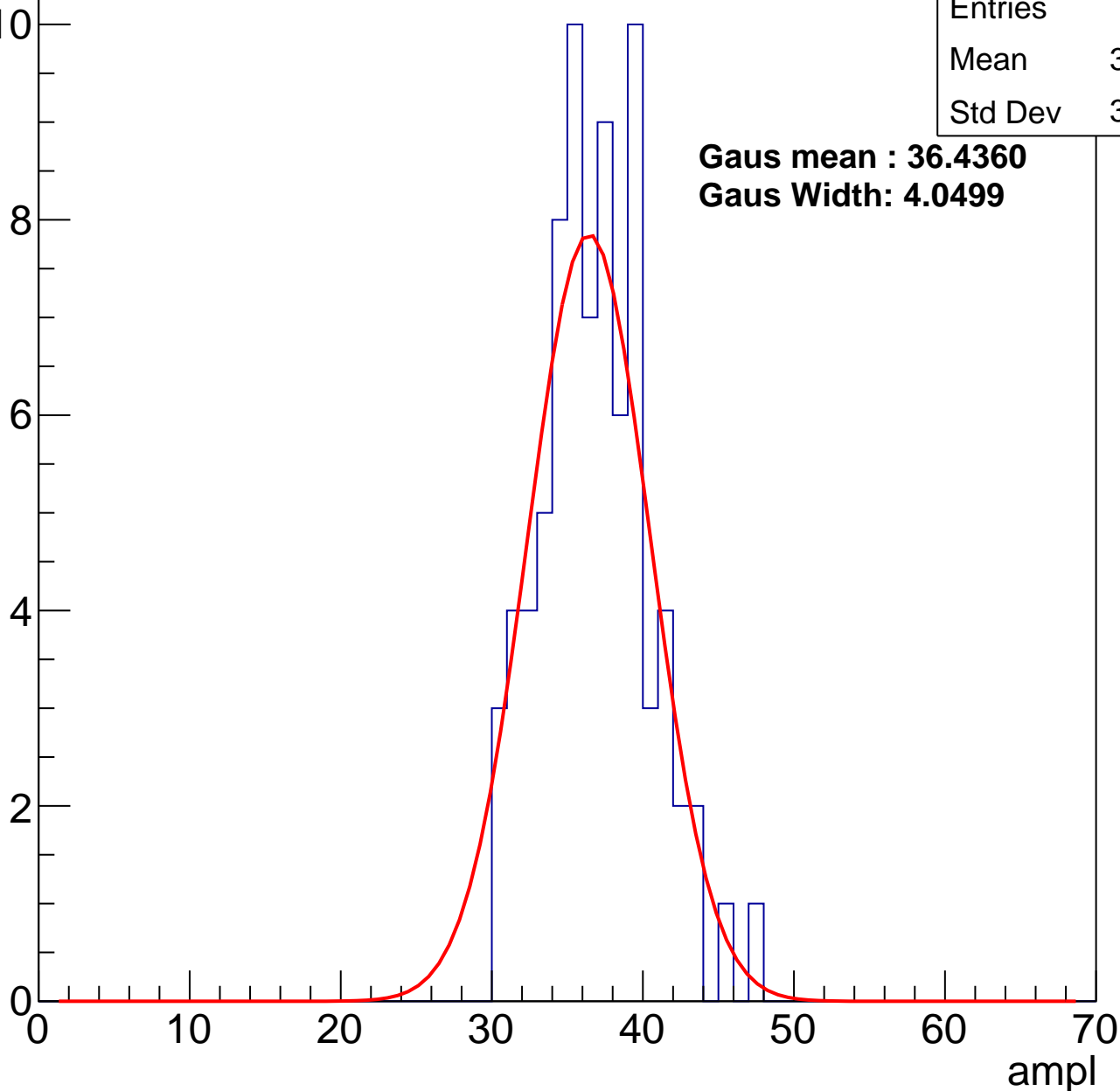
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.43
Std Dev	3.535

Gaus mean : 36.4360

Gaus Width: 4.0499



B1L102S, U11-ch14, adc2

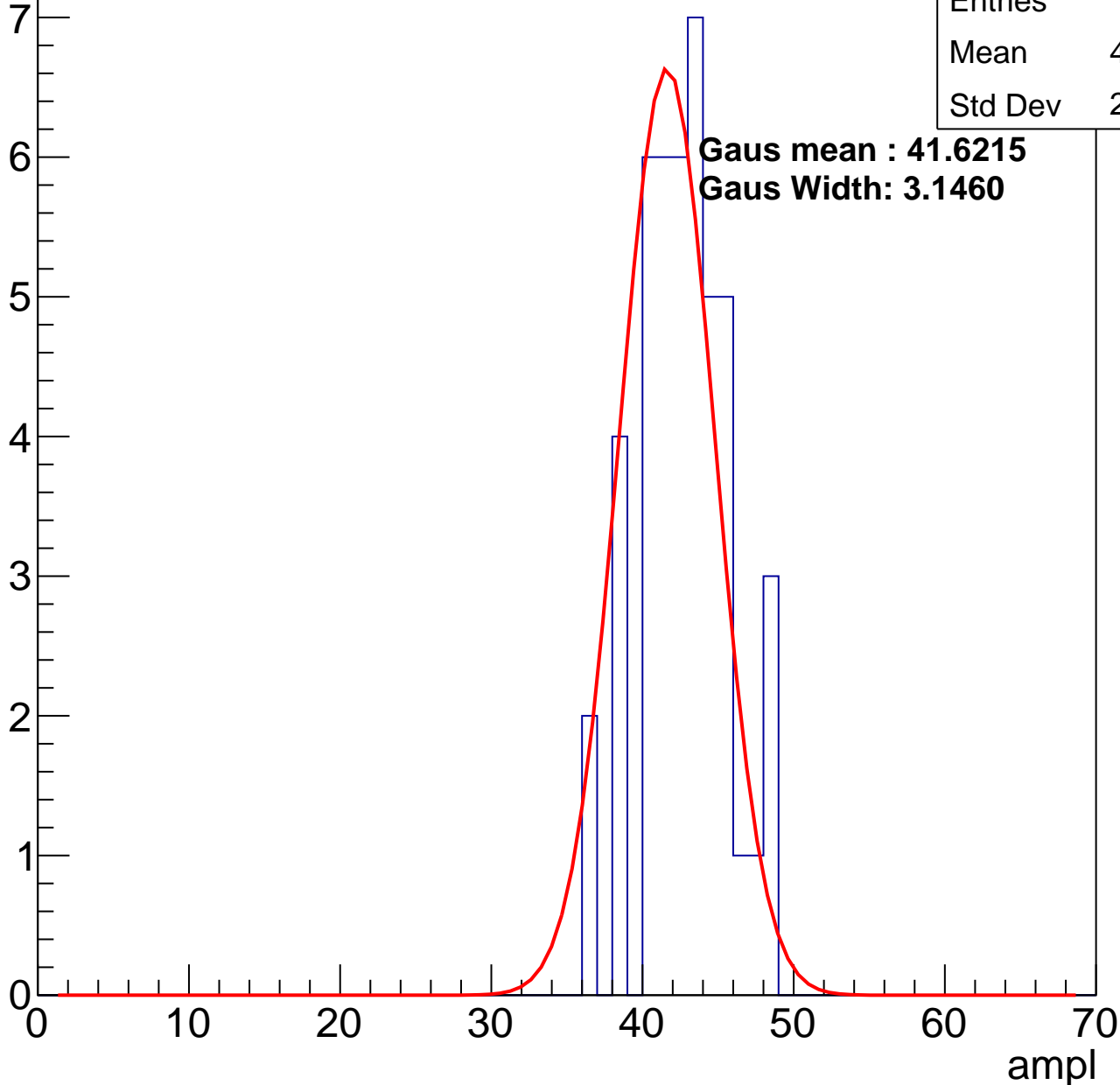
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	42.28
Std Dev	2.887

Gaus mean : 41.6215

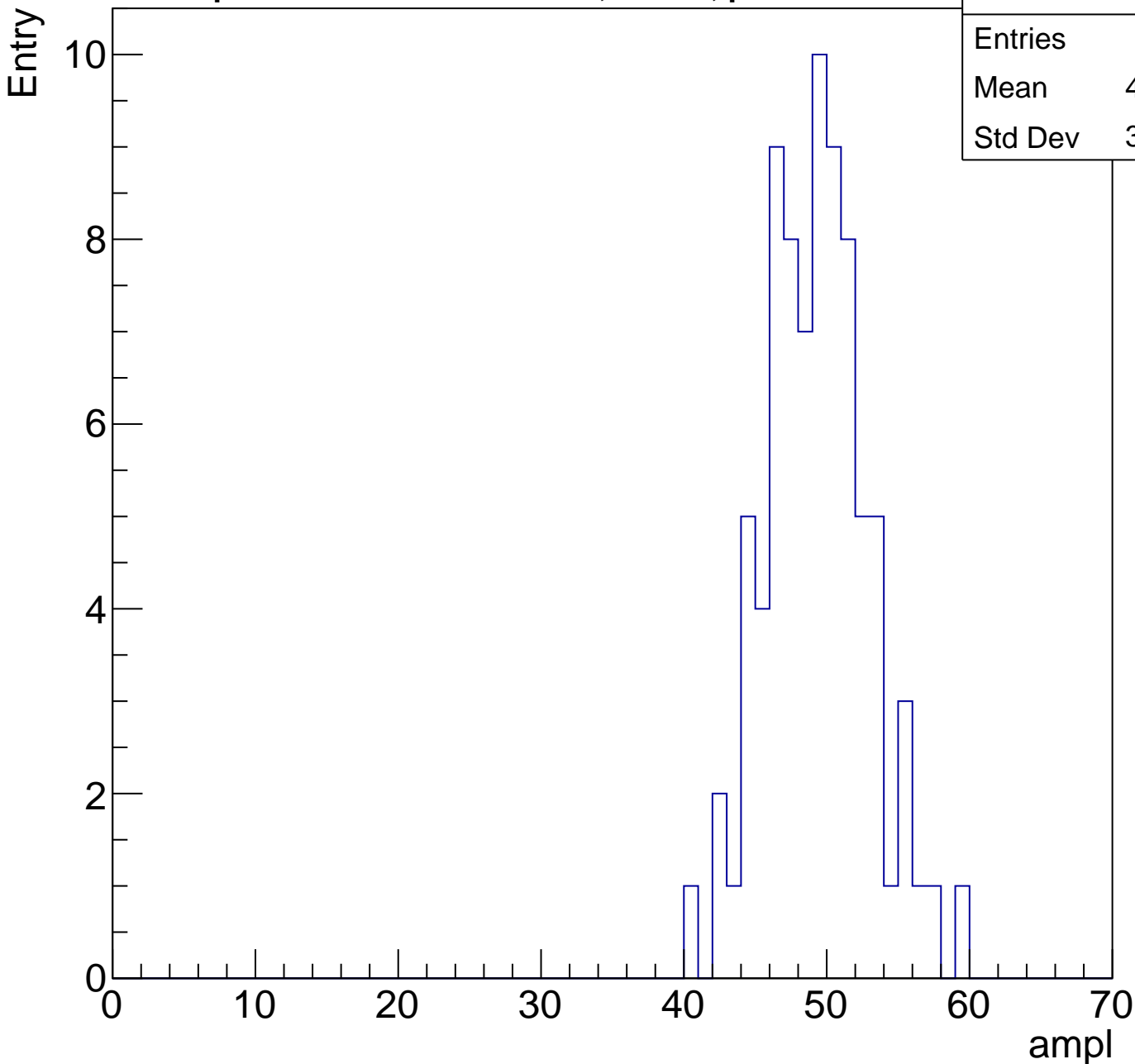
Gaus Width: 3.1460



B1L102S, U11-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	48.85
Std Dev	3.573

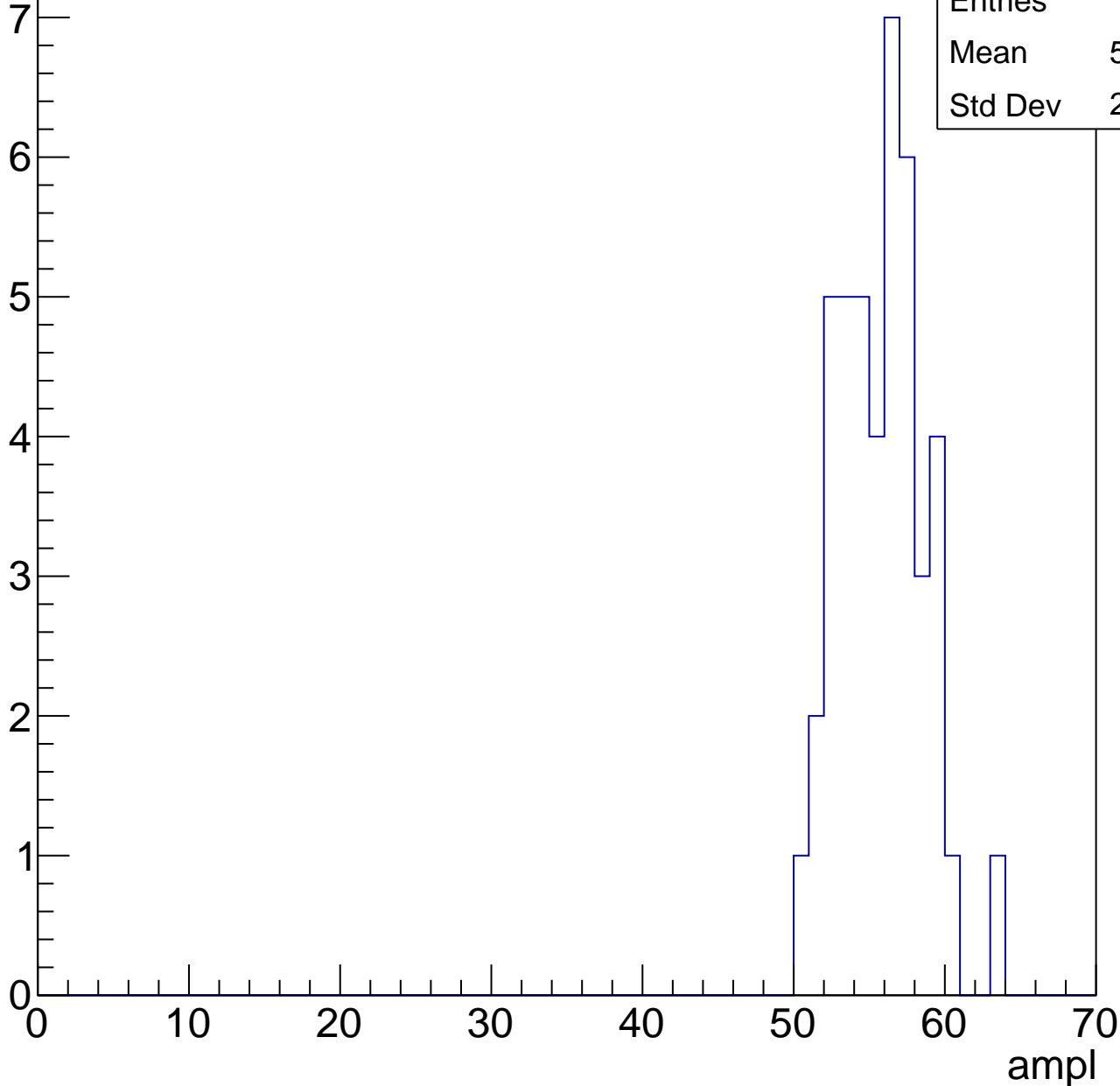


B1L102S, U11-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	55.32
Std Dev	2.753

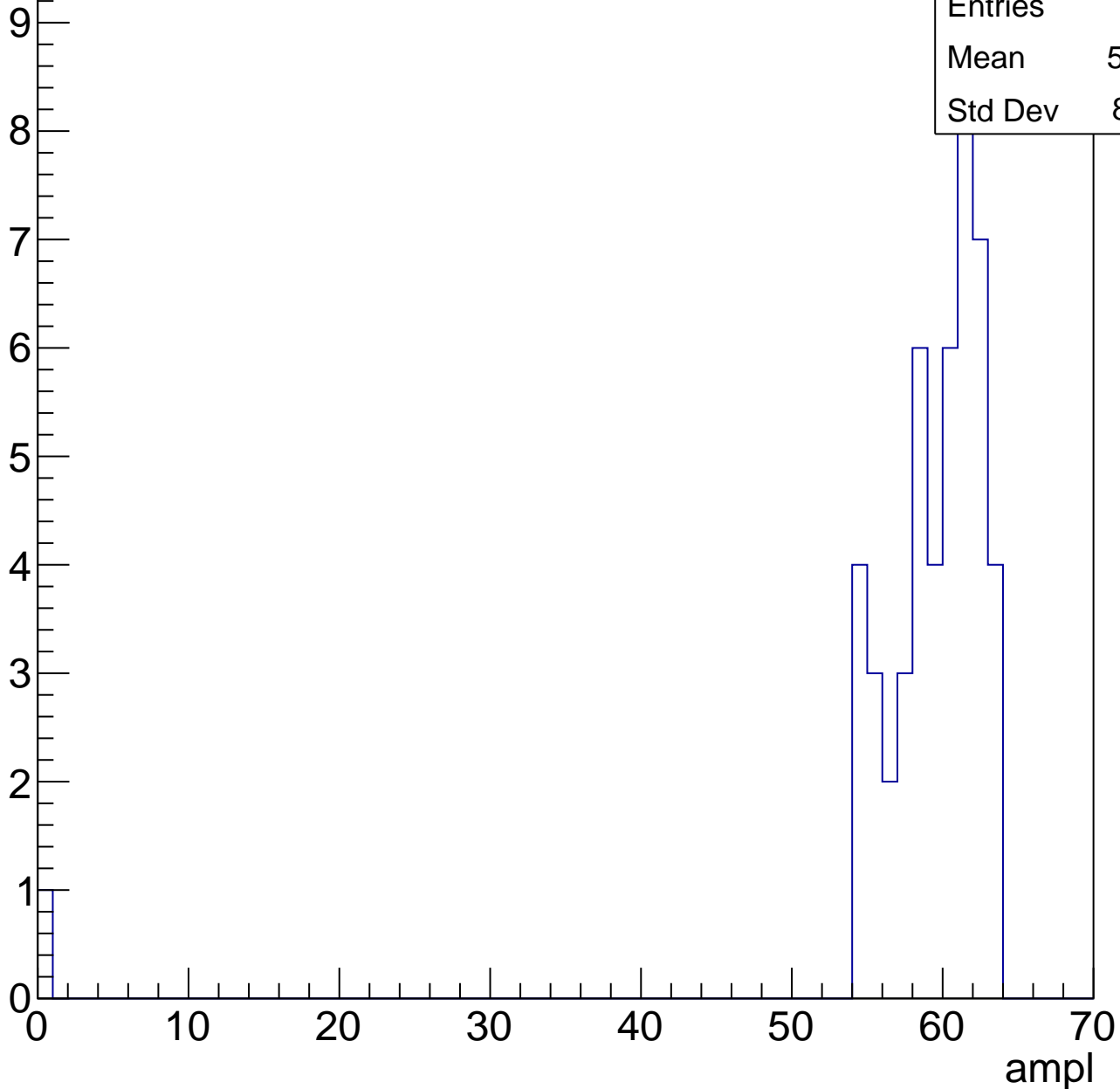


B1L102S, U11-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.02
Std Dev	8.791

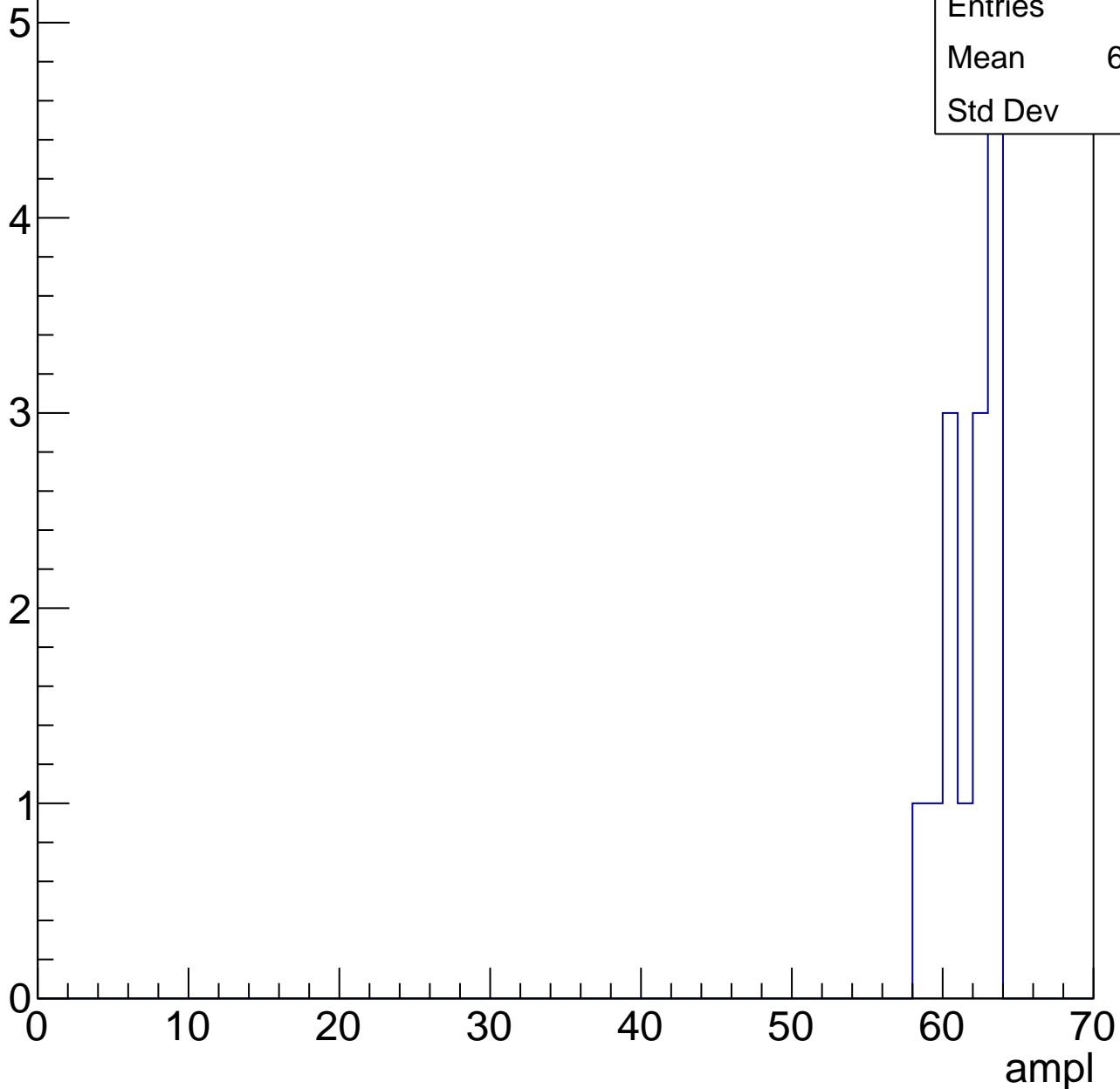


B1L102S, U11-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.36
Std Dev	1.63



B1L102S, U11-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch15, adc0

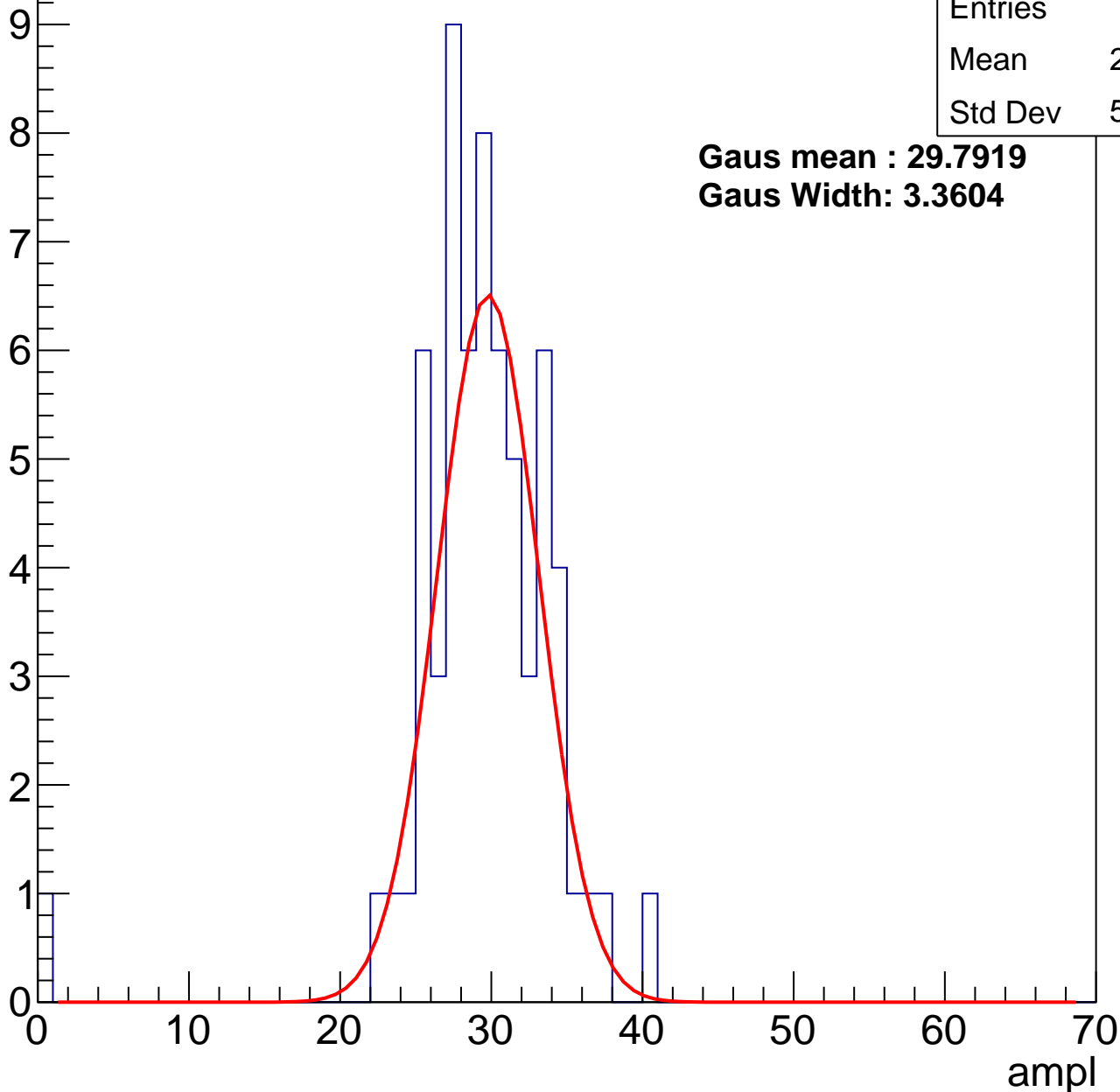
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.95
Std Dev	5.054

Gaus mean : 29.7919

Gaus Width: 3.3604



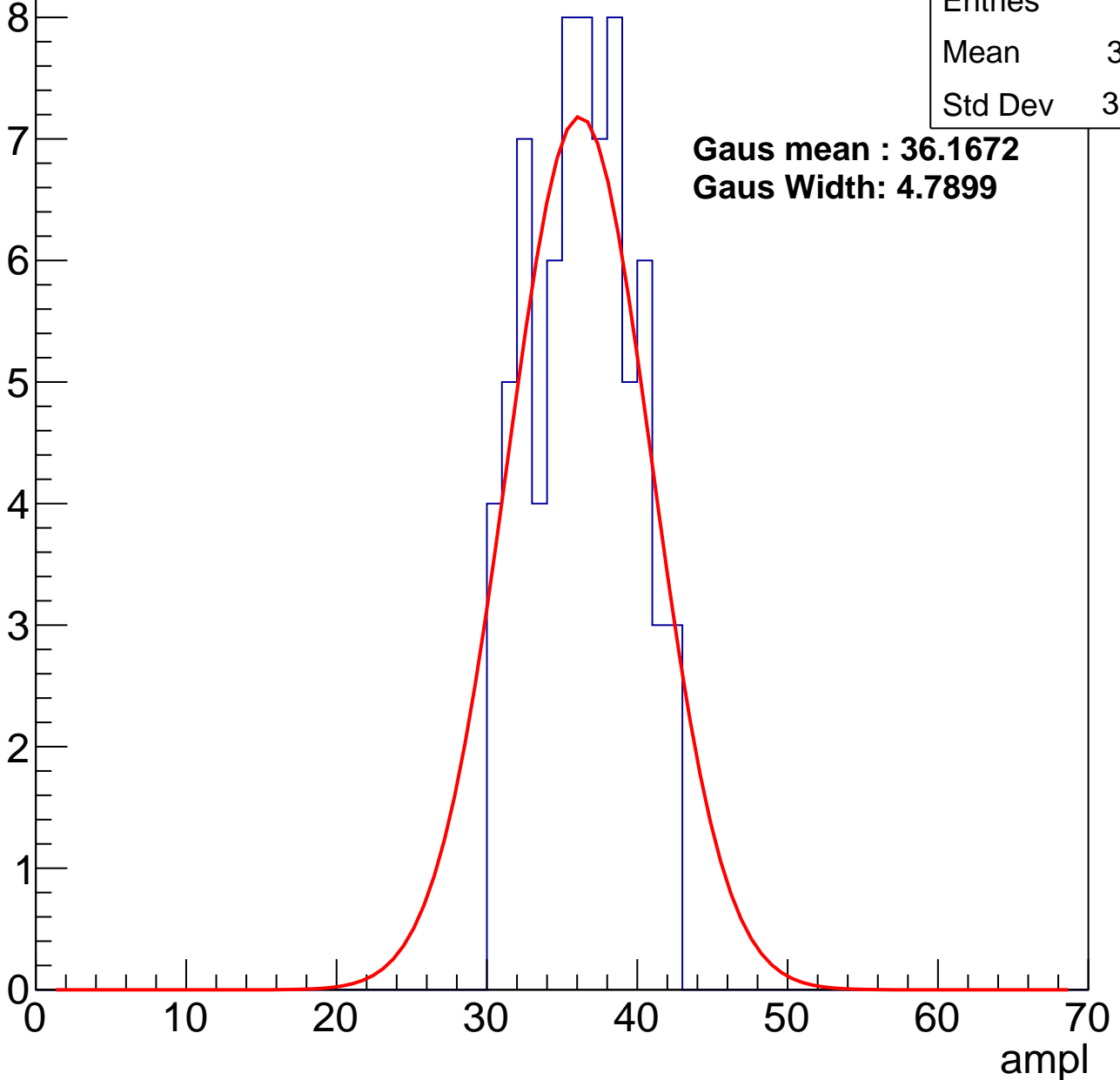
B1L102S, U11-ch15, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.81
Std Dev	3.307

Gaus mean : 36.1672
Gaus Width: 4.7899



B1L102S, U11-ch15, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.54
Std Dev	3.435

Gaus mean : 44.5345

Gaus Width: 3.7255

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

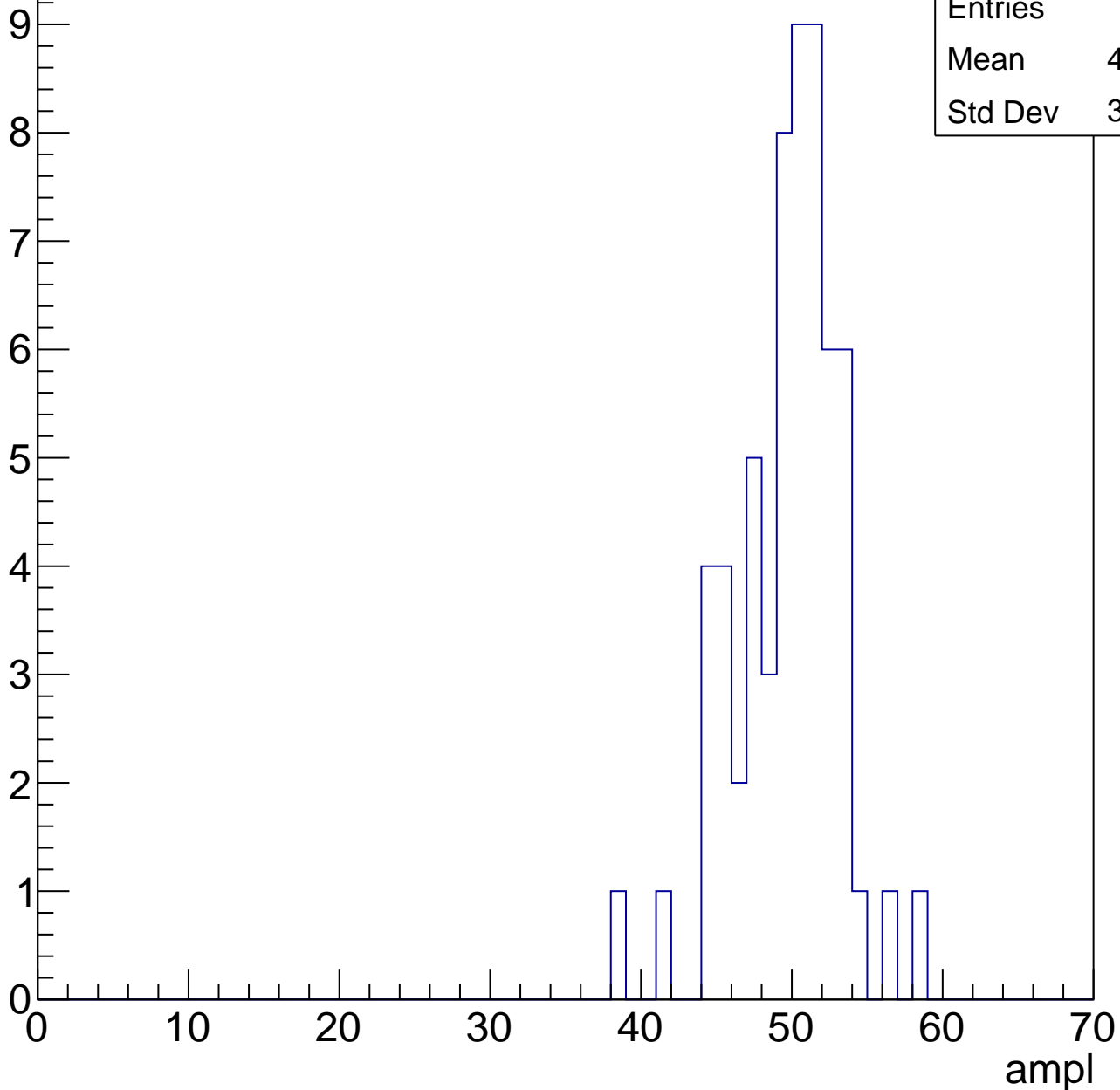
70

B1L102S, U11-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.26
Std Dev	3.473

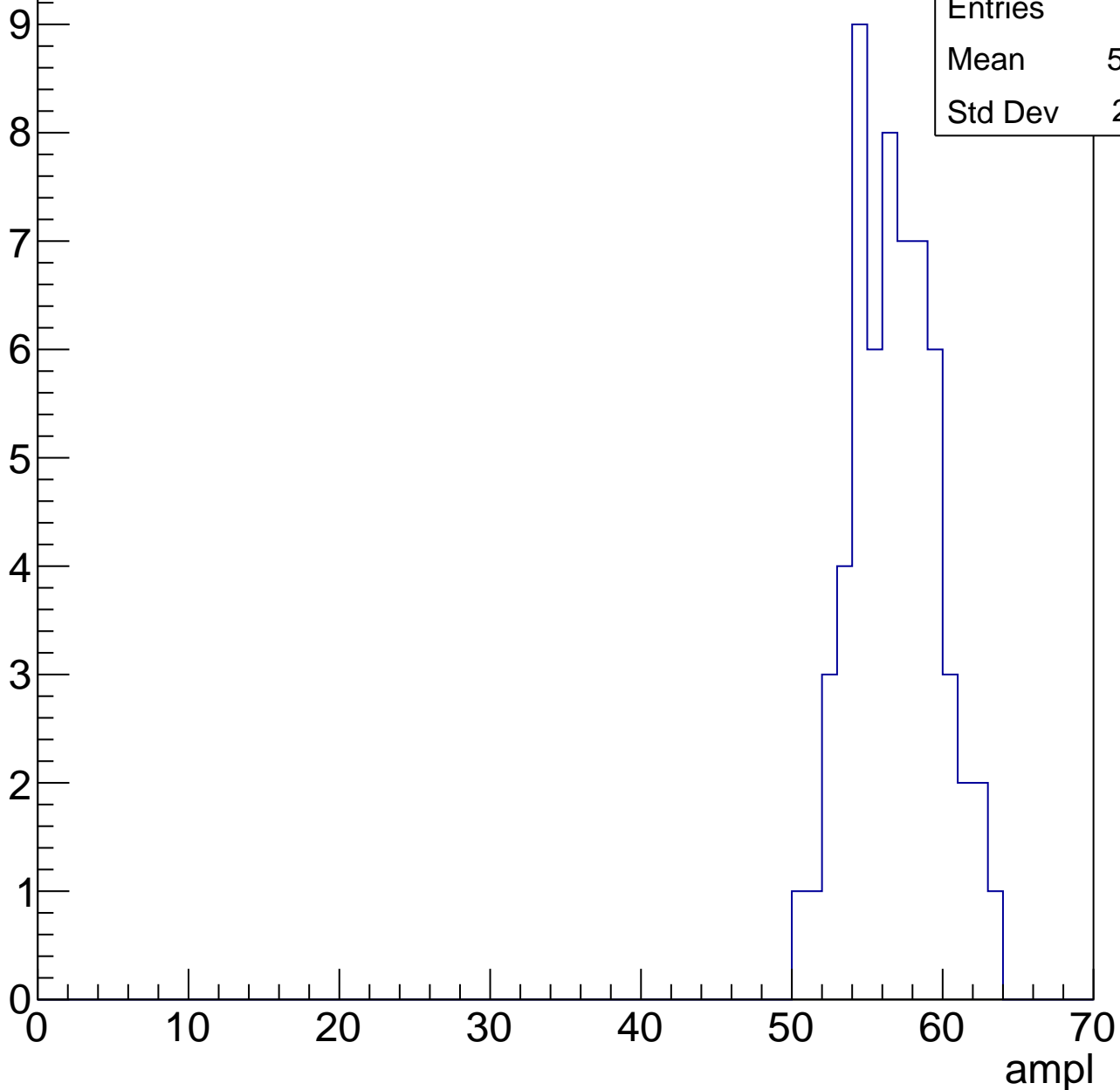


B1L102S, U11-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.35
Std Dev	2.851

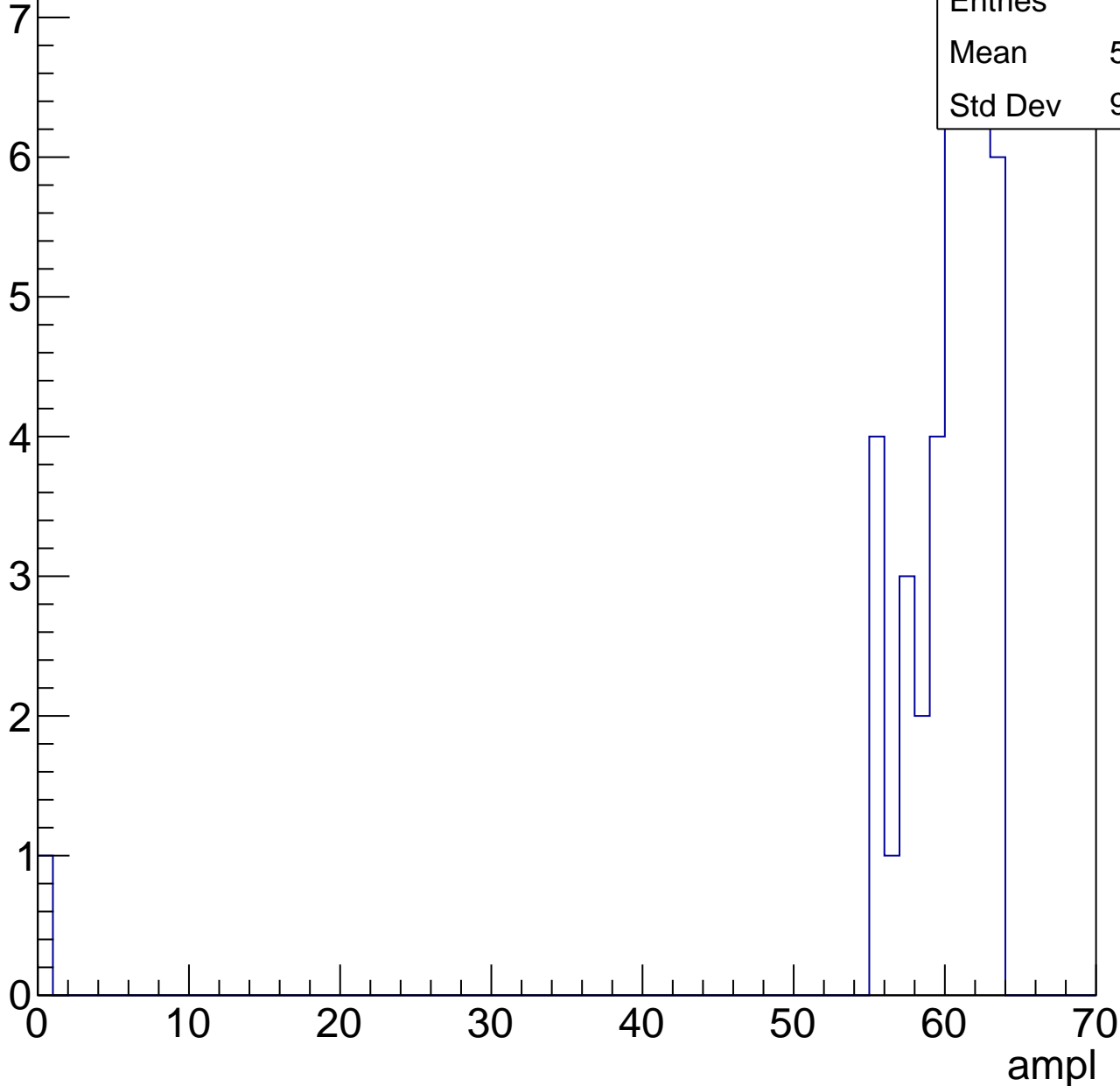


B1L102S, U11-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.52
Std Dev	9.452



B1L102S, U11-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch16, adc0

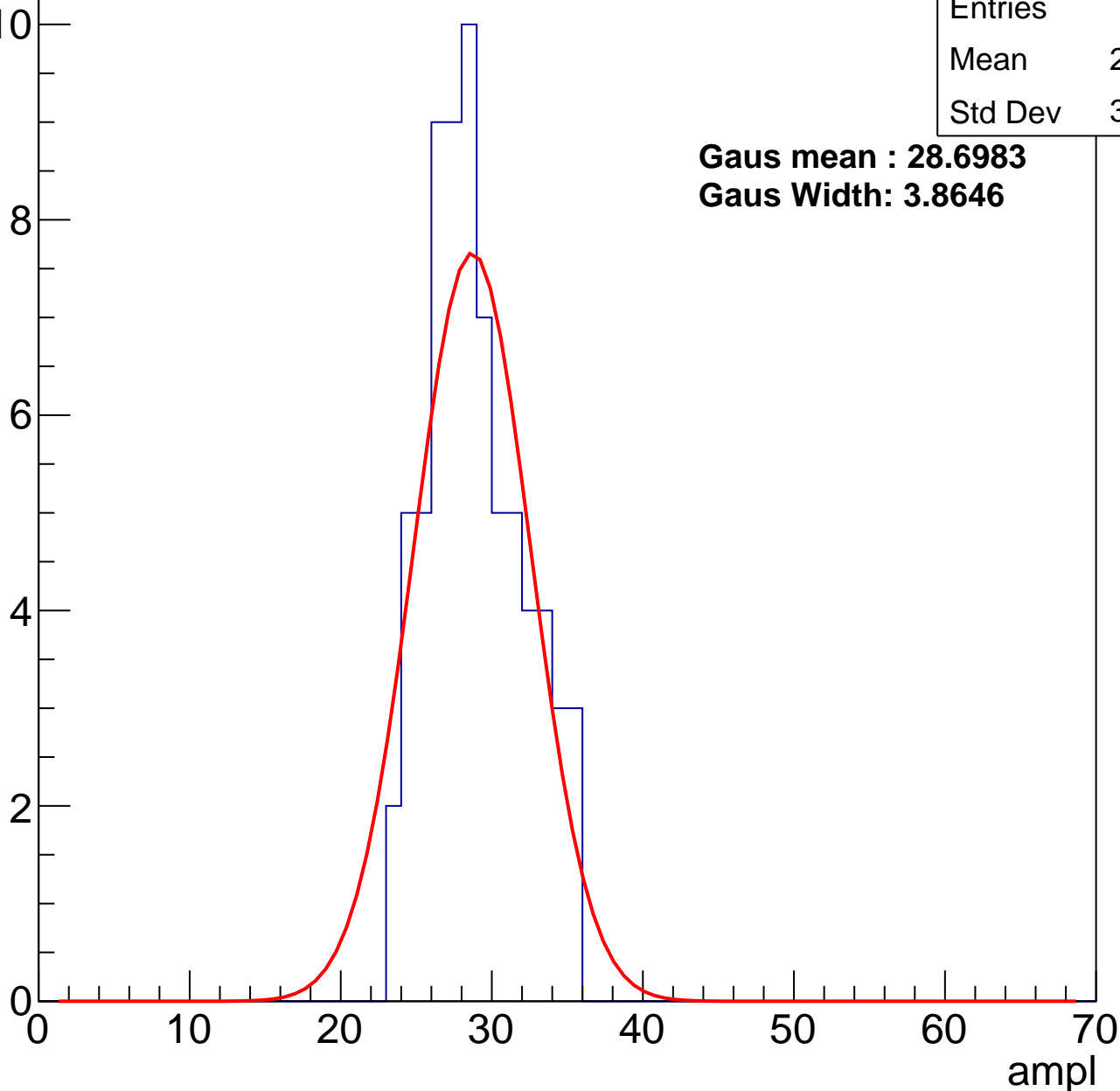
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.49
Std Dev	3.126

Gaus mean : 28.6983

Gaus Width: 3.8646



B1L102S, U11-ch16, adc1

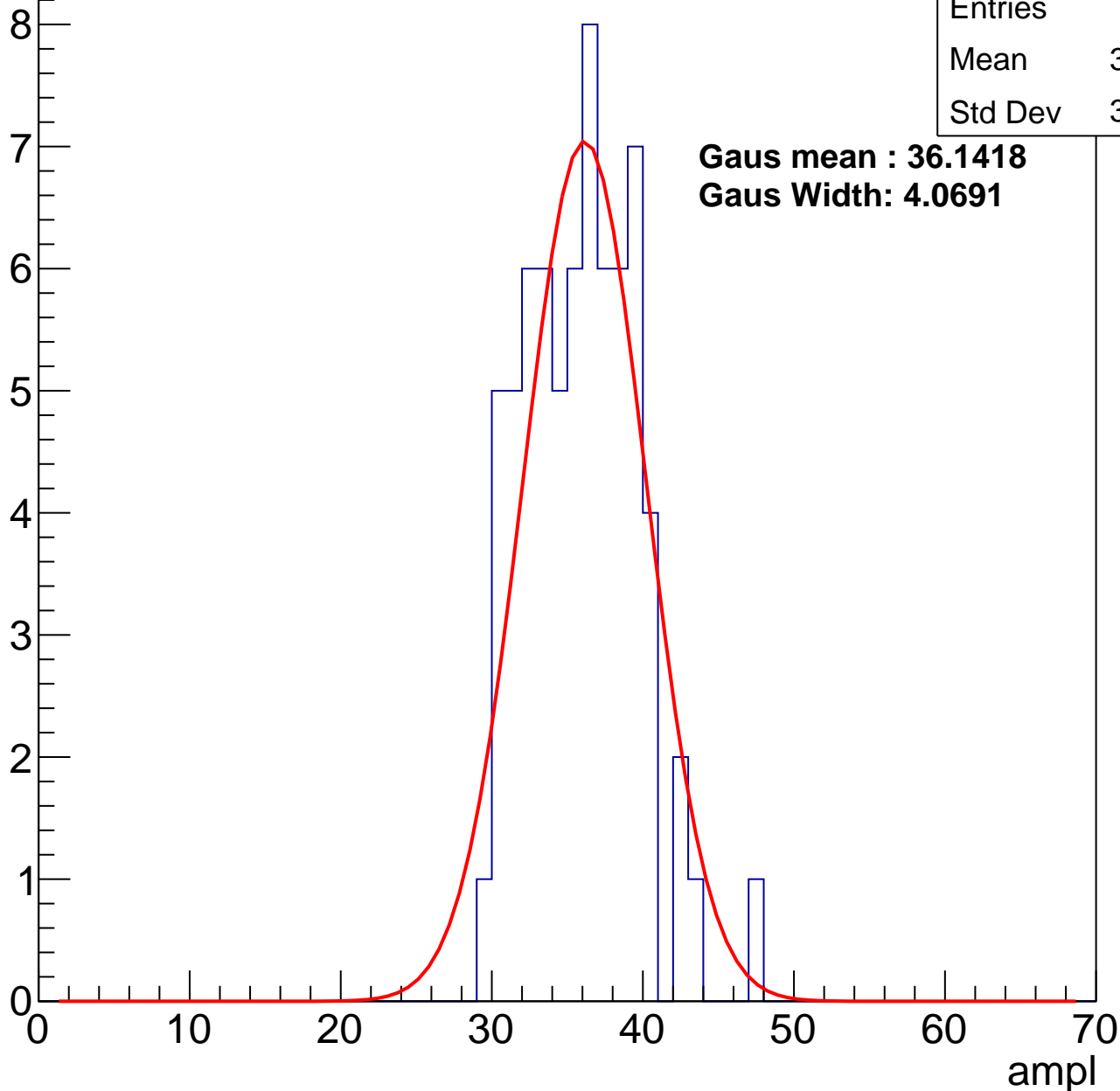
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.49
Std Dev	3.634

Gaus mean : 36.1418

Gaus Width: 4.0691



B1L102S, U11-ch16, adc2

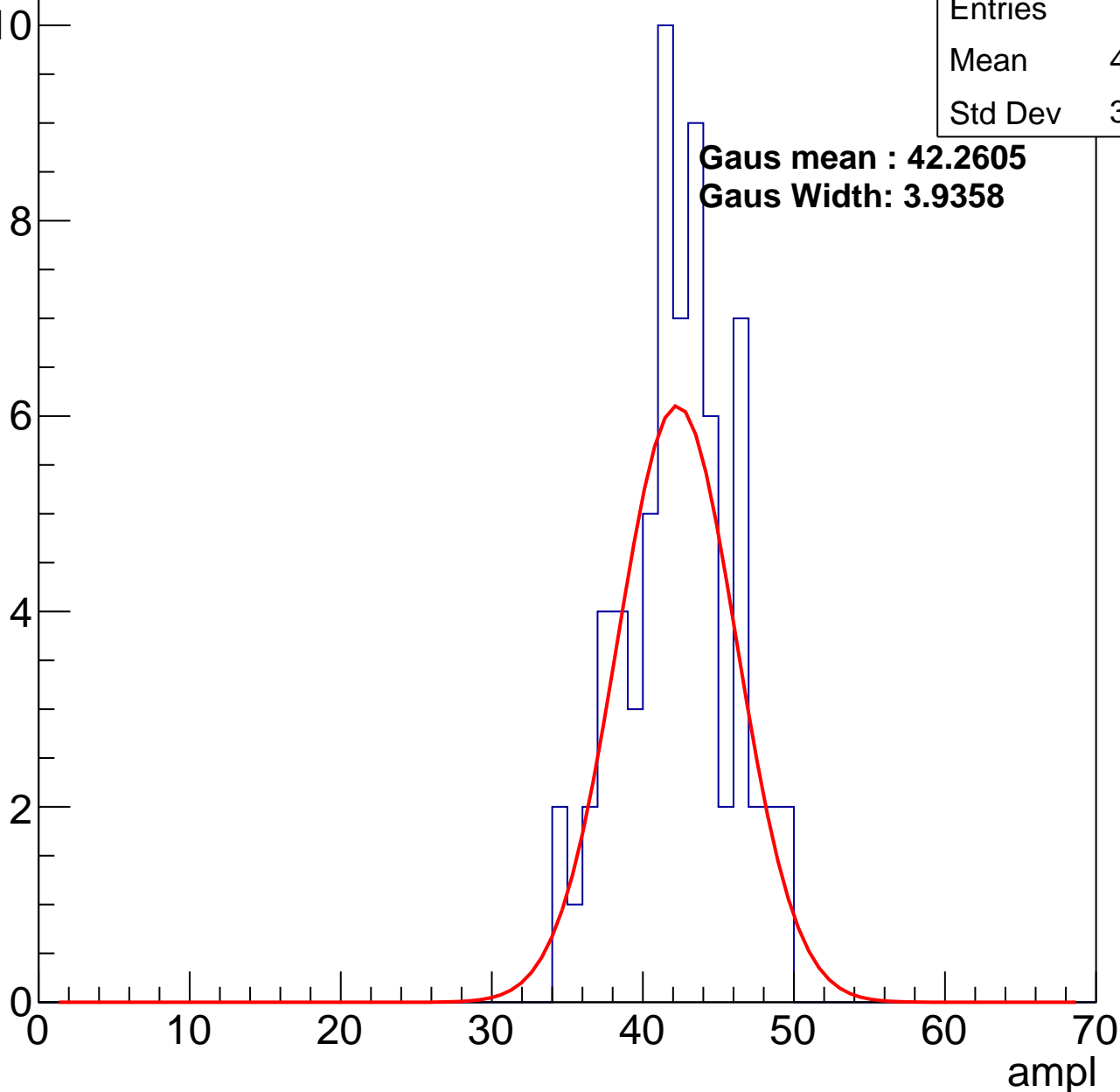
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.87
Std Dev	3.539

Gaus mean : 42.2605

Gaus Width: 3.9358

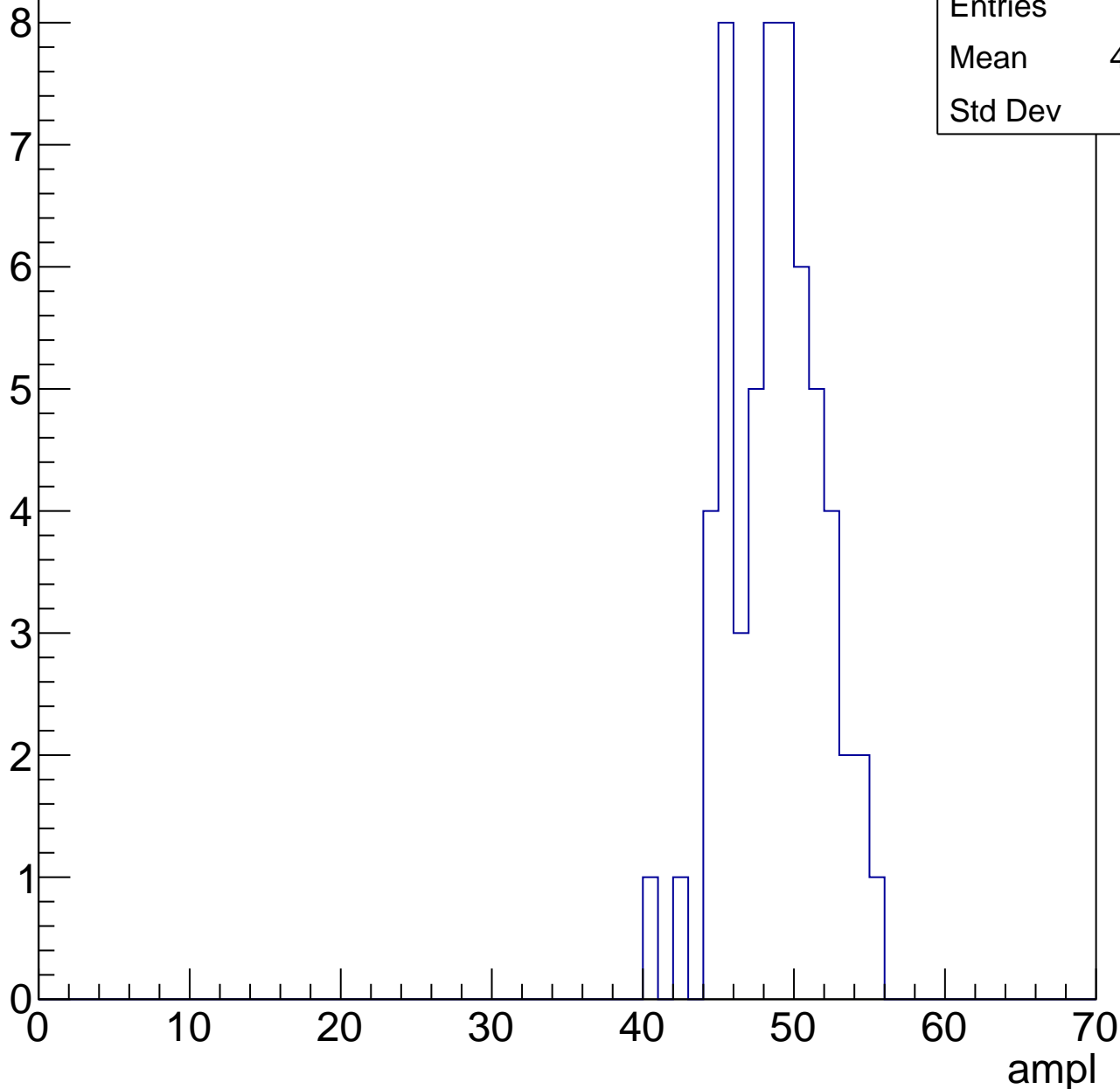


B1L102S, U11-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.26
Std Dev	3.11

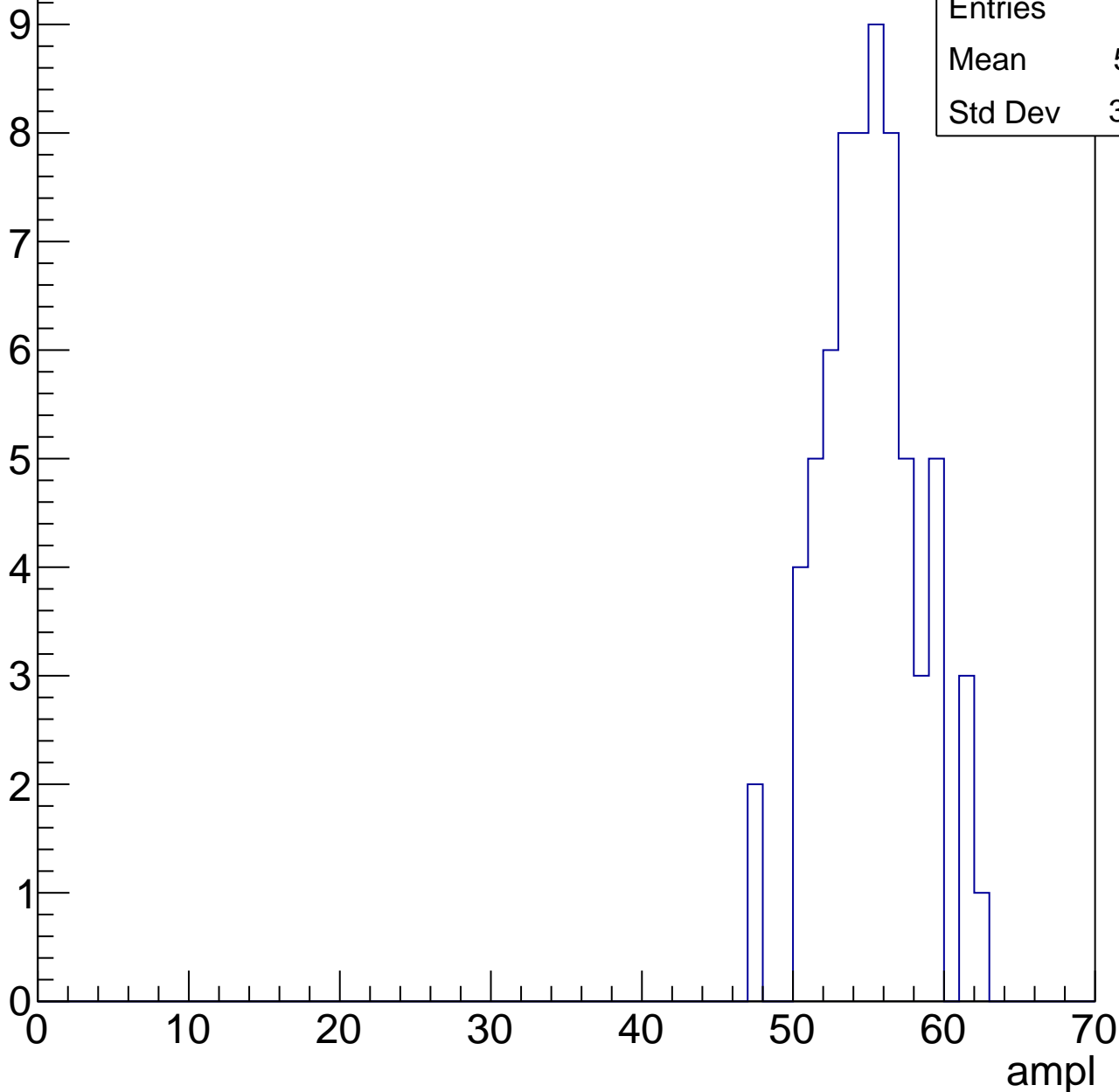


B1L102S, U11-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.61
Std Dev	3.195



B1L102S, U11-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

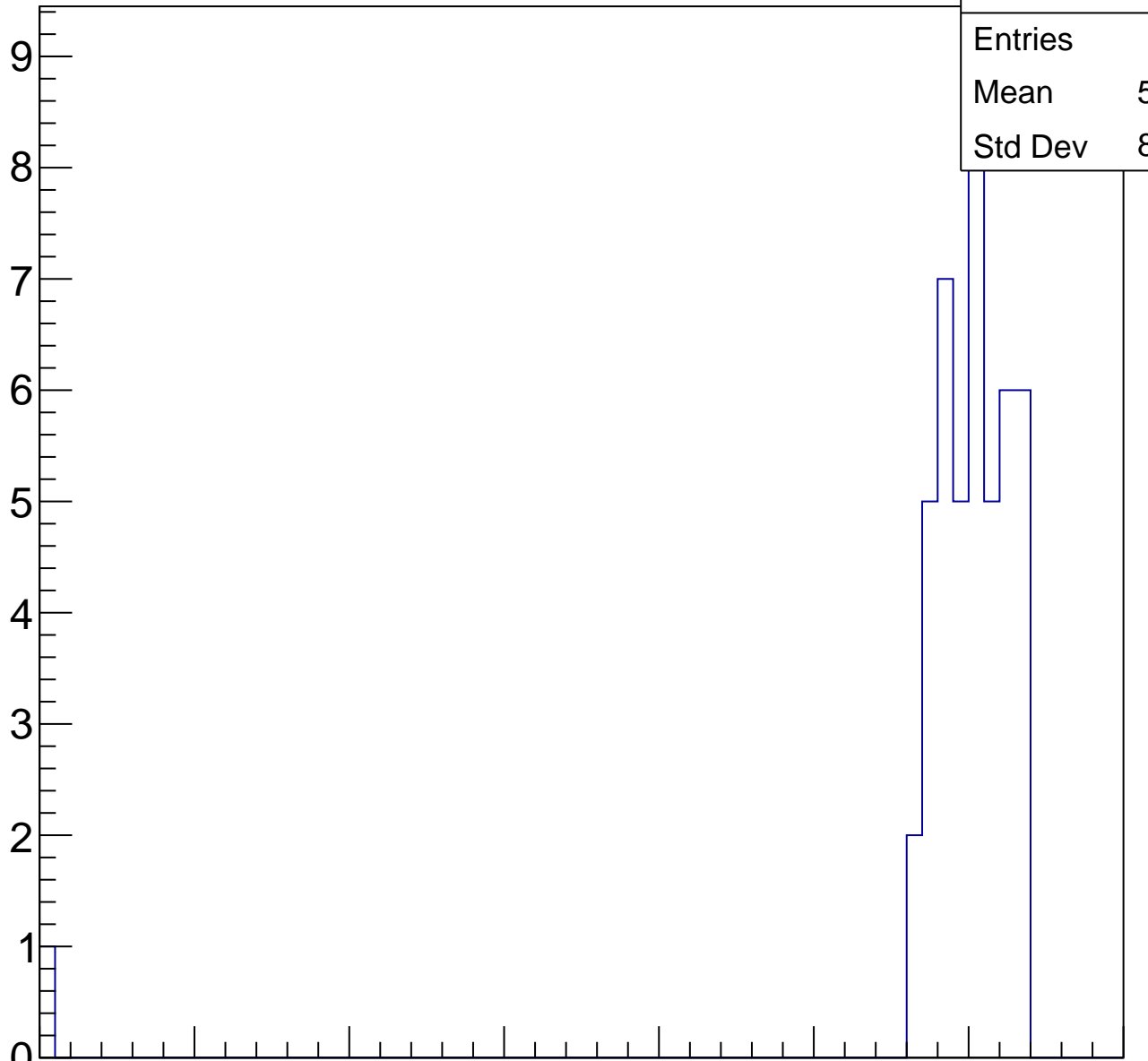
Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.54
Std Dev	8.963

ampl

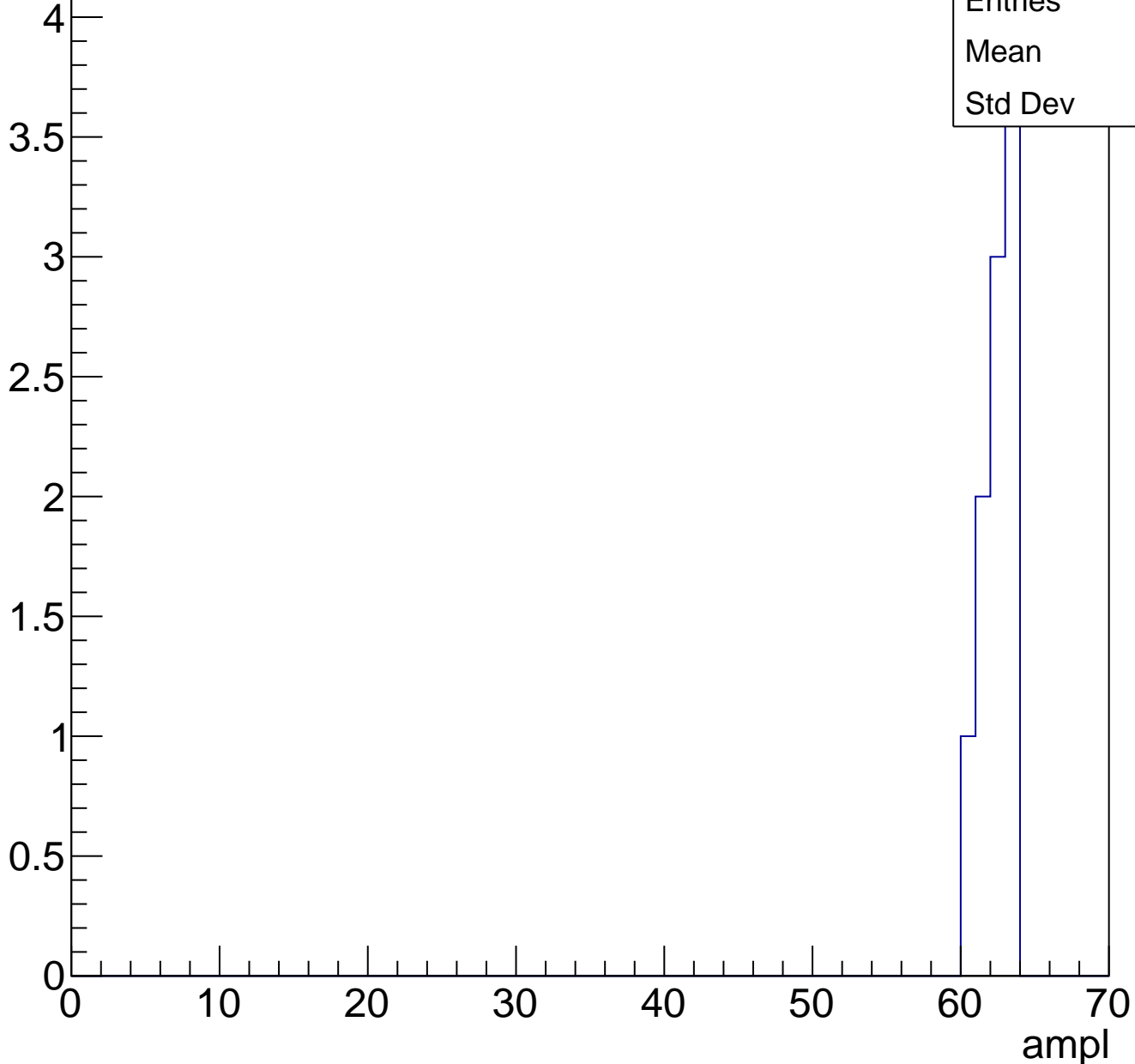
0 10 20 30 40 50 60 70



B1L102S, U11-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	62
Std Dev	1

B1L102S, U11-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch17, adc0

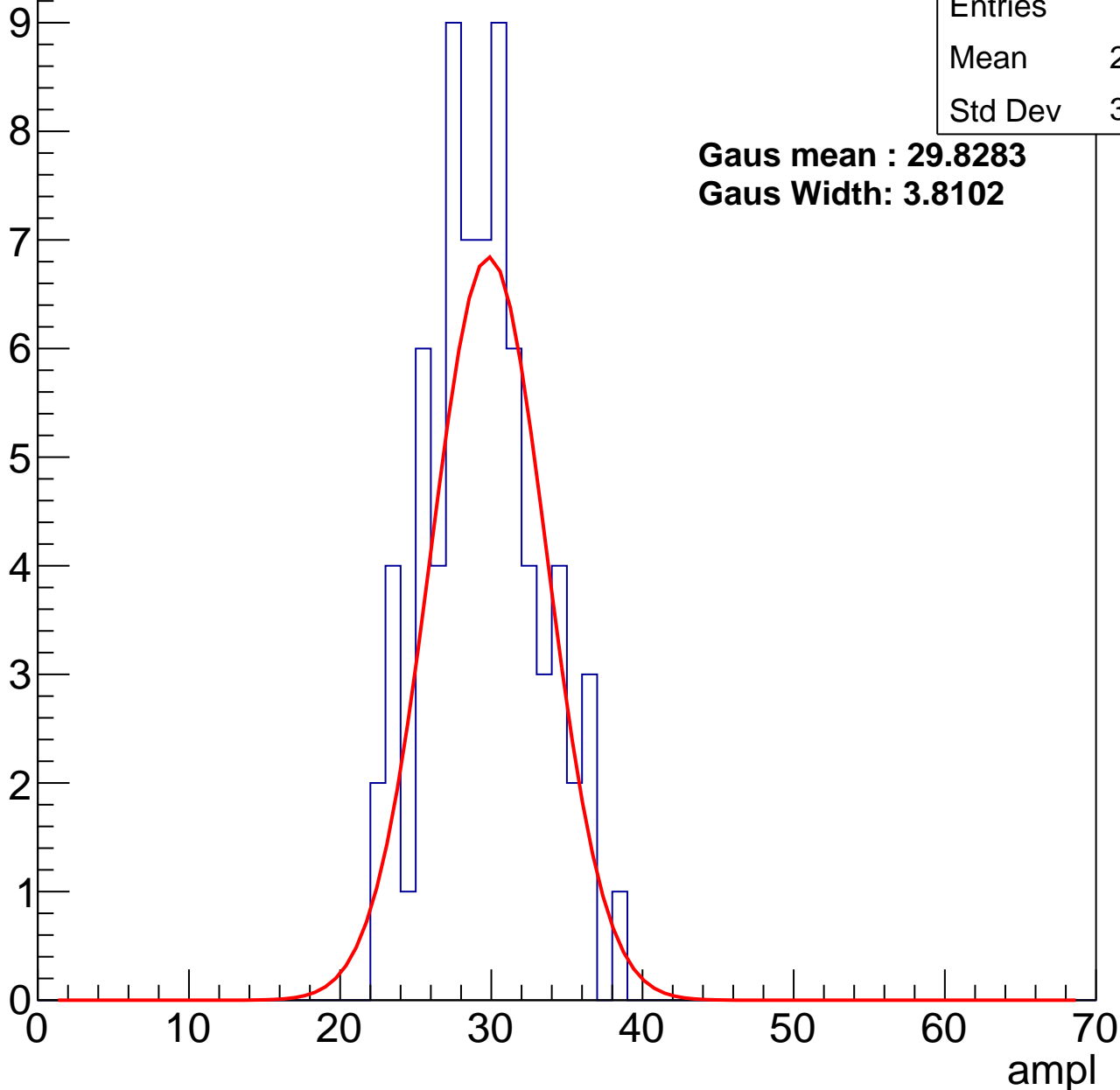
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.04
Std Dev	3.649

Gaus mean : 29.8283

Gaus Width: 3.8102



B1L102S, U11-ch17, adc1

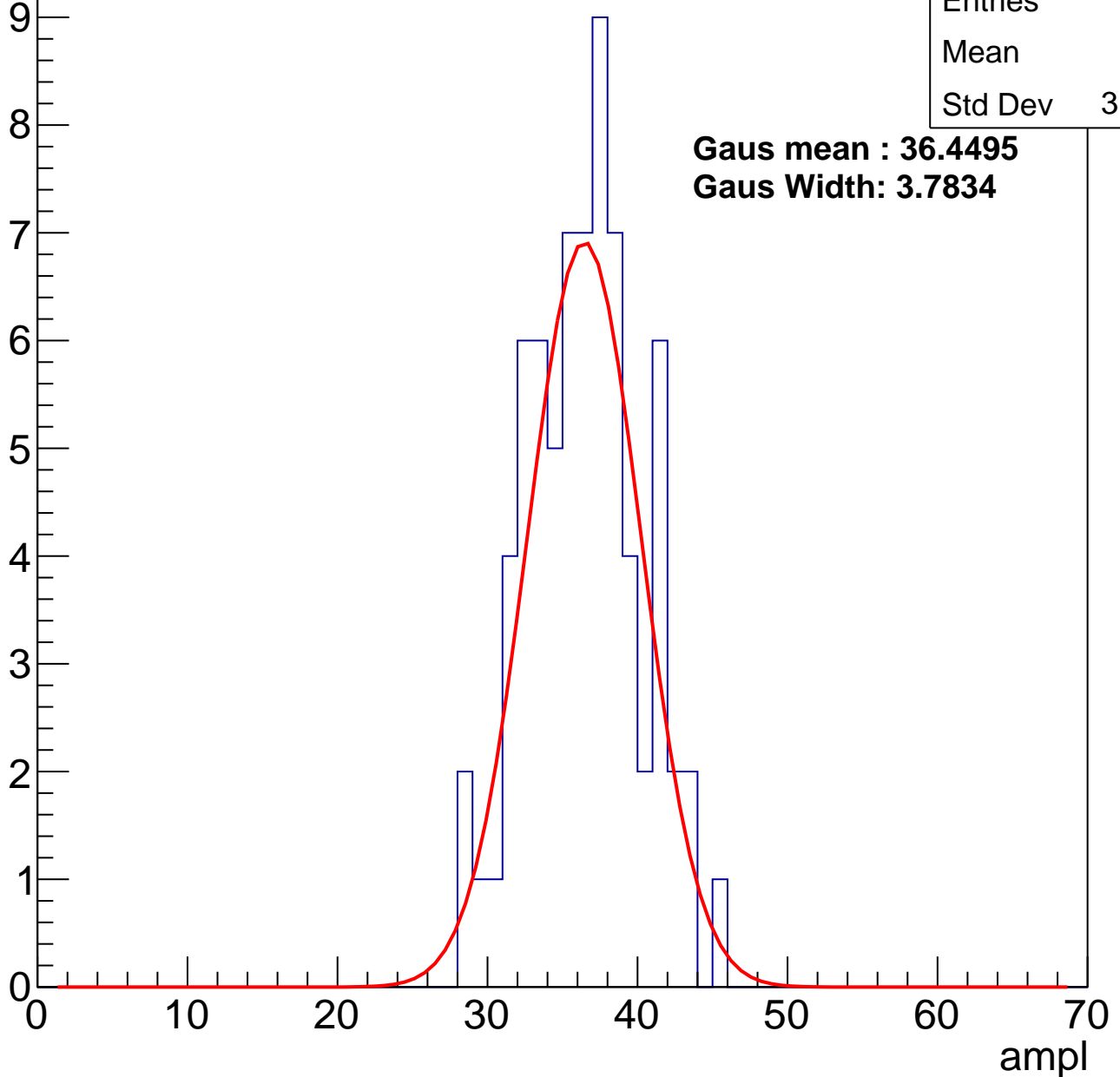
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36
Std Dev	3.719

Gaus mean : 36.4495

Gaus Width: 3.7834



B1L102S, U11-ch17, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.26
Std Dev	3.121

Gaus mean : 42.8040

Gaus Width: 3.1186

10

8

6

4

2

0

0

10

20

30

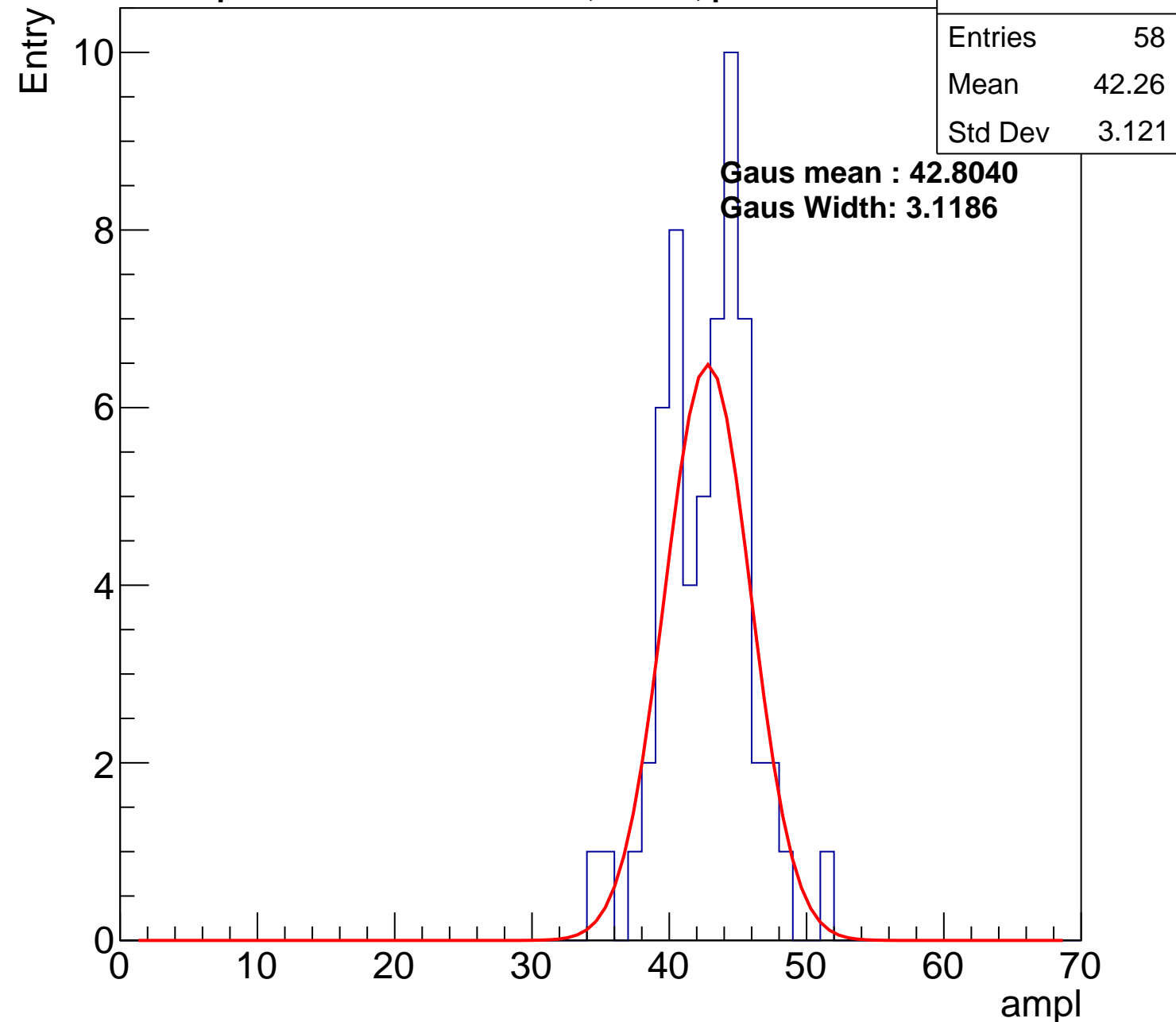
40

50

60

70

ampl



B1L102S, U11-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	49.65
Std Dev	3.327

Entry

10
8
6
4
2
0

ampl

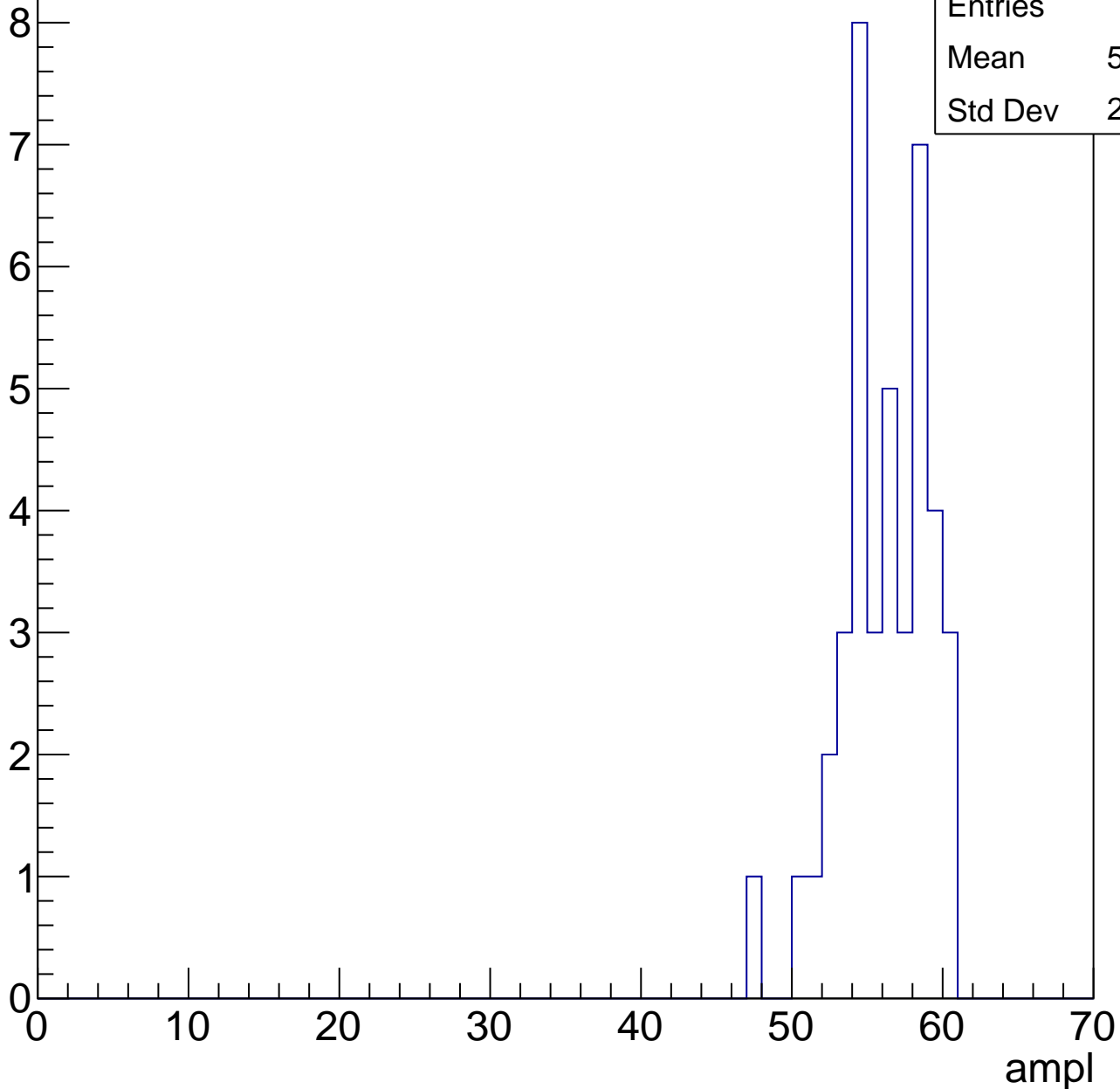
0 10 20 30 40 50 60 70

B1L102S, U11-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

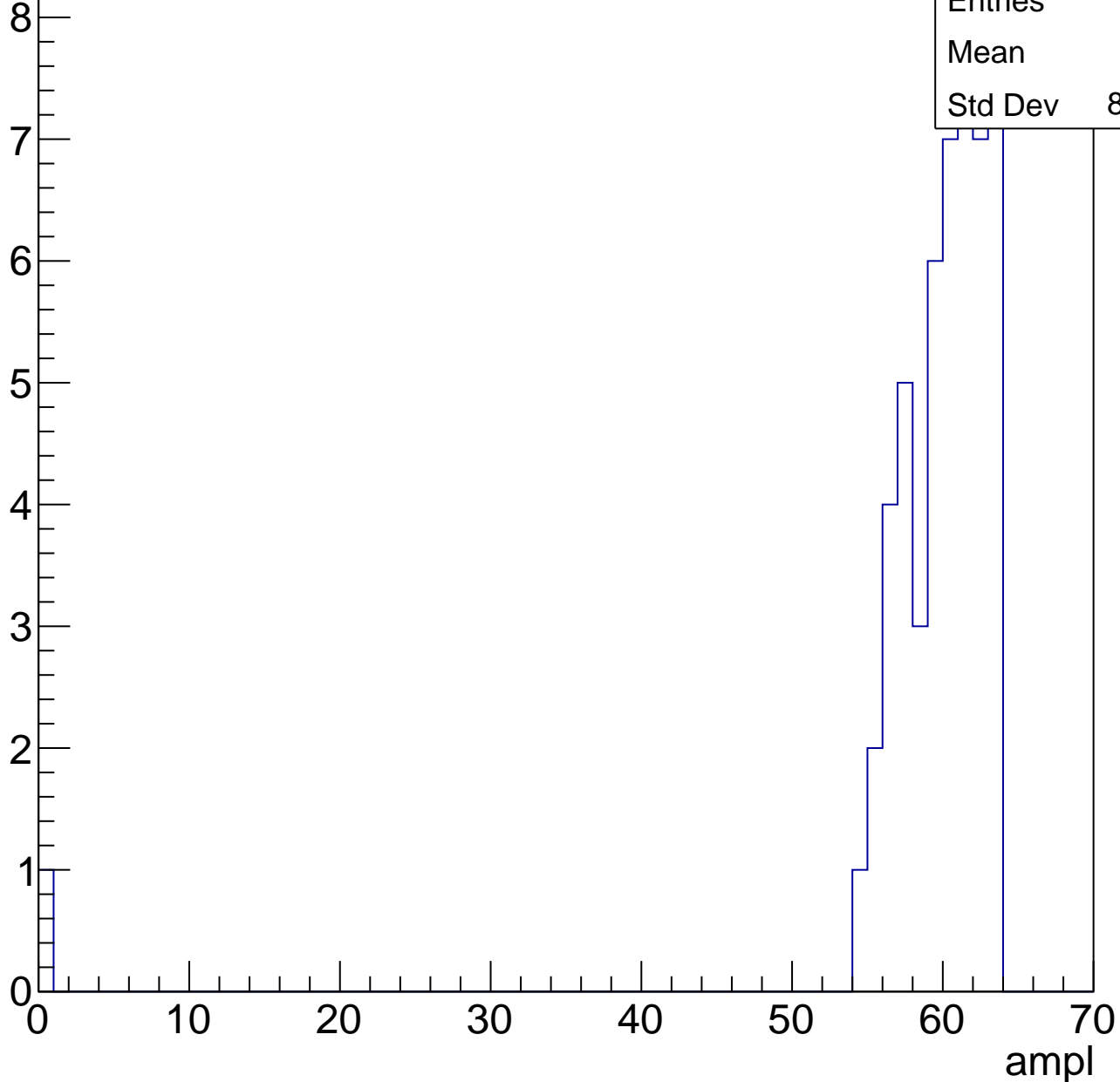
Entries	41
Mean	55.63
Std Dev	2.903



B1L102S, U11-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

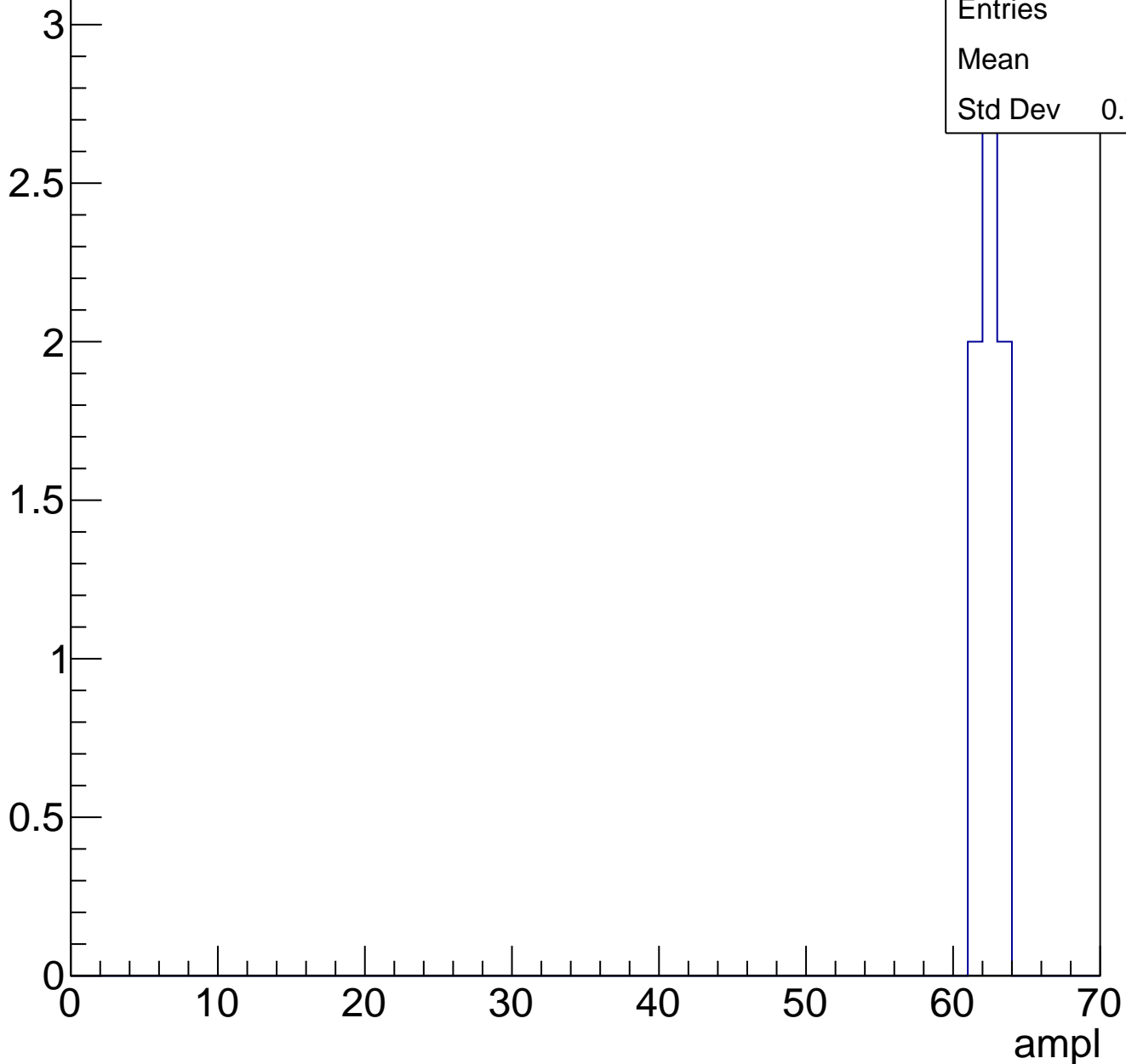


Entries	52
Mean	58.6
Std Dev	8.569

B1L102S, U11-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L102S, U11-ch18, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	30.29
Std Dev	3.876

Gaus mean : 30.0563

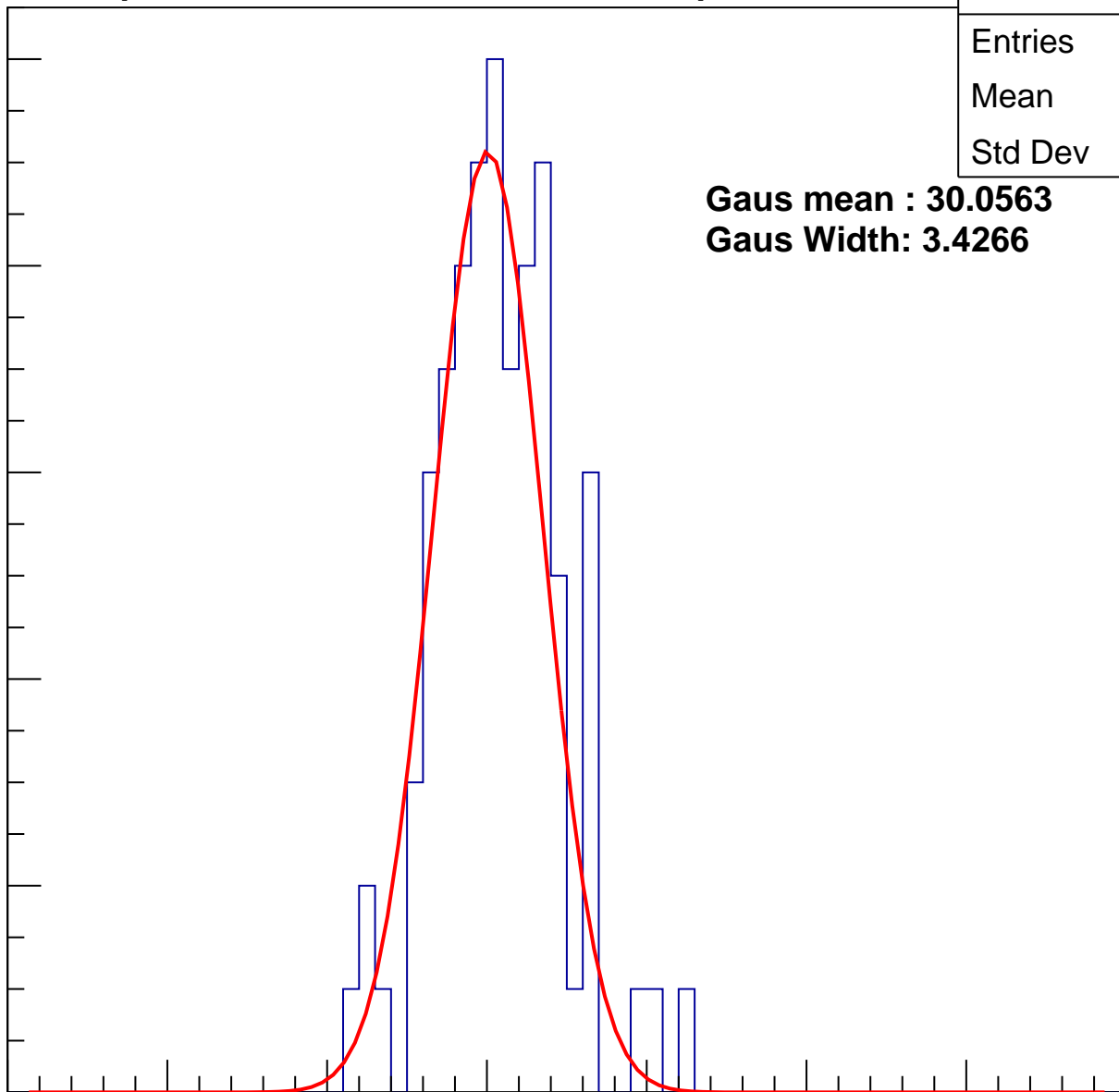
Gaus Width: 3.4266

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch18, adc1

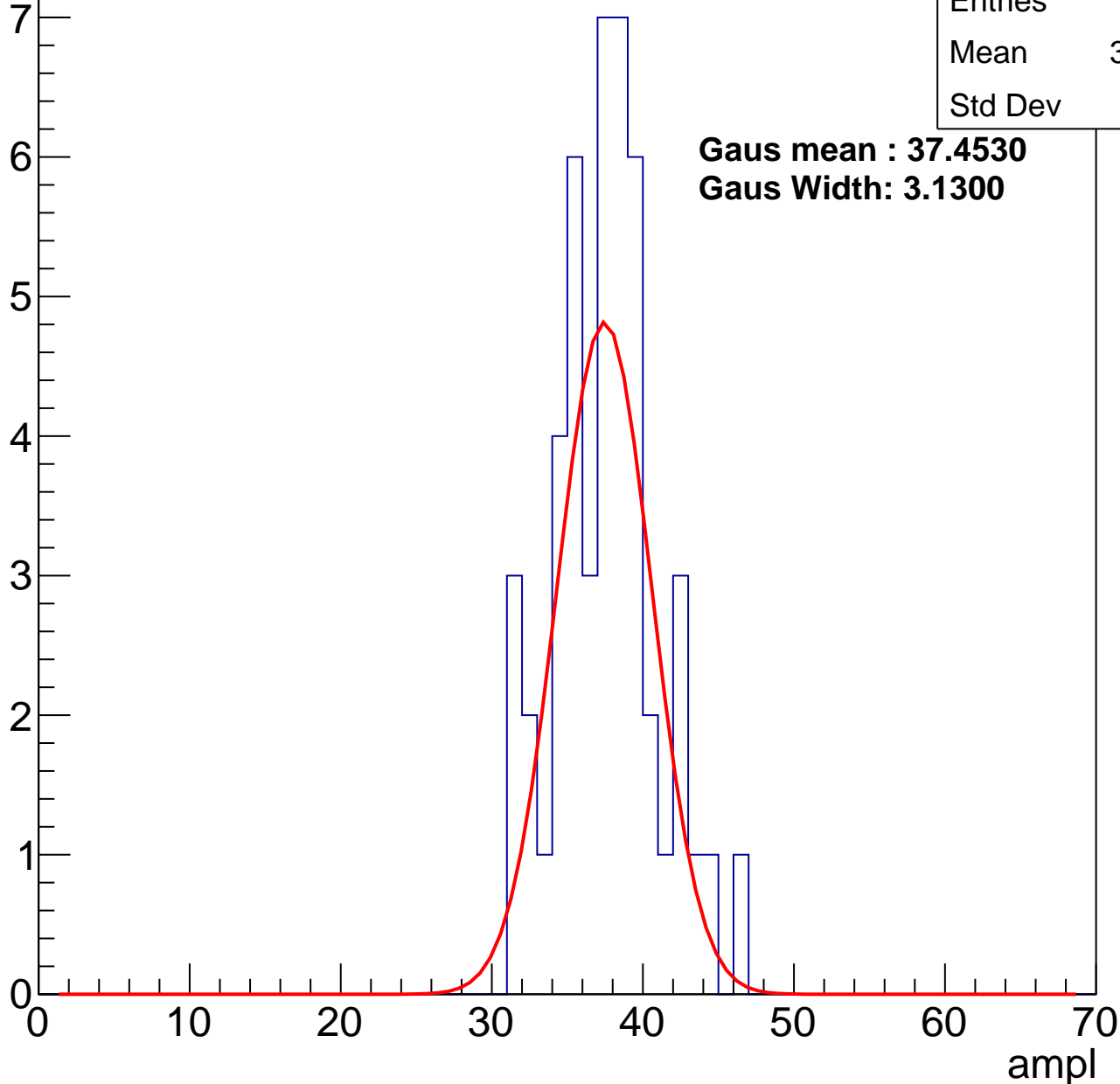
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	37.15
Std Dev	3.36

Gaus mean : 37.4530

Gaus Width: 3.1300



B1L102S, U11-ch18, adc2

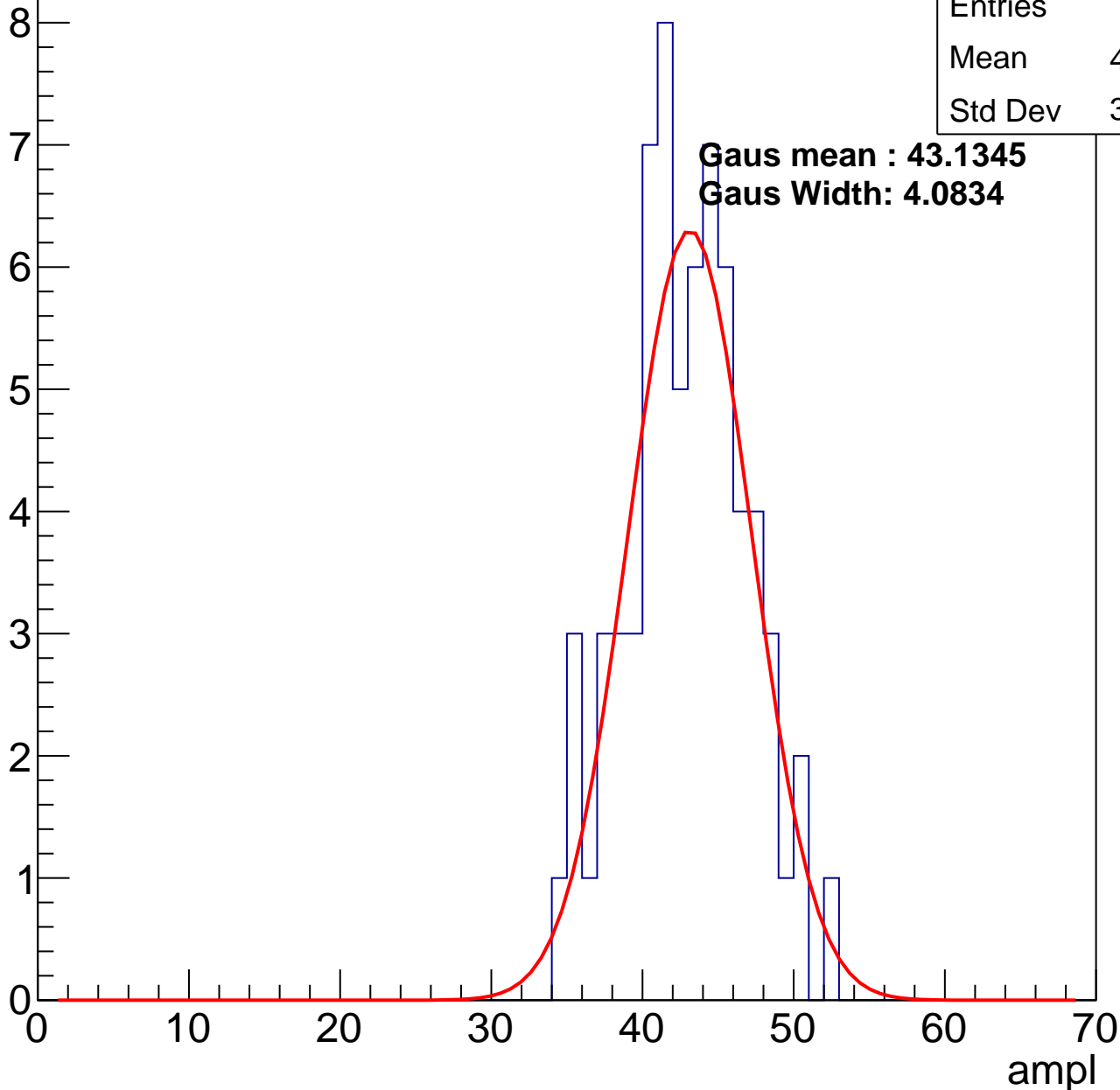
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.47
Std Dev	3.939

Gaus mean : 43.1345

Gaus Width: 4.0834

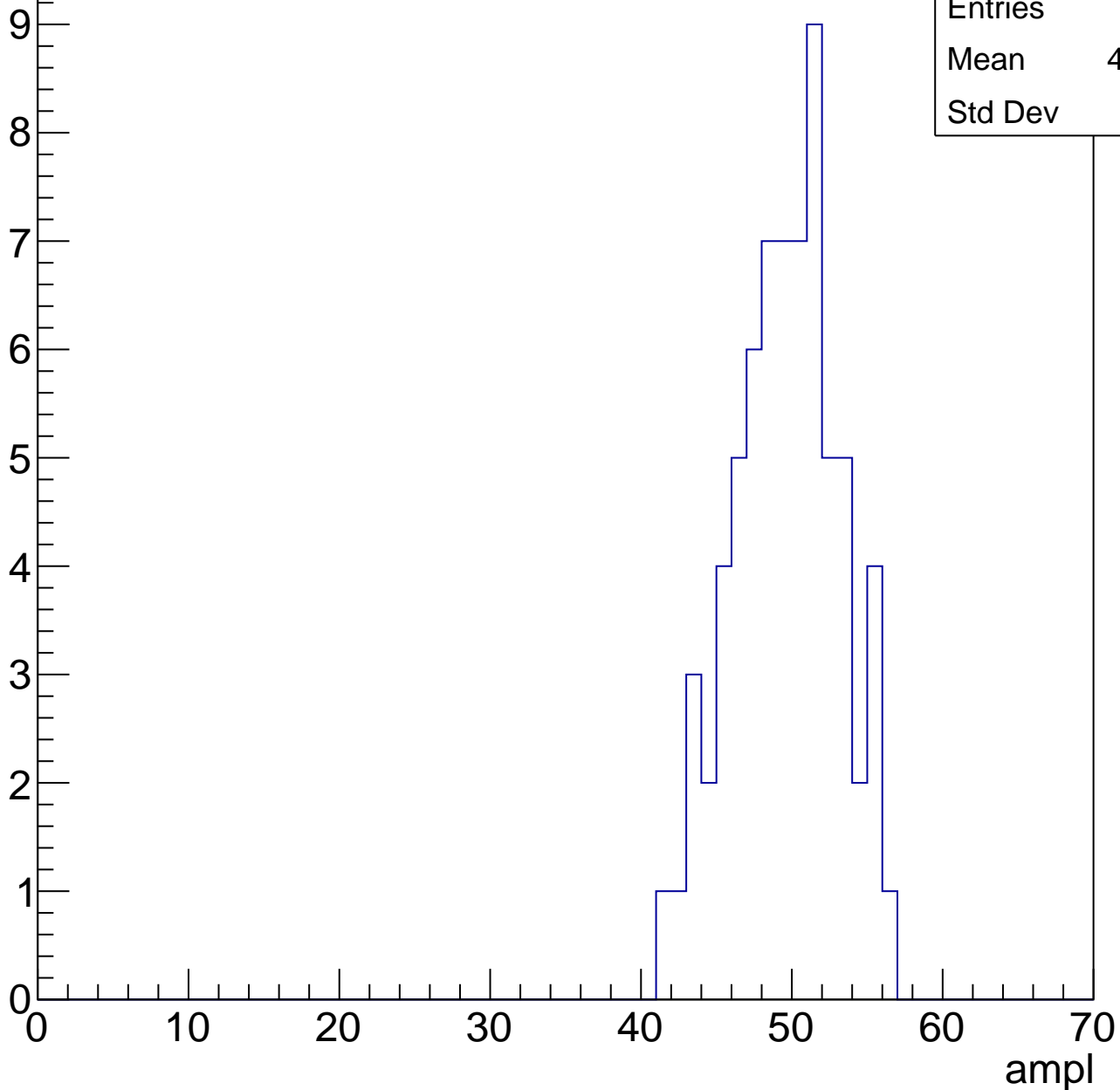


B1L102S, U11-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

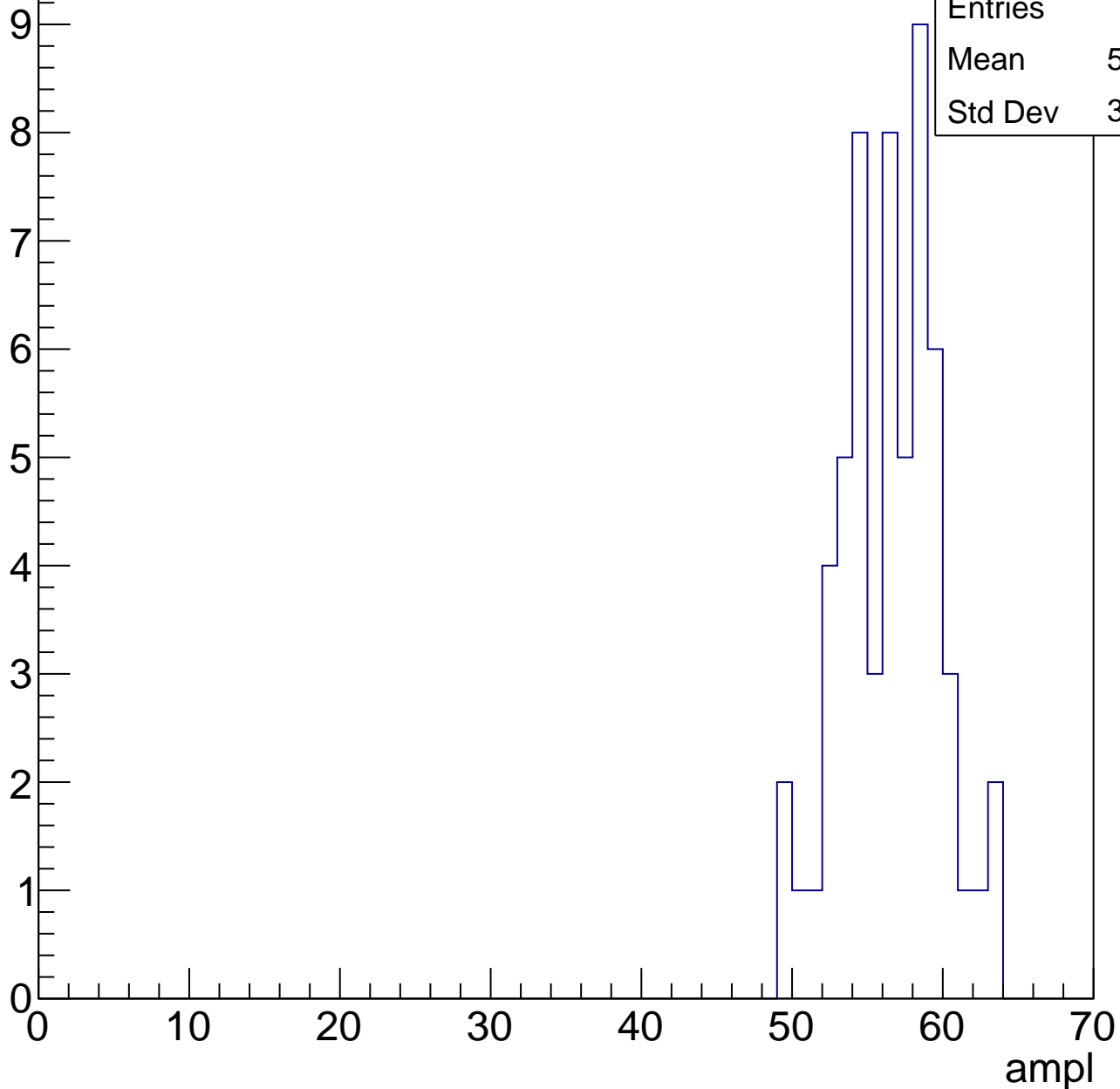
Entries	69
Mean	49.12
Std Dev	3.45



B1L102S, U11-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

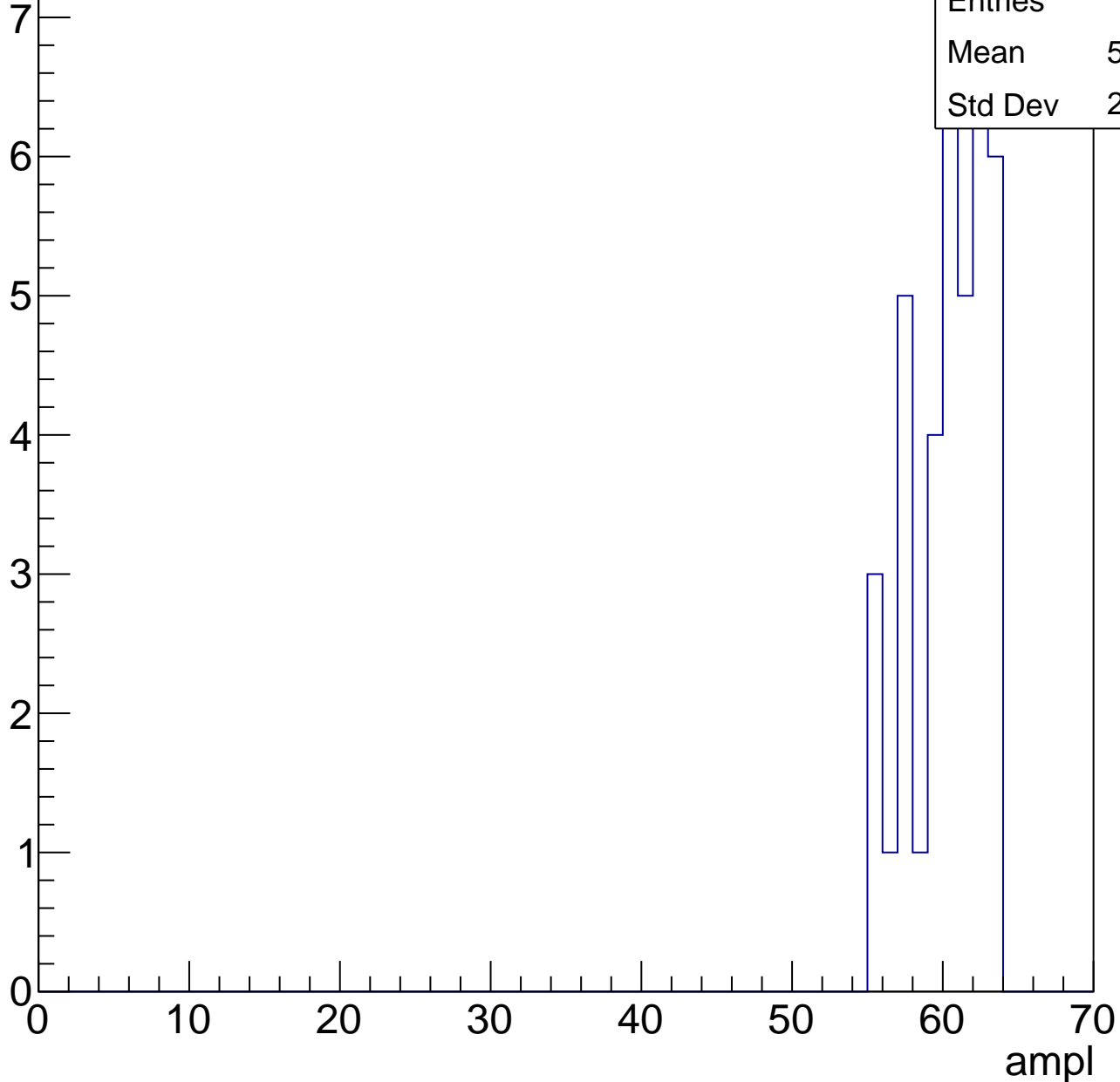
Entry



B1L102S, U11-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

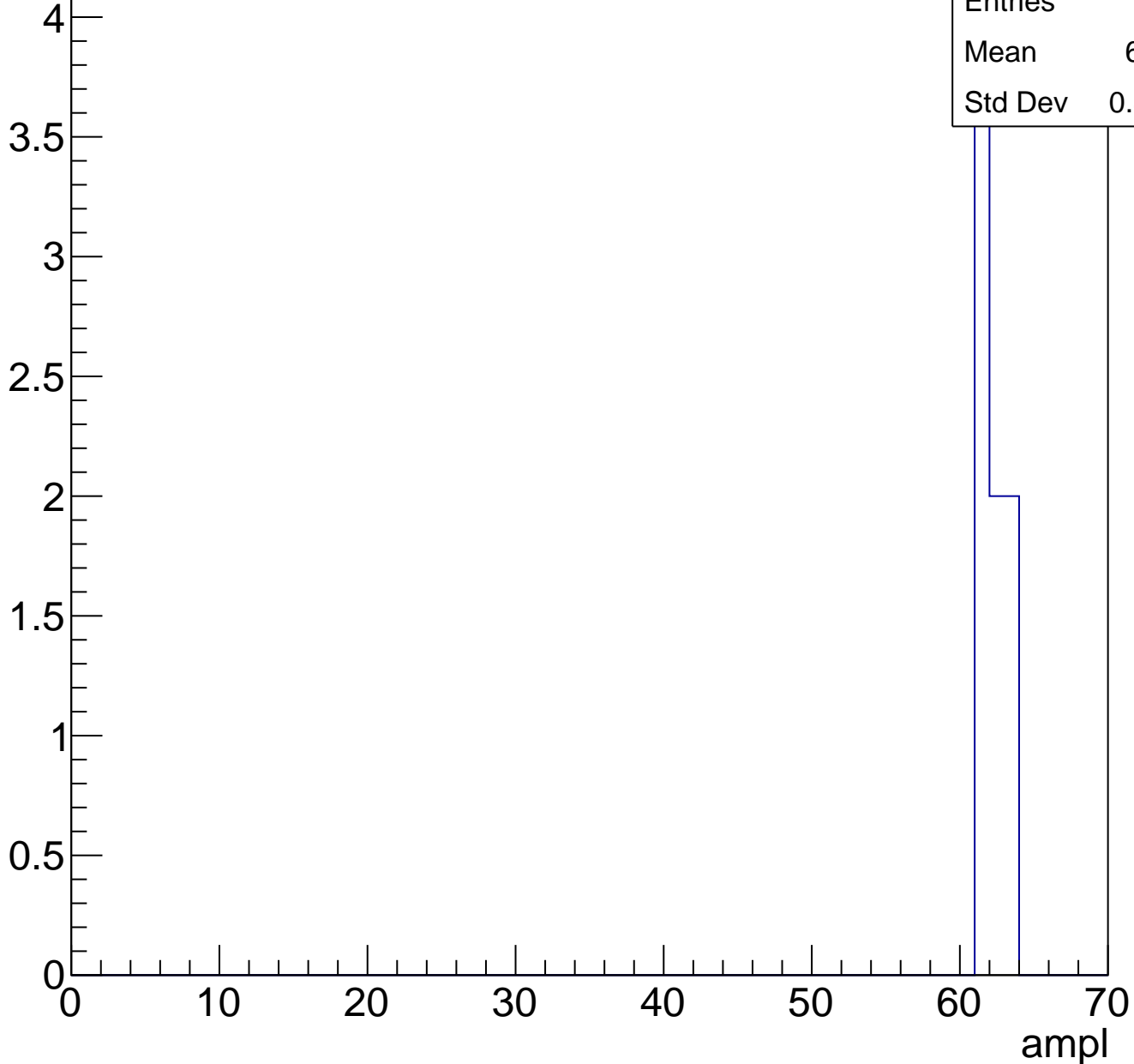
Entry



B1L102S, U11-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch19, adc0

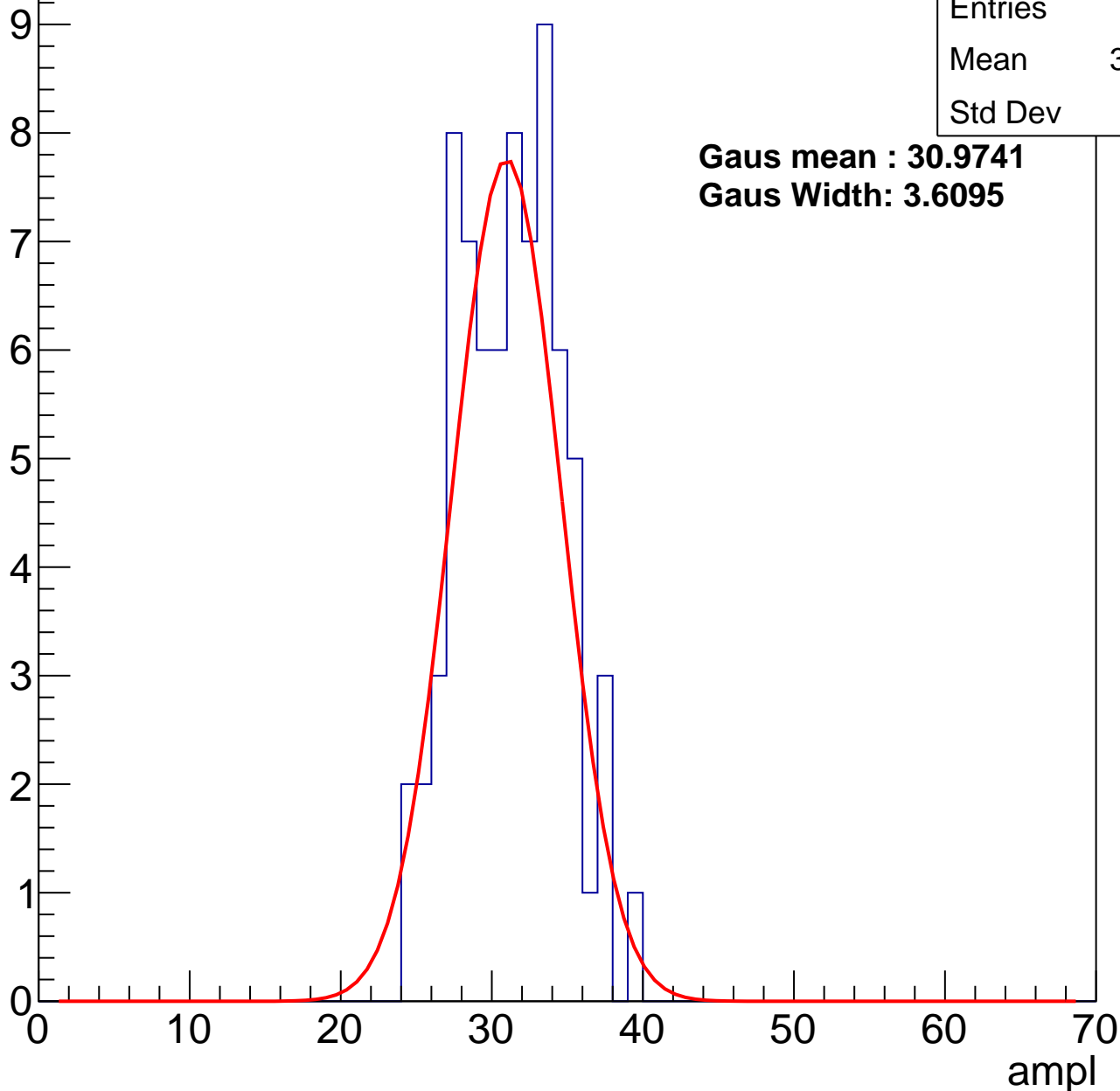
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	30.76
Std Dev	3.36

Gaus mean : 30.9741

Gaus Width: 3.6095



B1L102S, U11-ch19, adc1

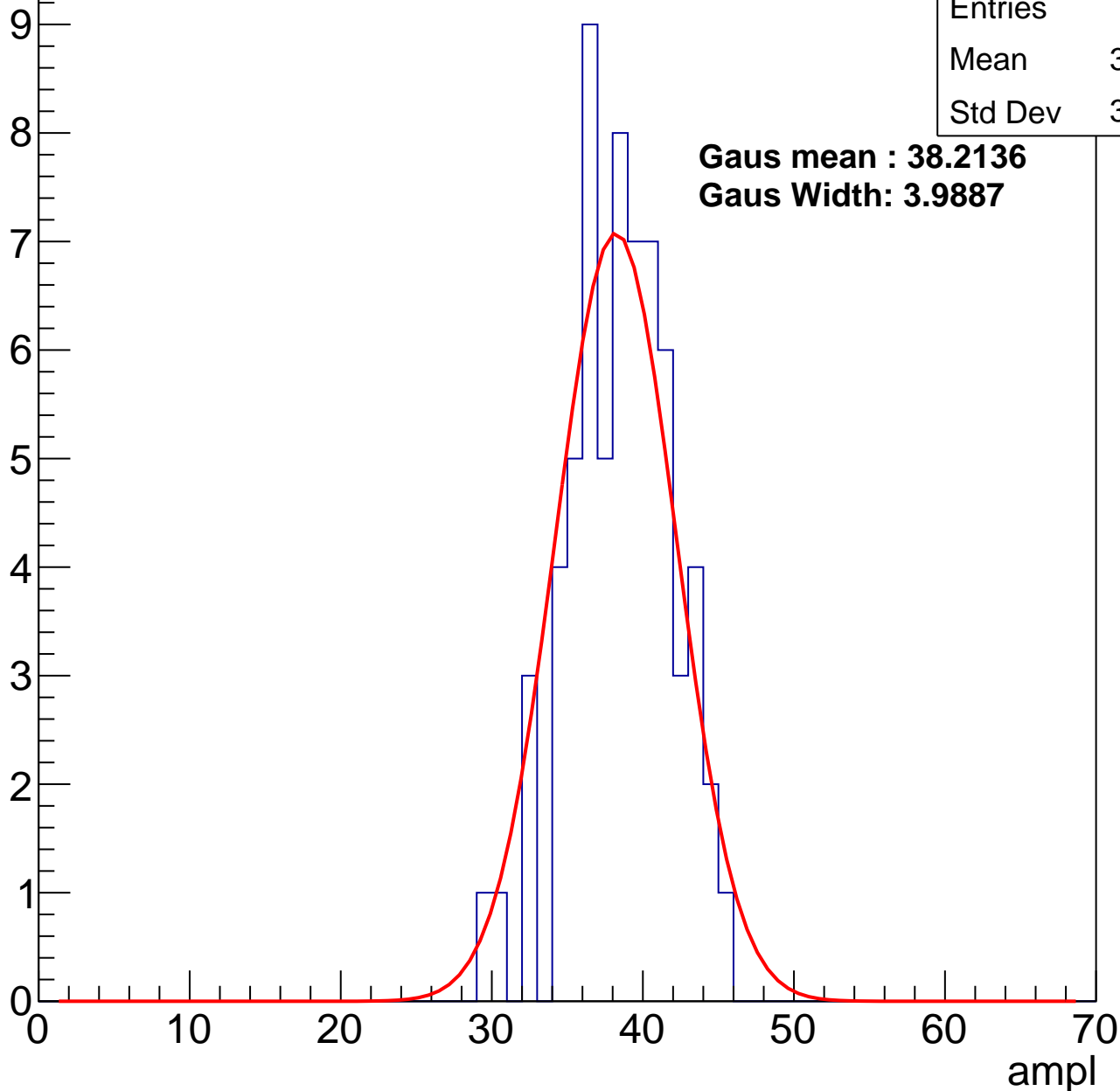
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	38.02
Std Dev	3.396

Gaus mean : 38.2136

Gaus Width: 3.9887



B1L102S, U11-ch19, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

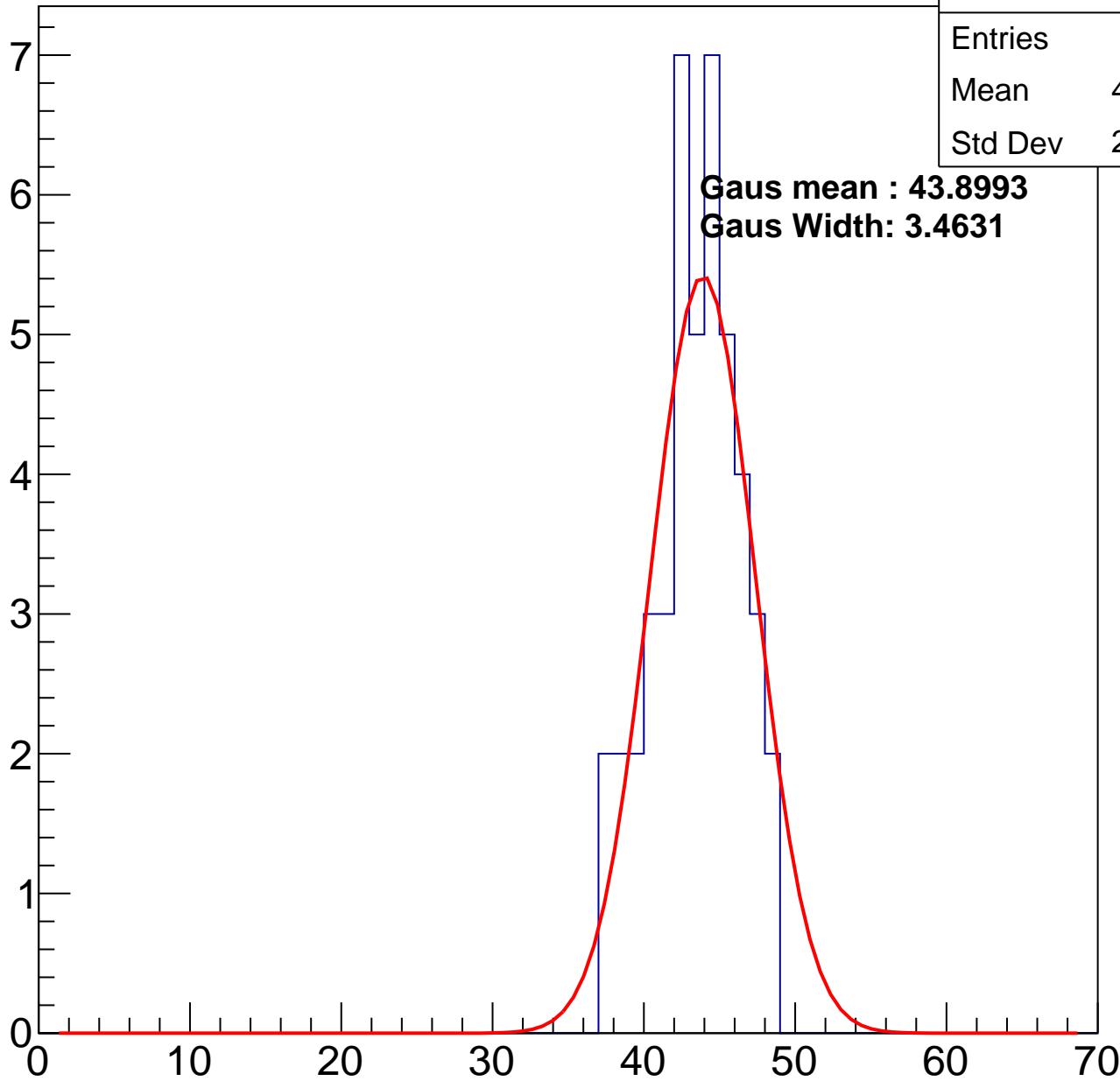
Entries	45
Mean	42.98
Std Dev	2.832

Gaus mean : 43.8993

Gaus Width: 3.4631

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	49.96
Std Dev	3.462

Entry

10

8

6

4

2

0

0

10

20

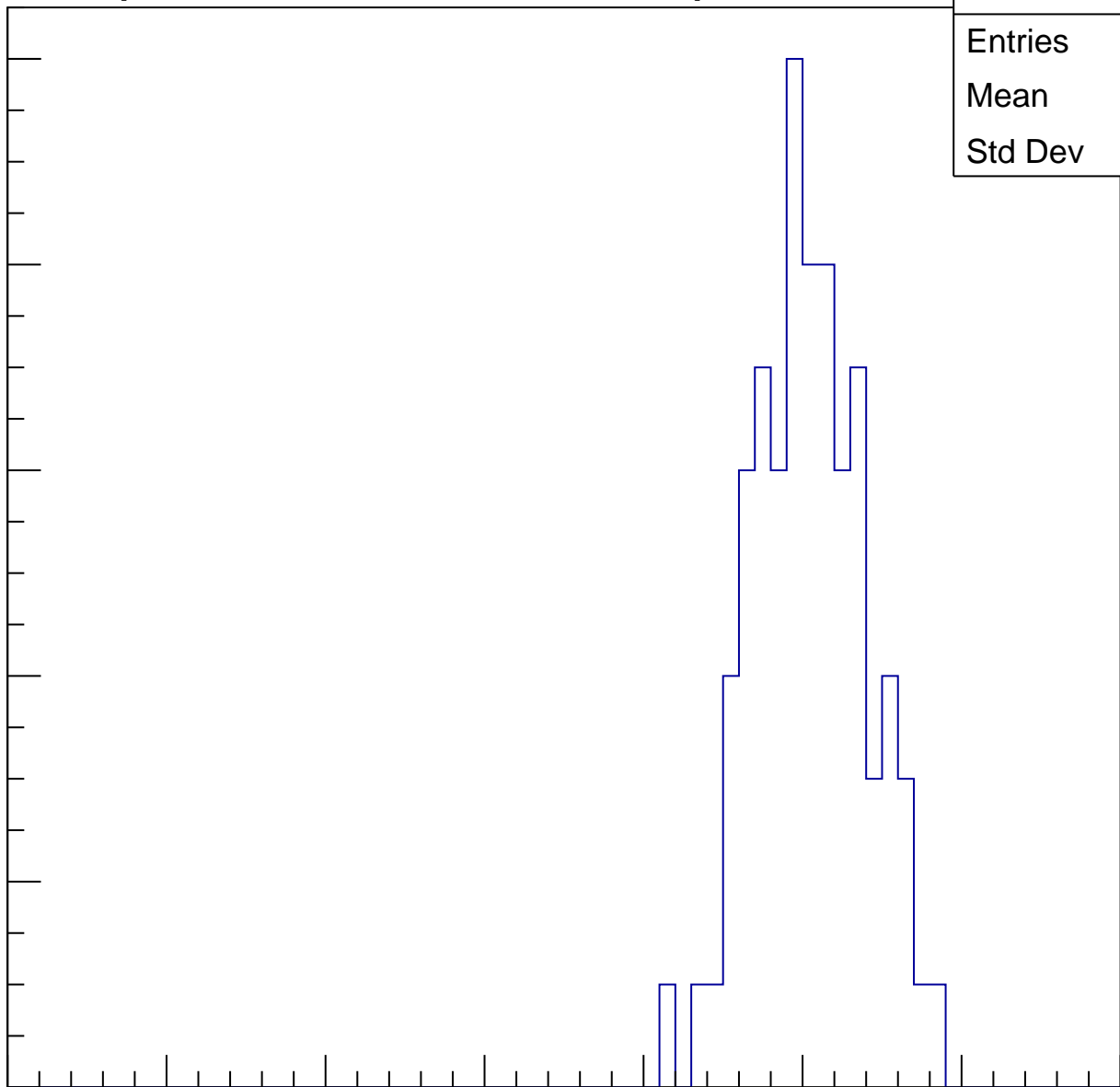
30

40

50

60

ampl

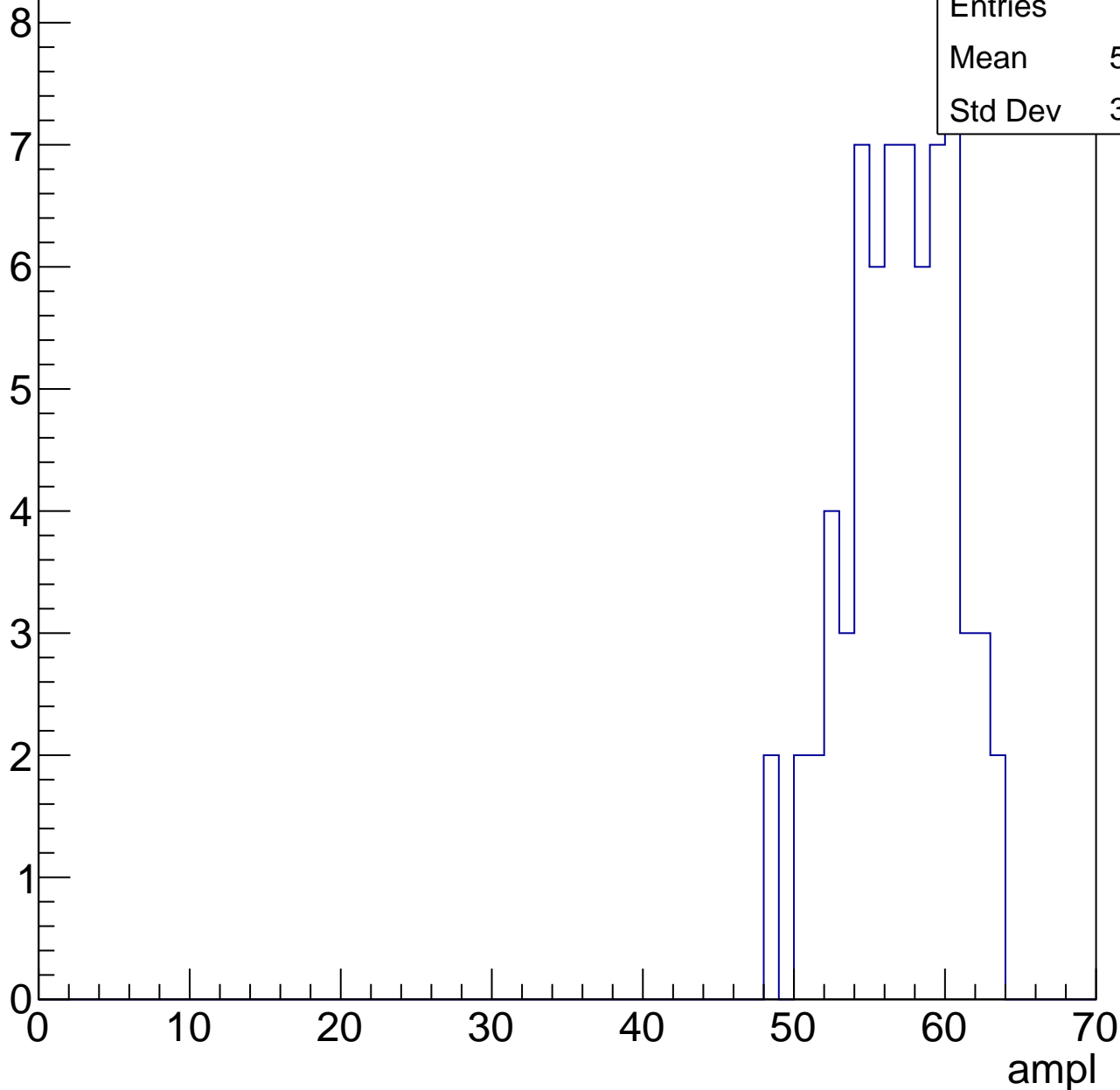


B1L102S, U11-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	56.52
Std Dev	3.529

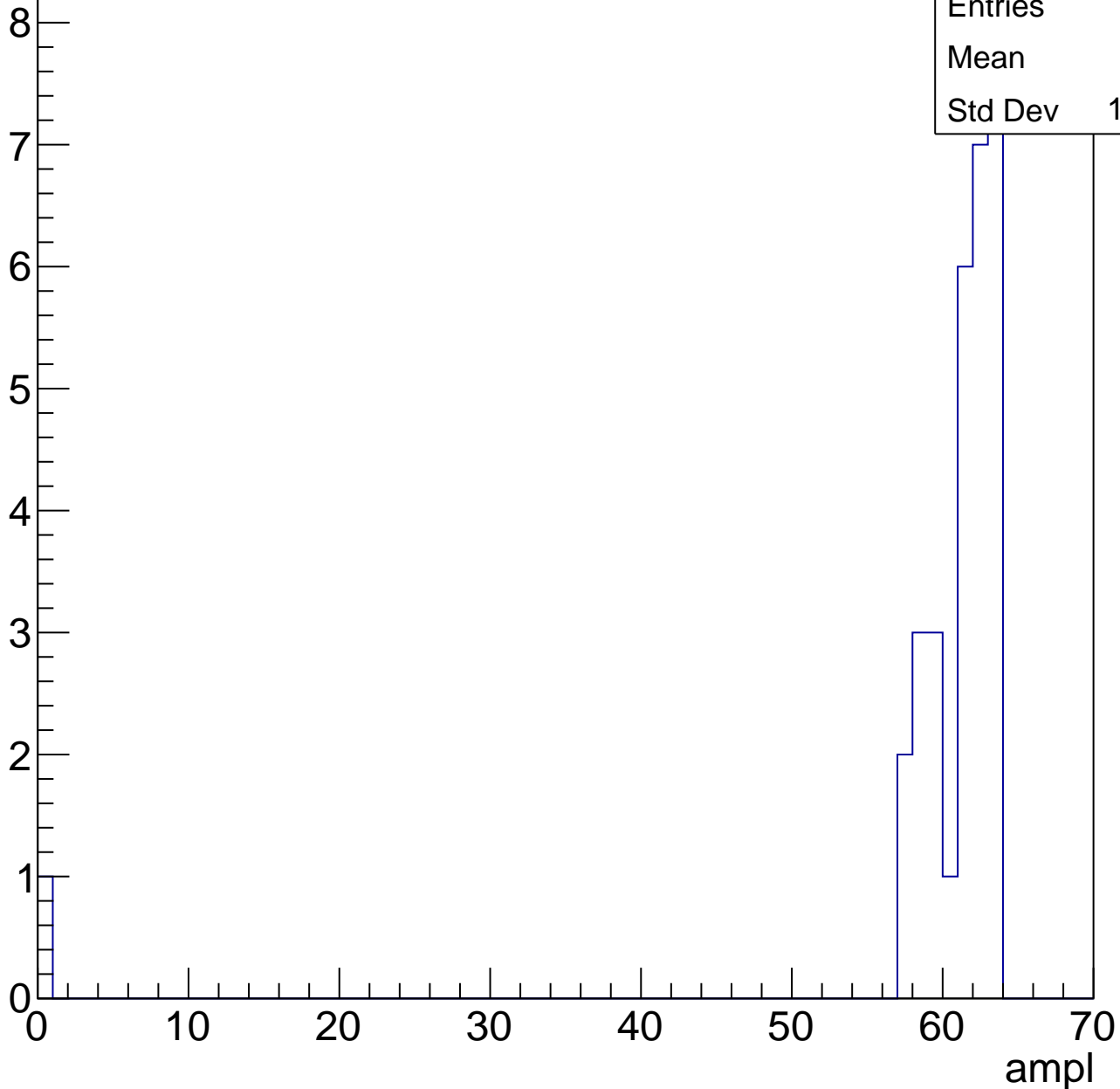


B1L102S, U11-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	59
Std Dev	10.94



B1L102S, U11-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	24
Std Dev	0

B1L102S, U11-ch20, adc0

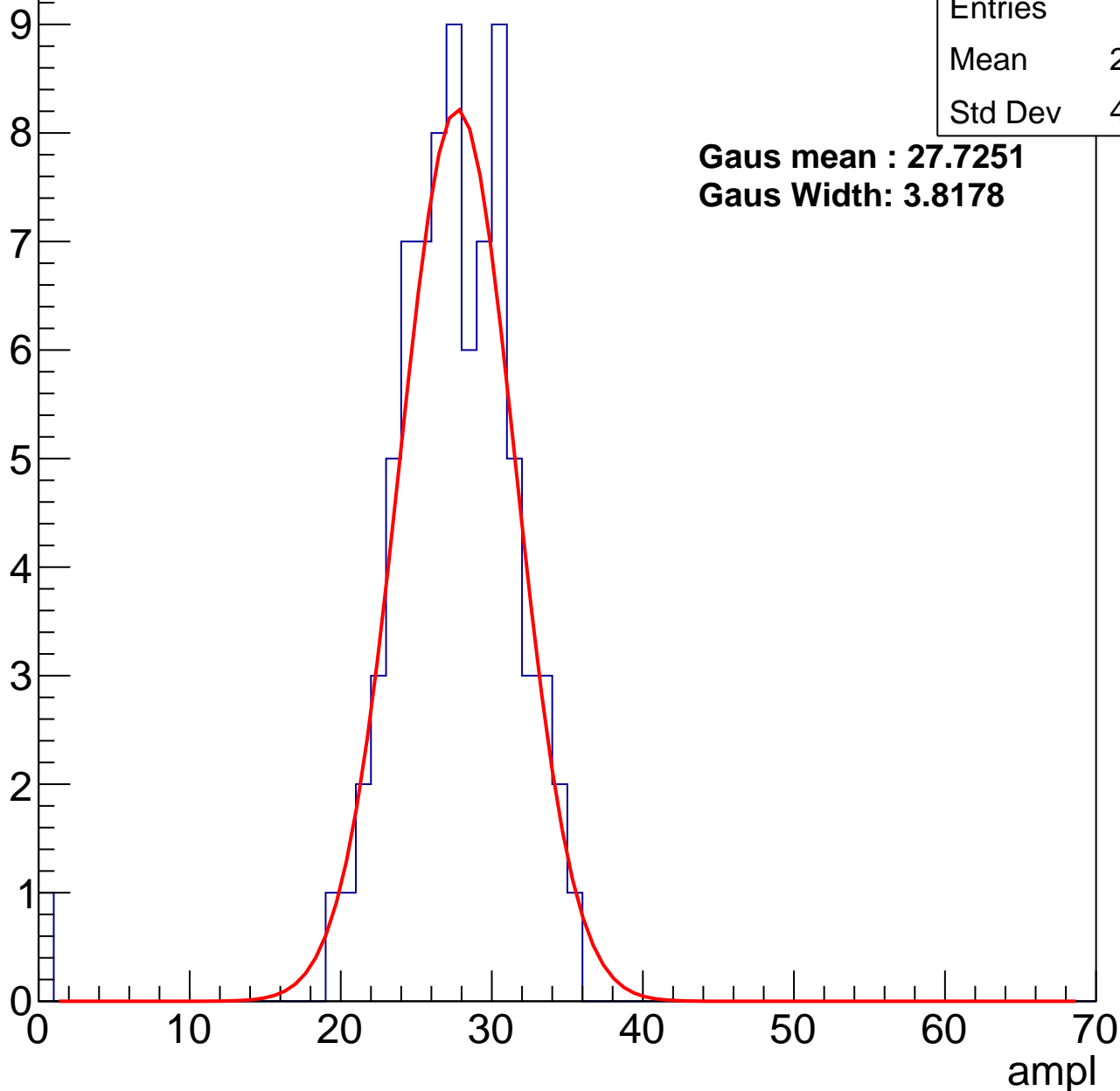
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	26.88
Std Dev	4.619

Gaus mean : 27.7251

Gaus Width: 3.8178



B1L102S, U11-ch20, adc1

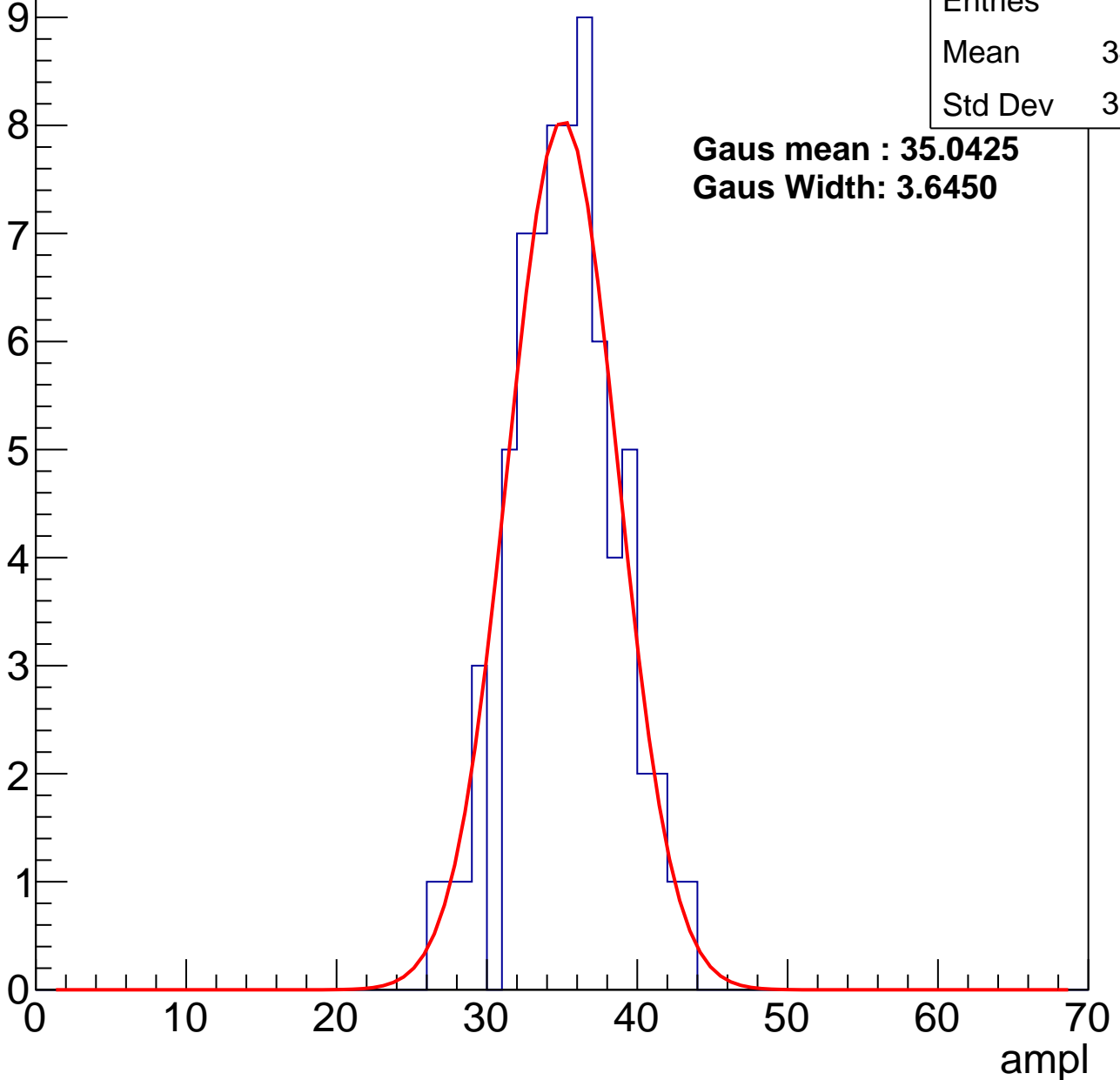
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.79
Std Dev	3.484

Gaus mean : 35.0425

Gaus Width: 3.6450



B1L102S, U11-ch20, adc2

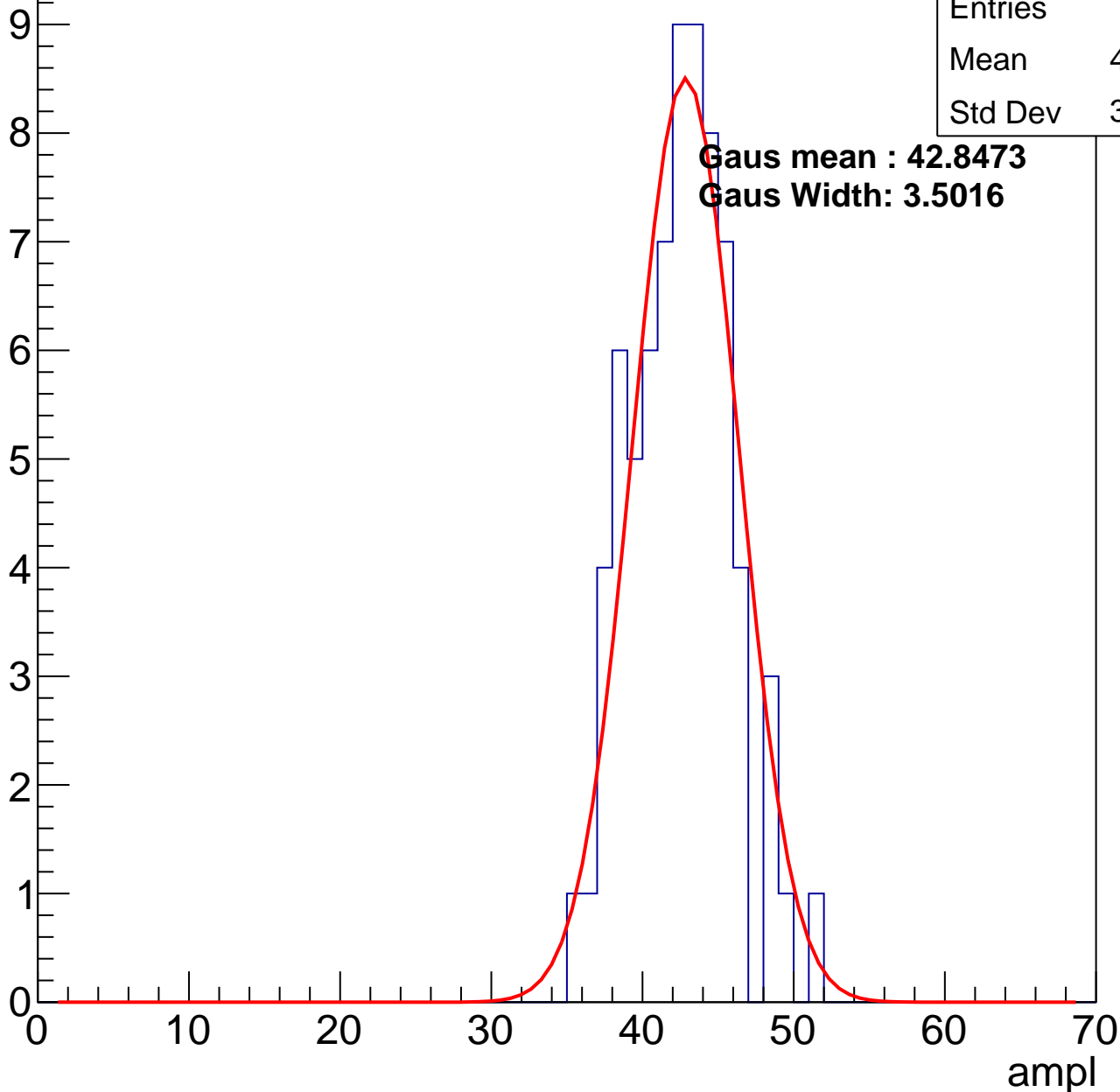
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.07
Std Dev	3.255

Gaus mean : 42.8473

Gaus Width: 3.5016

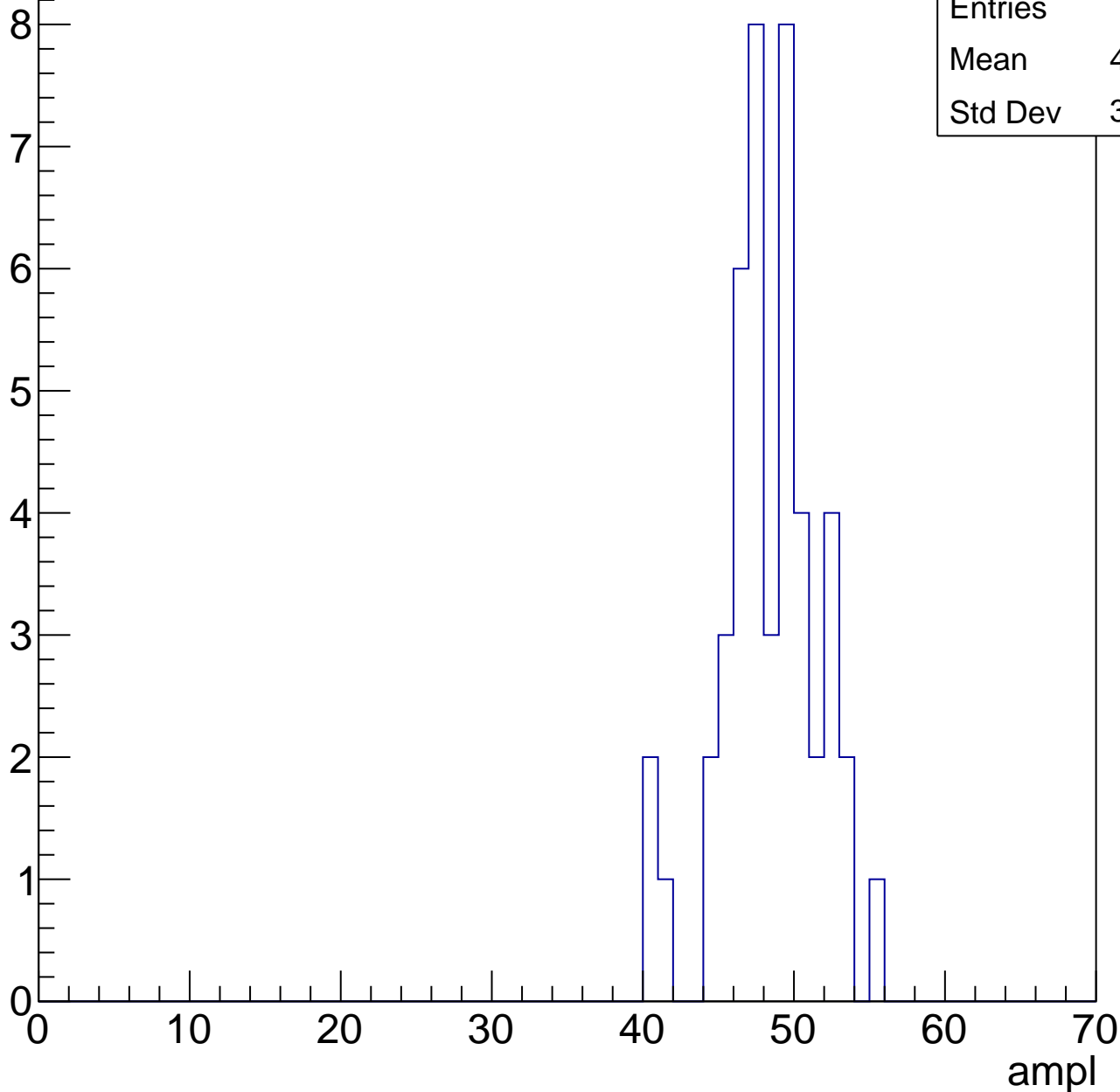


B1L102S, U11-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

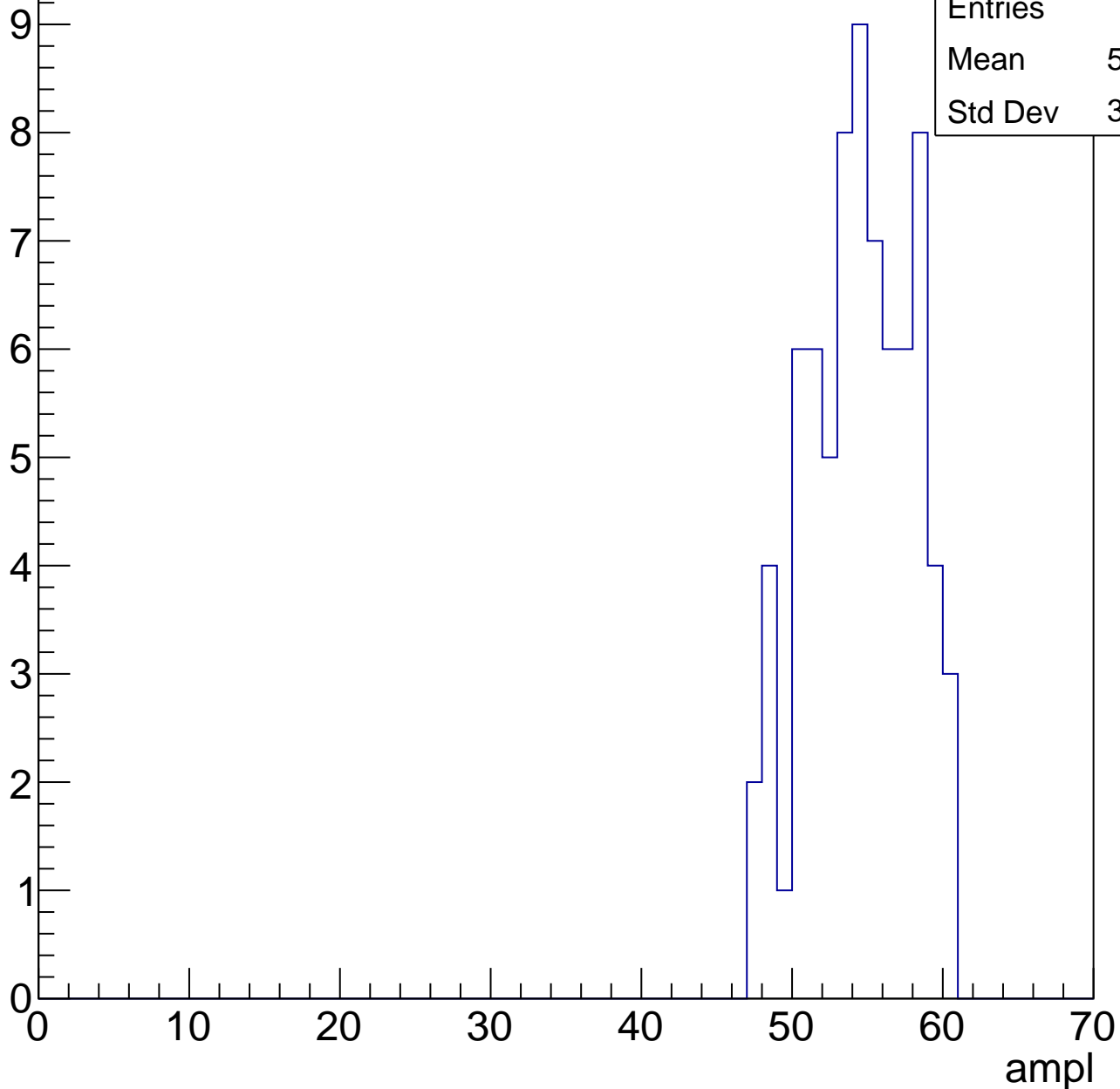
Entries	46
Mean	47.89
Std Dev	3.205



B1L102S, U11-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



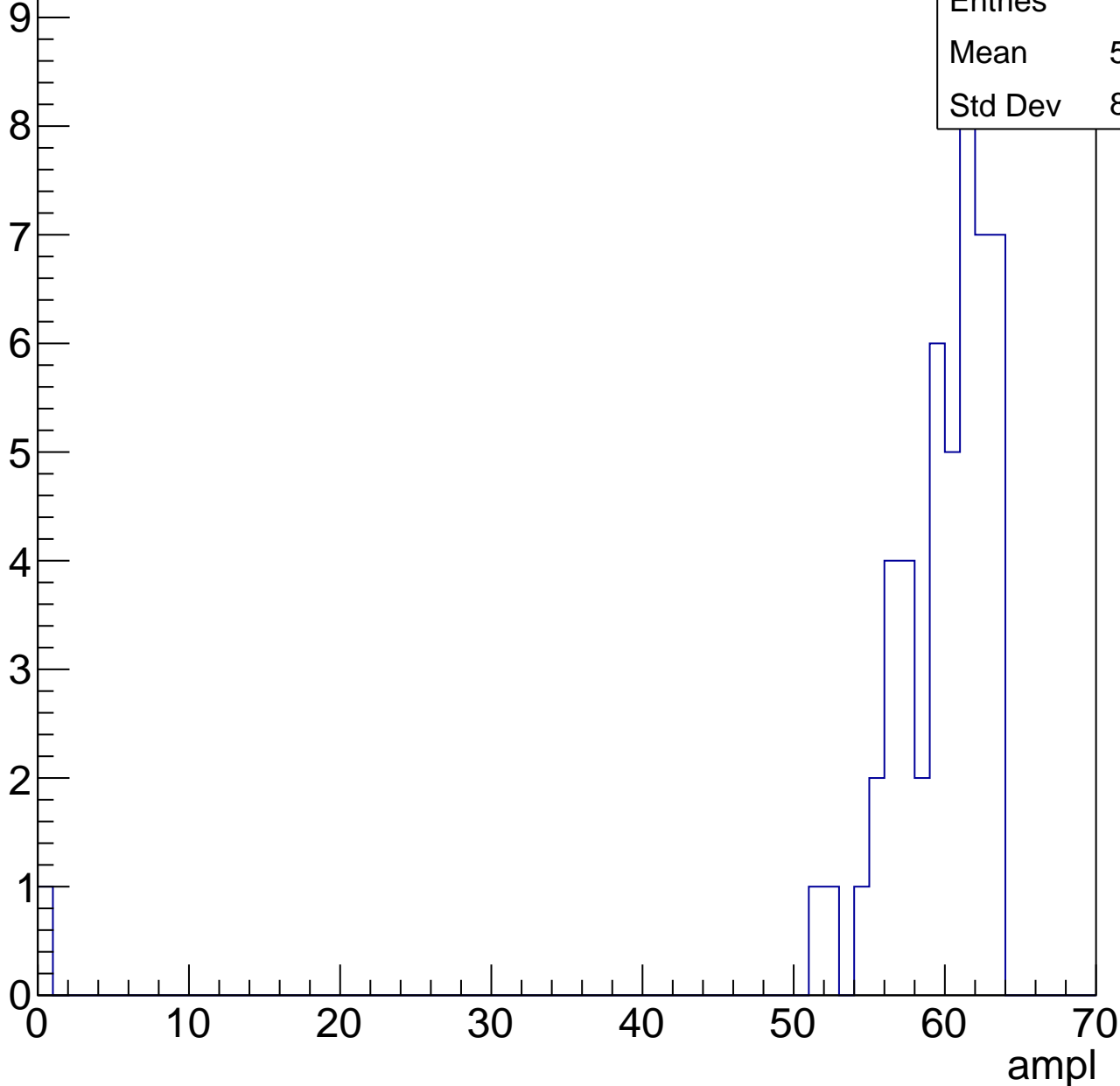
Entries	75
Mean	54.05
Std Dev	3.398

B1L102S, U11-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.26
Std Dev	8.825



B1L102S, U11-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.78

Std Dev

1.133

Entries	9
Mean	61.78
Std Dev	1.133

B1L102S, U11-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	18
Std Dev	0

ampl

B1L102S, U11-ch21, adc0

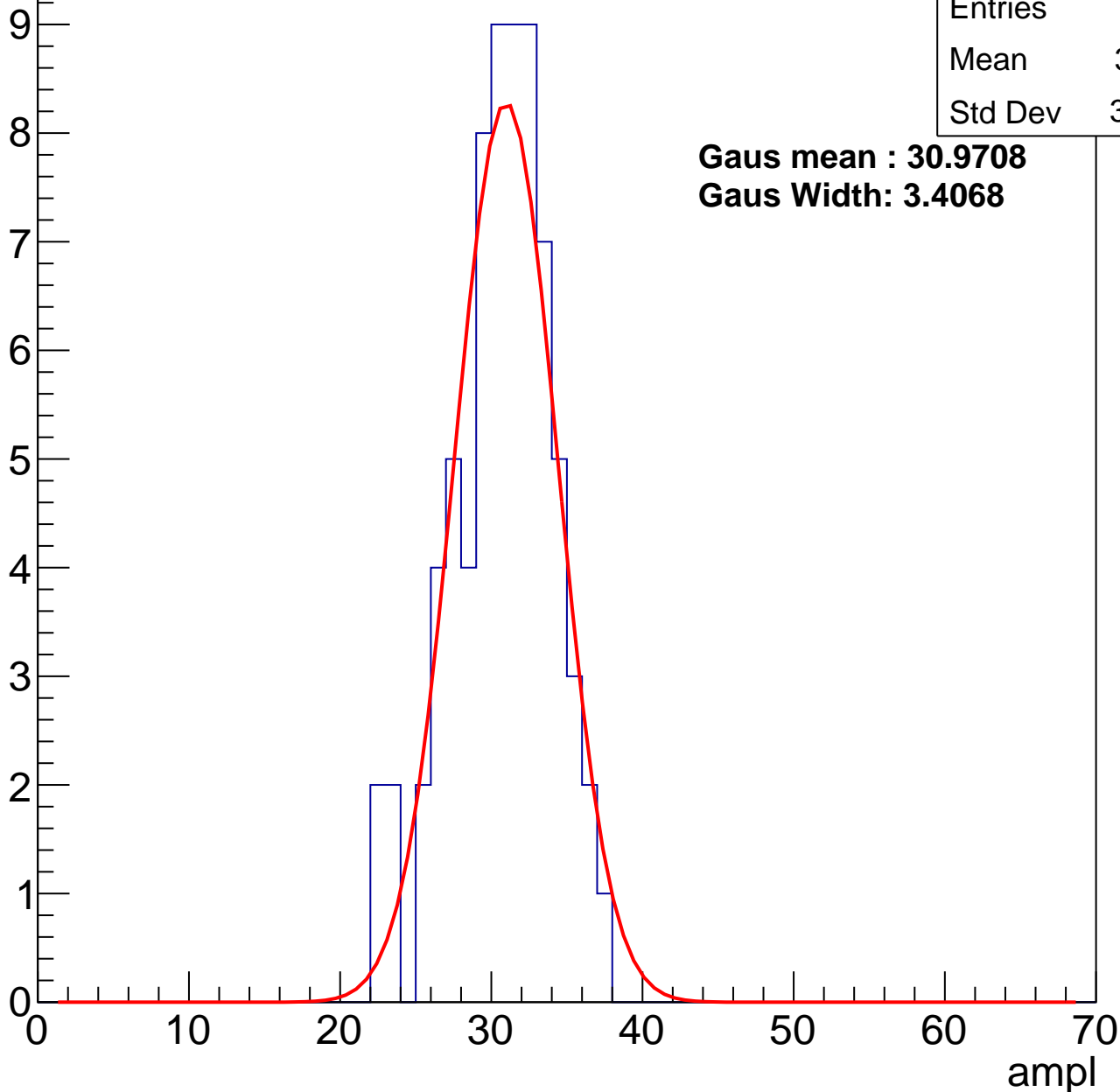
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	30.21
Std Dev	3.312

Gaus mean : 30.9708

Gaus Width: 3.4068



B1L102S, U11-ch21, adc1

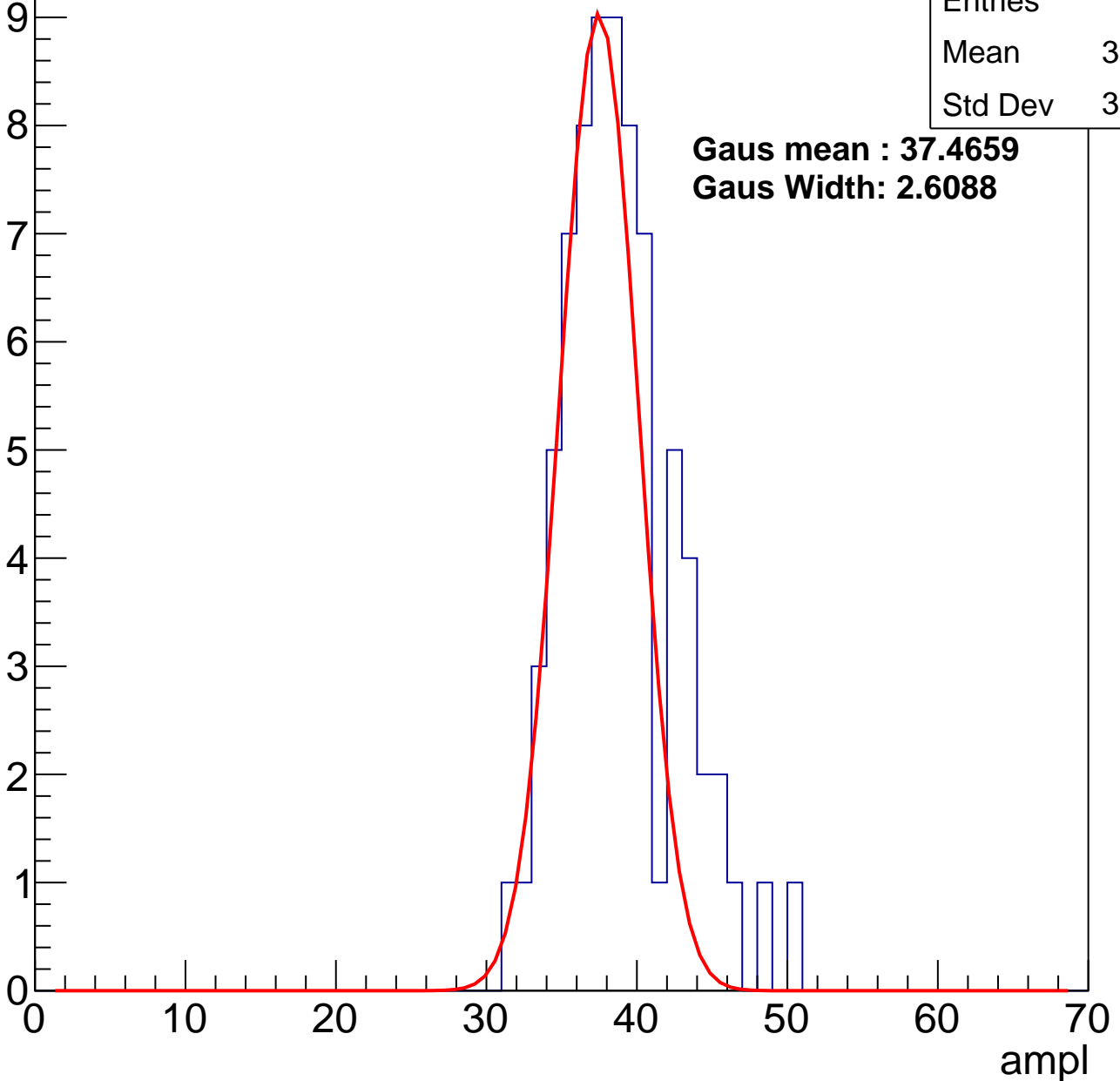
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	38.36
Std Dev	3.719

Gaus mean : 37.4659

Gaus Width: 2.6088



B1L102S, U11-ch21, adc2

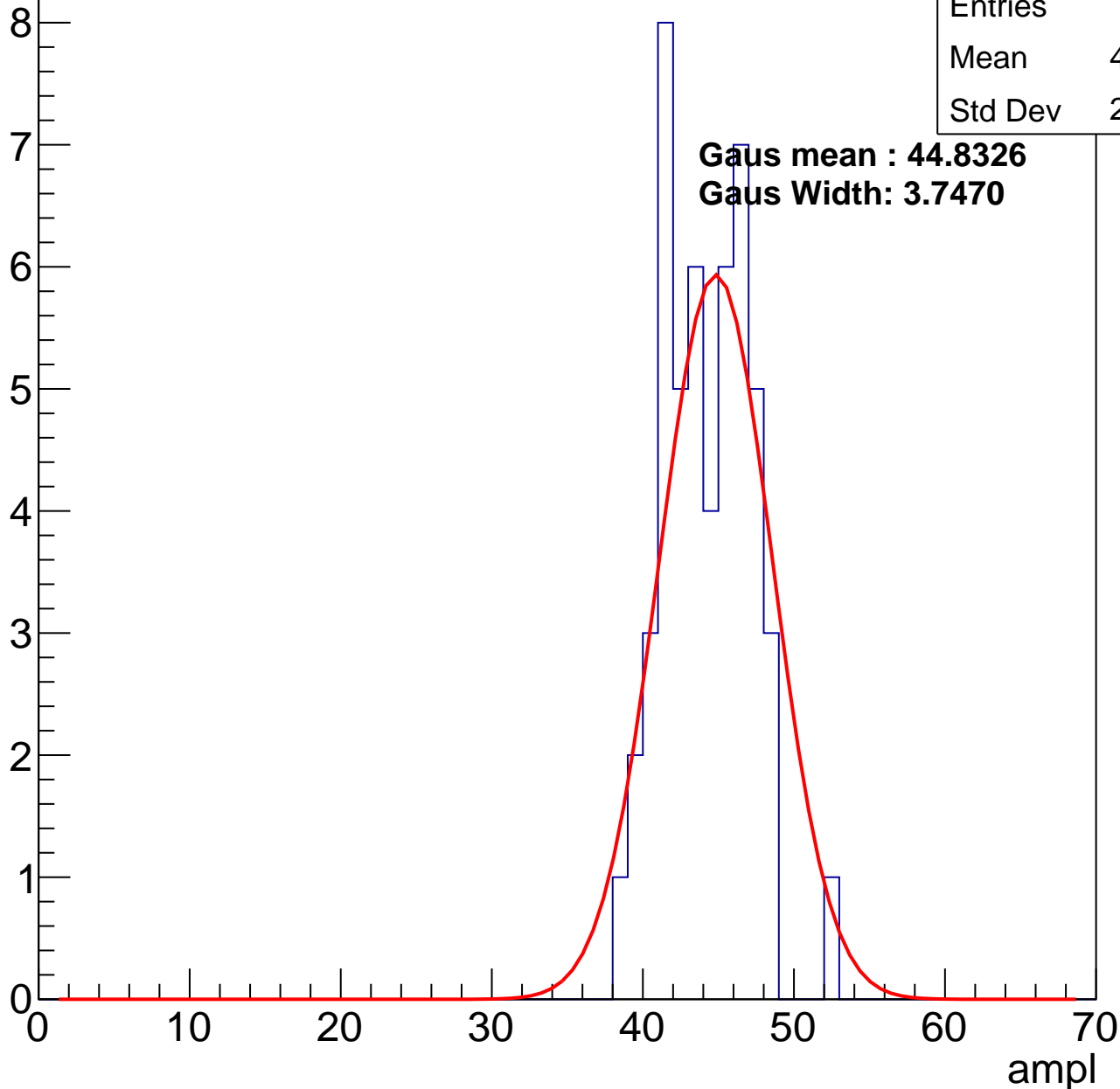
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	43.75
Std Dev	2.869

Gaus mean : 44.8326

Gaus Width: 3.7470

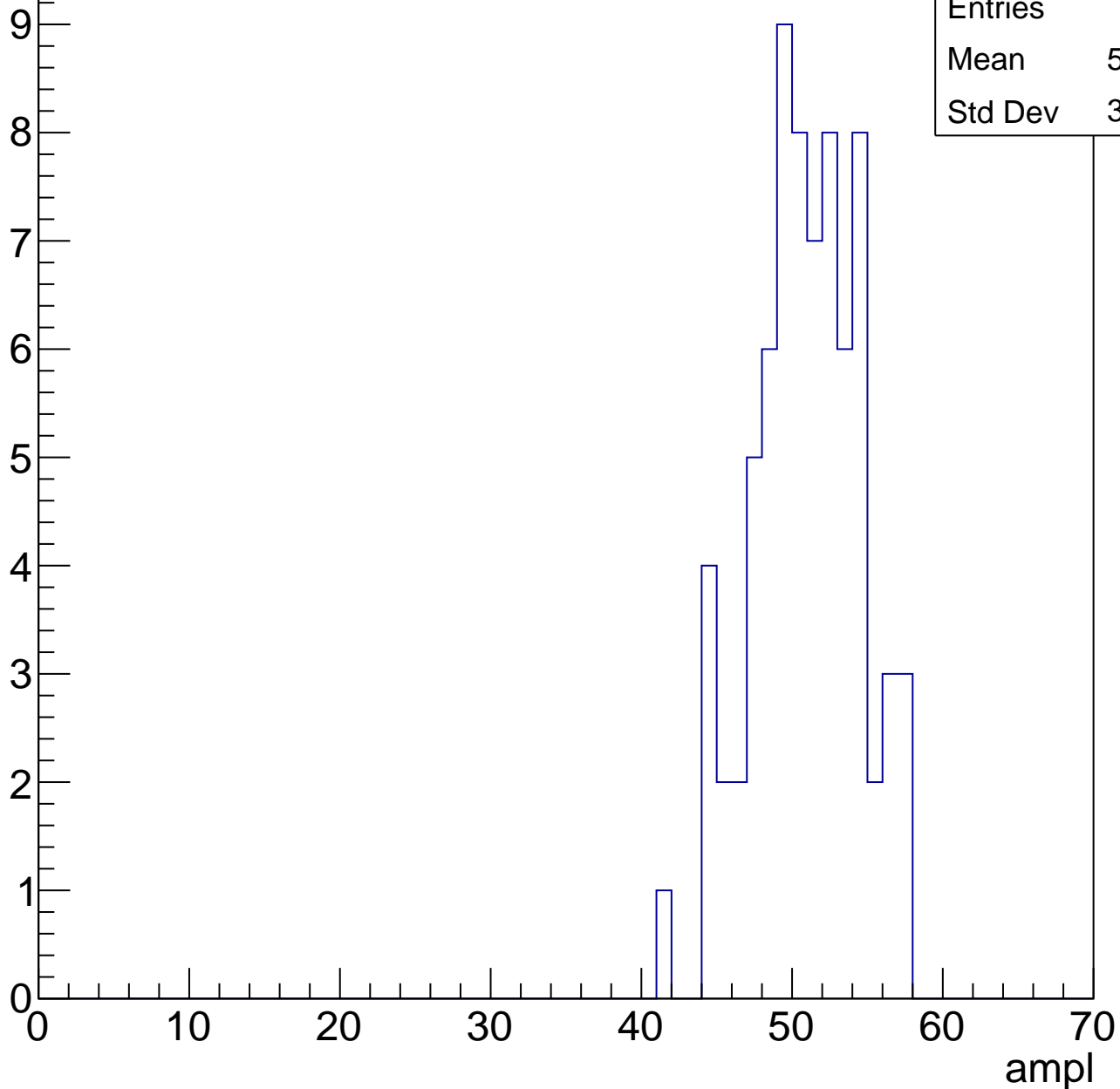


B1L102S, U11-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

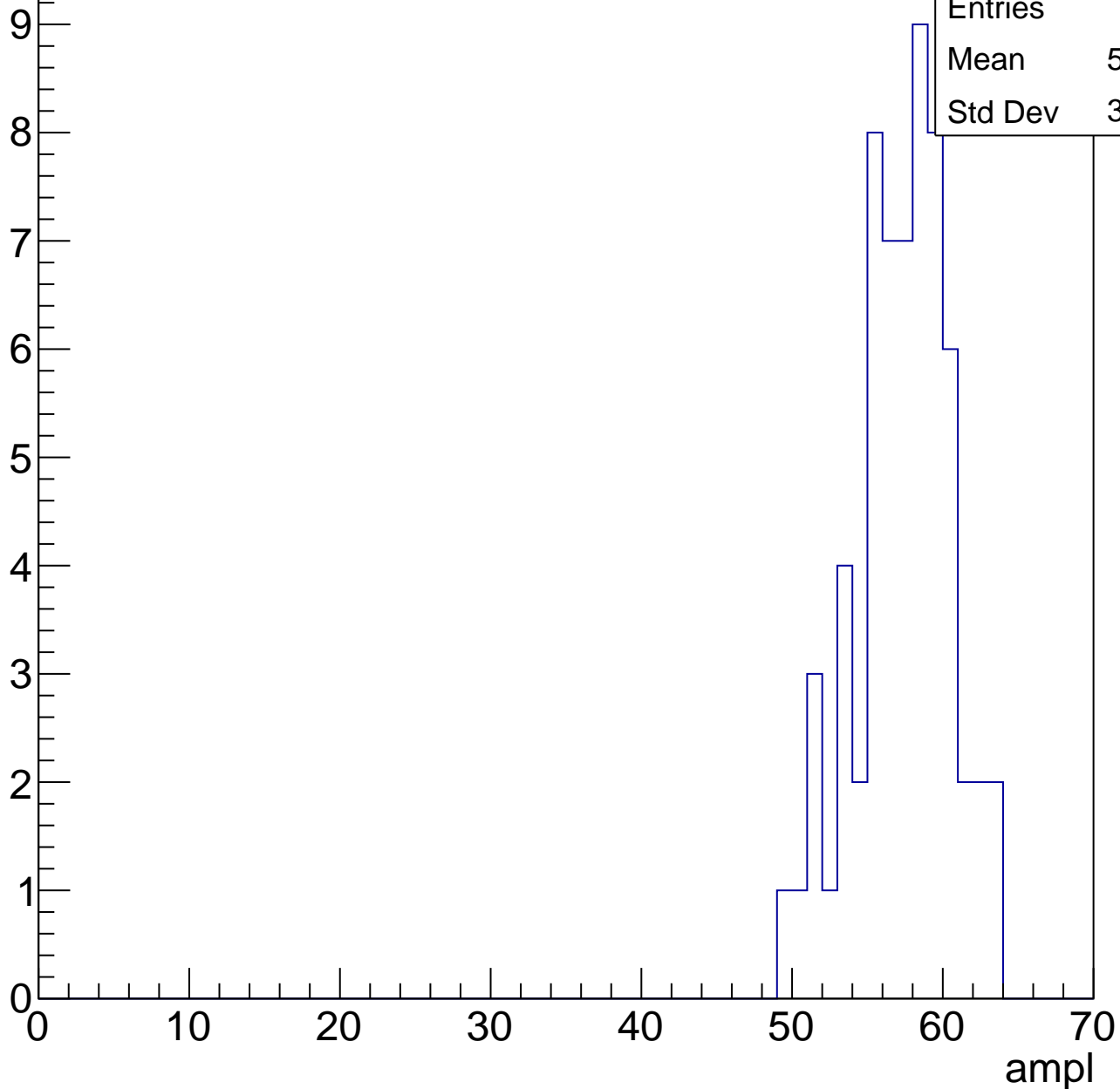
Entries	74
Mean	50.47
Std Dev	3.477



B1L102S, U11-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

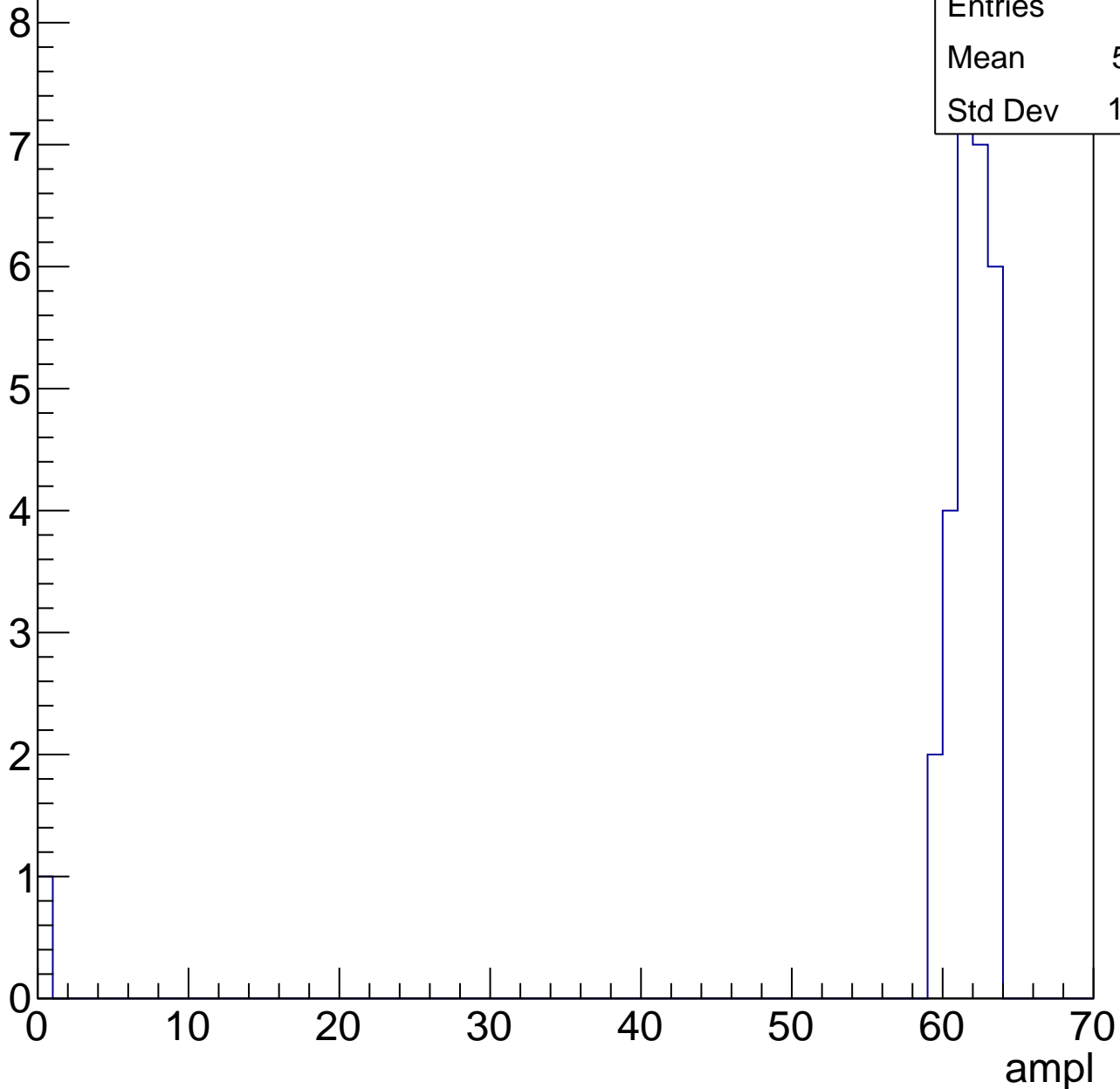


B1L102S, U11-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	59.21
Std Dev	11.46



B1L102S, U11-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	61.5
Std Dev	2.062

B1L102S, U11-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch22, adc0

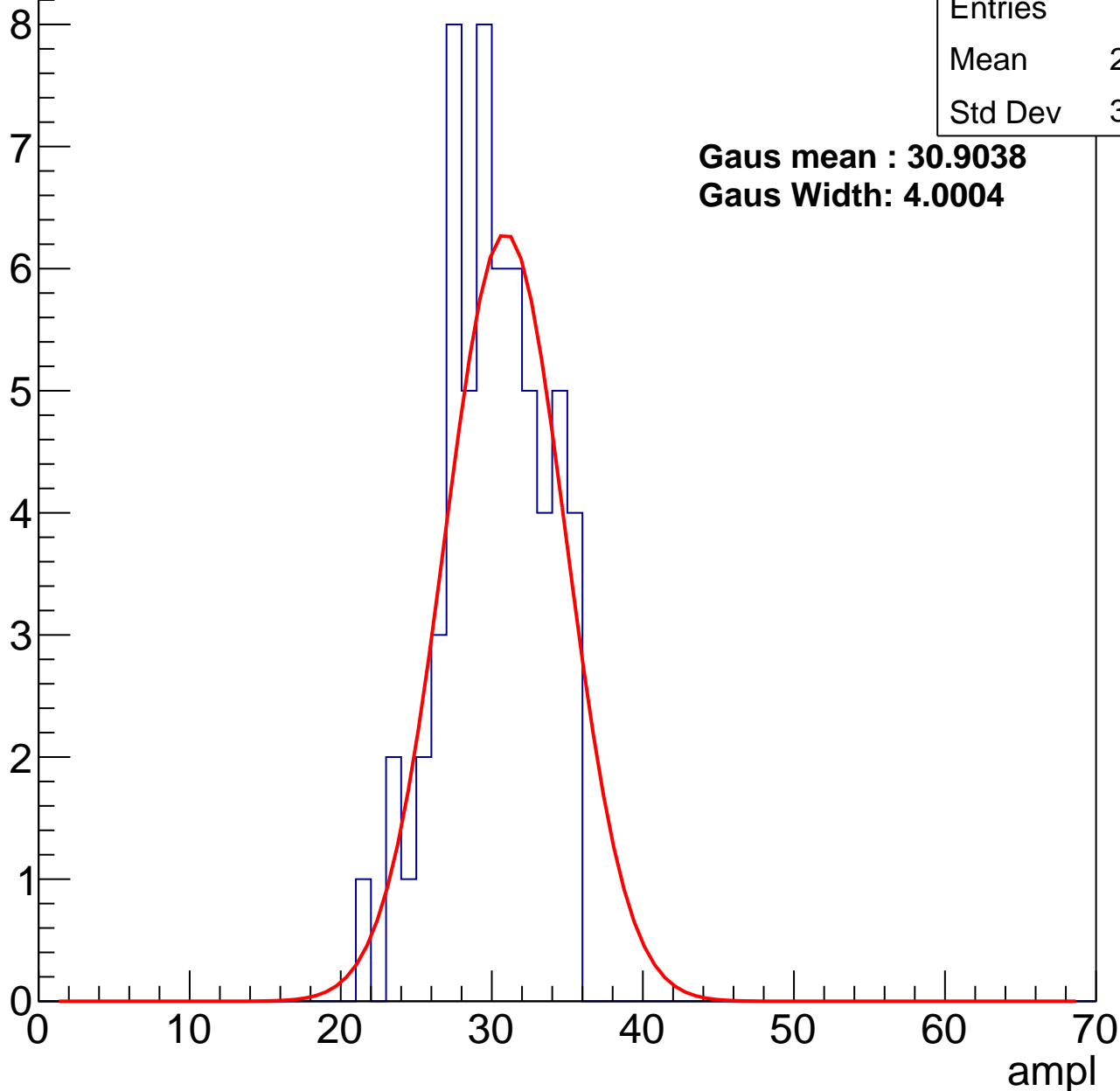
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.58
Std Dev	3.278

Gaus mean : 30.9038

Gaus Width: 4.0004



B1L102S, U11-ch22, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	36.78
Std Dev	5.543

Gaus mean : 37.3474

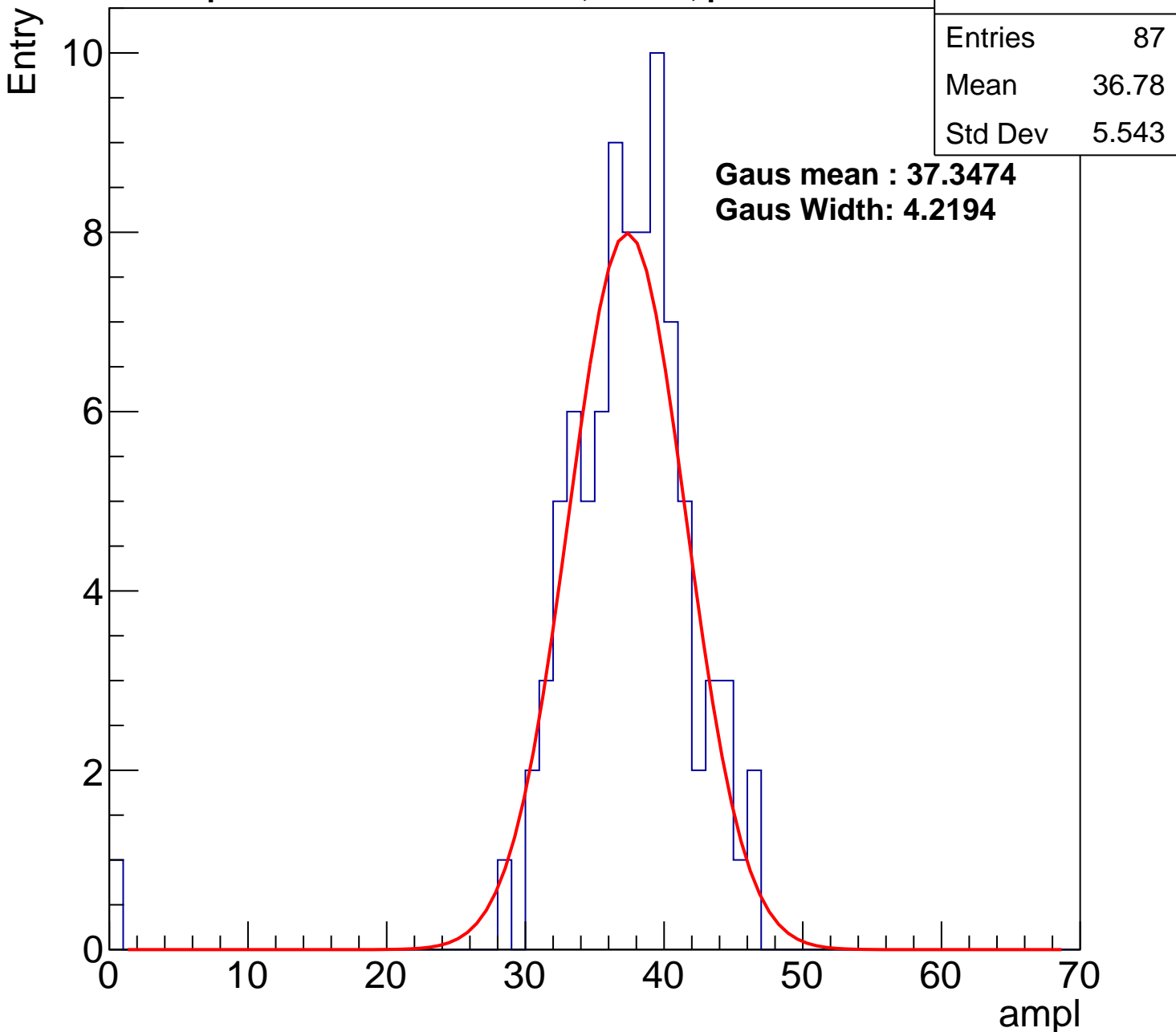
Gaus Width: 4.2194

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch22, adc2

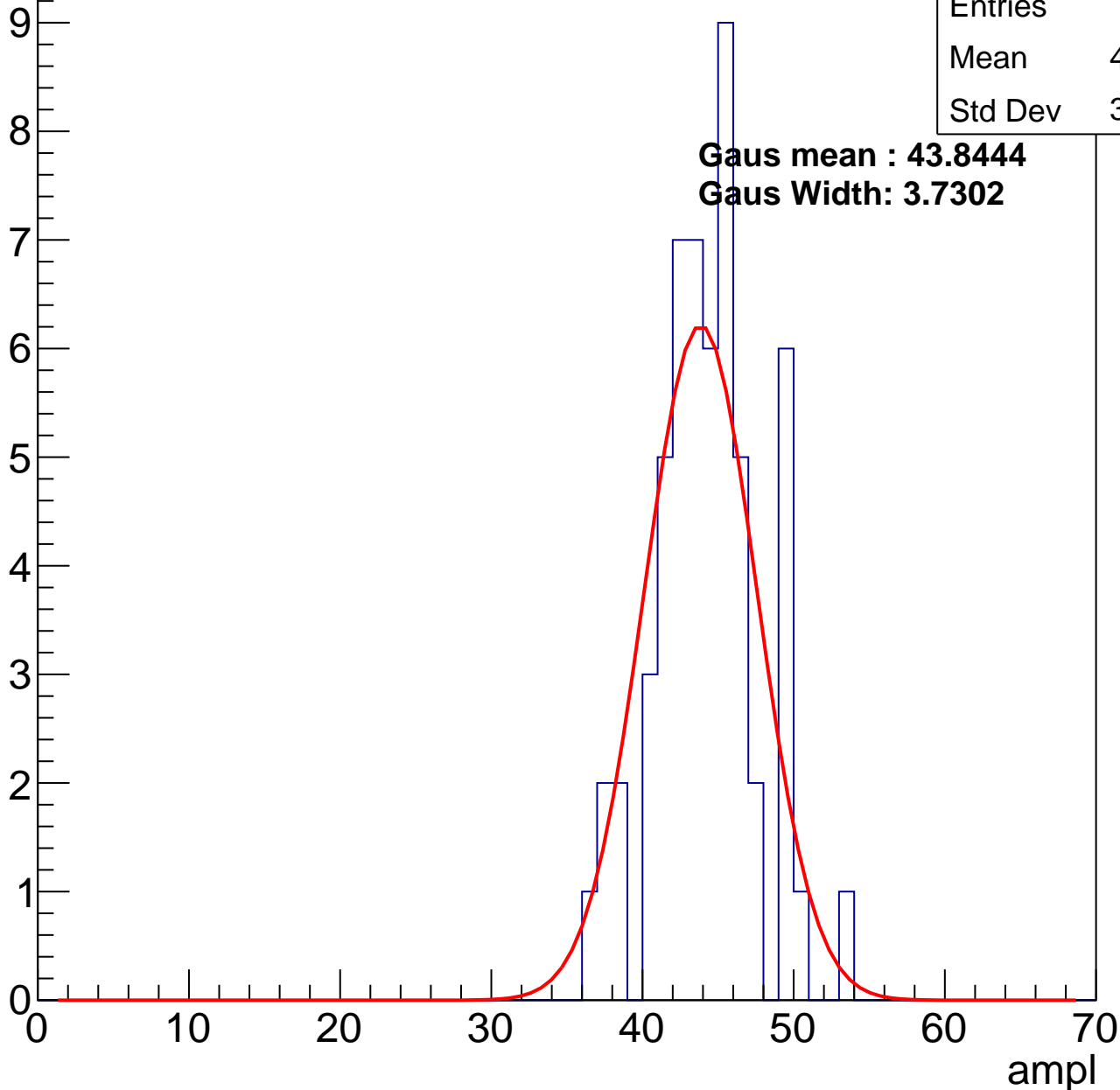
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.79
Std Dev	3.437

Gaus mean : 43.8444

Gaus Width: 3.7302

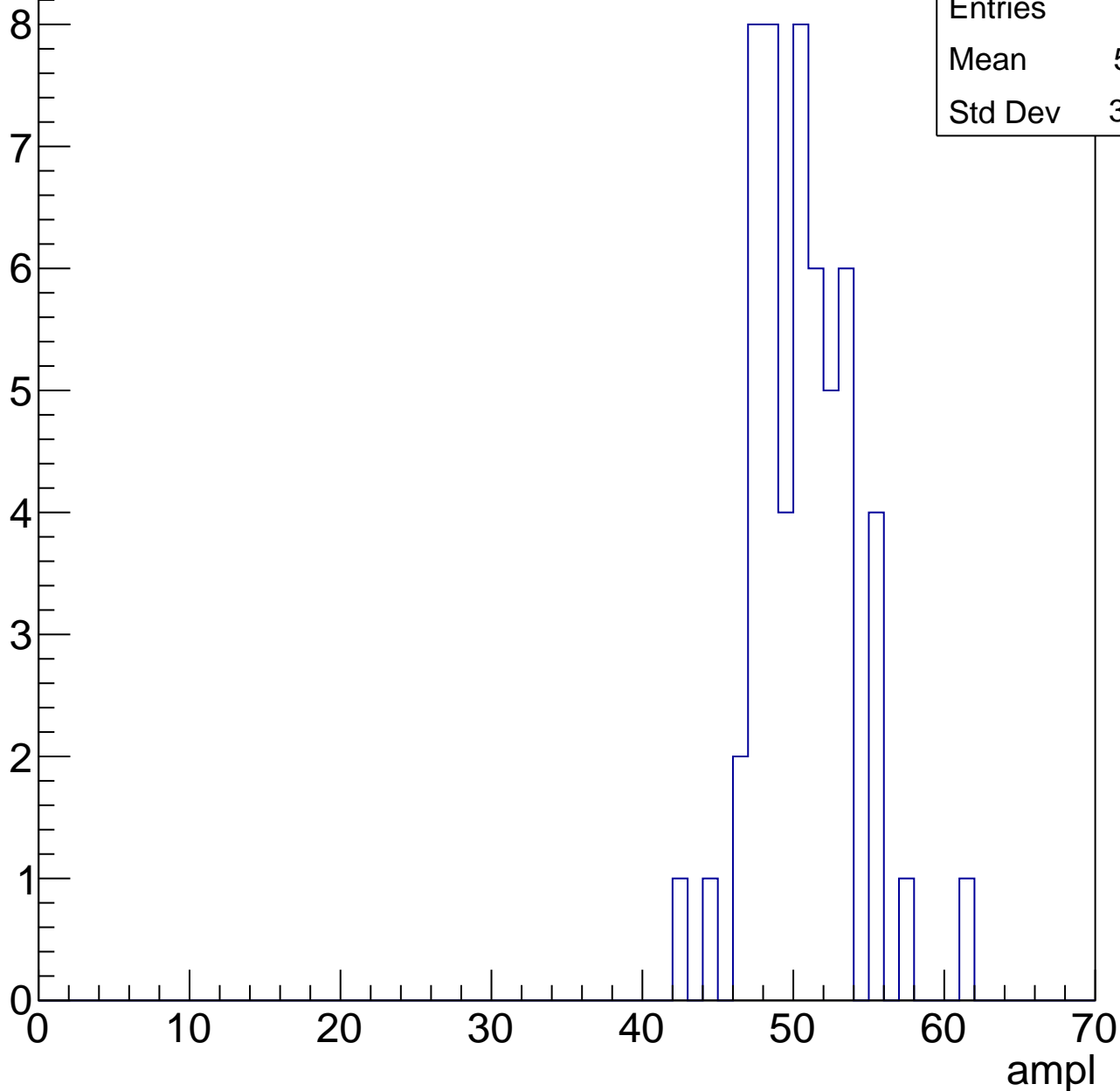


B1L102S, U11-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.11
Std Dev	3.273



B1L102S, U11-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	55.31
Std Dev	3.528

Entry

10

8

6

4

2

0

0

10

20

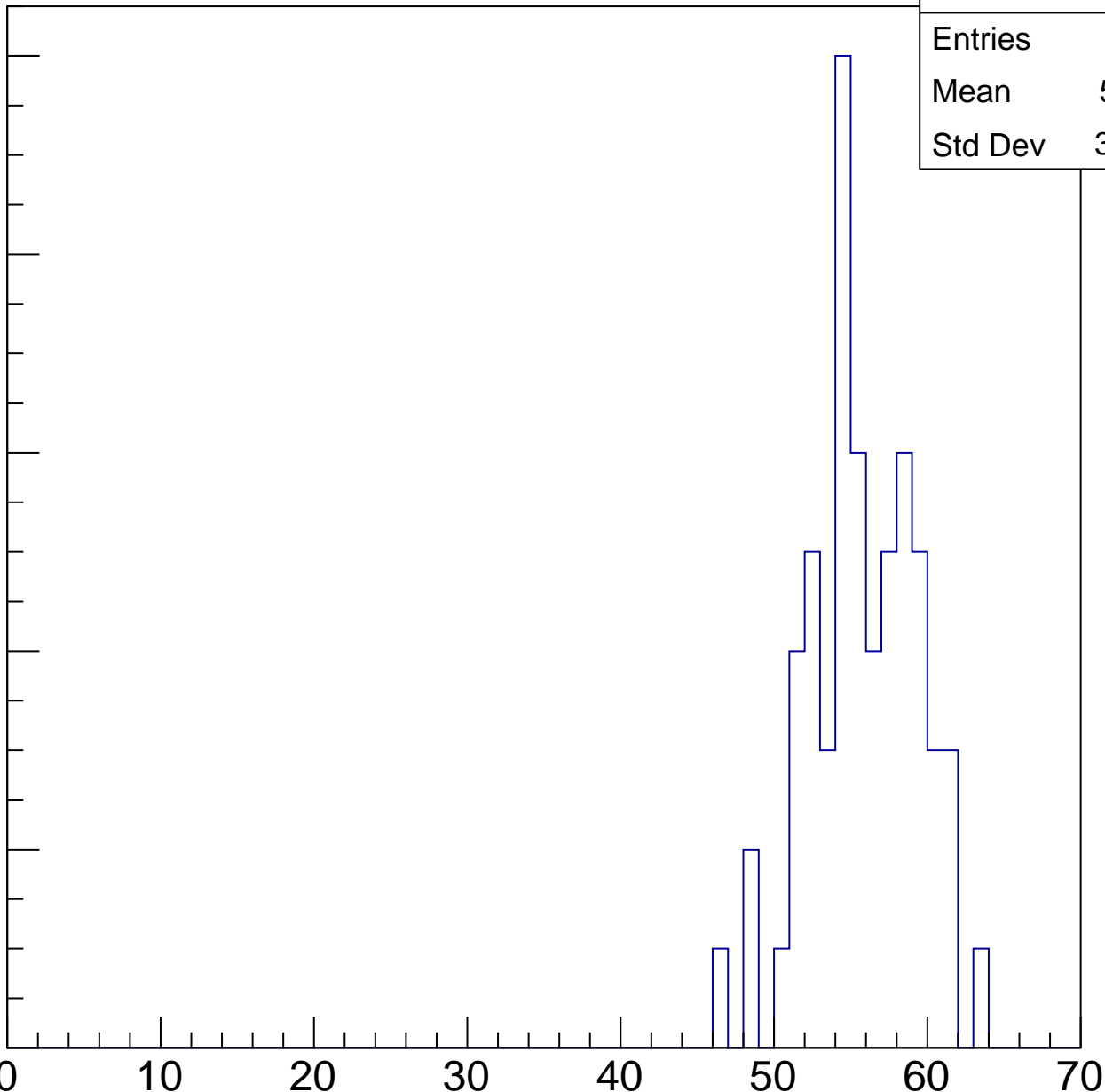
30

40

50

60

ampl

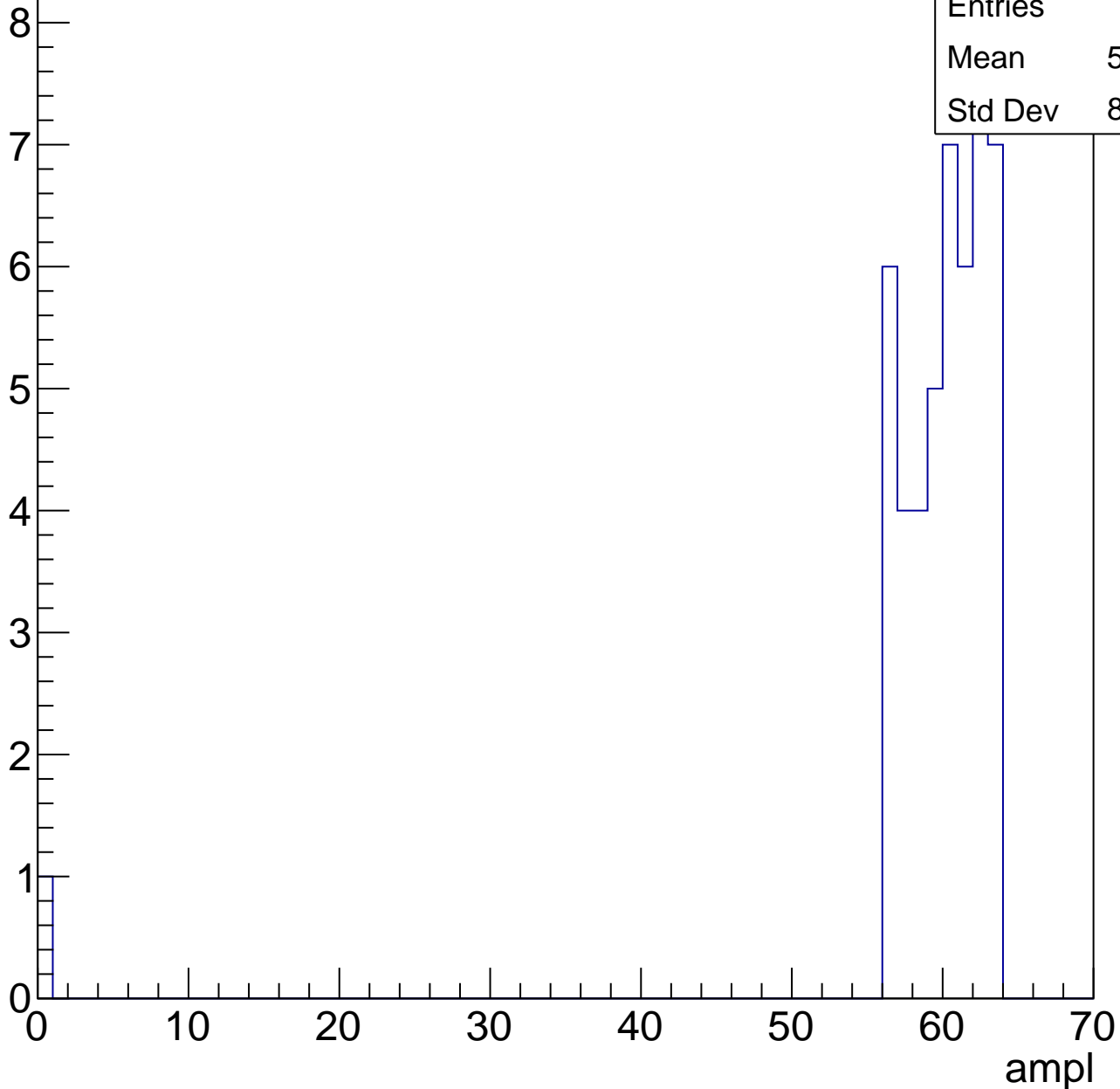


B1L102S, U11-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.62
Std Dev	8.854



B1L102S, U11-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch23, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	28.48
Std Dev	4.784

Gaus mean : 29.4666

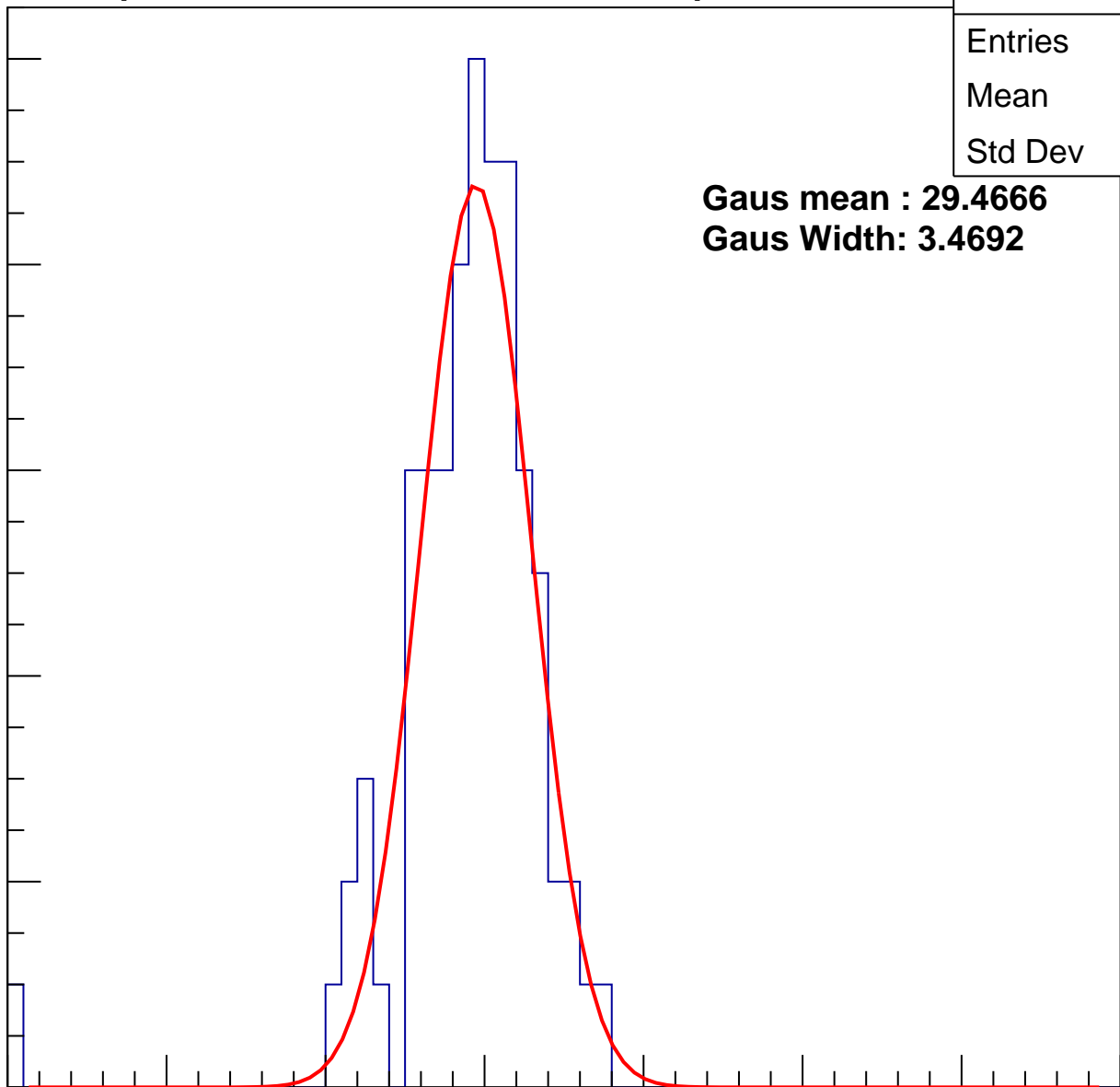
Gaus Width: 3.4692

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch23, adc1

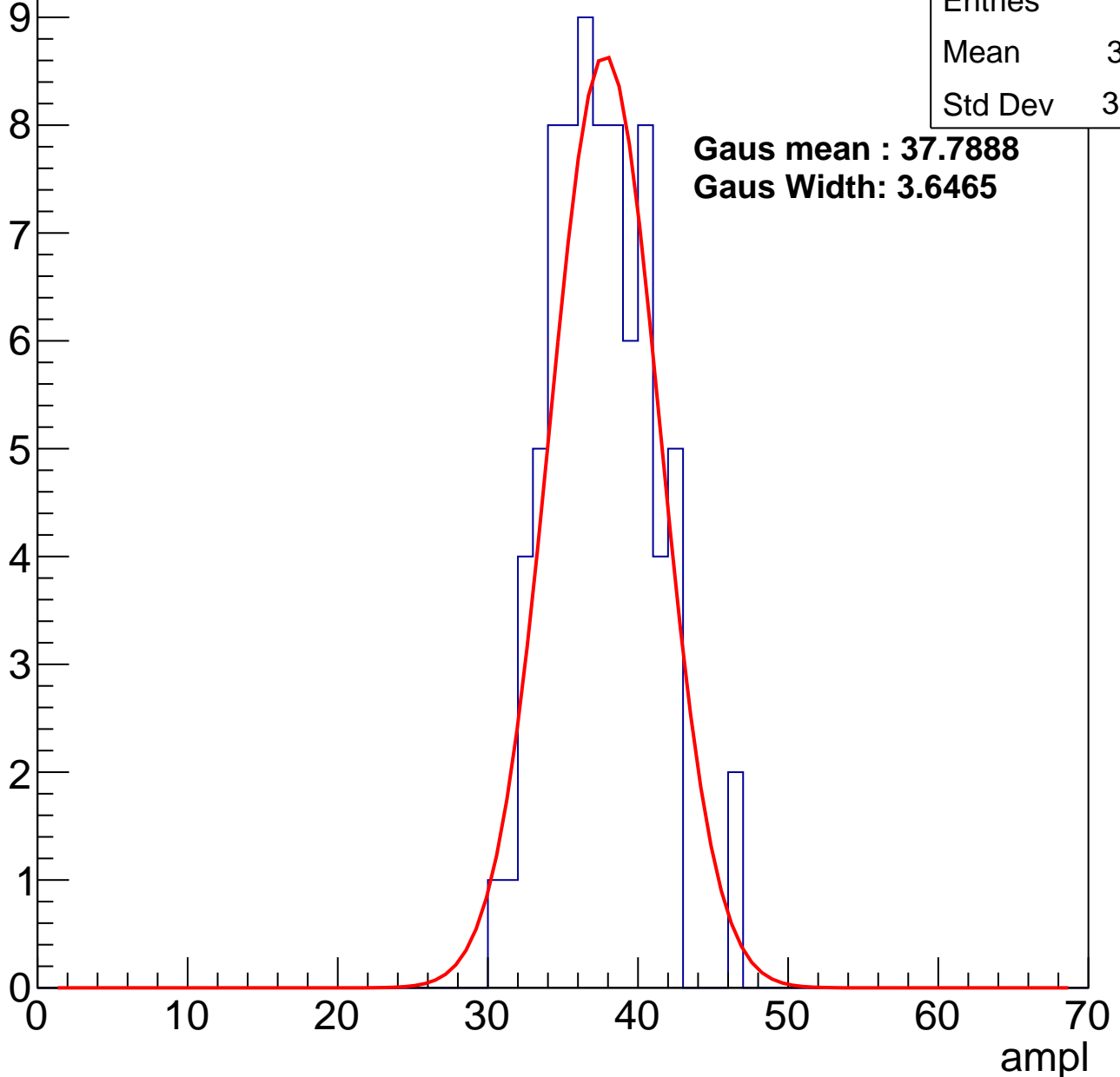
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	37.01
Std Dev	3.289

Gaus mean : 37.7888

Gaus Width: 3.6465



B1L102S, U11-ch23, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.54
Std Dev	2.847

Gaus mean : 44.1837

Gaus Width: 2.7301

10

8

6

4

2

0

ampl

0

10

20

30

40

50

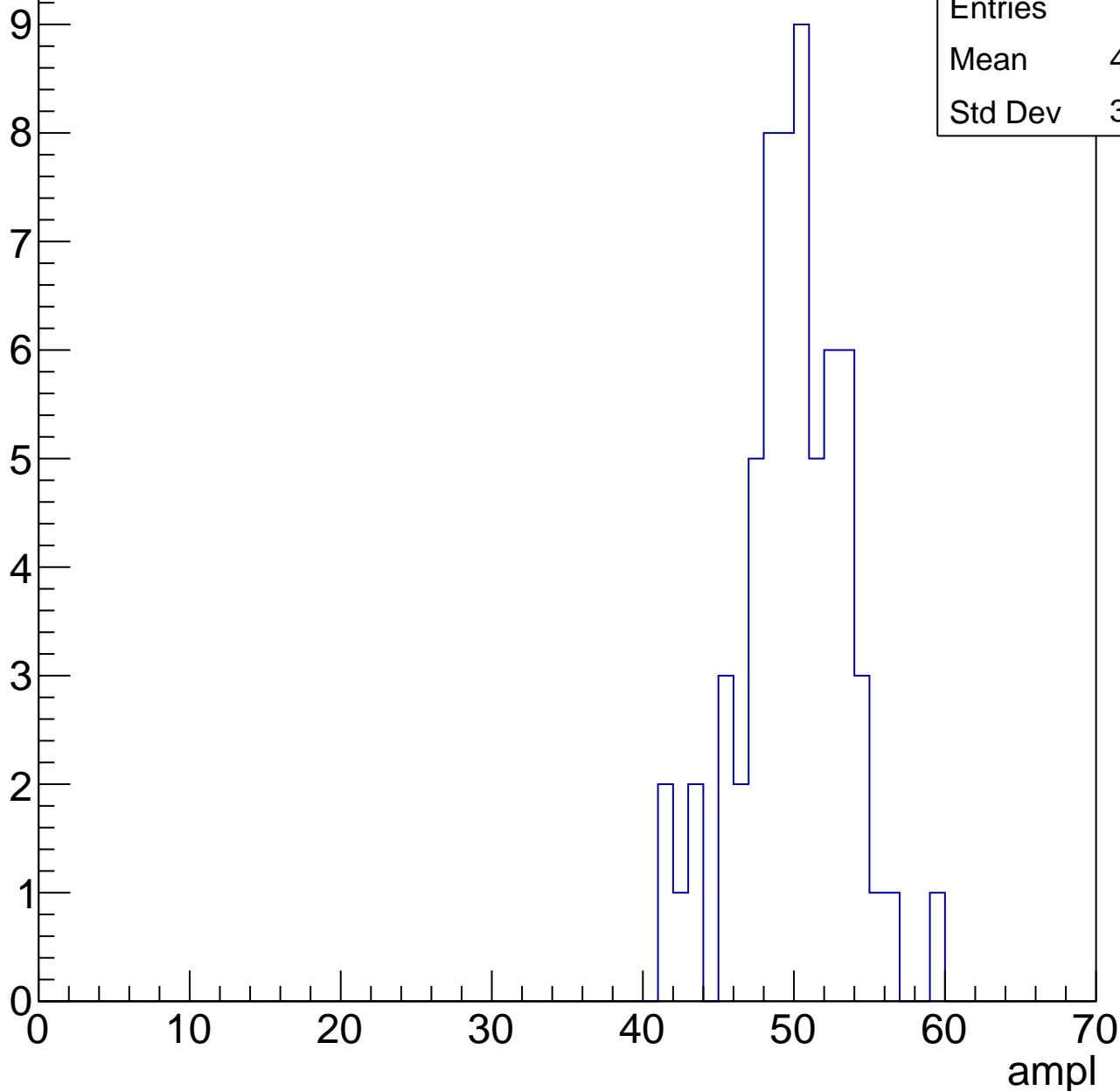
60

70

B1L102S, U11-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

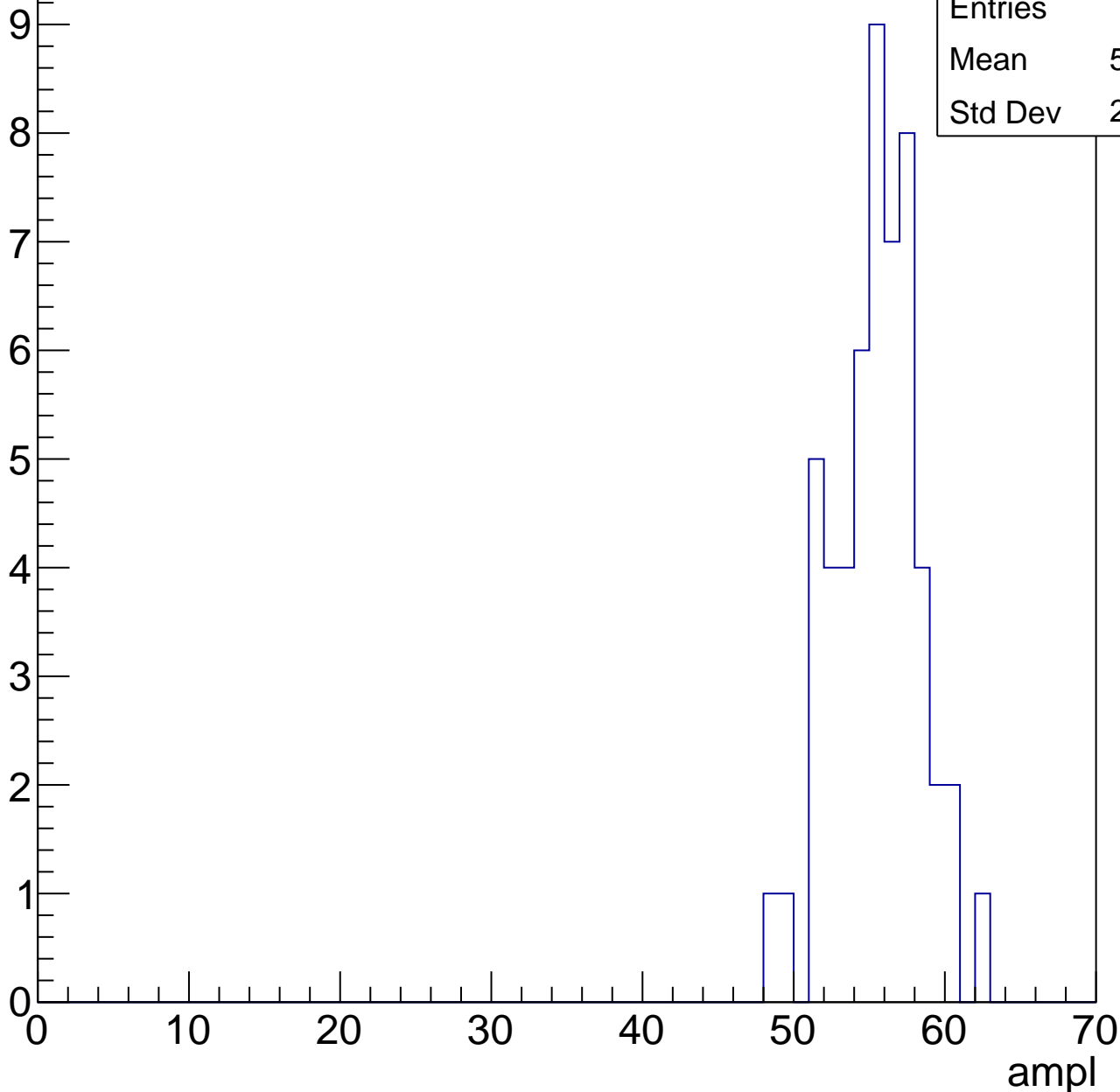


B1L102S, U11-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.02
Std Dev	2.819

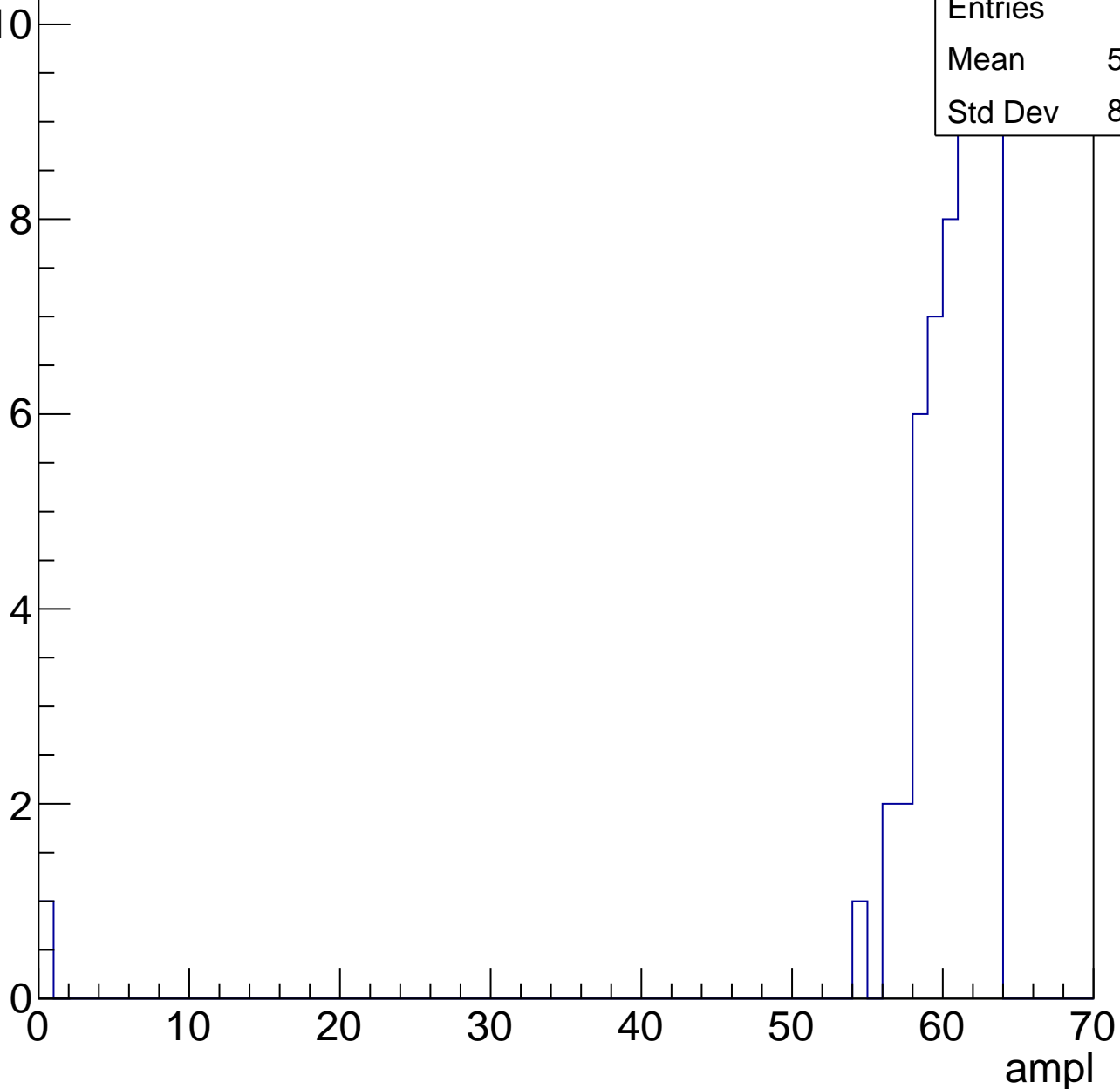


B1L102S, U11-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	59.27
Std Dev	8.262



B1L102S, U11-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch24, adc0

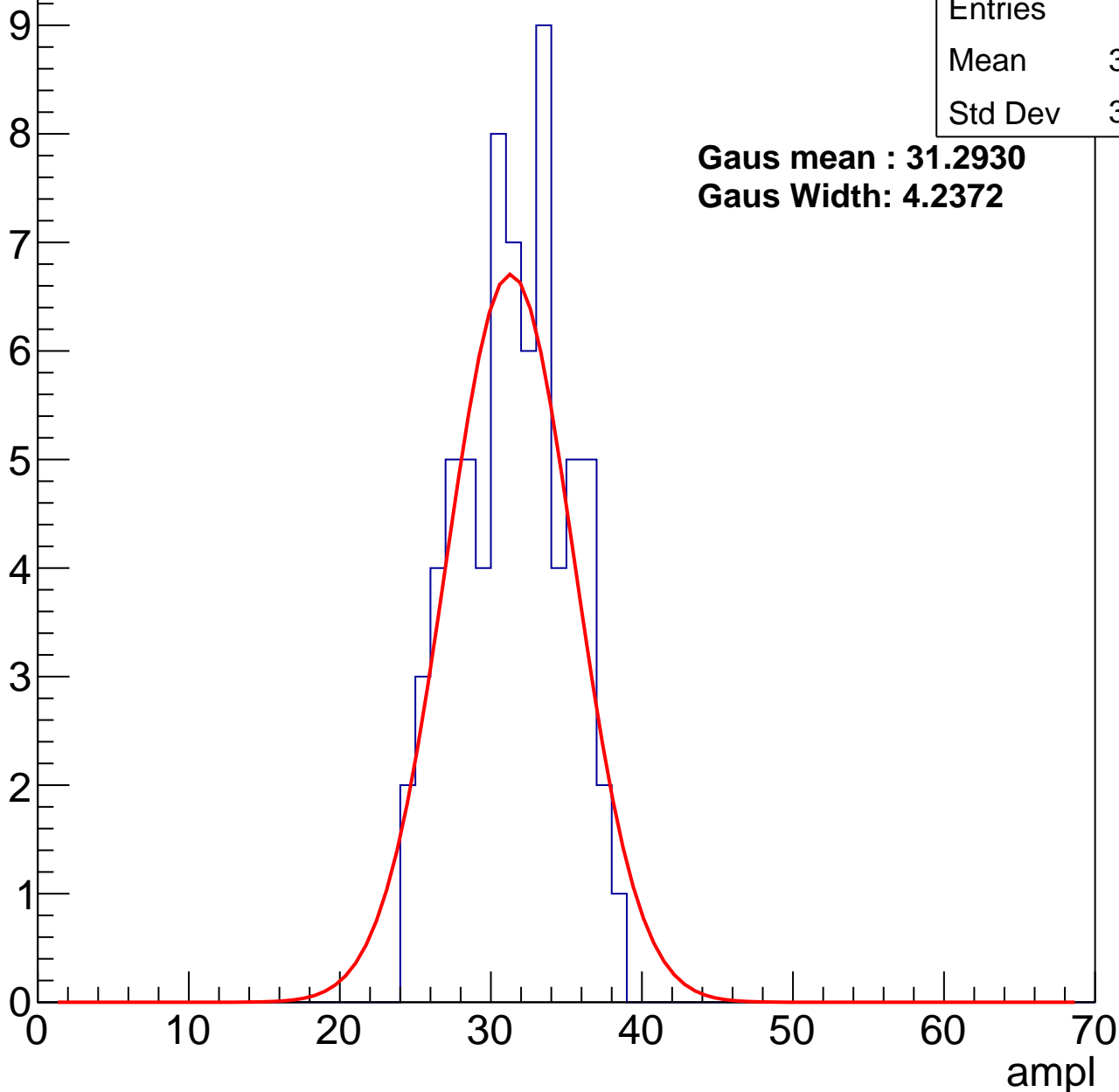
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	30.96
Std Dev	3.503

Gaus mean : 31.2930

Gaus Width: 4.2372



B1L102S, U11-ch24, adc1

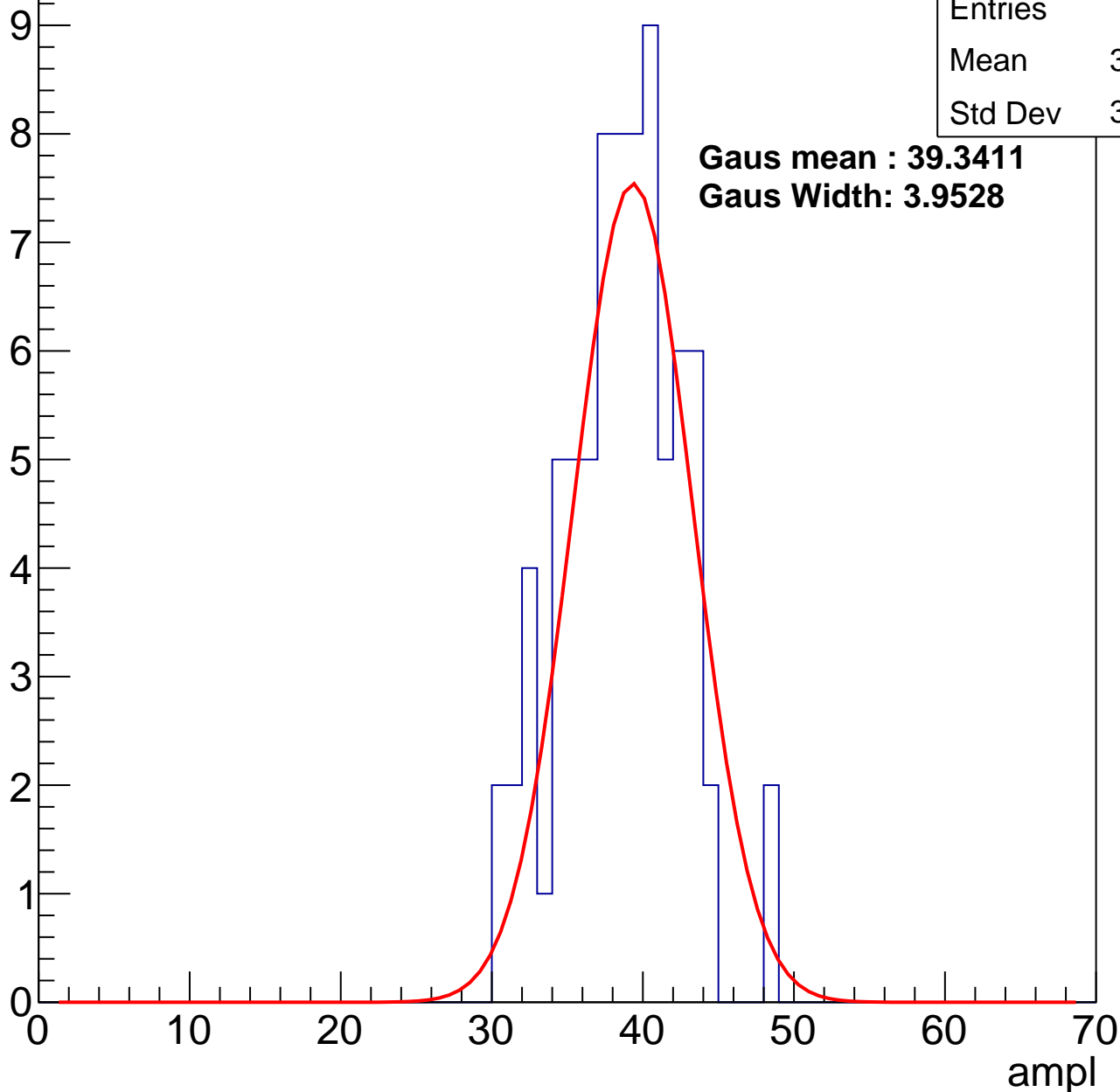
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	38.19
Std Dev	3.843

Gaus mean : 39.3411

Gaus Width: 3.9528



B1L102S, U11-ch24, adc2

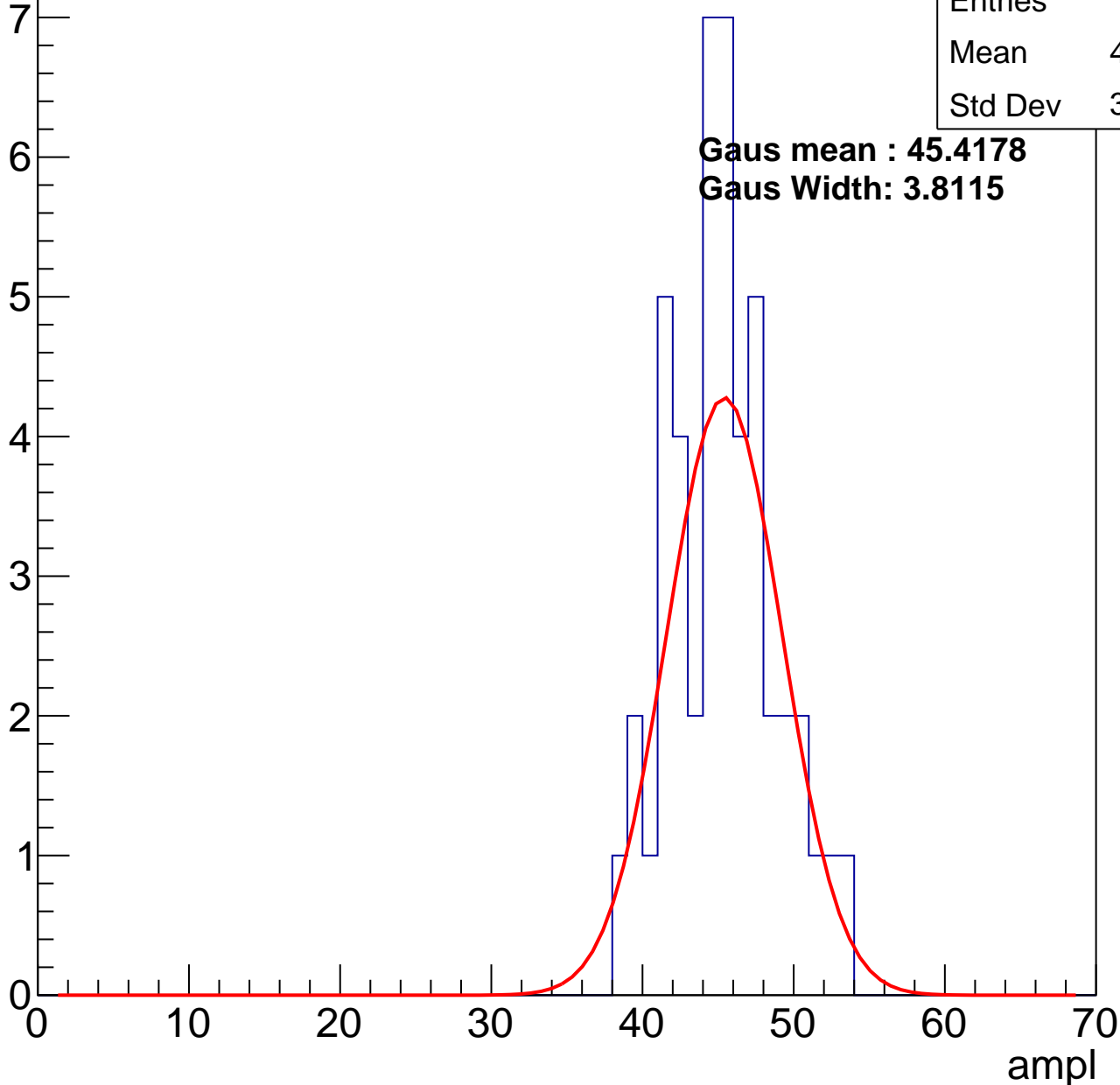
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	44.83
Std Dev	3.417

Gaus mean : 45.4178

Gaus Width: 3.8115

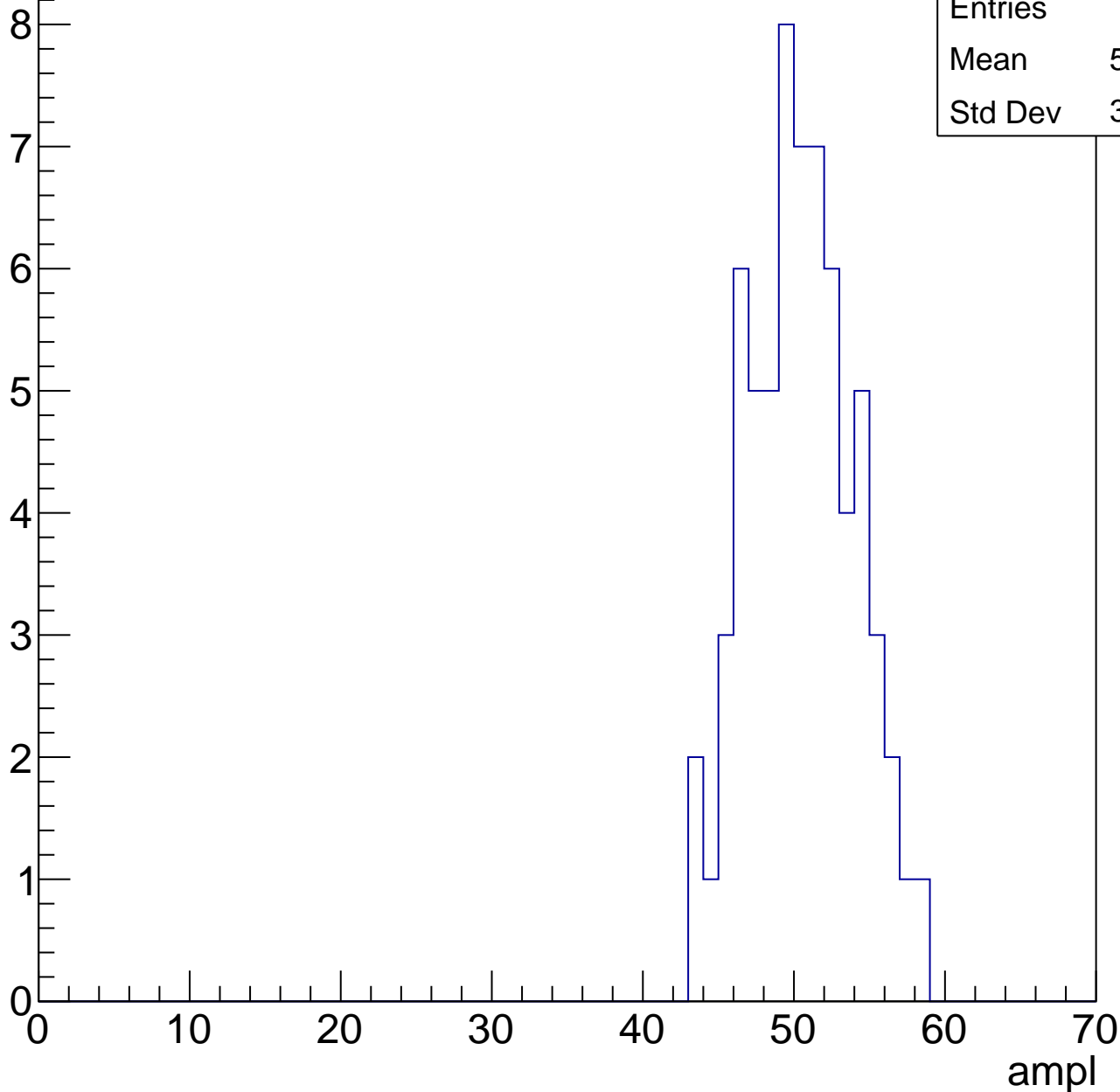


B1L102S, U11-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.02
Std Dev	3.449

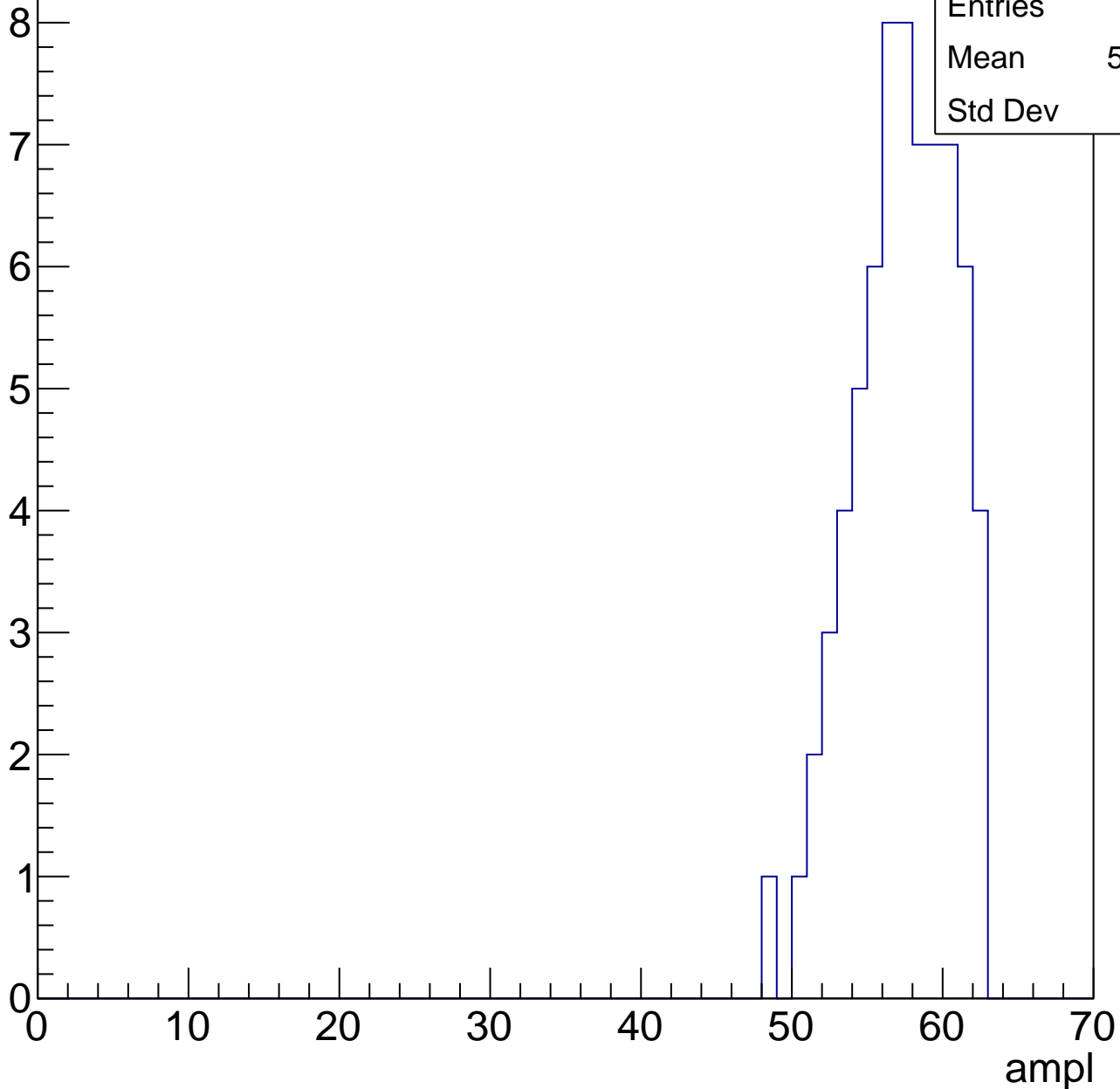


B1L102S, U11-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	56.88
Std Dev	3.21

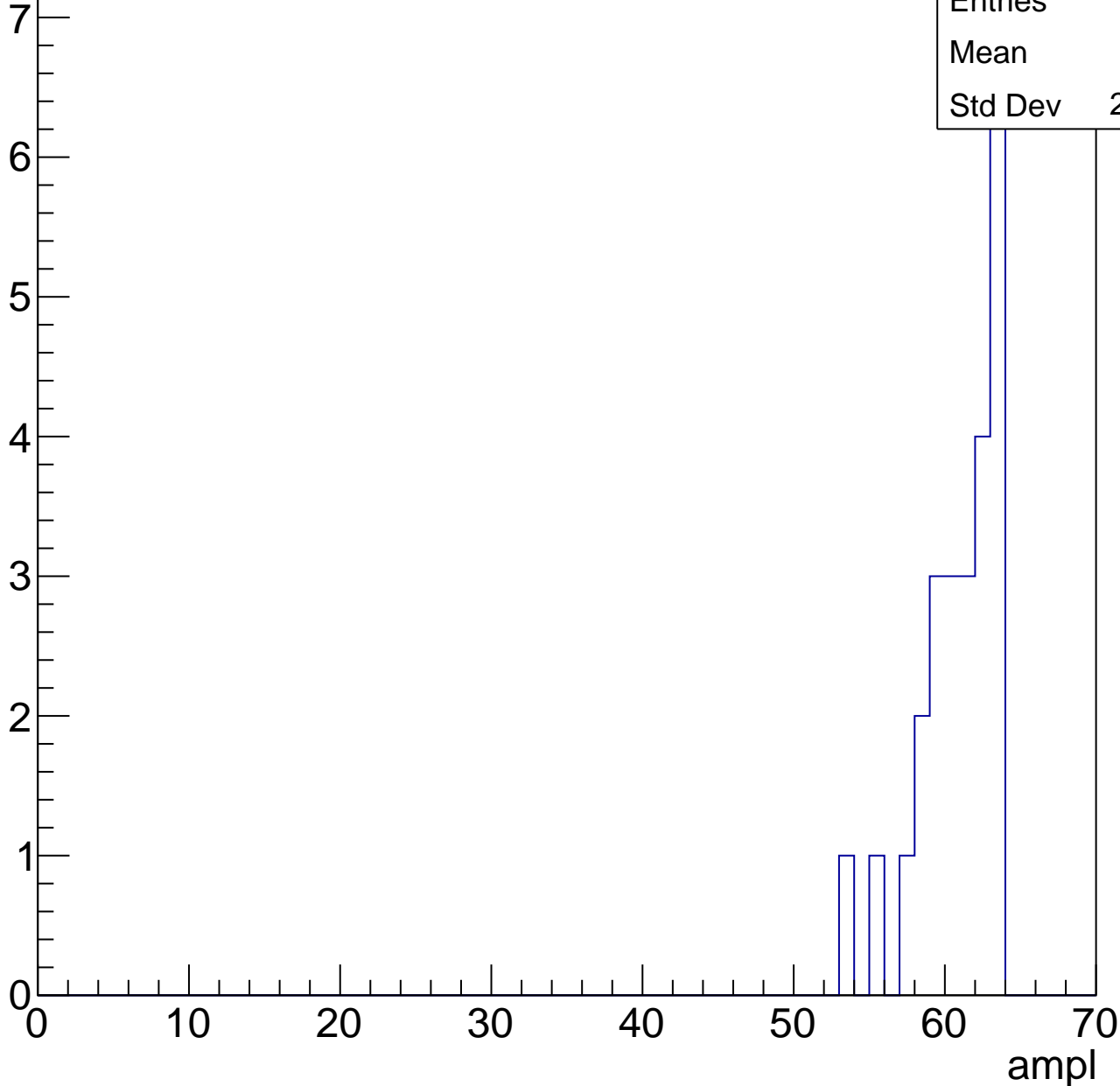


B1L102S, U11-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	60.4
Std Dev	2.623



B1L102S, U11-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch25, adc0

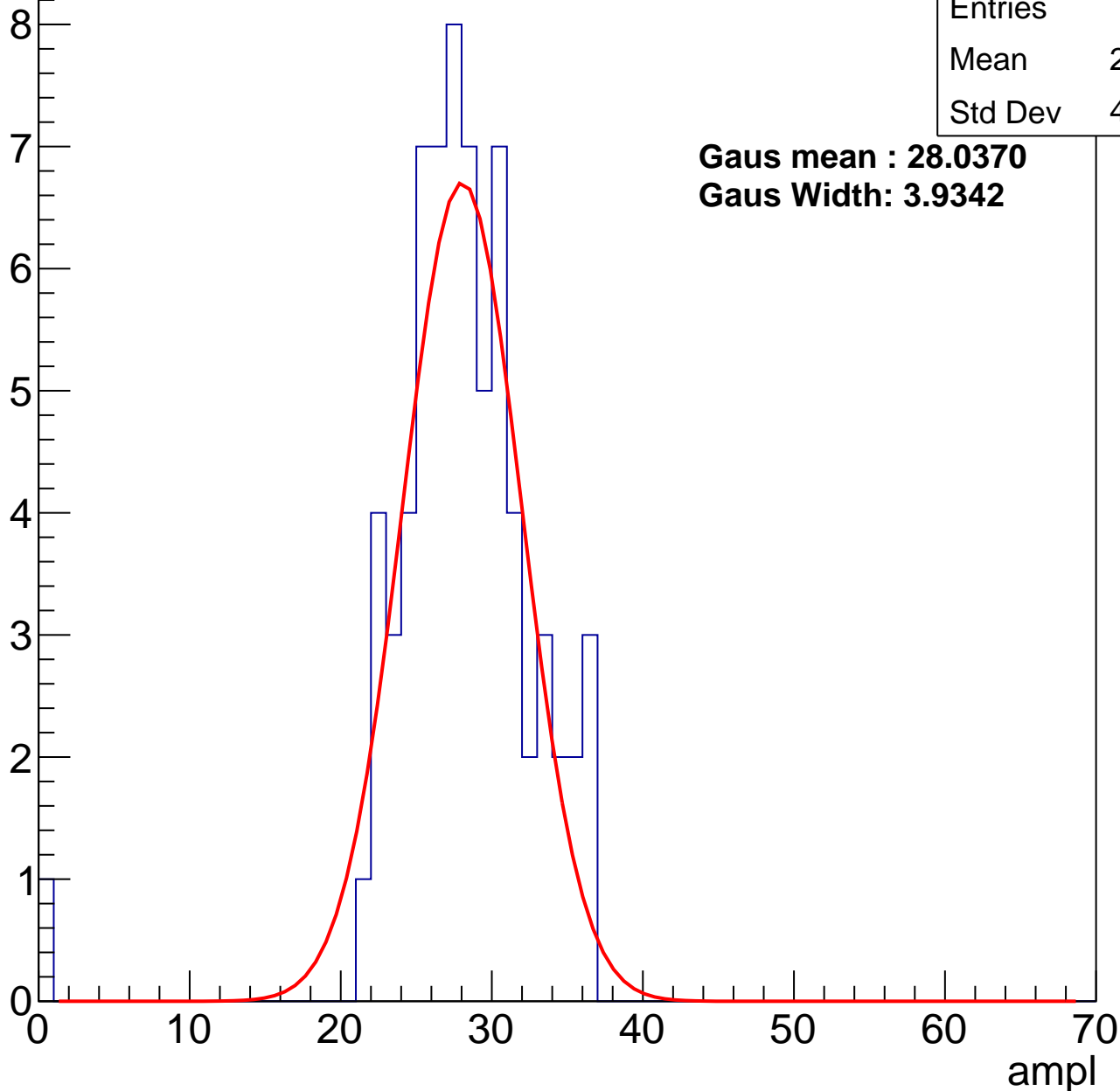
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.59
Std Dev	4.987

Gaus mean : 28.0370

Gaus Width: 3.9342



B1L102S, U11-ch25, adc1

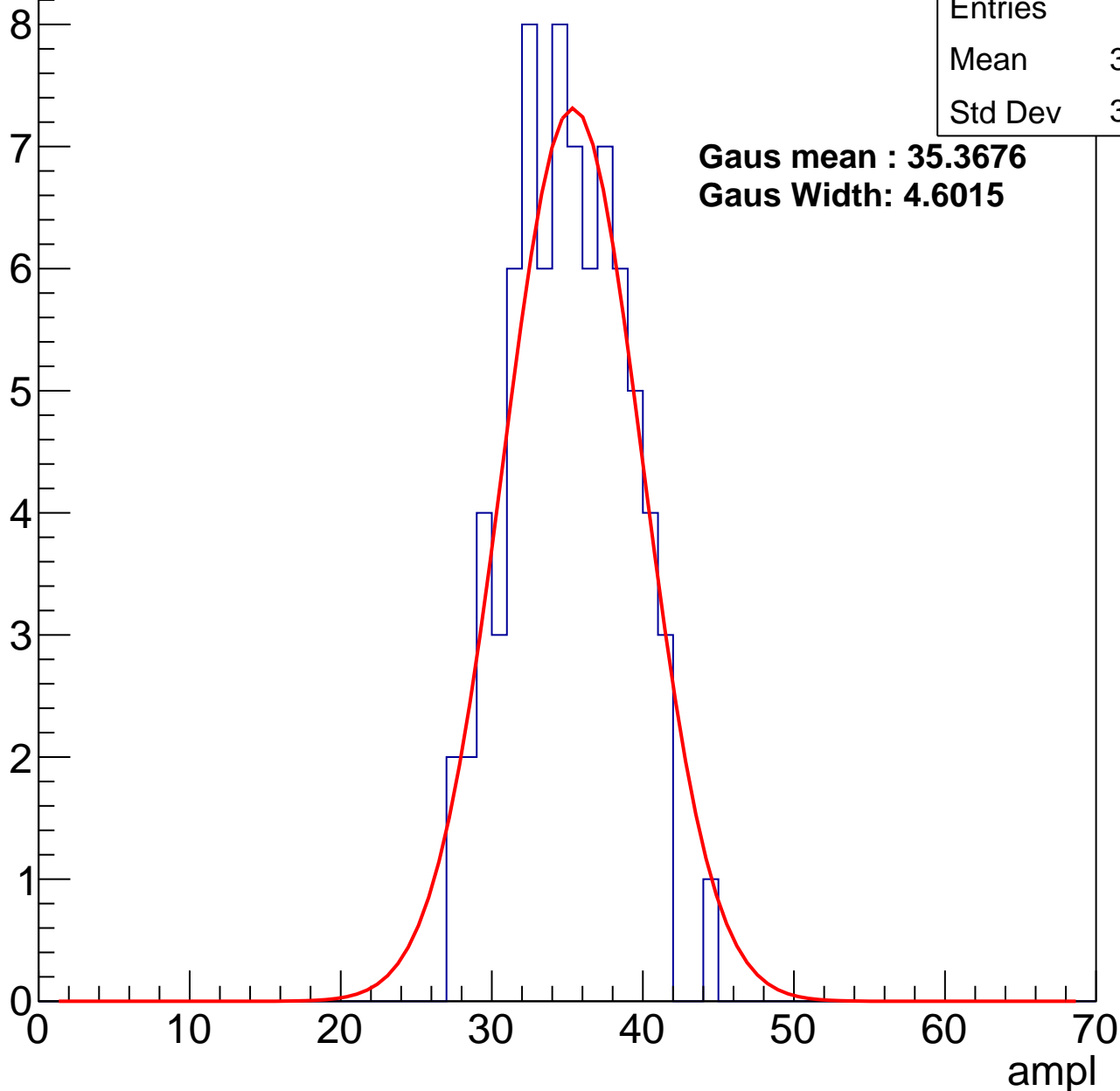
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	34.59
Std Dev	3.736

Gaus mean : 35.3676

Gaus Width: 4.6015



B1L102S, U11-ch25, adc2

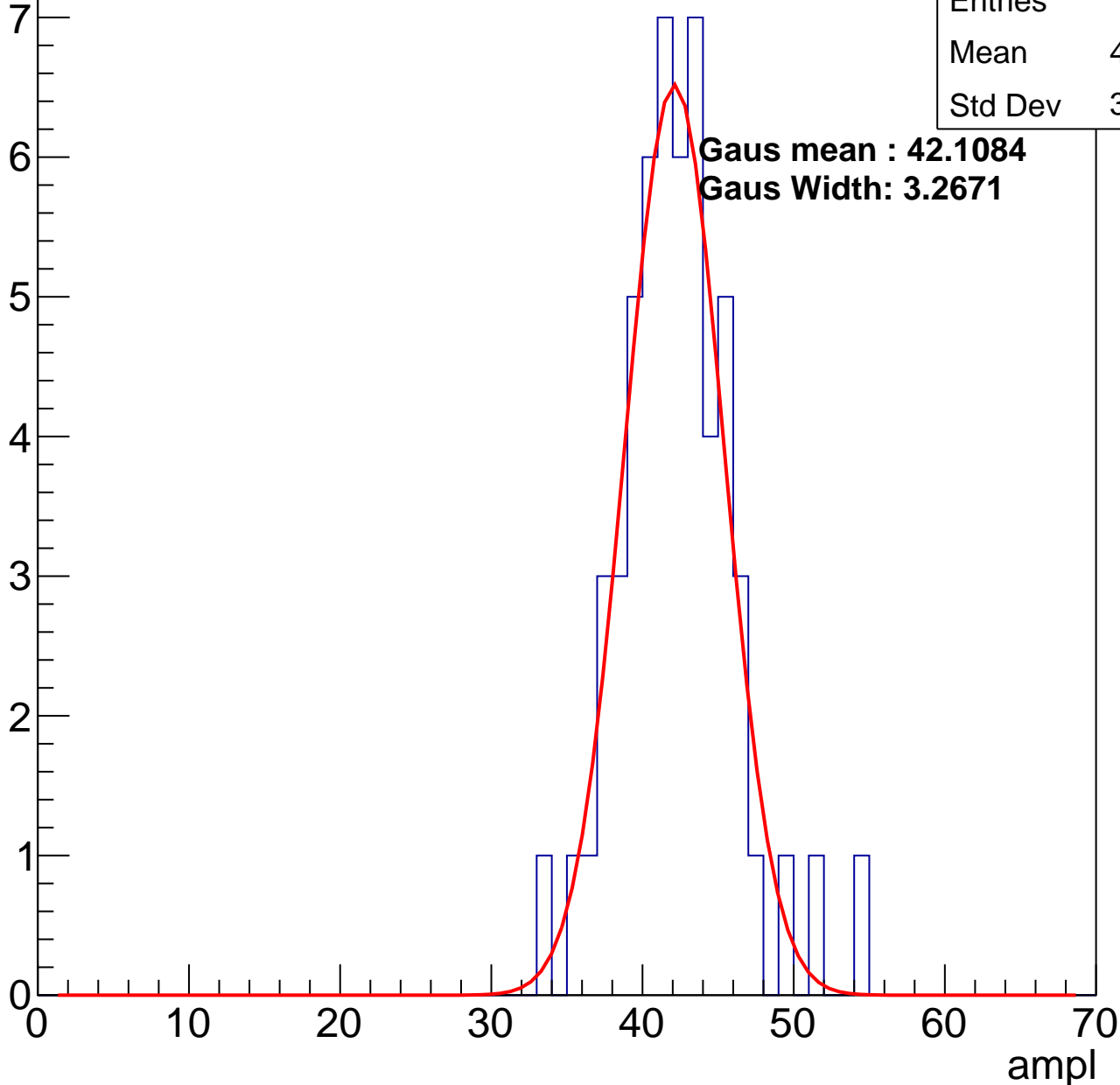
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.86
Std Dev	3.729

Gaus mean : 42.1084

Gaus Width: 3.2671



B1L102S, U11-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	47.73
Std Dev	3.161

Entry

10

8

6

4

2

0

0

10

20

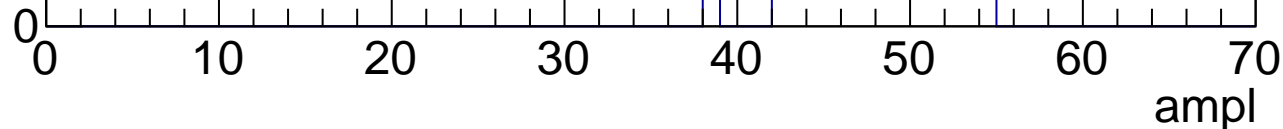
30

40

50

60

ampl

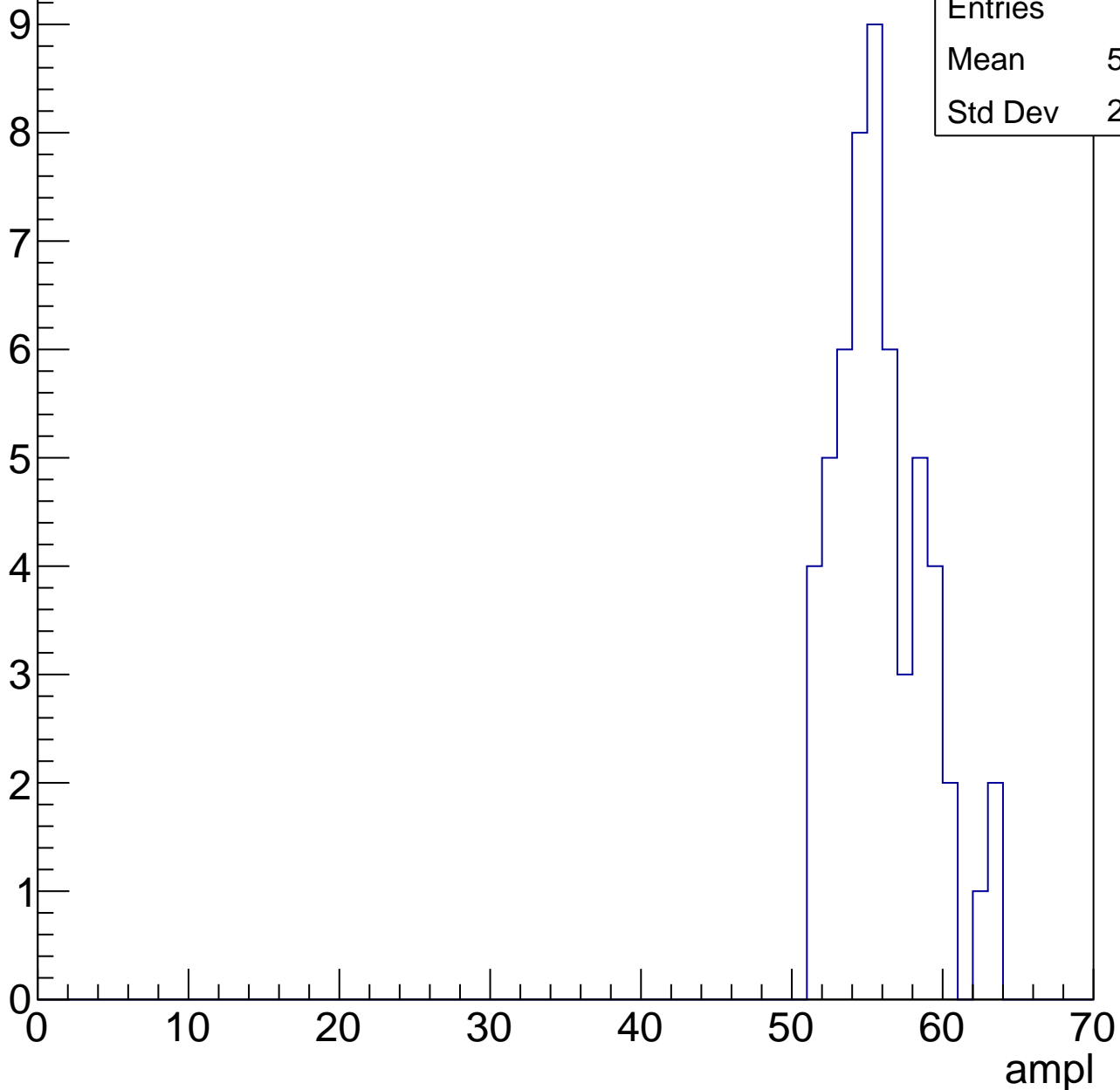


B1L102S, U11-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.45
Std Dev	2.965

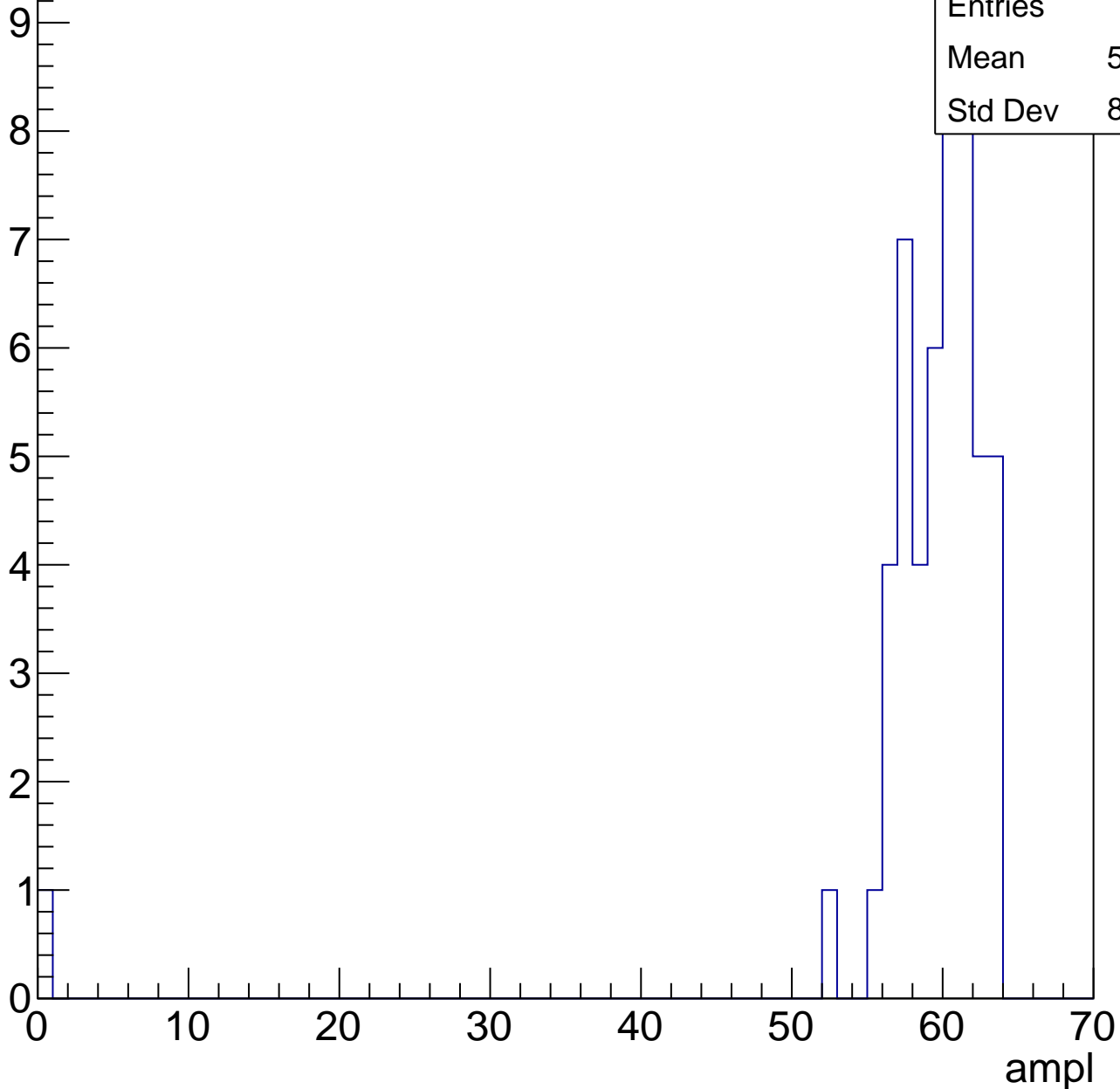


B1L102S, U11-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

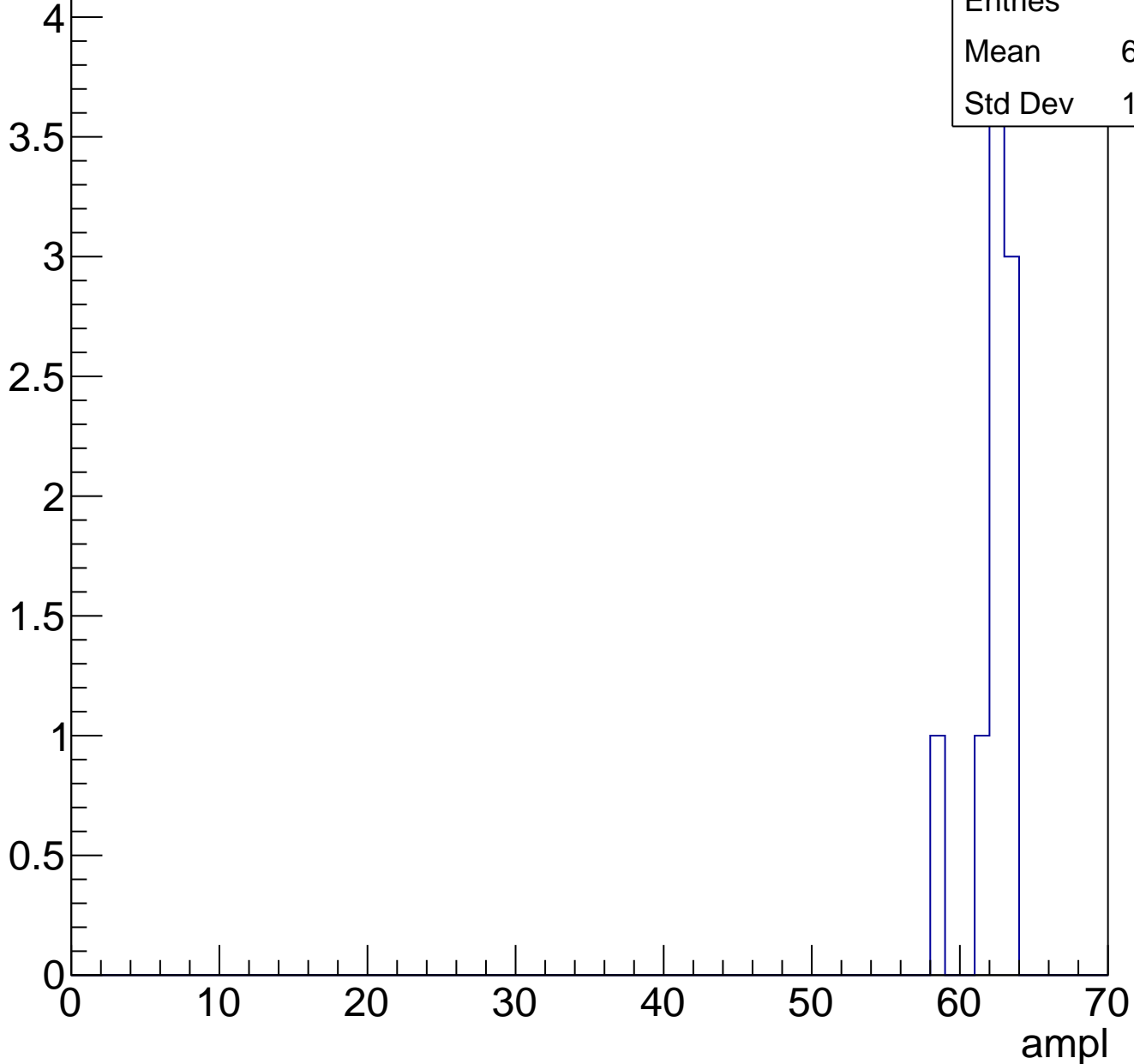
Entries	51
Mean	58.24
Std Dev	8.579



B1L102S, U11-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch26, adc0

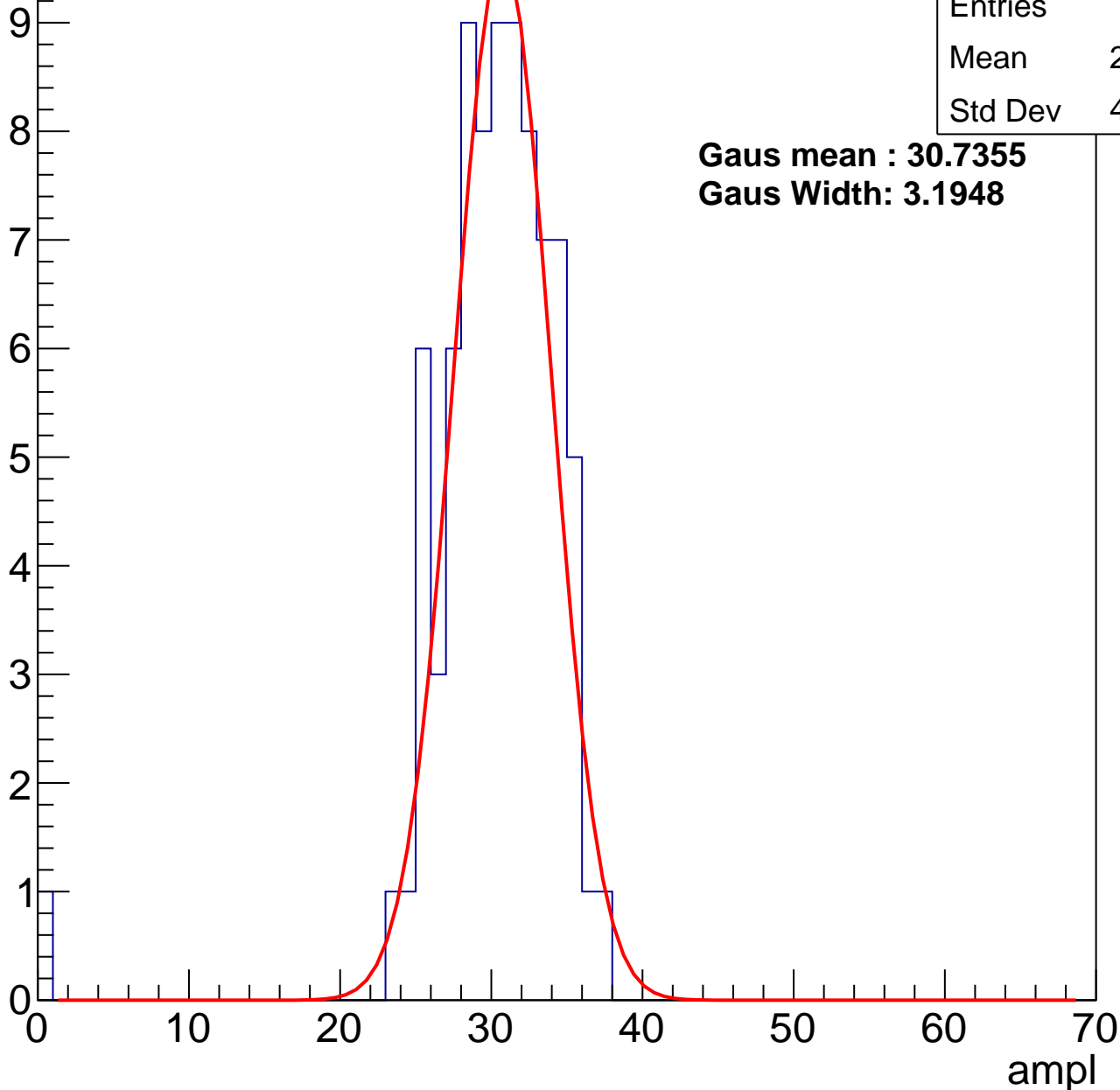
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	29.79
Std Dev	4.558

Gaus mean : 30.7355

Gaus Width: 3.1948



B1L102S, U11-ch26, adc1

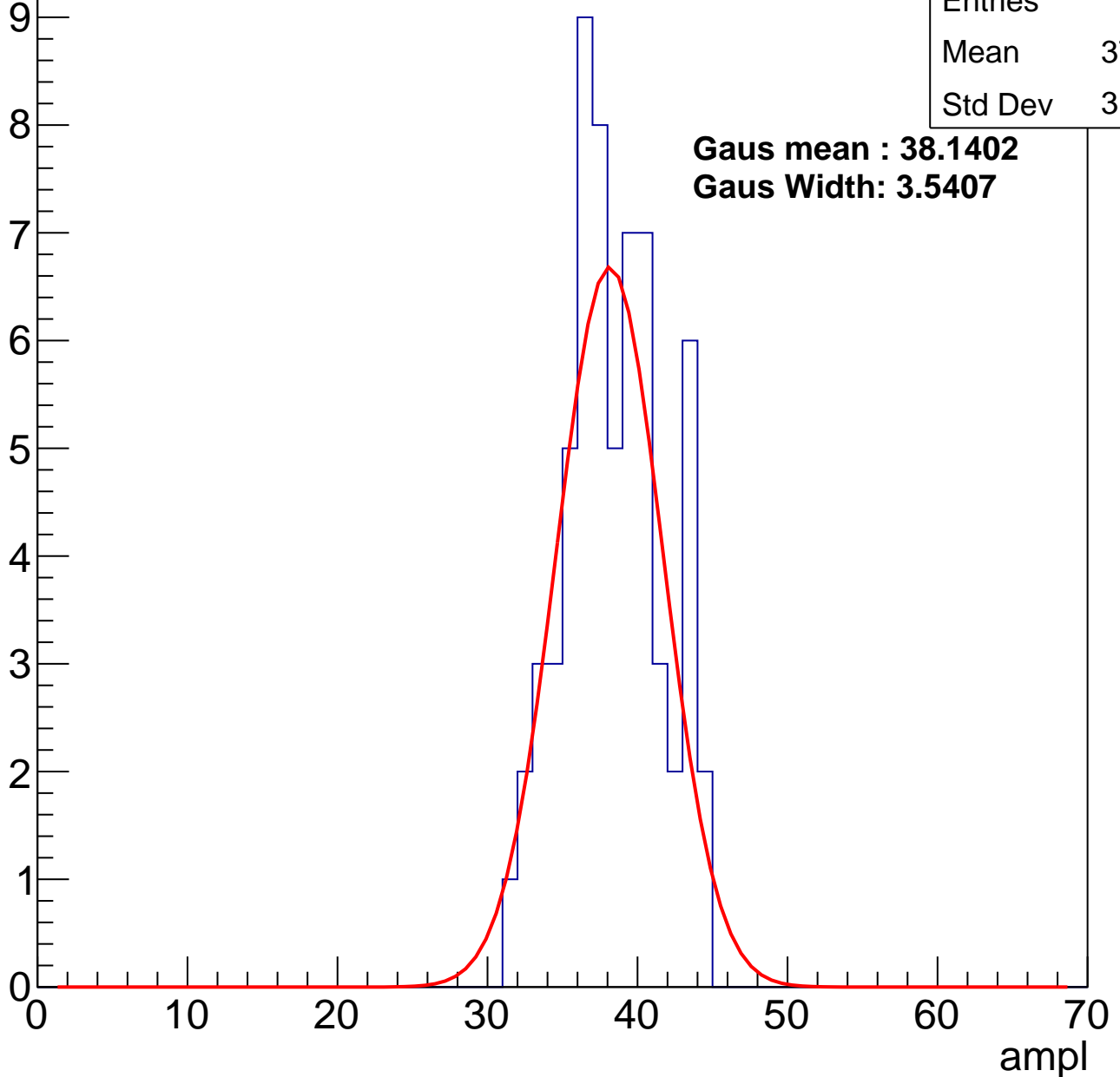
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.89
Std Dev	3.208

Gaus mean : 38.1402

Gaus Width: 3.5407



B1L102S, U11-ch26, adc2

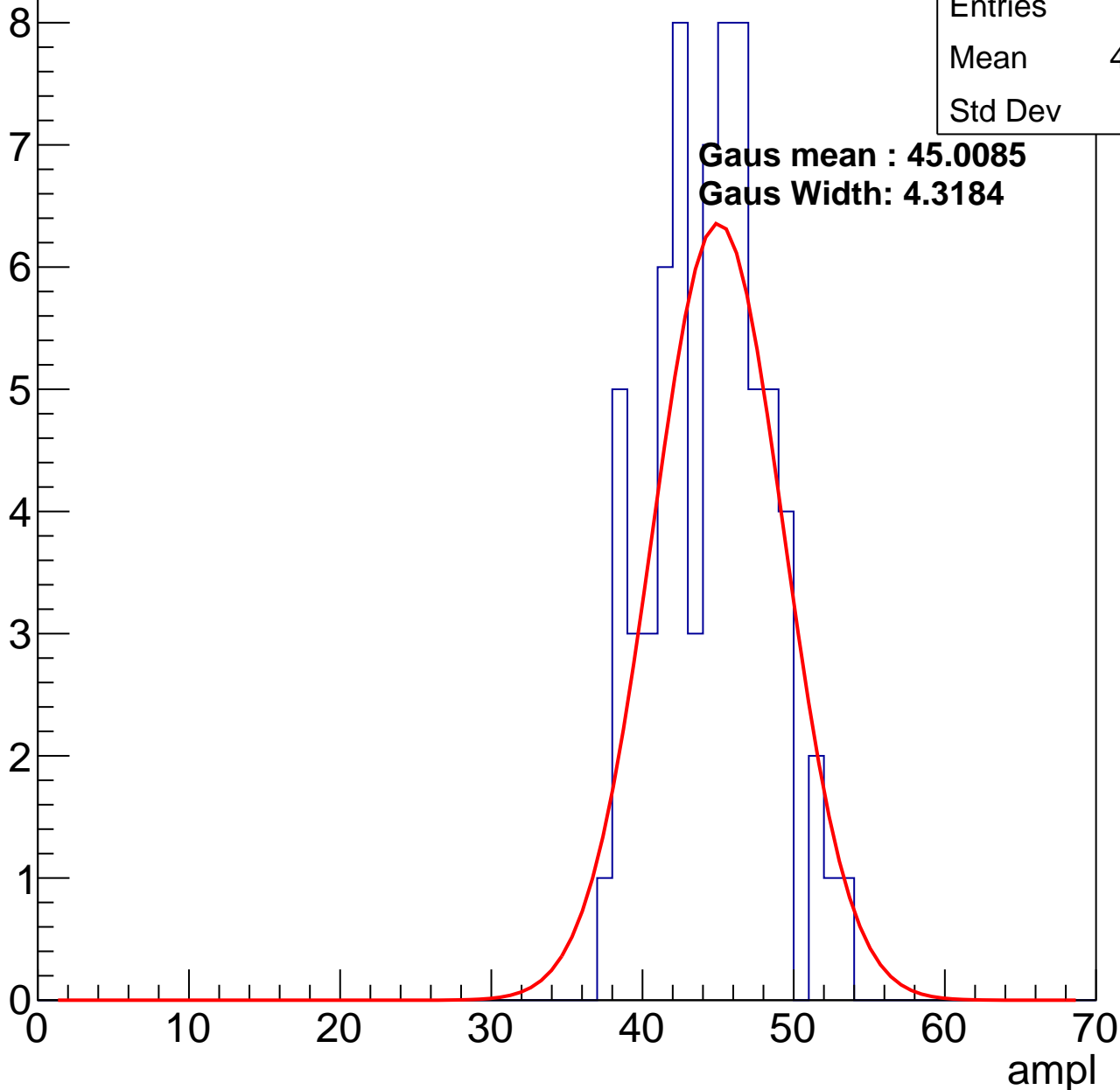
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	44.13
Std Dev	3.68

Gaus mean : 45.0085

Gaus Width: 4.3184

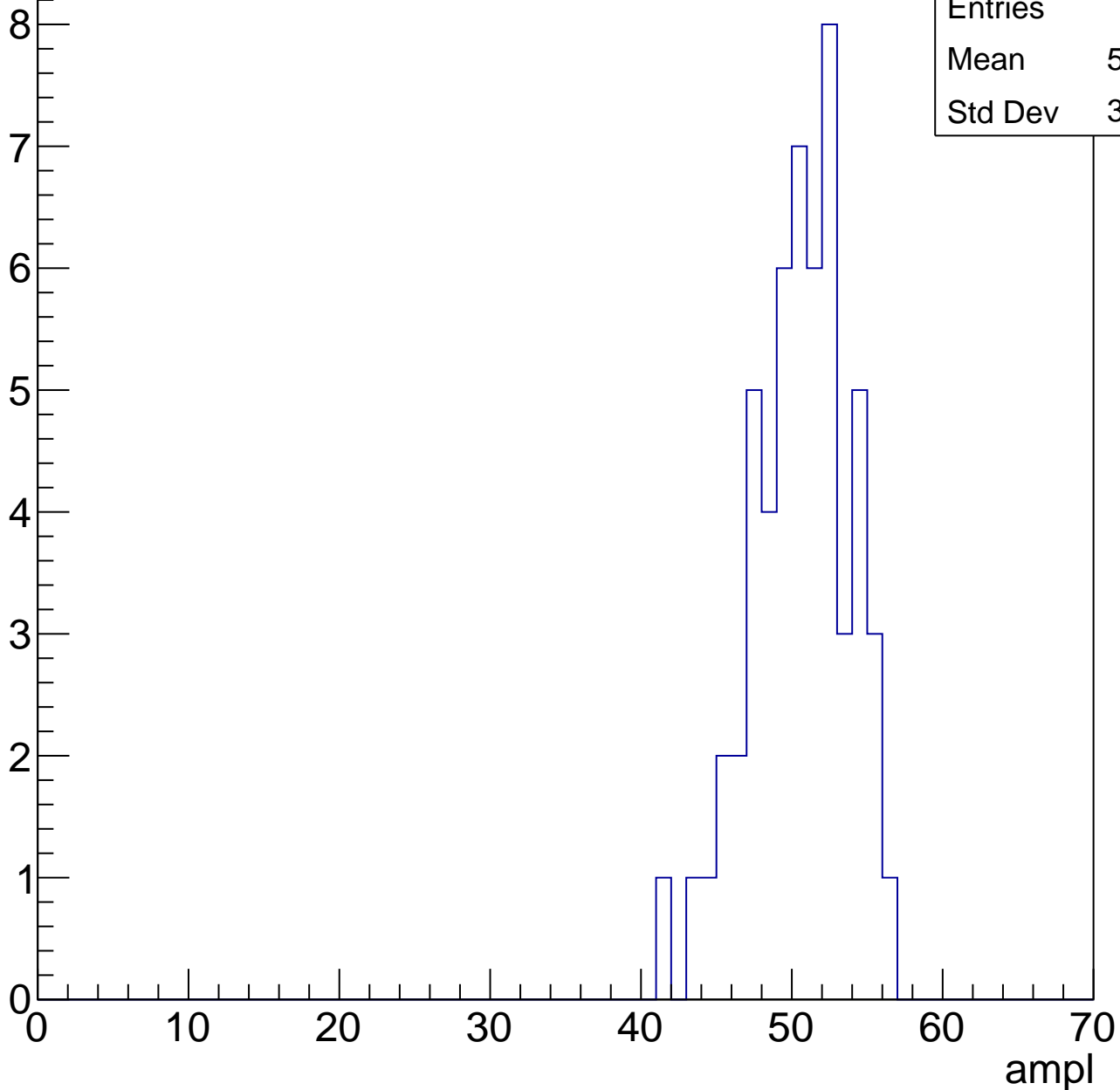


B1L102S, U11-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.05
Std Dev	3.222

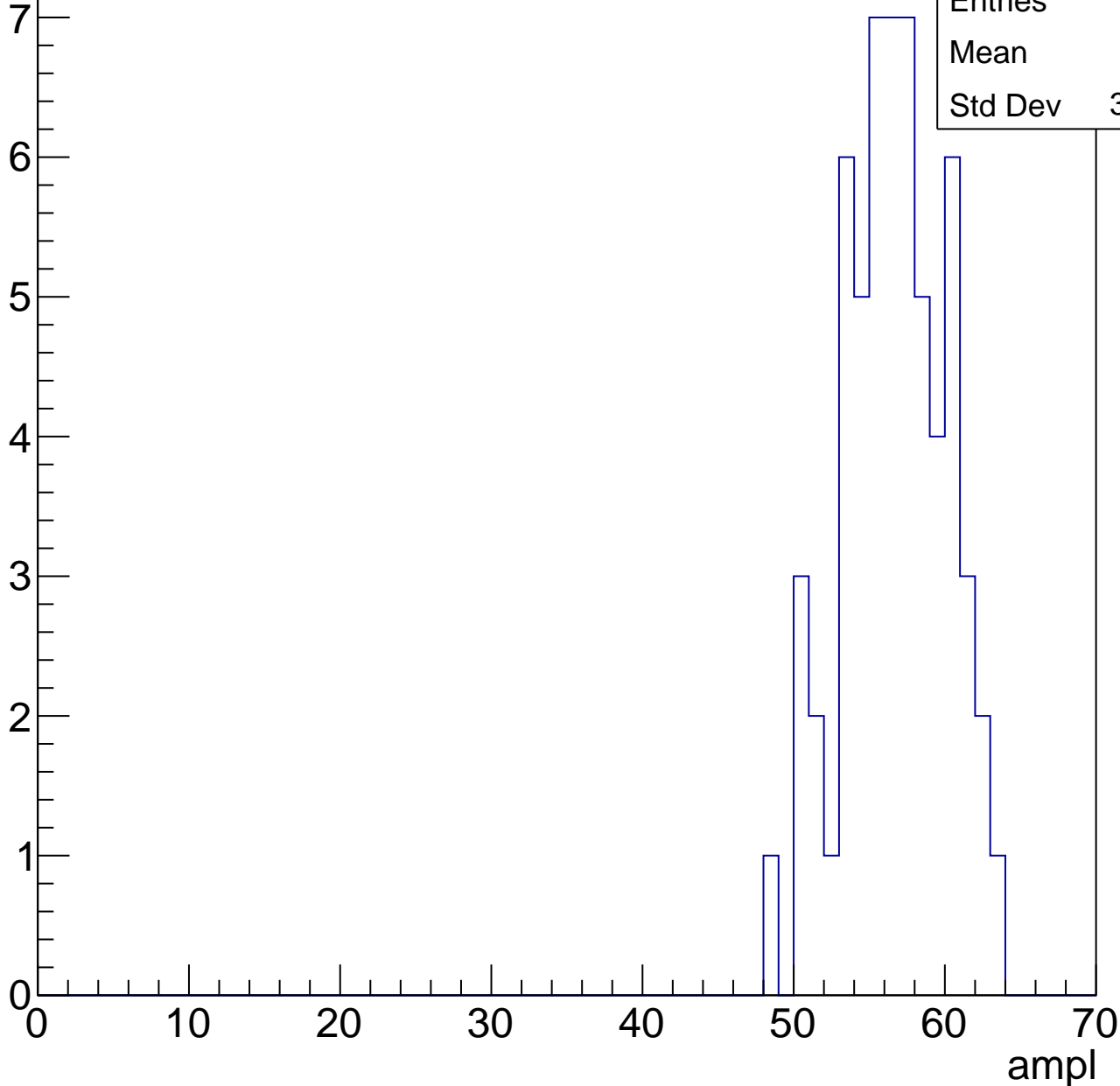


B1L102S, U11-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.2
Std Dev	3.346

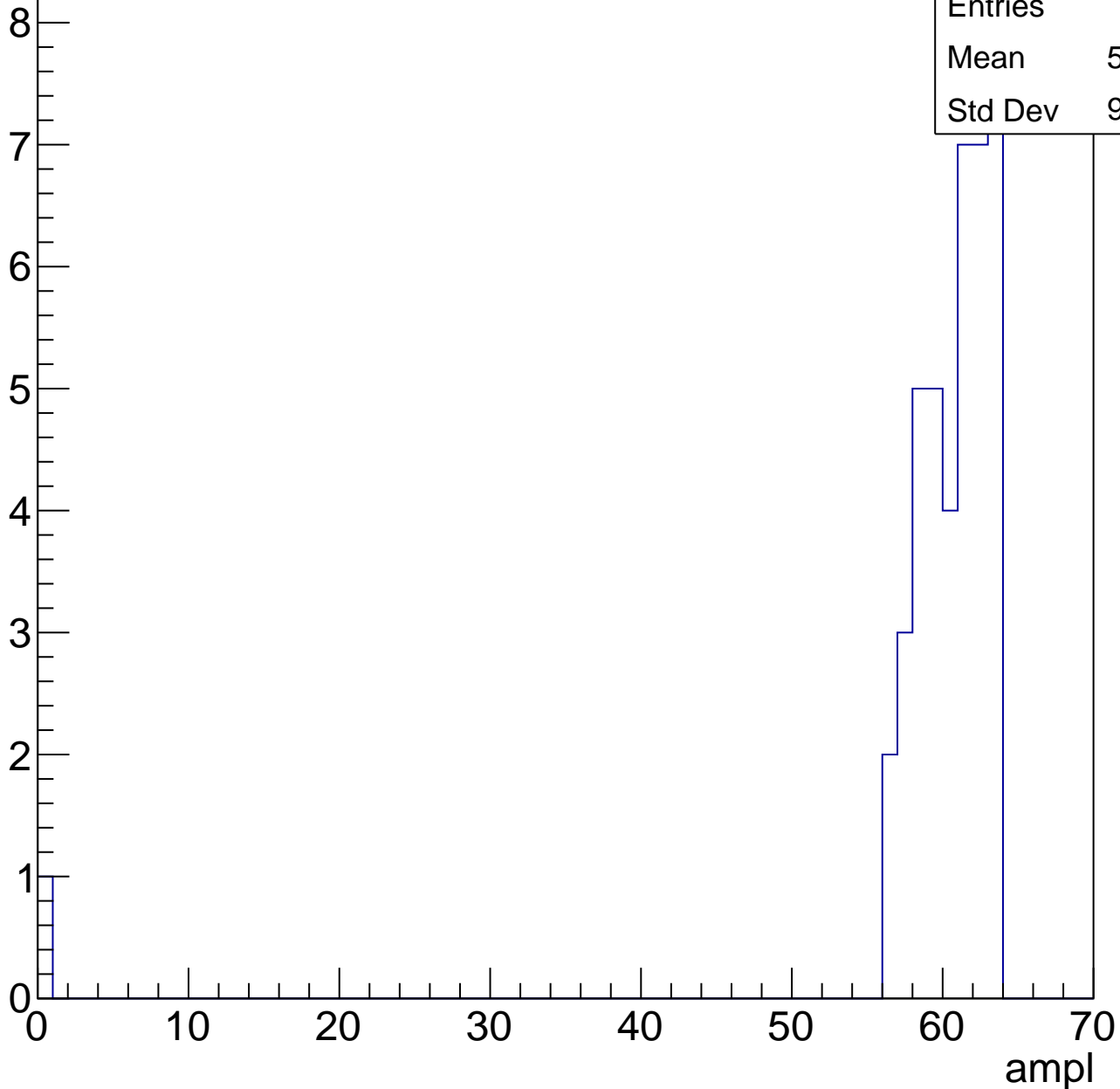


B1L102S, U11-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

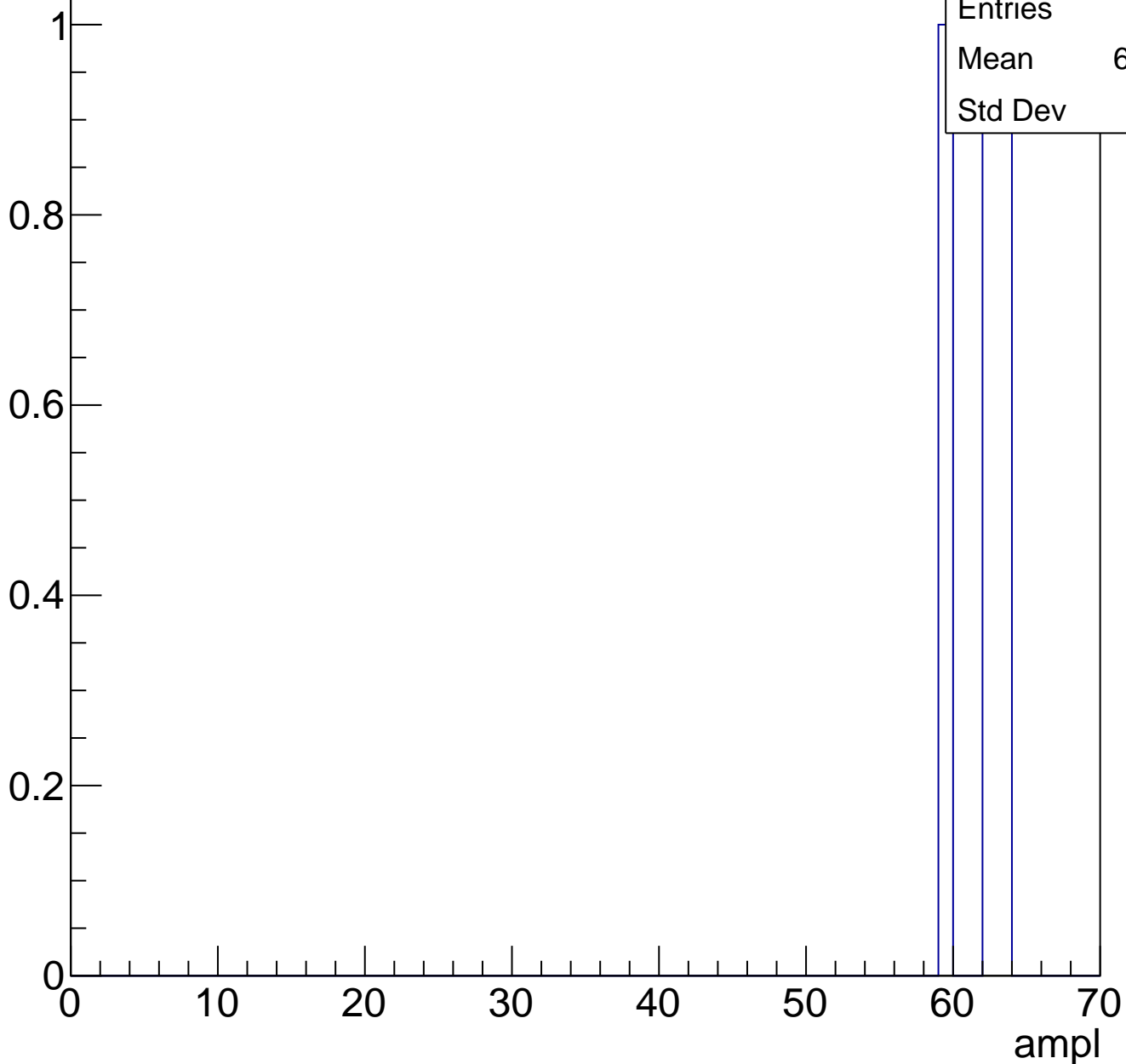
Entries	42
Mean	58.88
Std Dev	9.434



B1L102S, U11-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

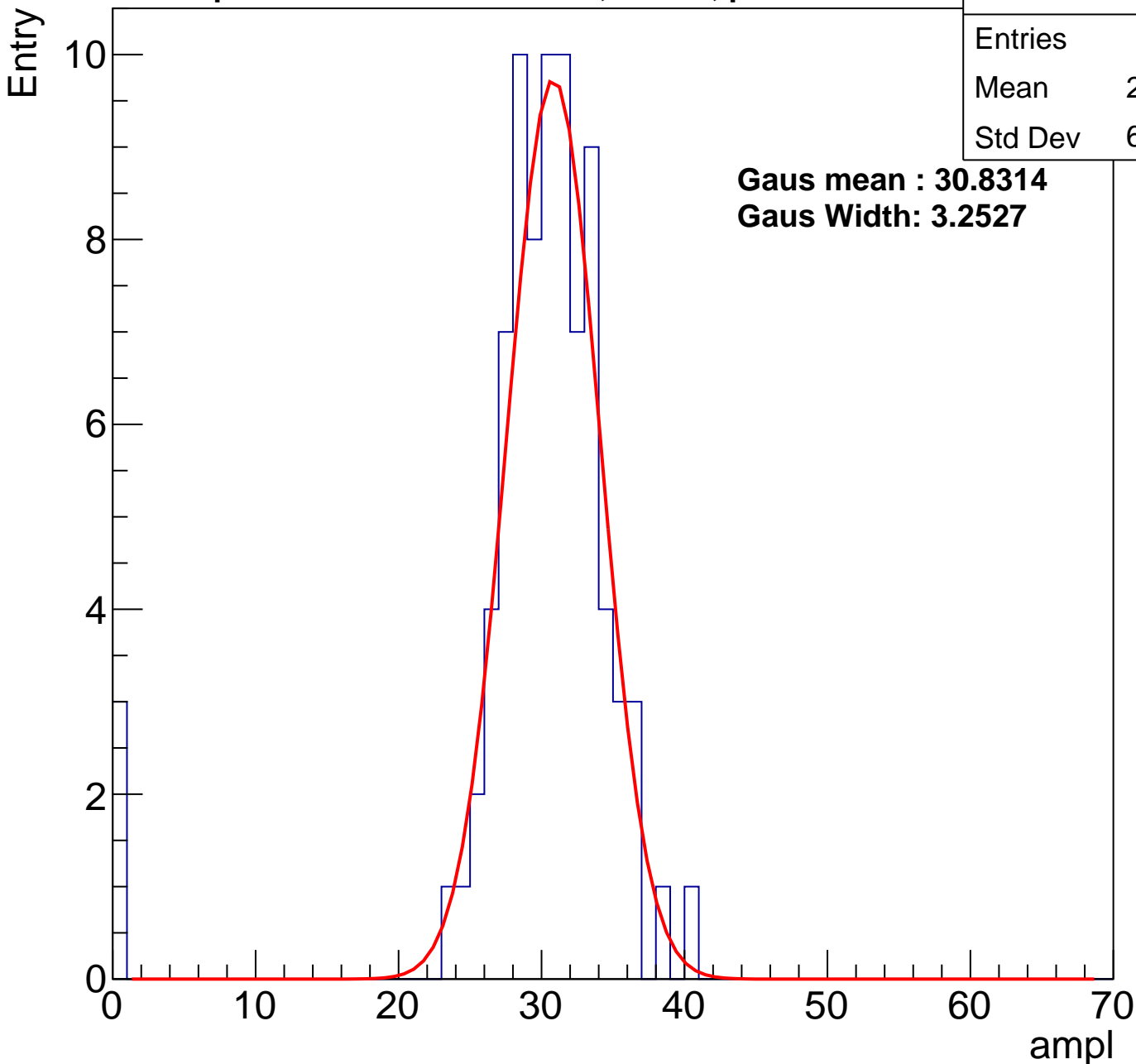


B1L102S, U11-ch27, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	29.29
Std Dev	6.452

Gaus mean : 30.8314
Gaus Width: 3.2527



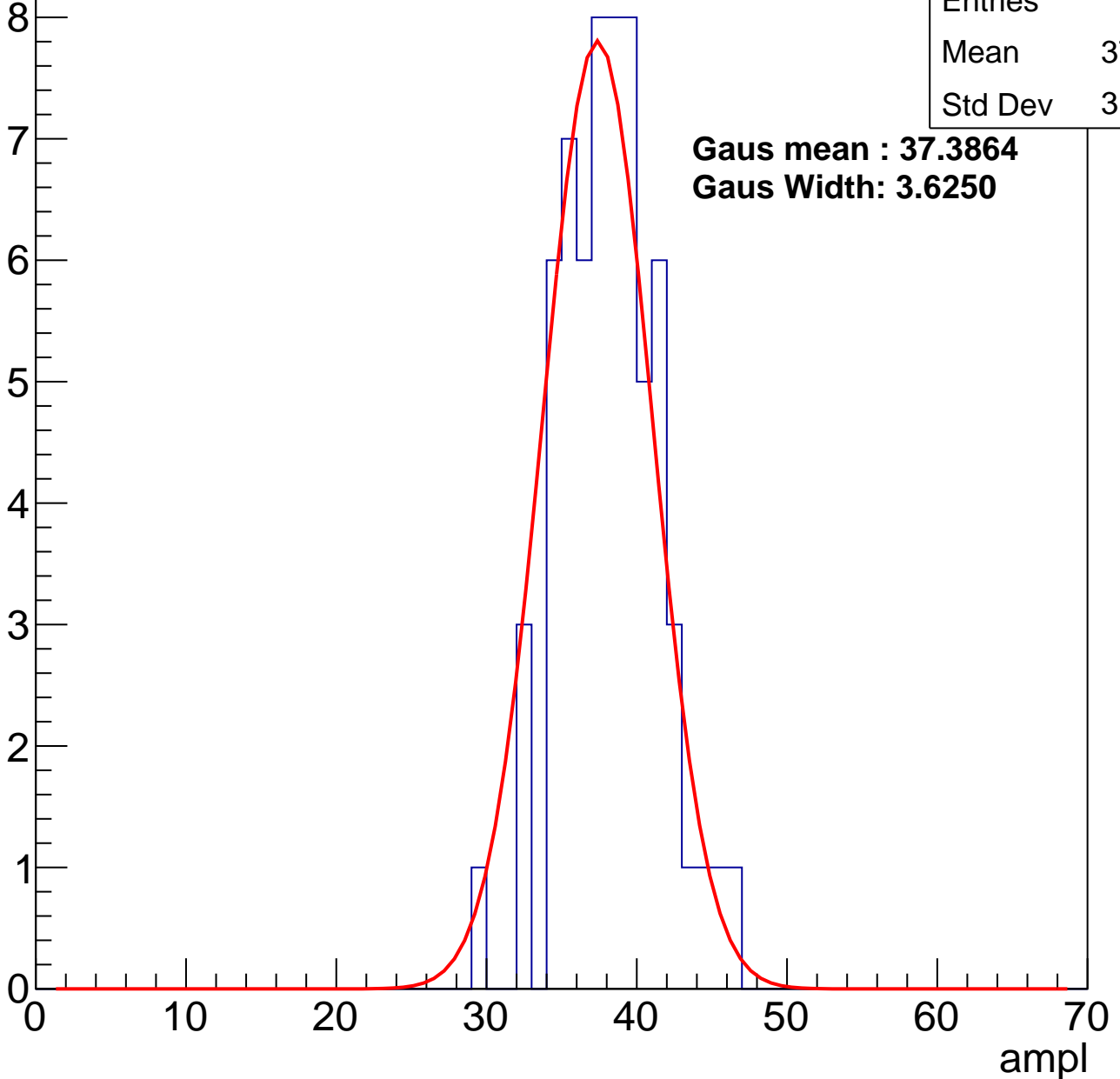
B1L102S, U11-ch27, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.72
Std Dev	3.232

Gaus mean : 37.3864
Gaus Width: 3.6250



B1L102S, U11-ch27, adc2

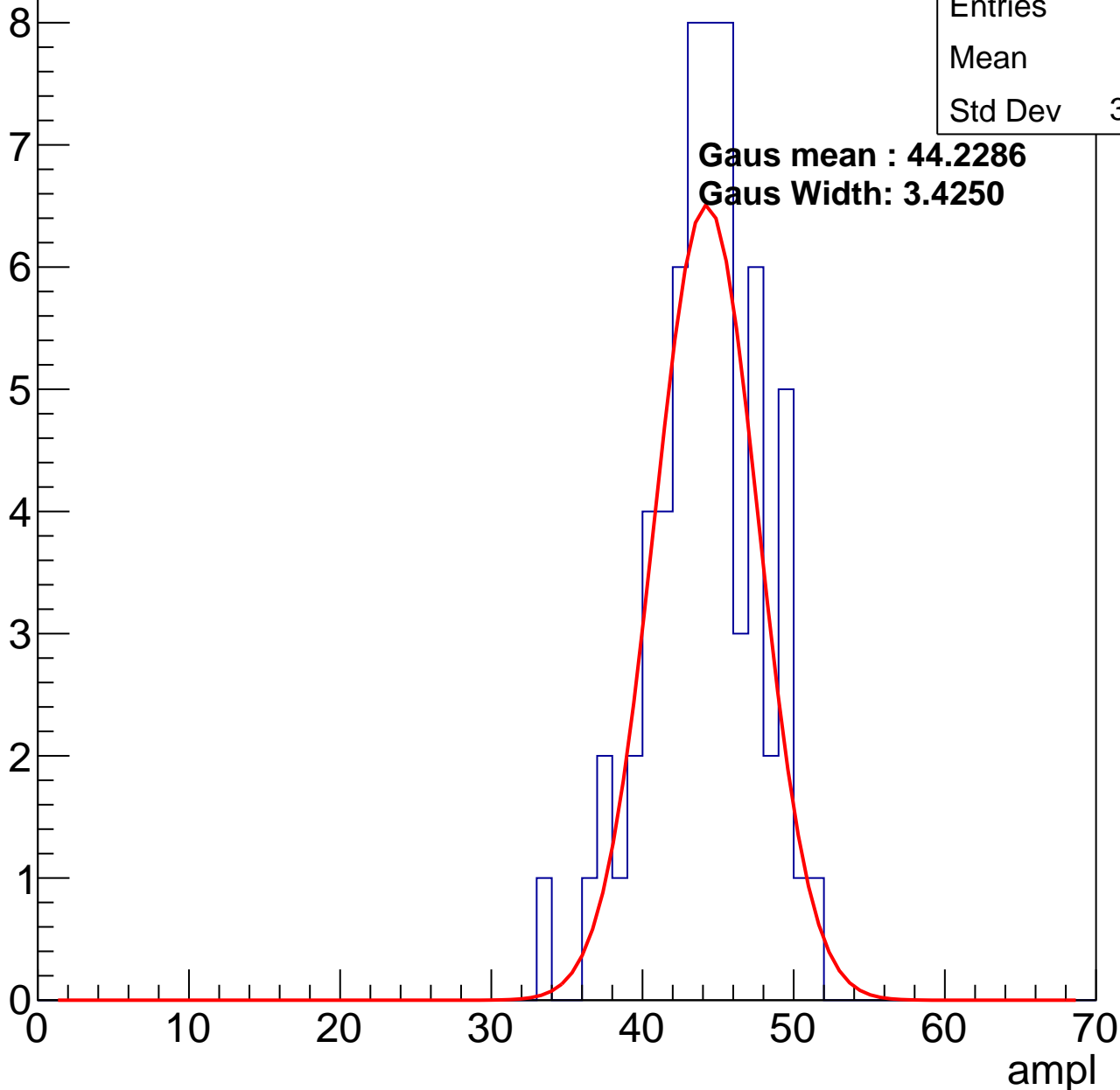
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.7
Std Dev	3.588

Gaus mean : 44.2286

Gaus Width: 3.4250

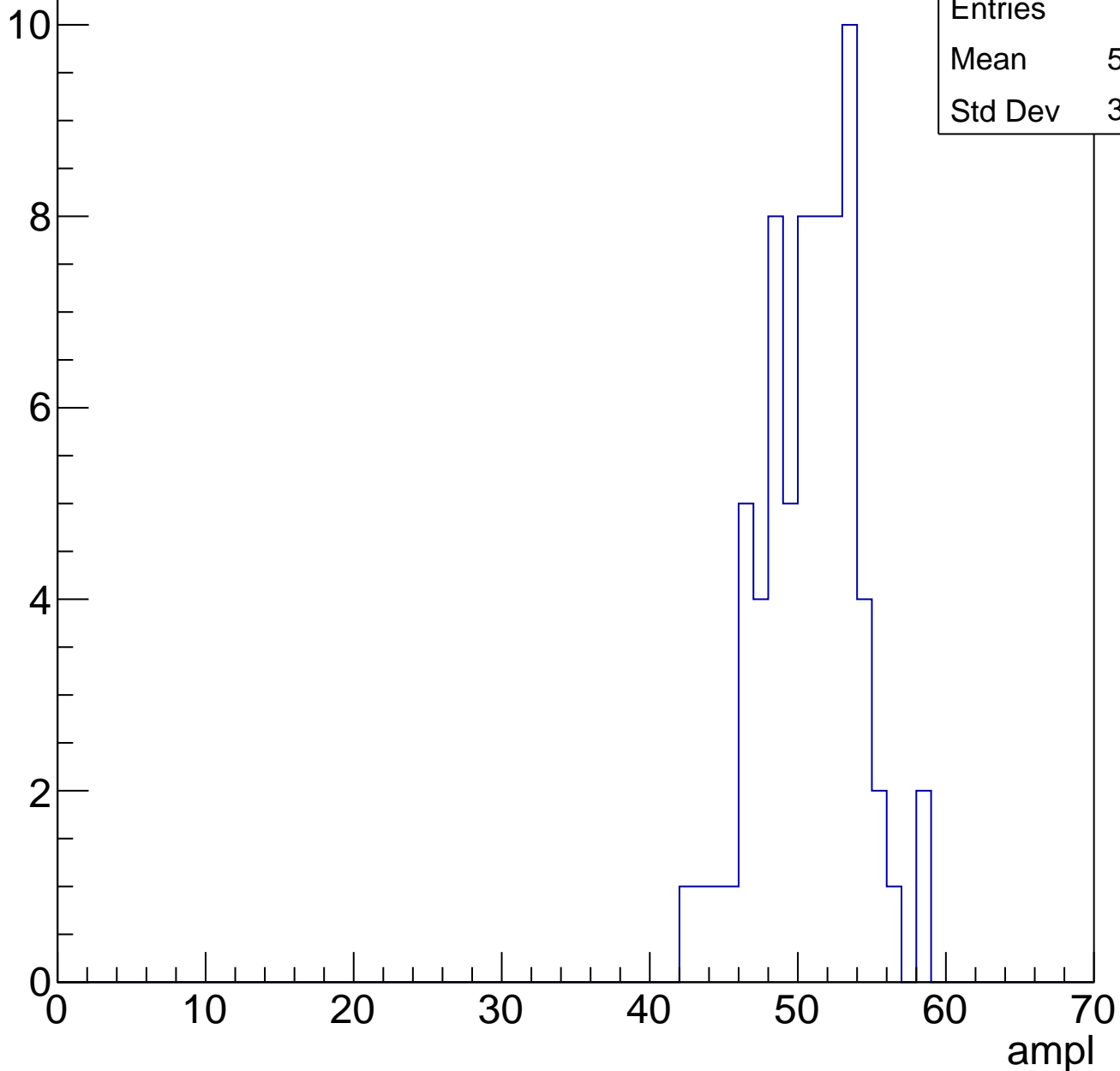


B1L102S, U11-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	50.33
Std Dev	3.247

Entry

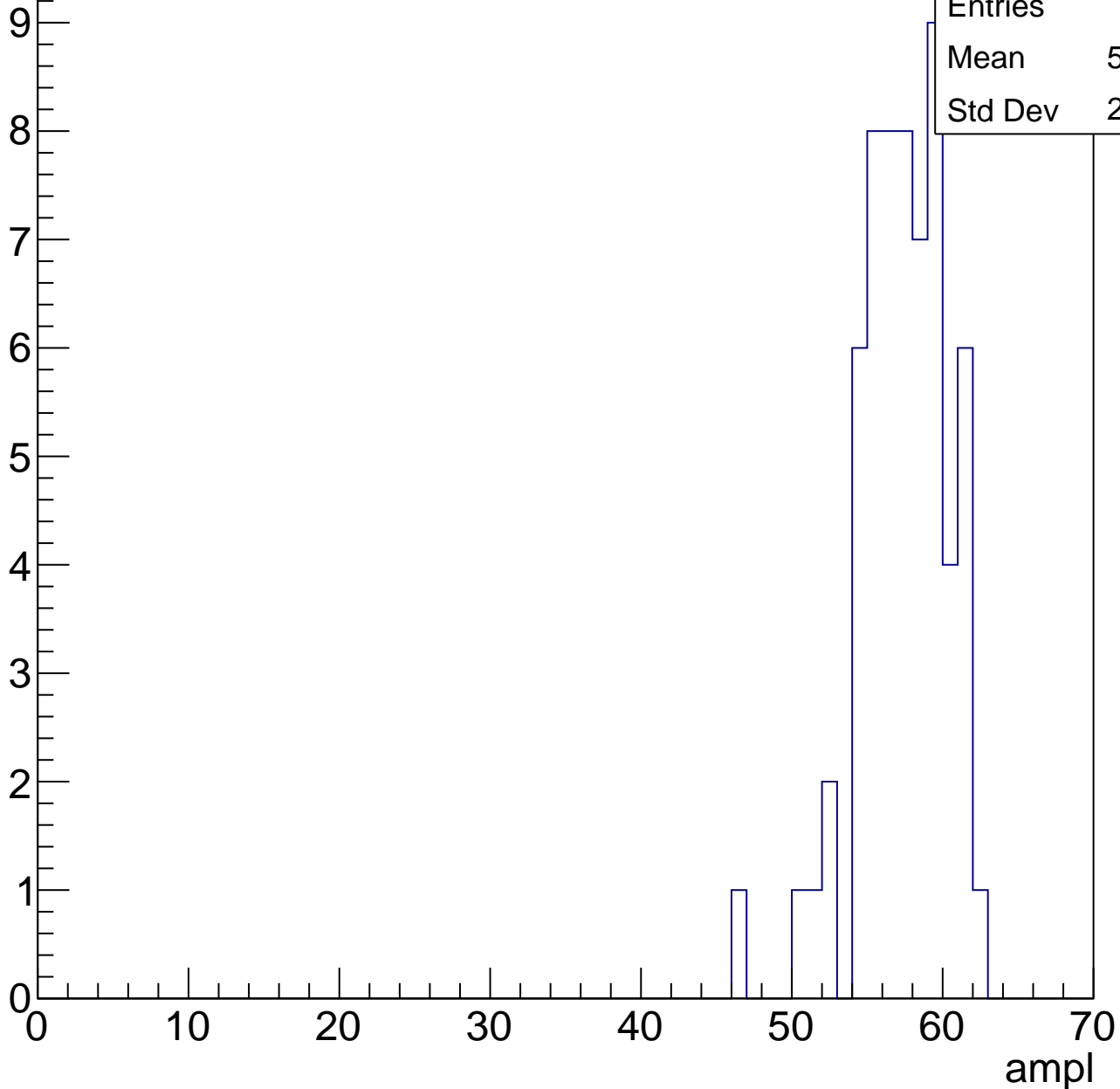


B1L102S, U11-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.84
Std Dev	2.974

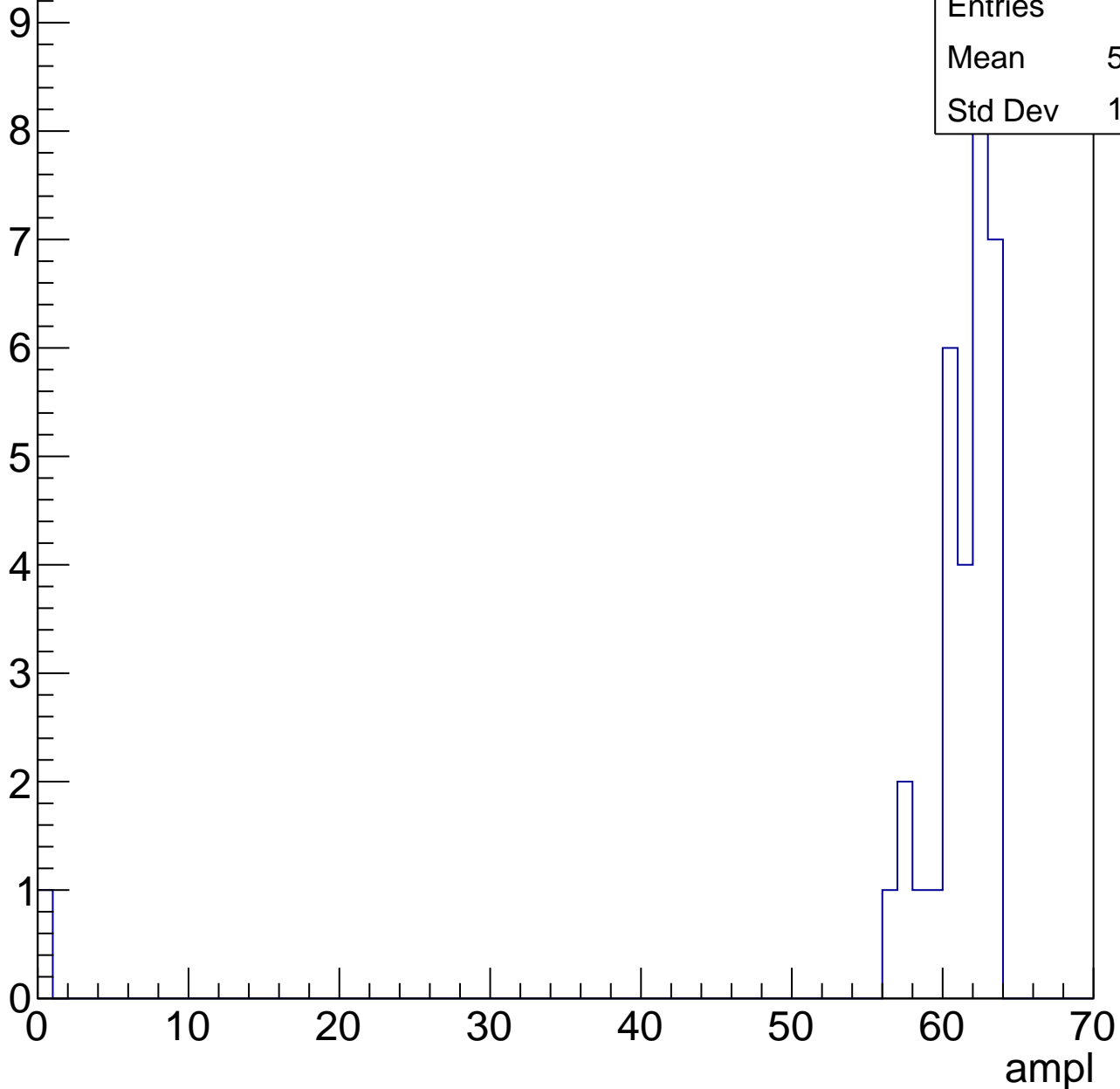


B1L102S, U11-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	59.06
Std Dev	10.77



B1L102S, U11-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L102S, U11-ch28, adc0

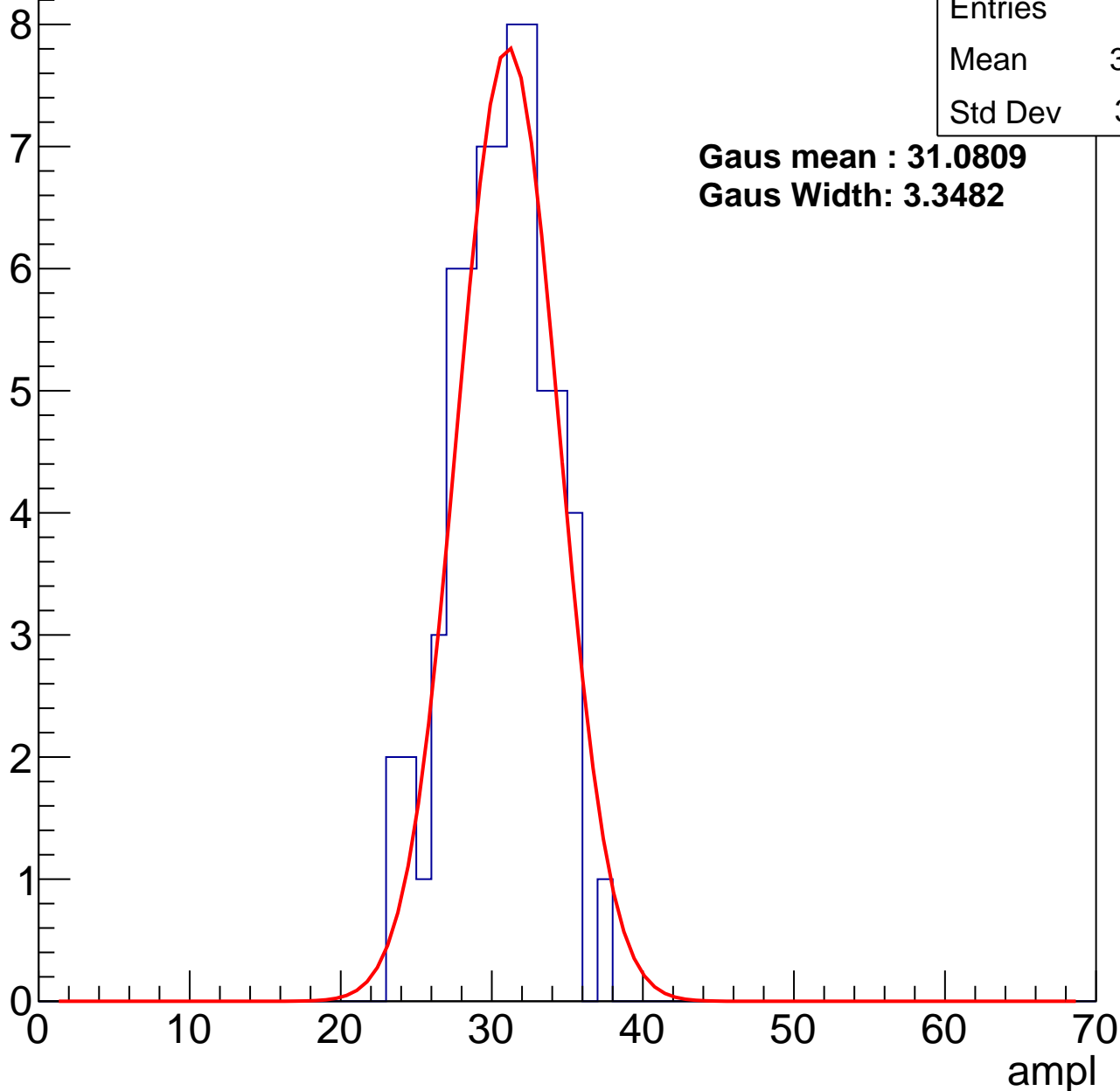
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.09
Std Dev	3.141

Gaus mean : 31.0809

Gaus Width: 3.3482



B1L102S, U11-ch28, adc1

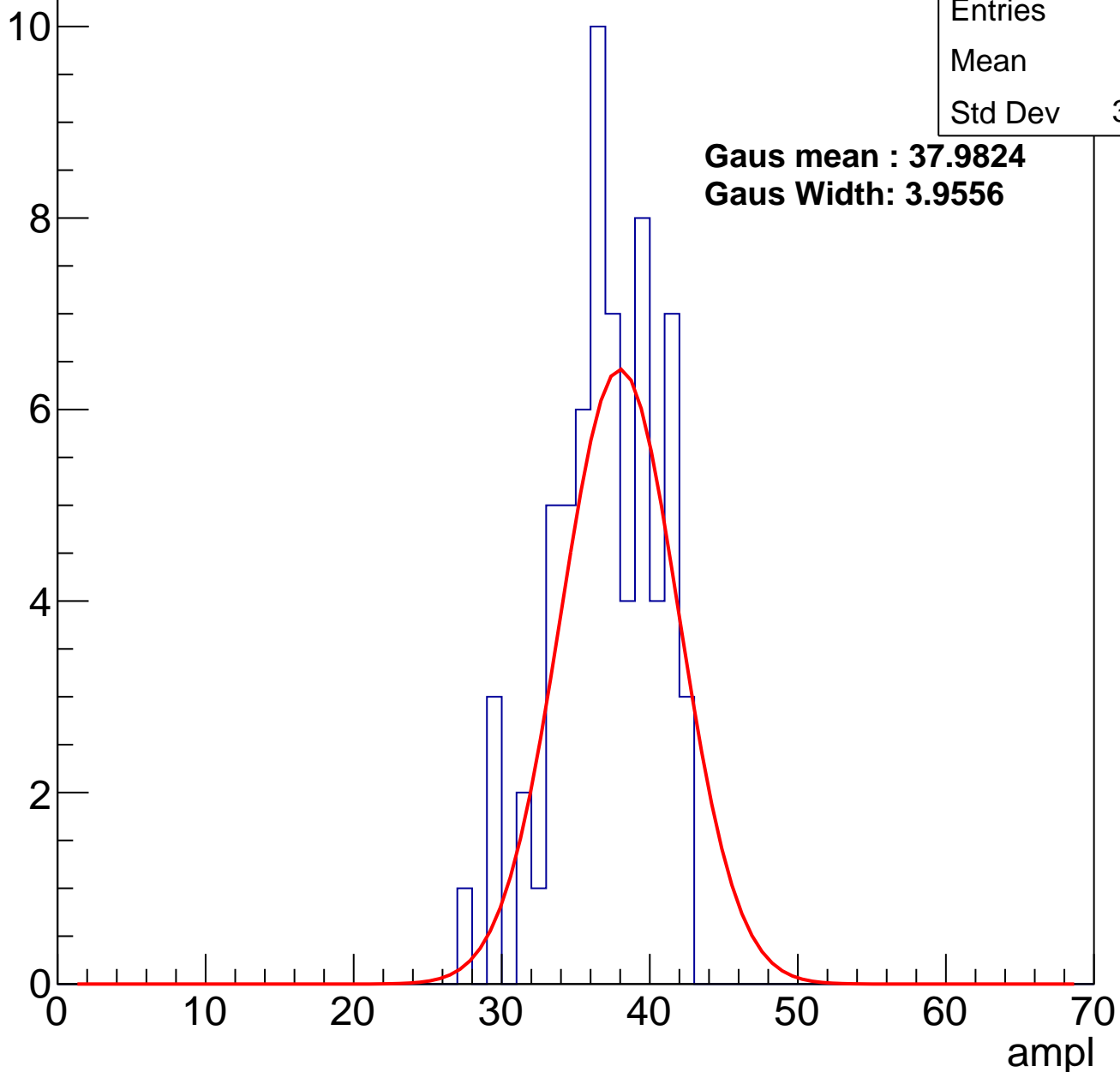
calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	36.5
Std Dev	3.461

Gaus mean : 37.9824

Gaus Width: 3.9556

Entry



B1L102S, U11-ch28, adc2

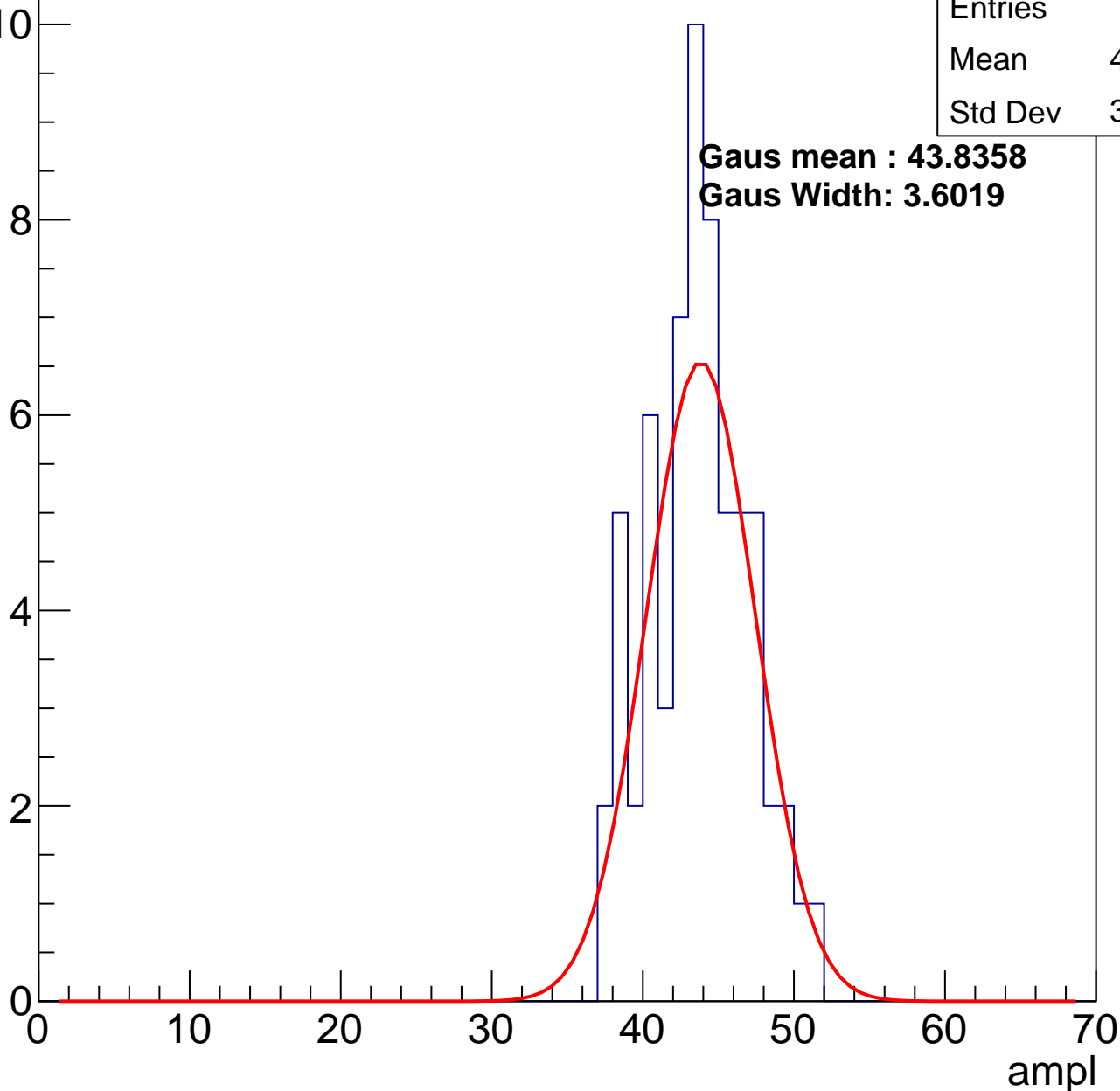
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.22
Std Dev	3.276

Gaus mean : 43.8358

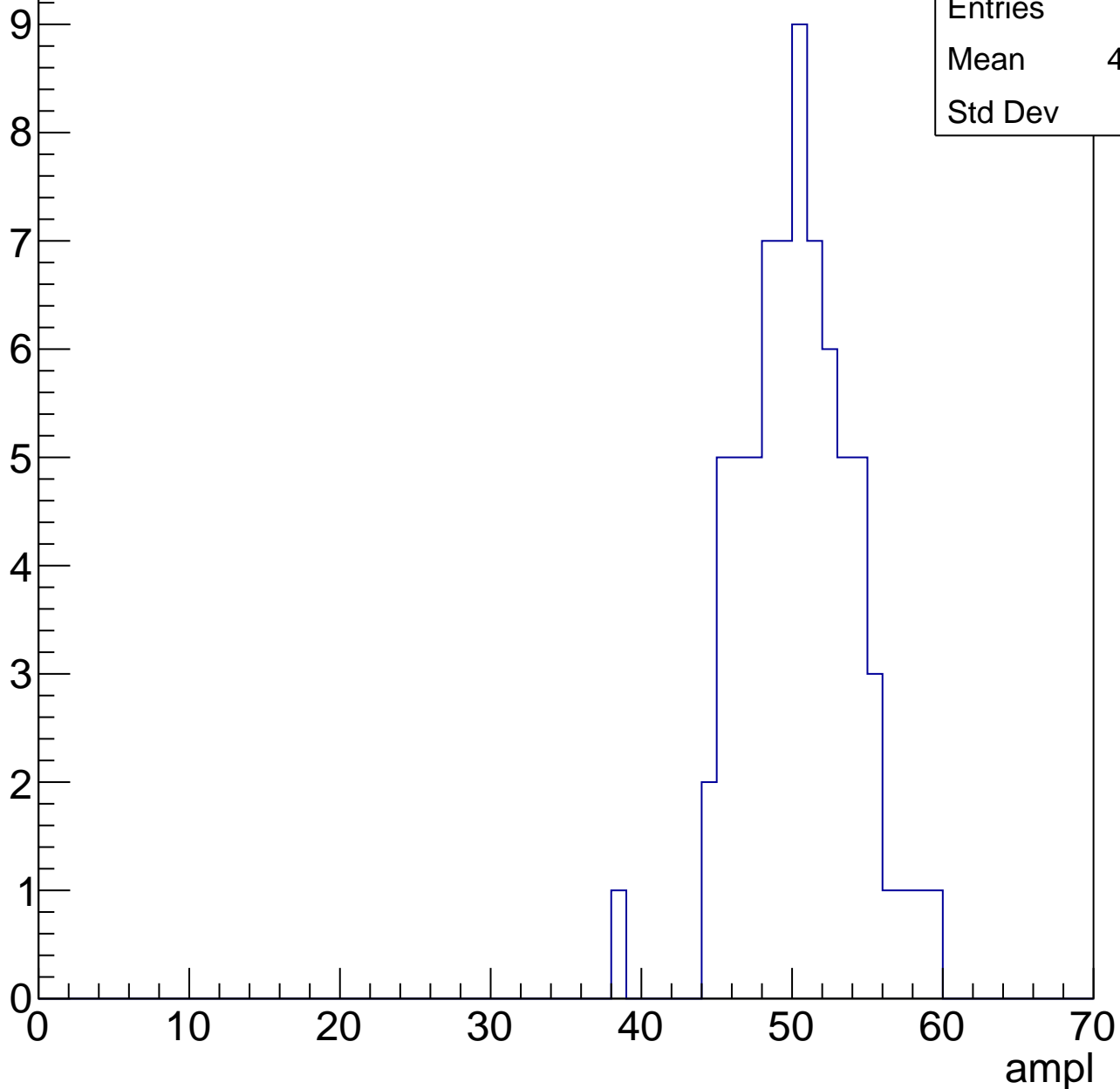
Gaus Width: 3.6019



B1L102S, U11-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

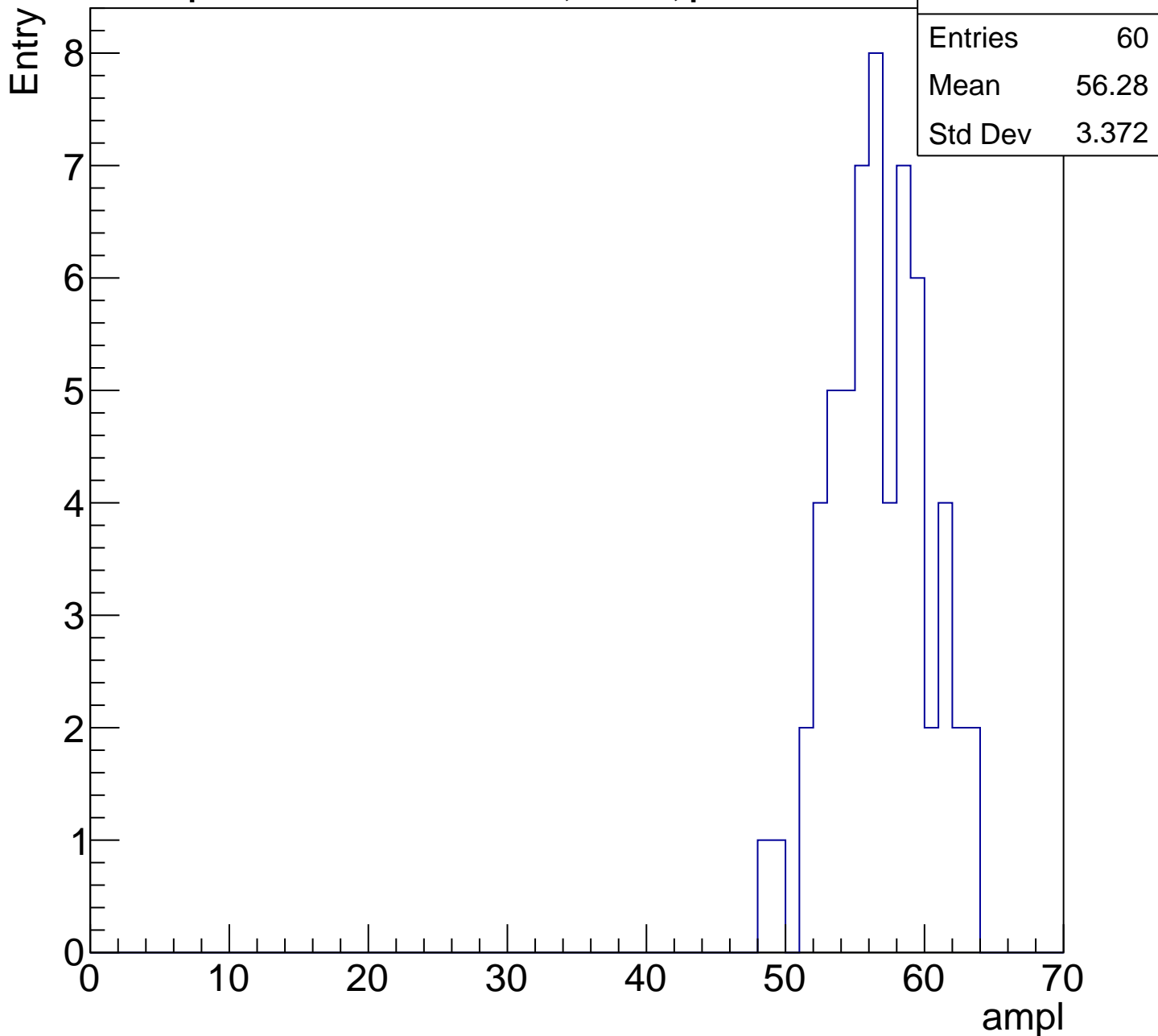
Entry



Entries	71
Mean	49.92
Std Dev	3.68

B1L102S, U11-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U11-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	58.62
Std Dev	9.979

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

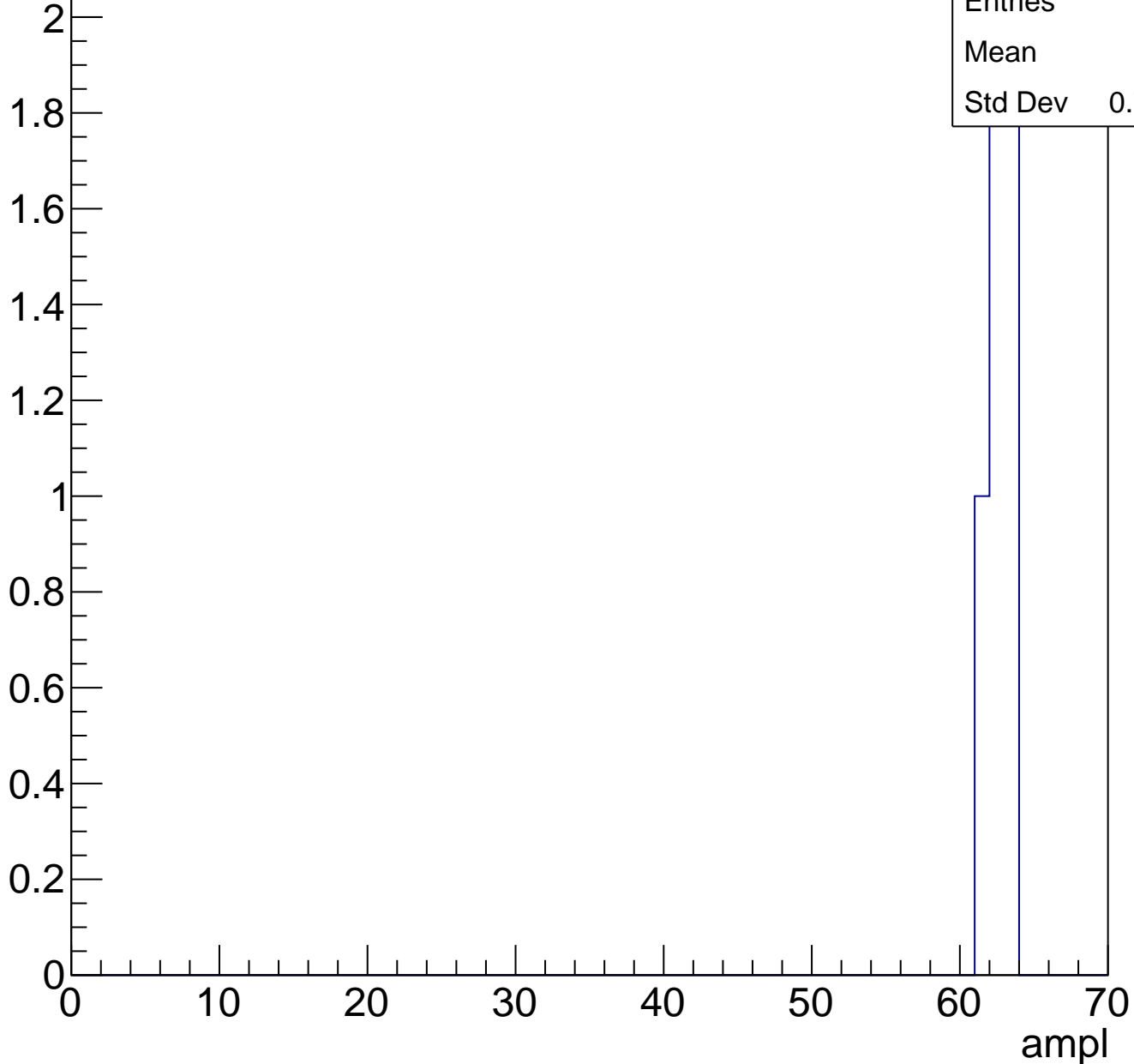
7

8

B1L102S, U11-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L102S, U11-ch29, adc0

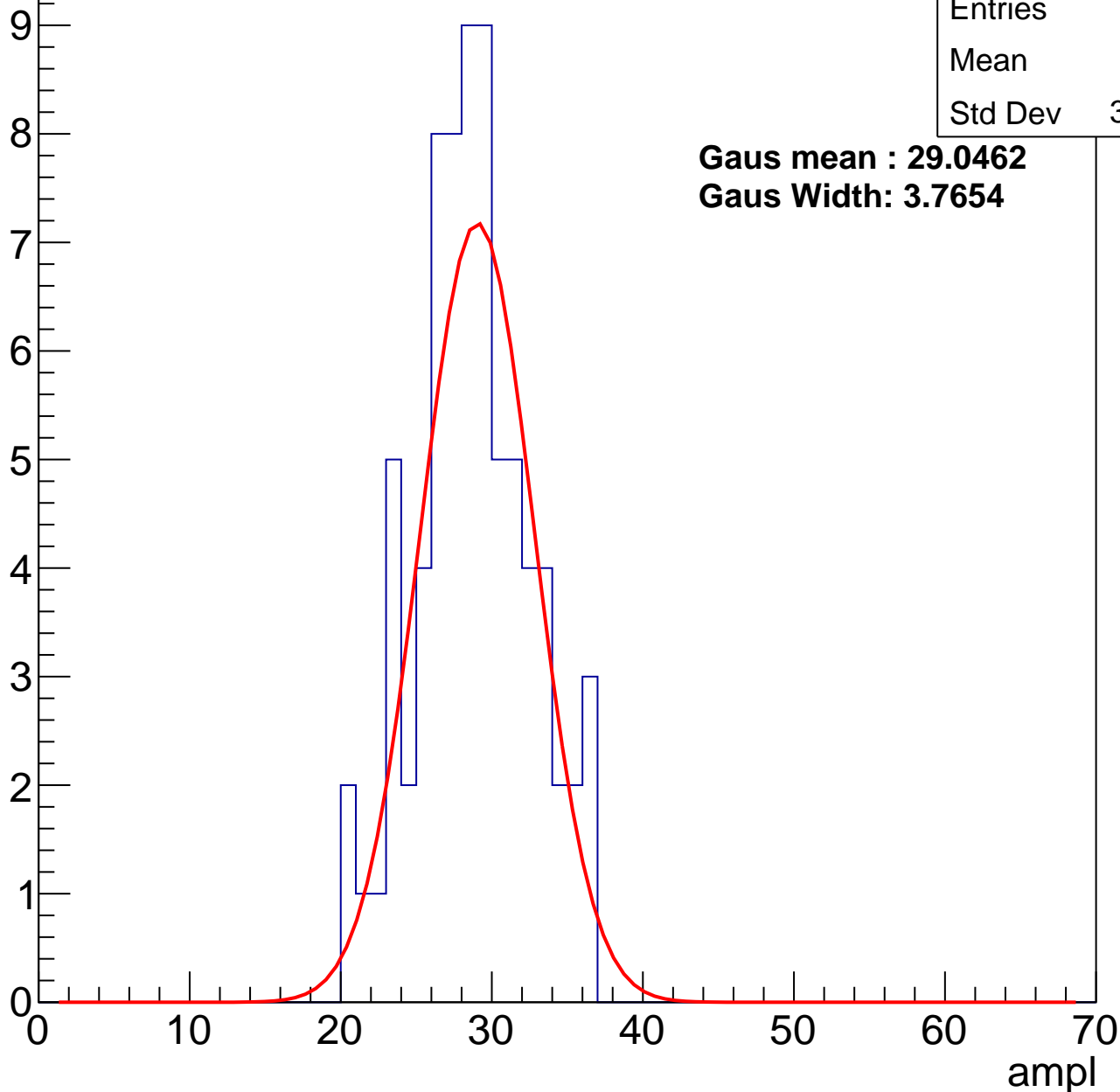
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.3
Std Dev	3.748

Gaus mean : 29.0462

Gaus Width: 3.7654



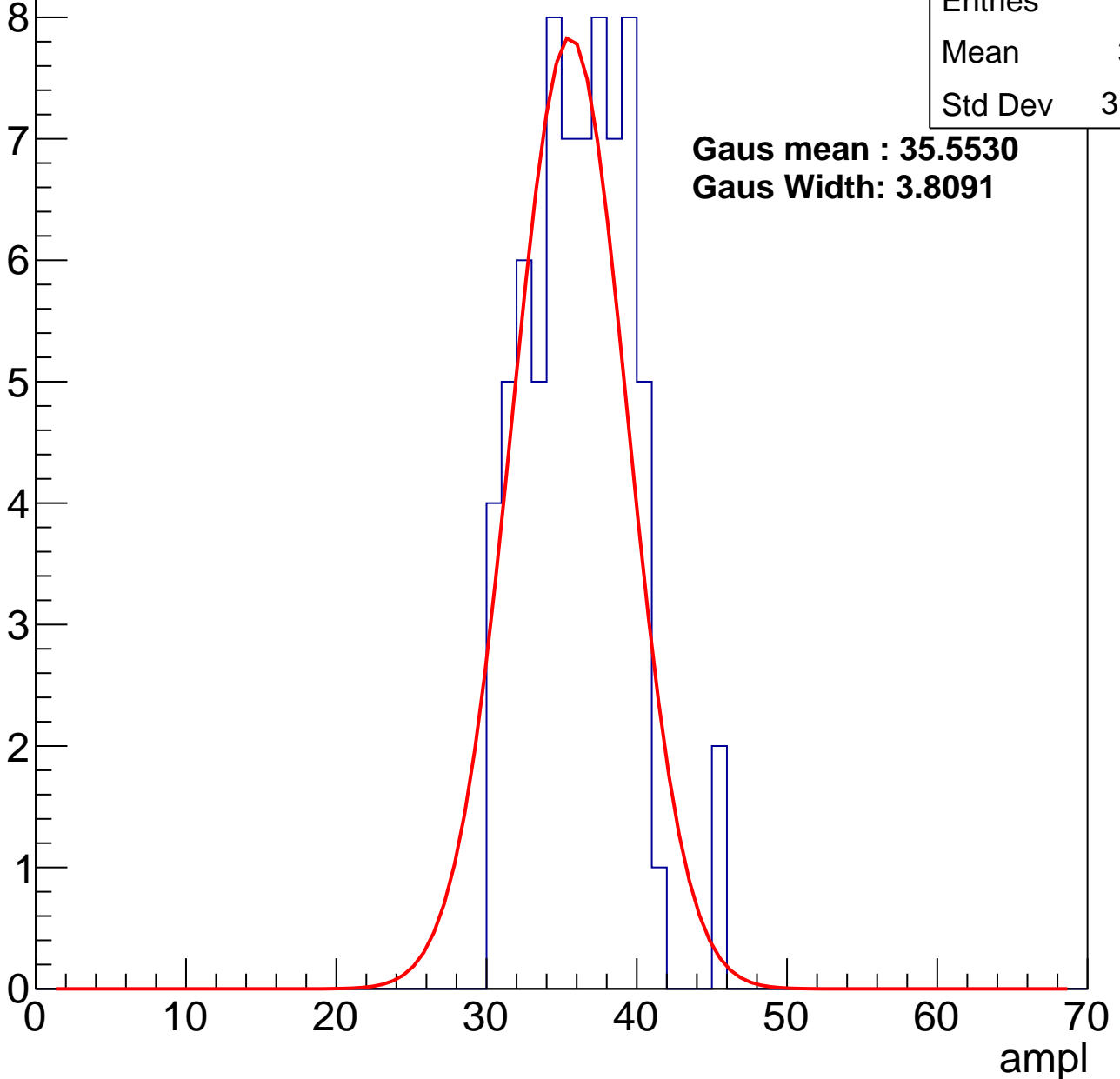
B1L102S, U11-ch29, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.7
Std Dev	3.346

Gaus mean : 35.5530
Gaus Width: 3.8091



B1L102S, U11-ch29, adc2

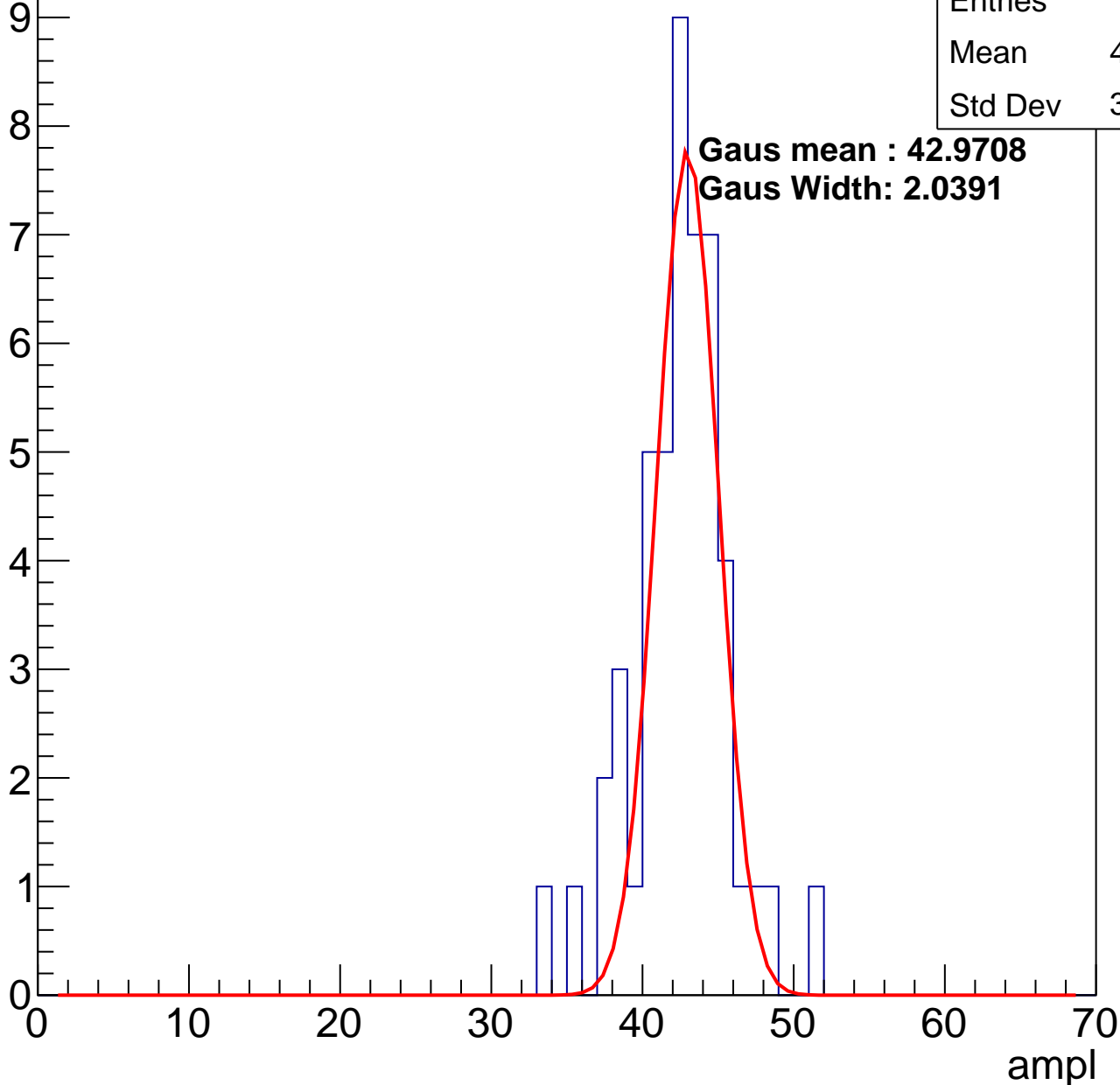
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.02
Std Dev	3.165

Gaus mean : 42.9708

Gaus Width: 2.0391

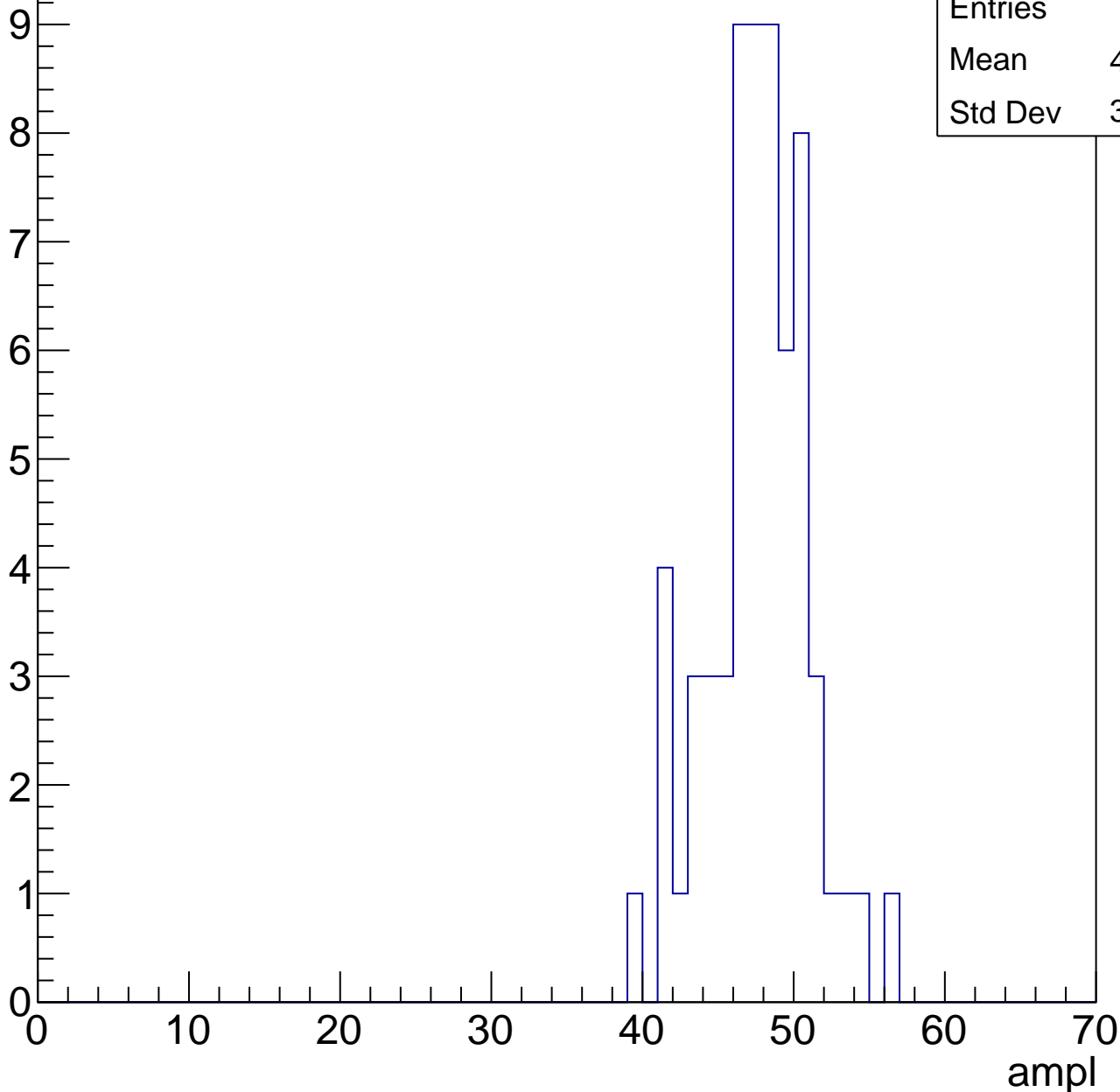


B1L102S, U11-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.17
Std Dev	3.264

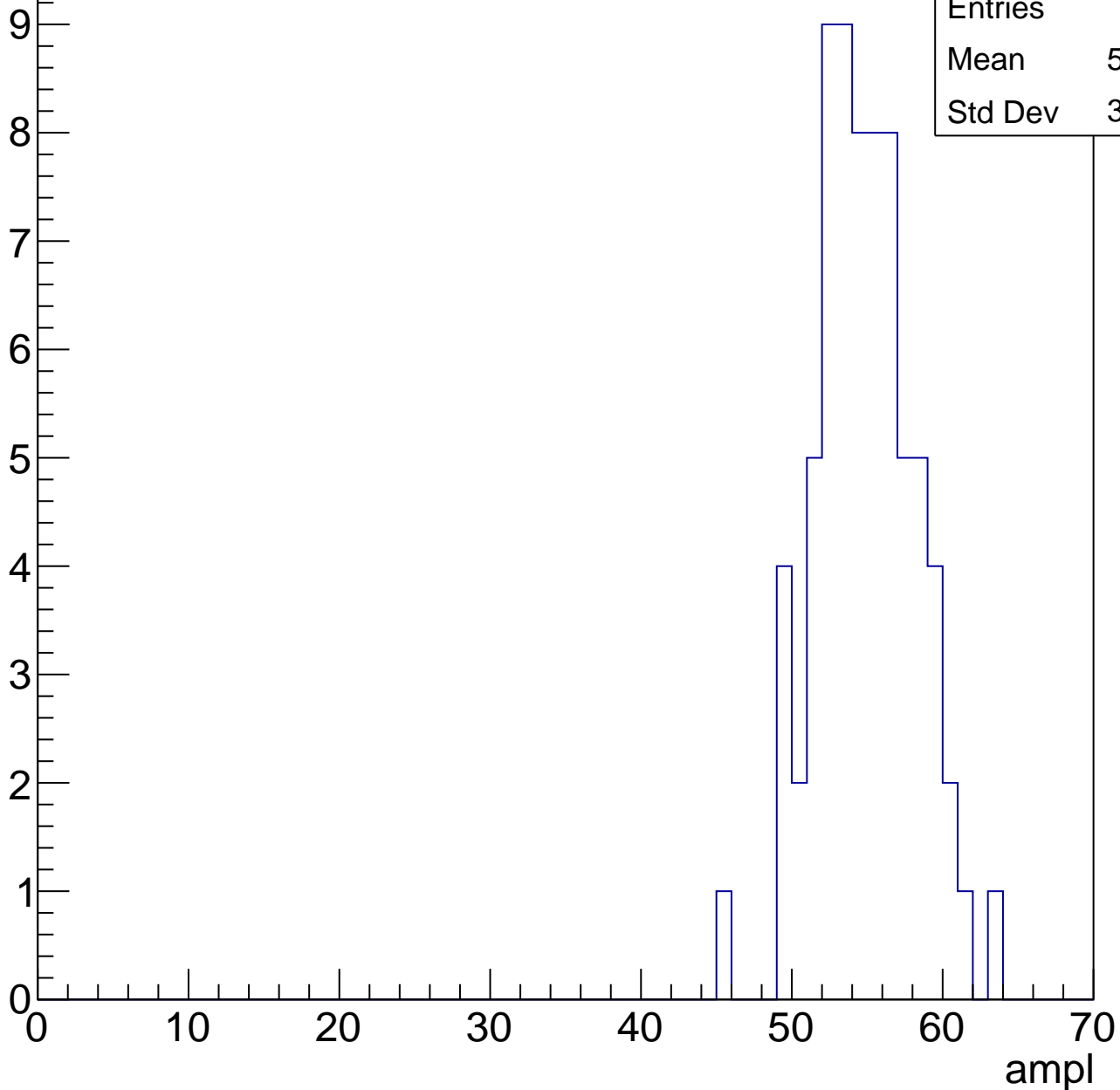


B1L102S, U11-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	54.39
Std Dev	3.247

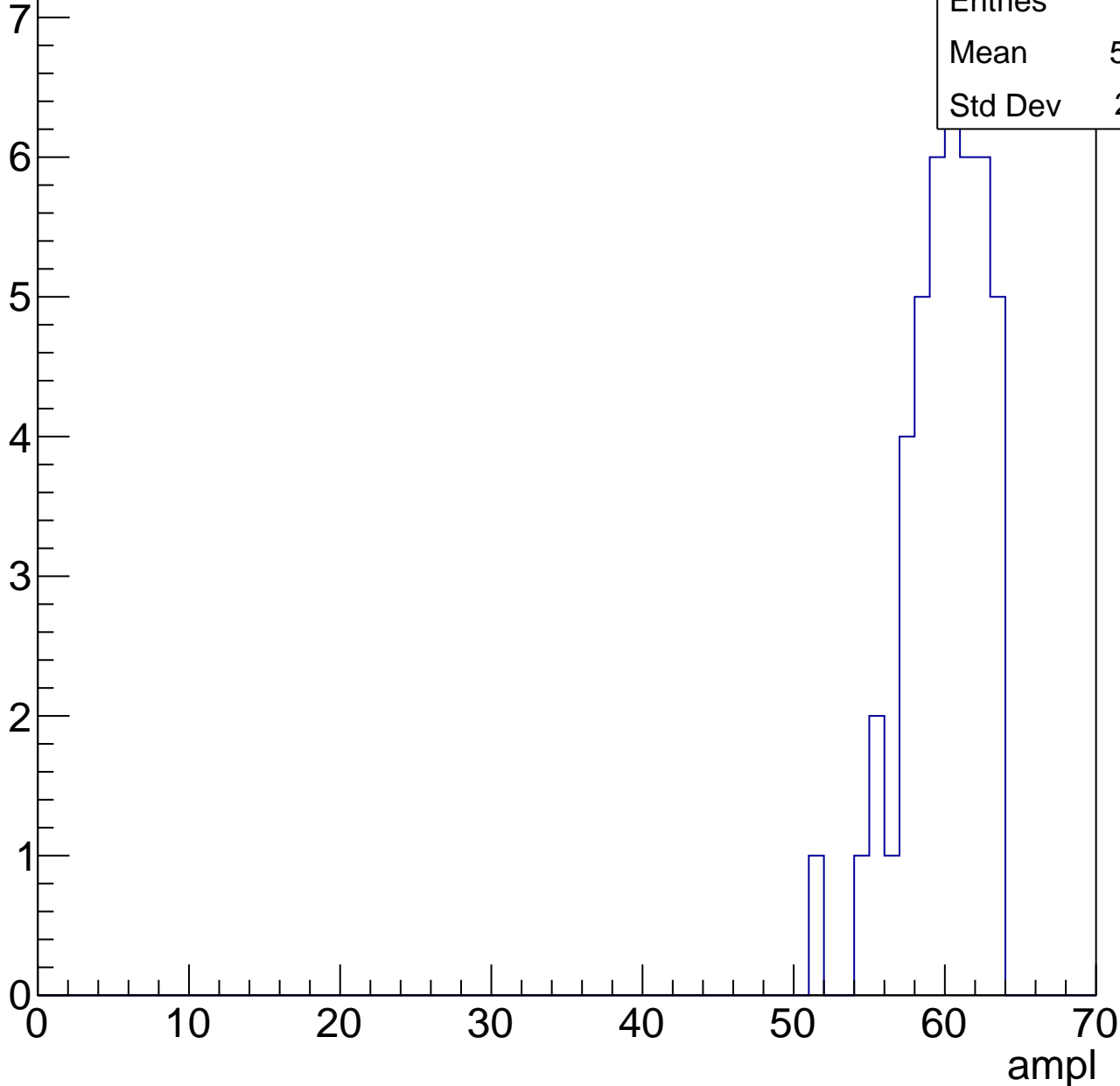


B1L102S, U11-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

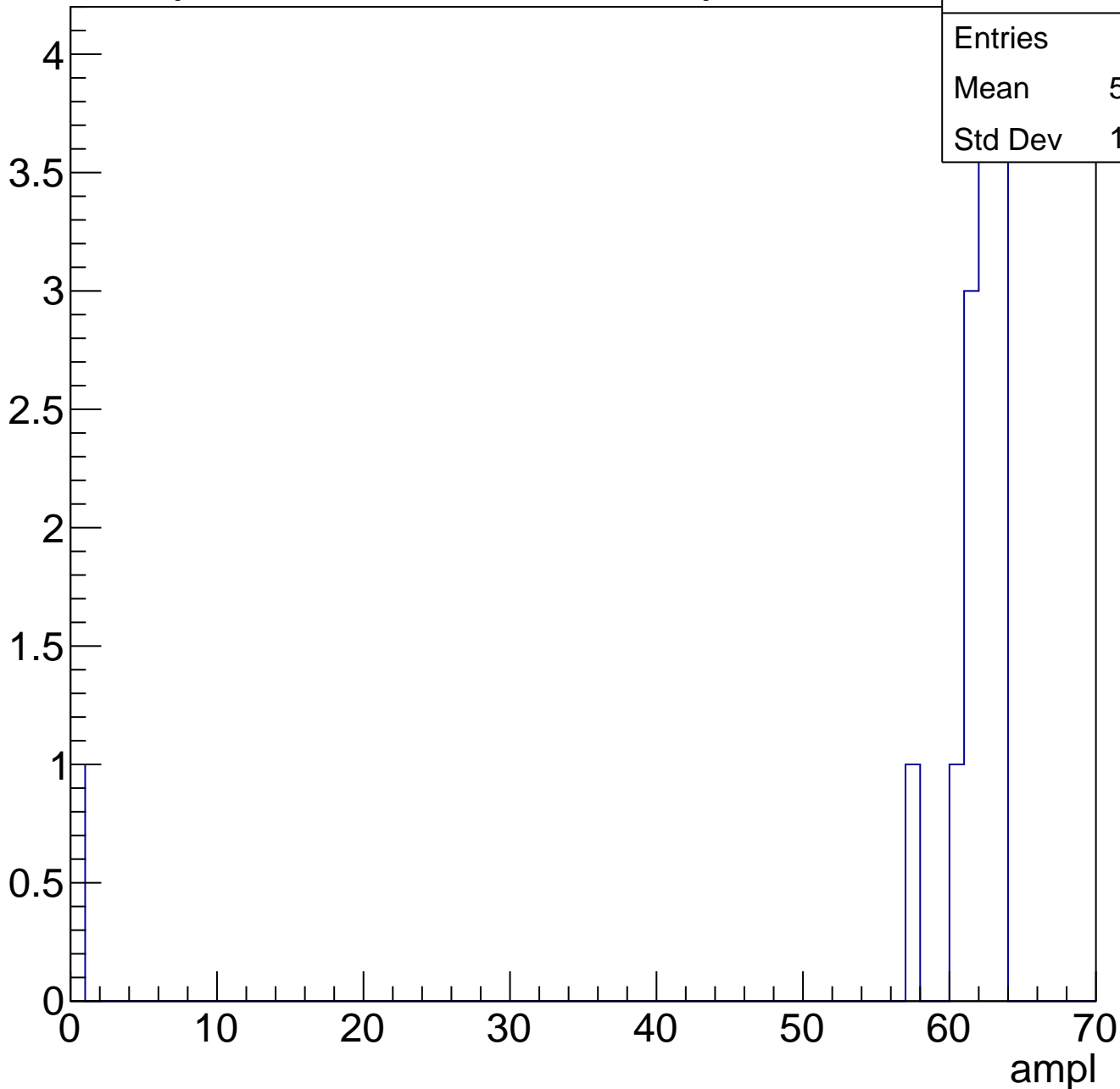
Entries	44
Mean	59.45
Std Dev	2.641



B1L102S, U11-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch30, adc0

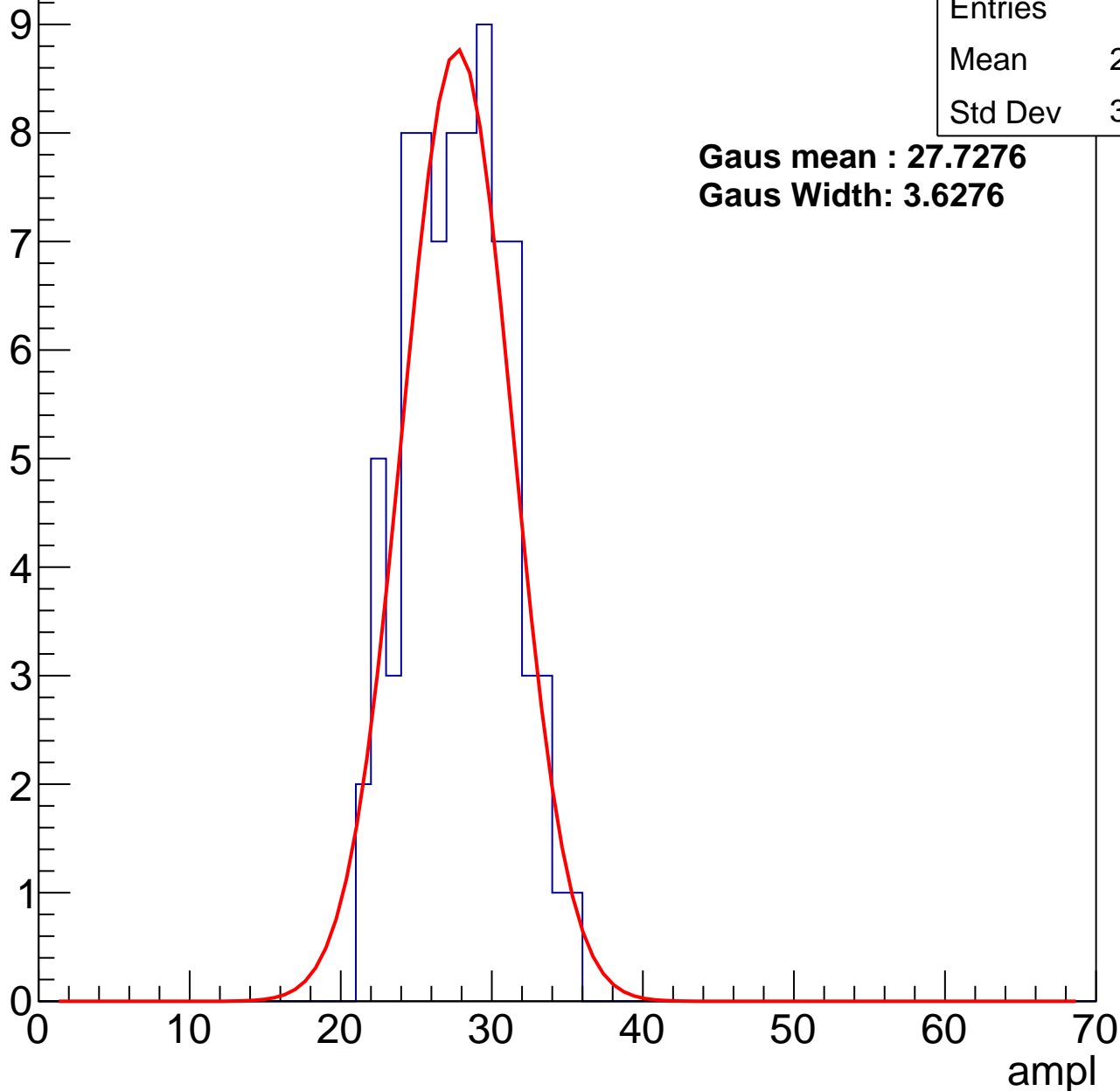
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.34
Std Dev	3.282

Gaus mean : 27.7276

Gaus Width: 3.6276



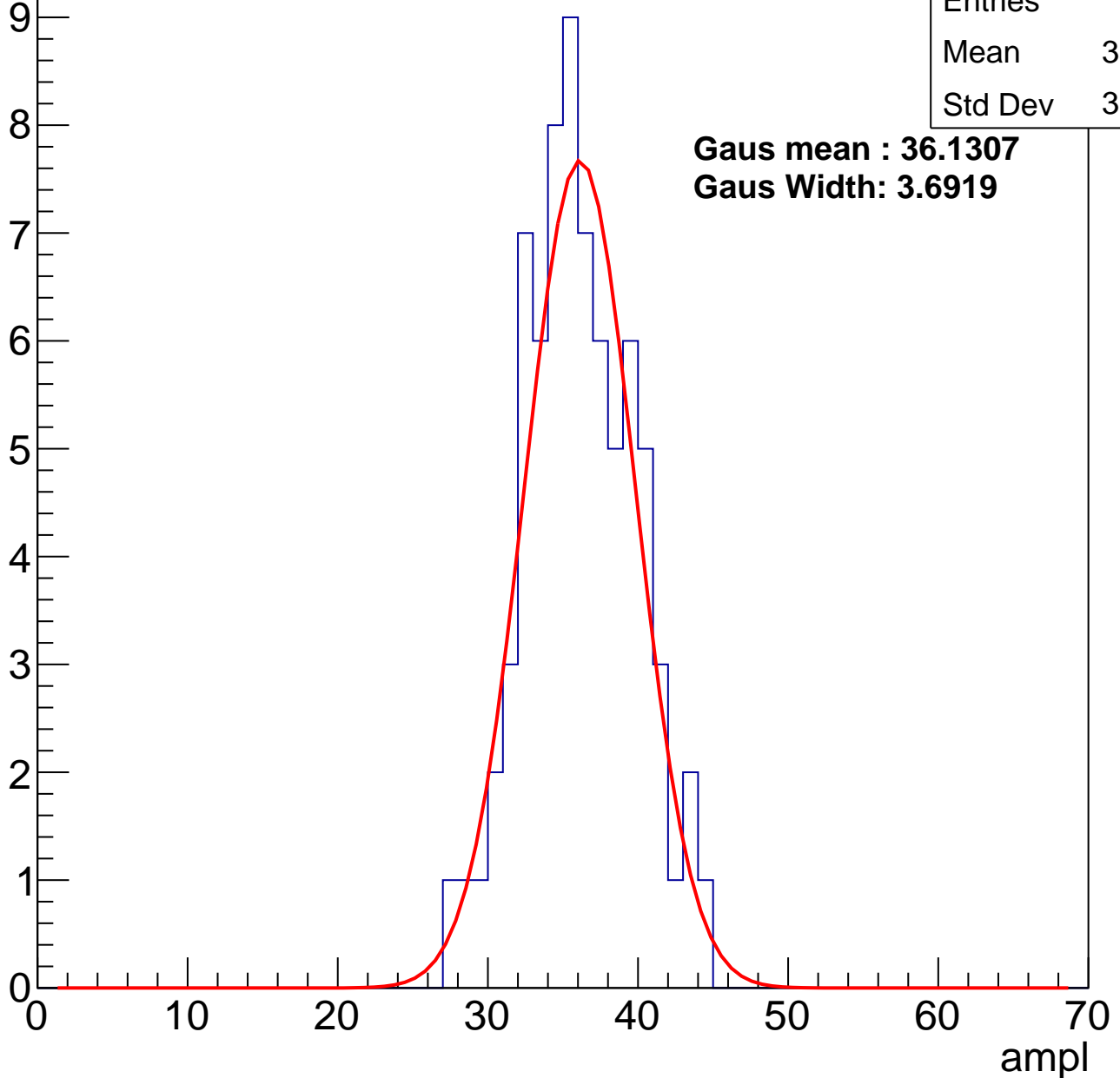
B1L102S, U11-ch30, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.66
Std Dev	3.625

Gaus mean : 36.1307
Gaus Width: 3.6919



B1L102S, U11-ch30, adc2

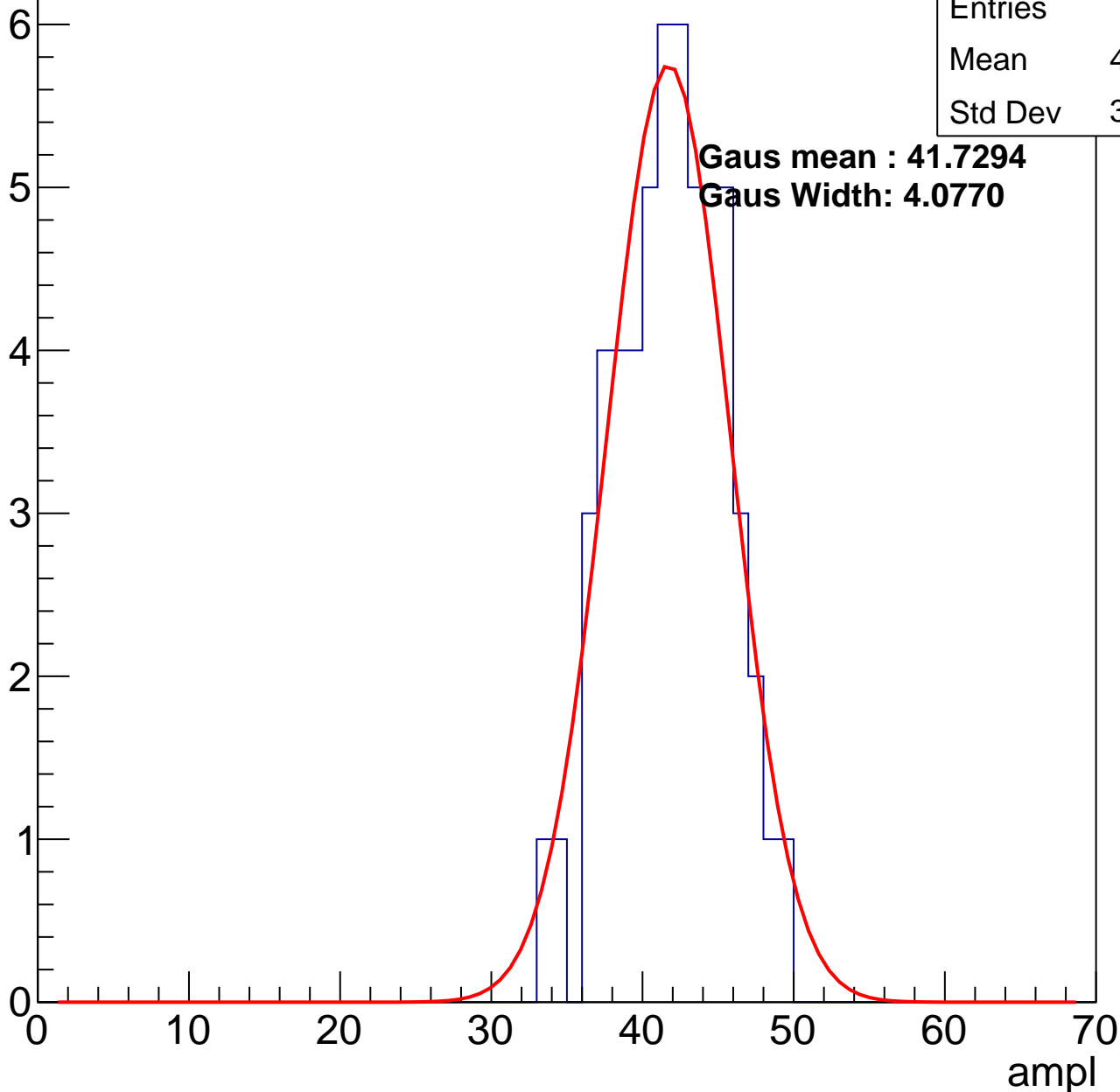
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.39
Std Dev	3.564

Gaus mean : 41.7294

Gaus Width: 4.0770

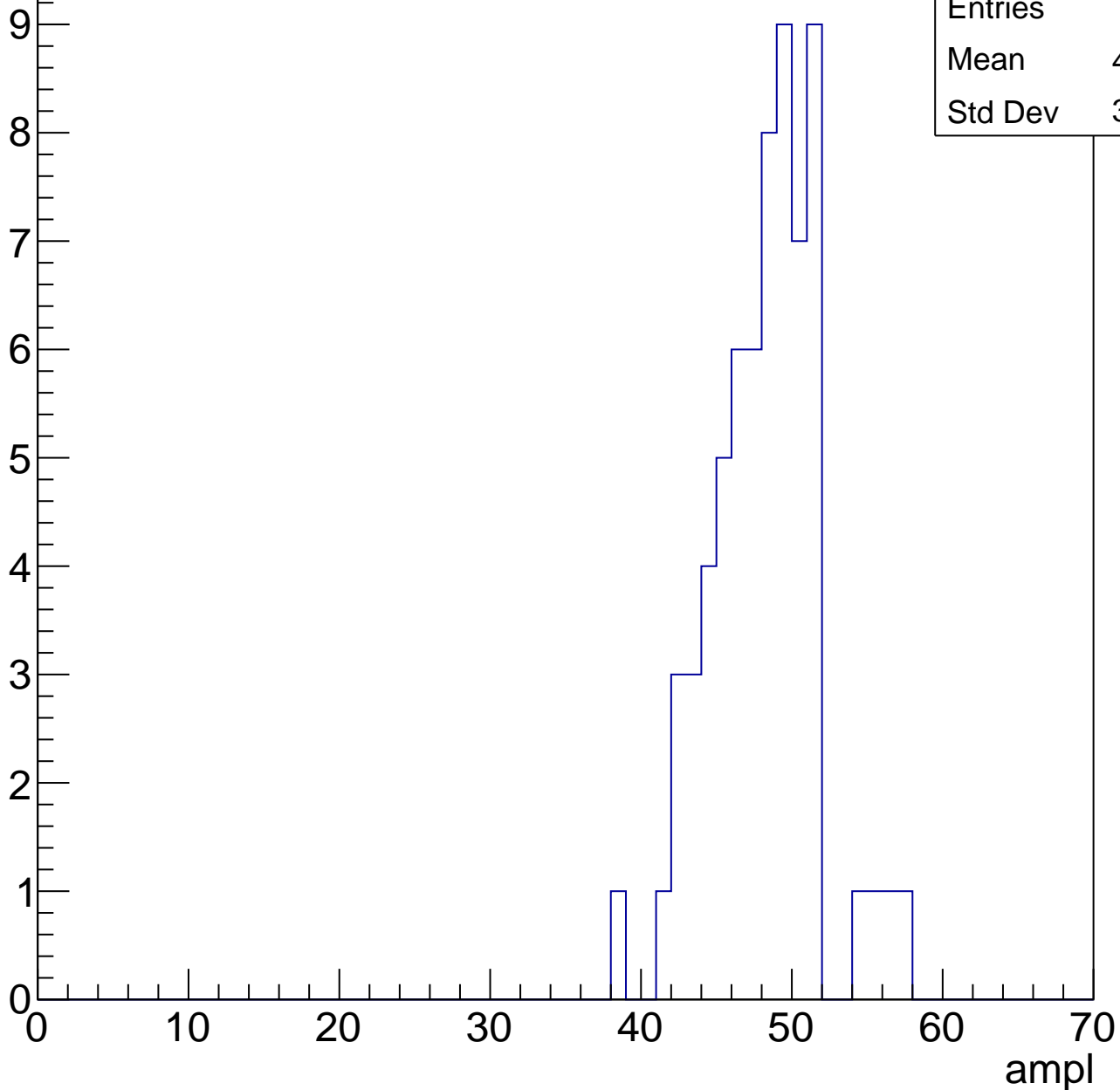


B1L102S, U11-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.71
Std Dev	3.511



B1L102S, U11-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	58
Mean	53.98
Std Dev	3.099

Entry

10

8

6

4

2

0

0

10

20

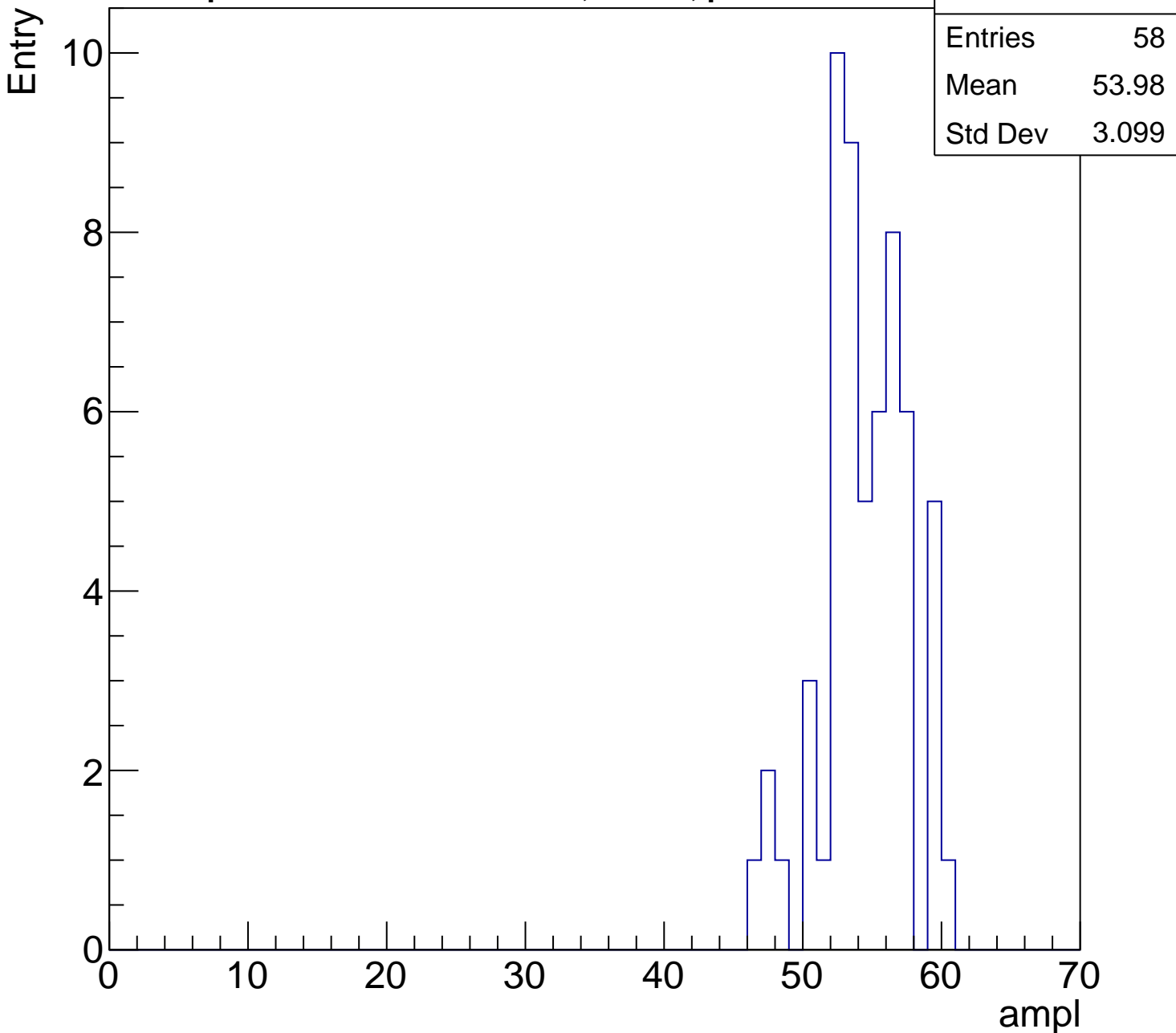
30

40

50

60

ampl

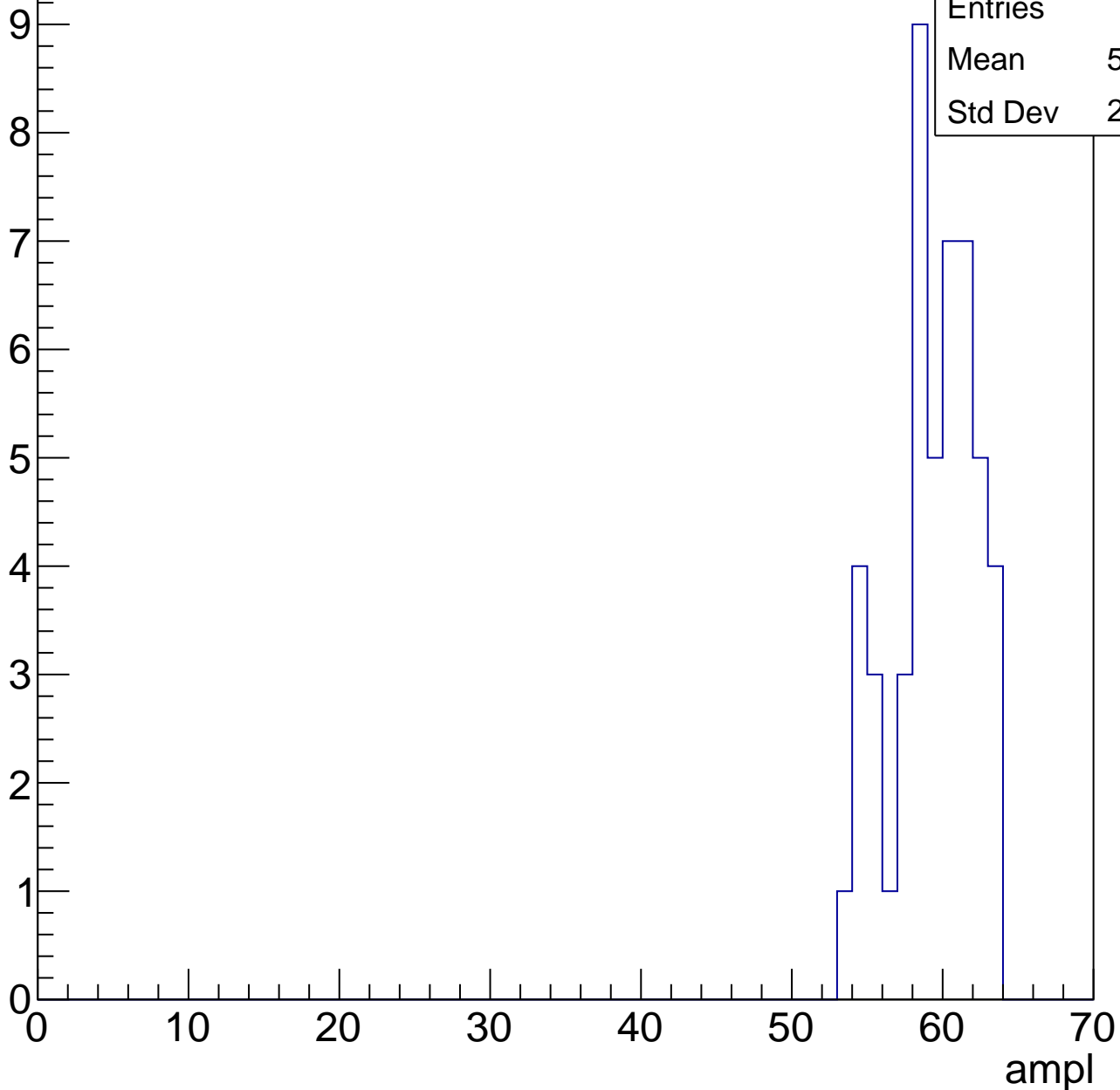


B1L102S, U11-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.92
Std Dev	2.702

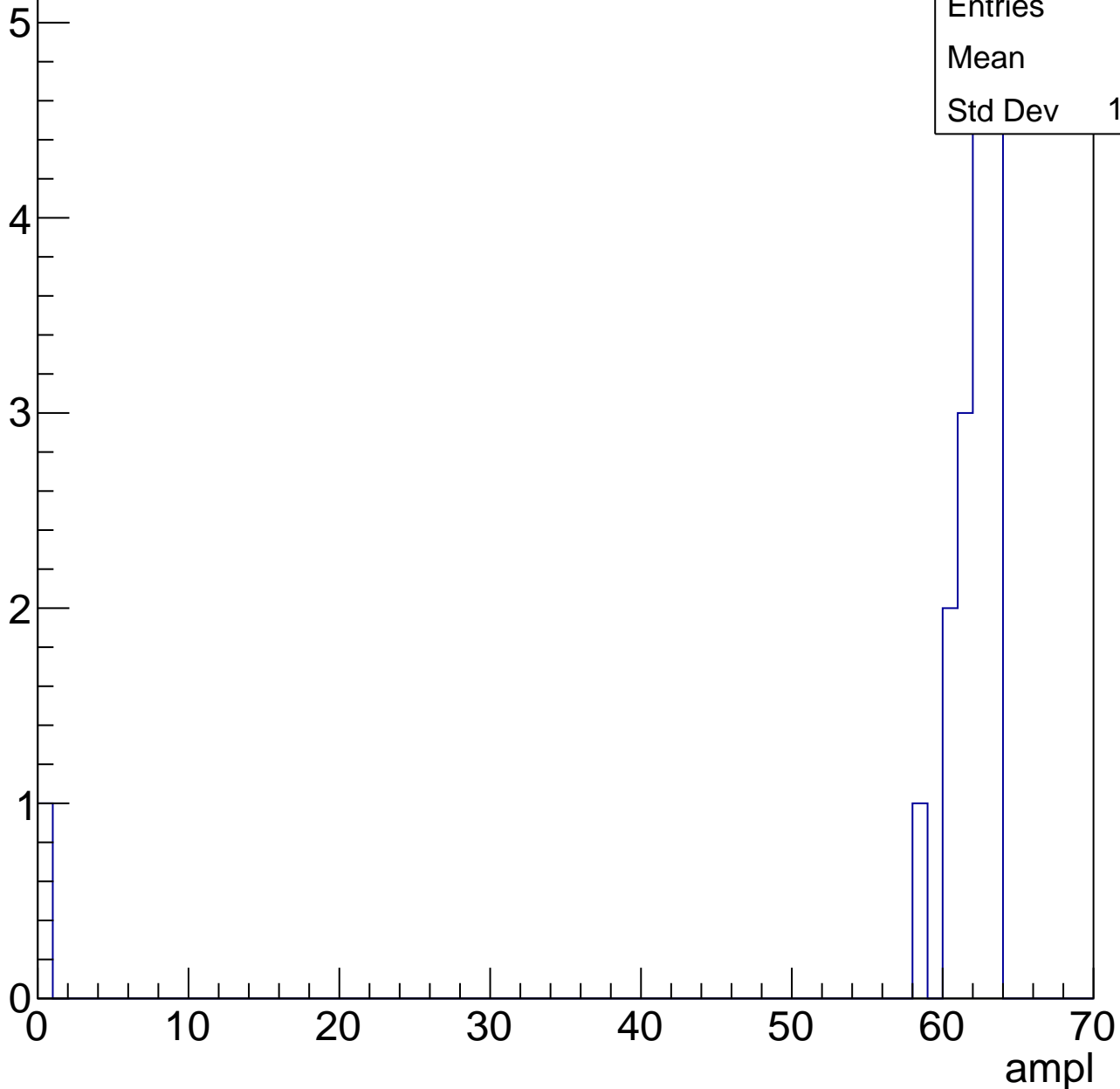


B1L102S, U11-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

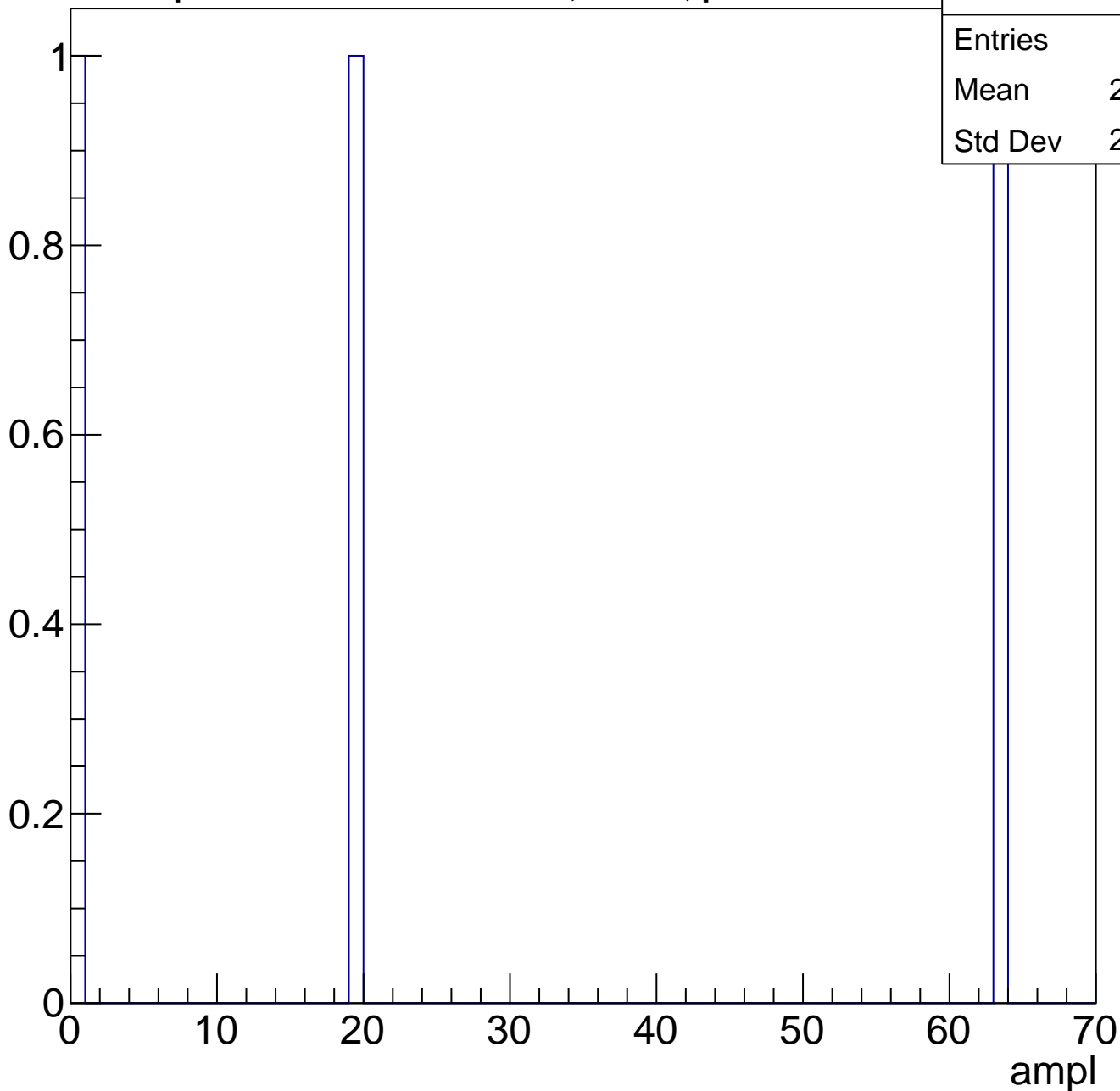
Entries	17
Mean	58
Std Dev	14.56



B1L102S, U11-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L102S, U11-ch31, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	28.91
Std Dev	5.026

Gaus mean : 29.6611

Gaus Width: 4.3570

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L102S, U11-ch31, adc1

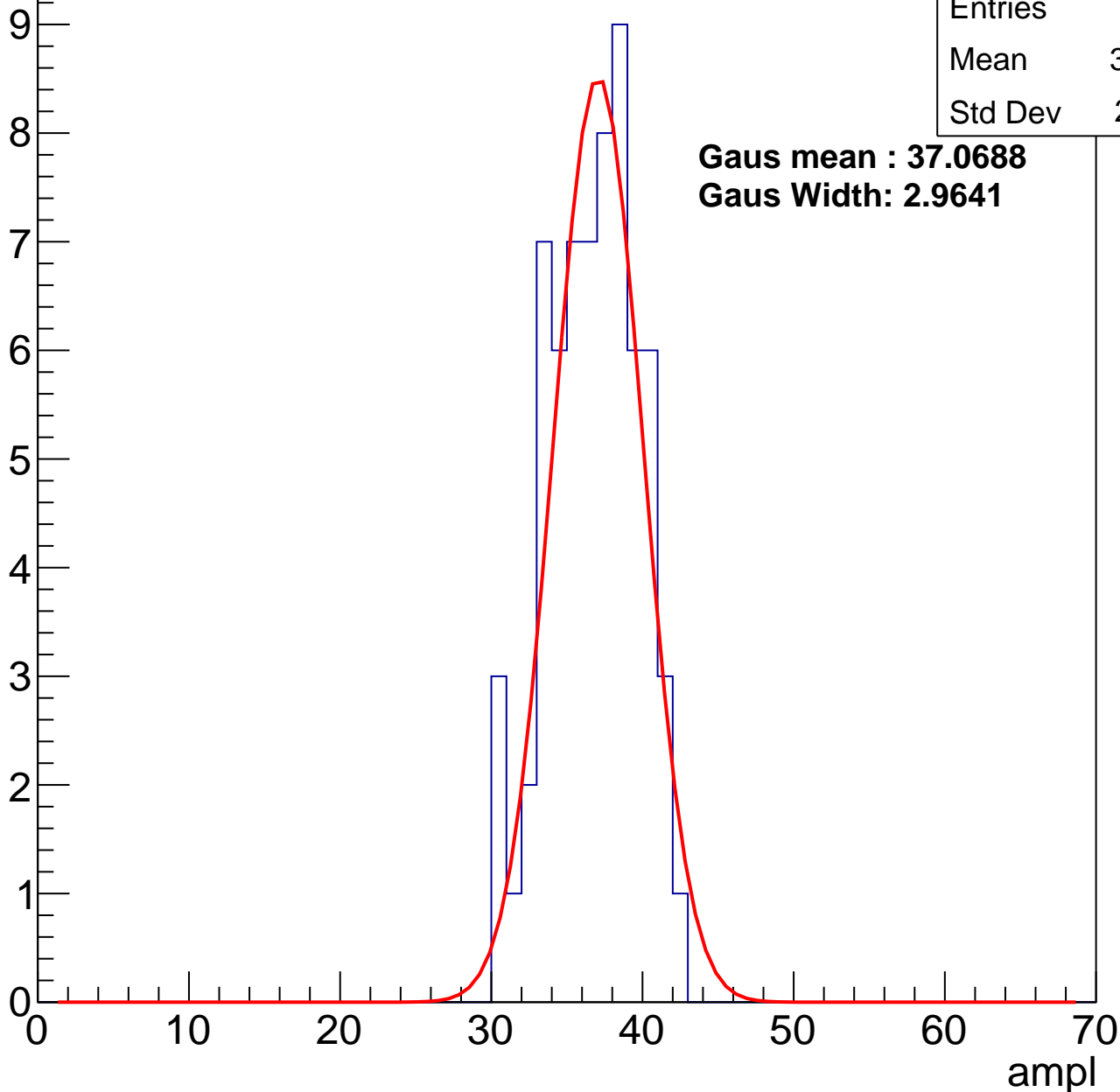
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.27
Std Dev	2.911

Gaus mean : 37.0688

Gaus Width: 2.9641



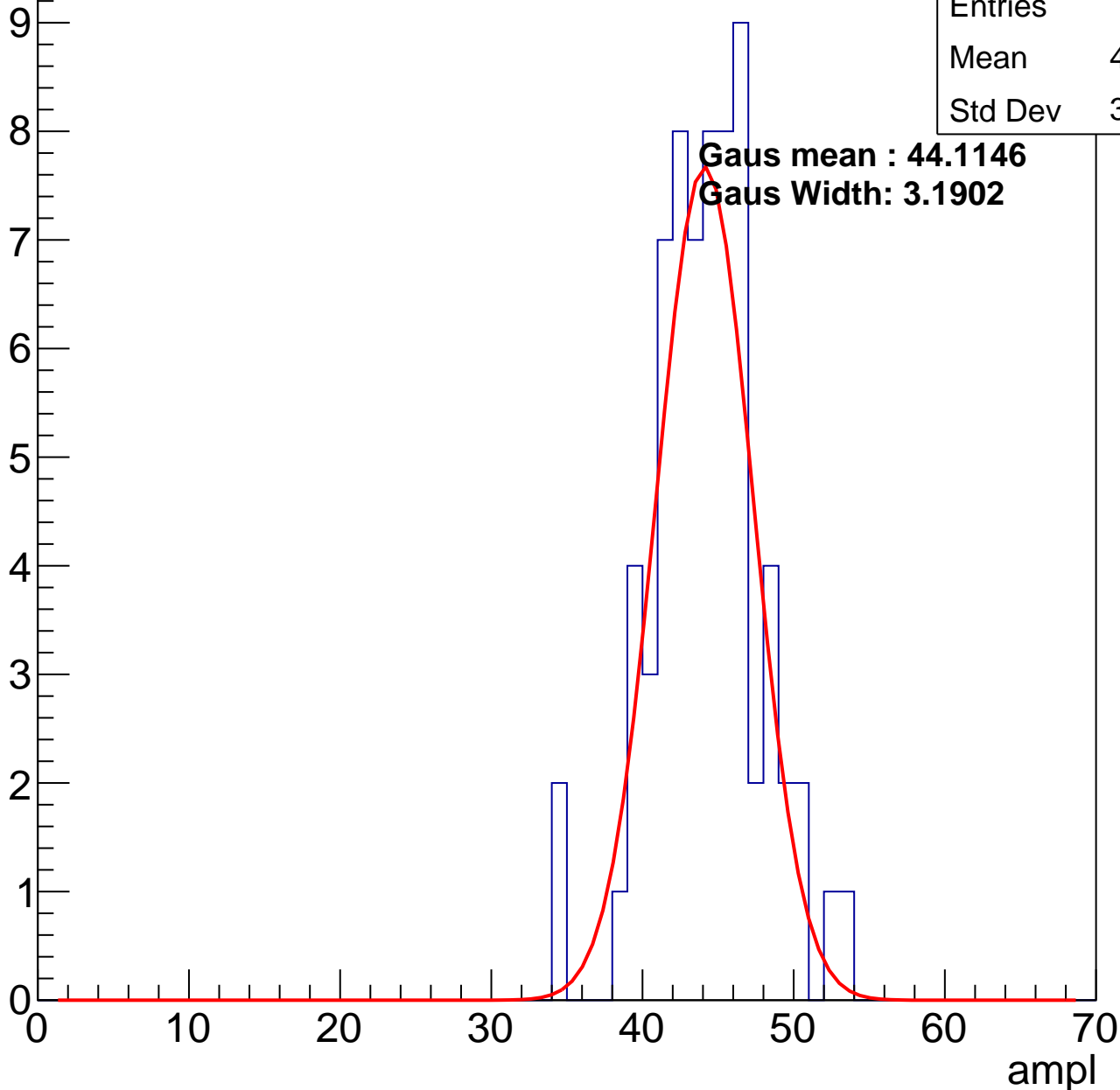
B1L102S, U11-ch31, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.78
Std Dev	3.563

Gaus mean : 44.1146
Gaus Width: 3.1902

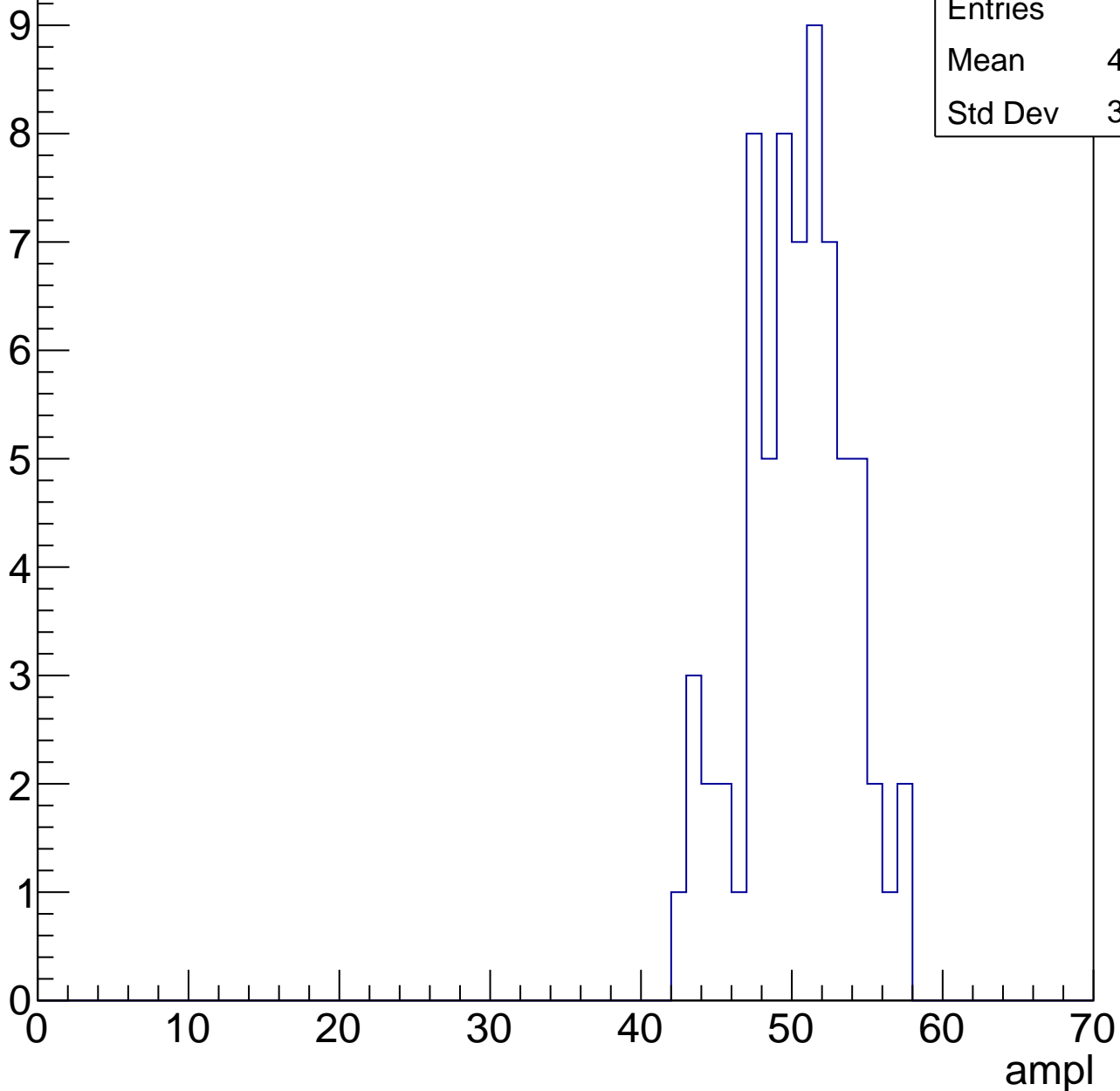


B1L102S, U11-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.87
Std Dev	3.417

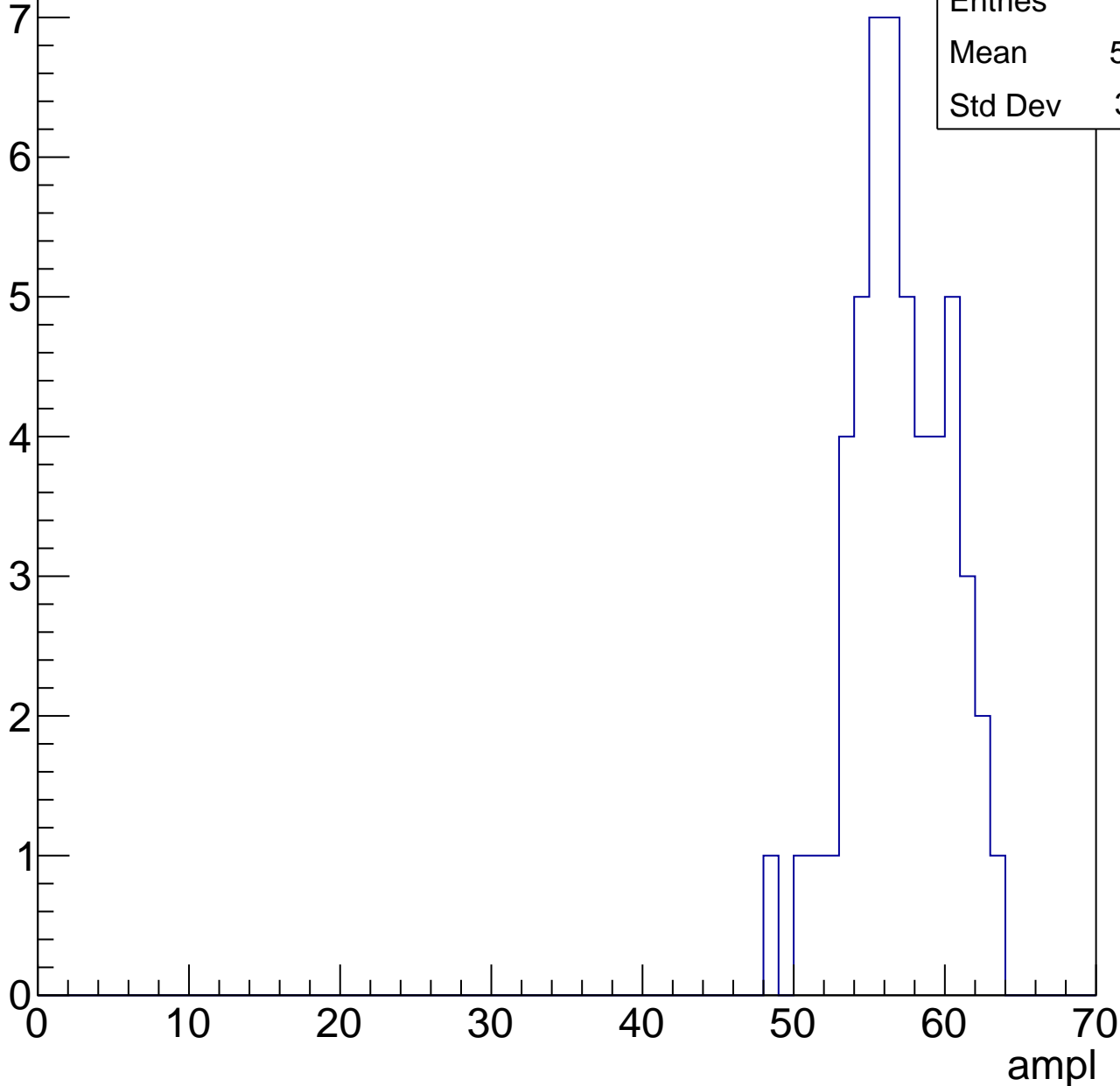


B1L102S, U11-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.53
Std Dev	3.201

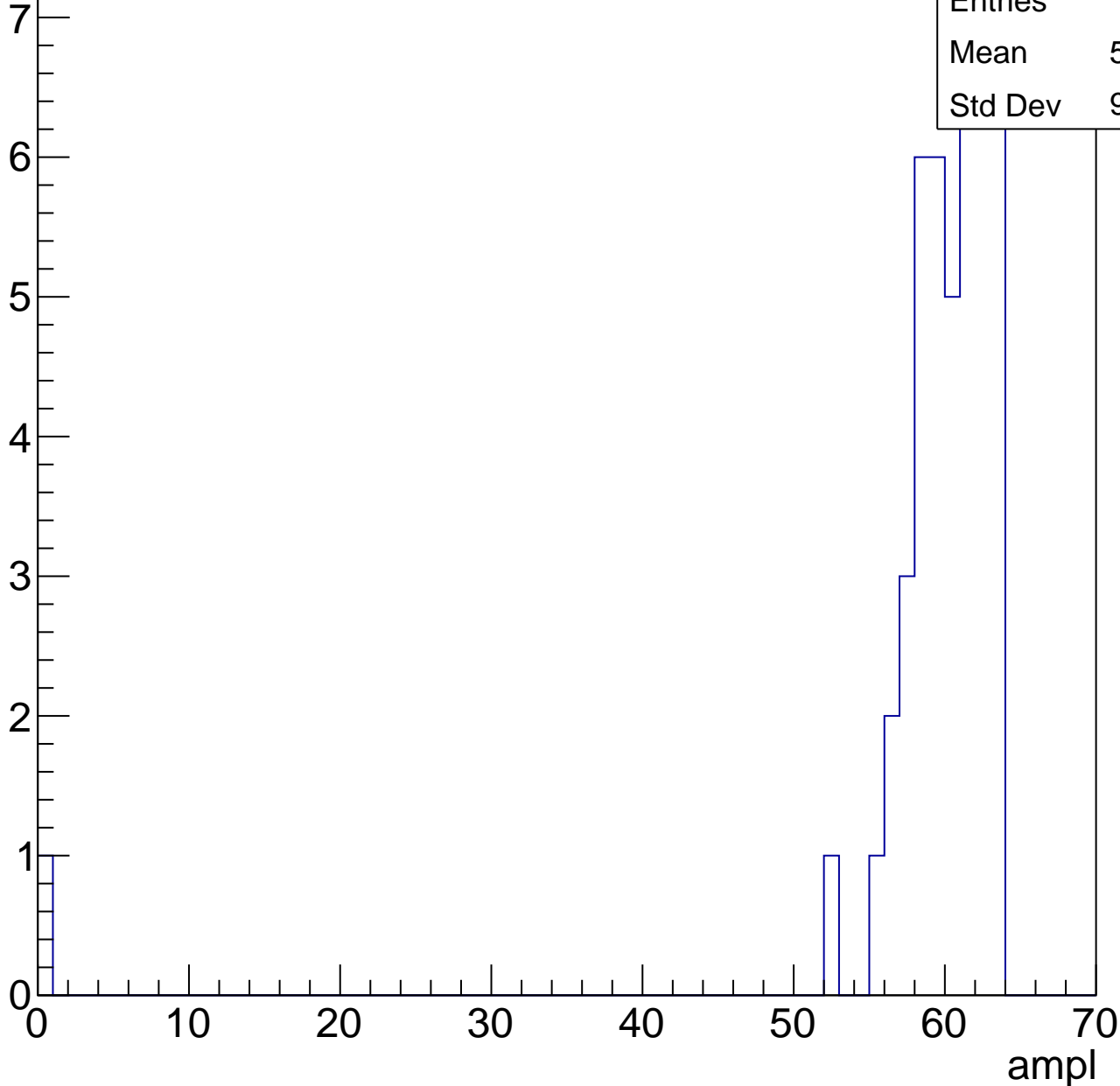


B1L102S, U11-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.57
Std Dev	9.067



B1L102S, U11-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U11-ch32, adc0

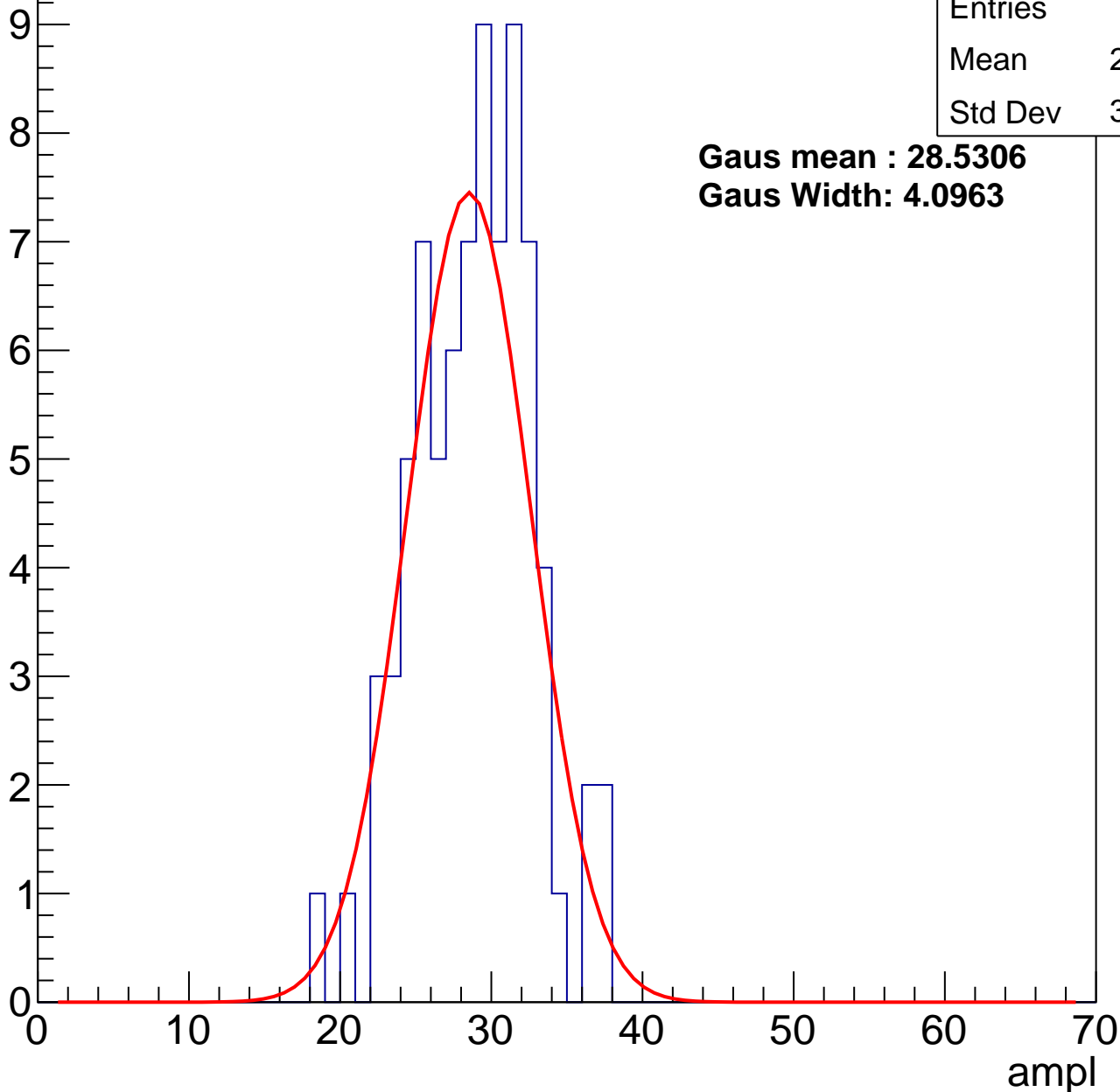
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.38
Std Dev	3.833

Gaus mean : 28.5306

Gaus Width: 4.0963



B1L102S, U11-ch32, adc1

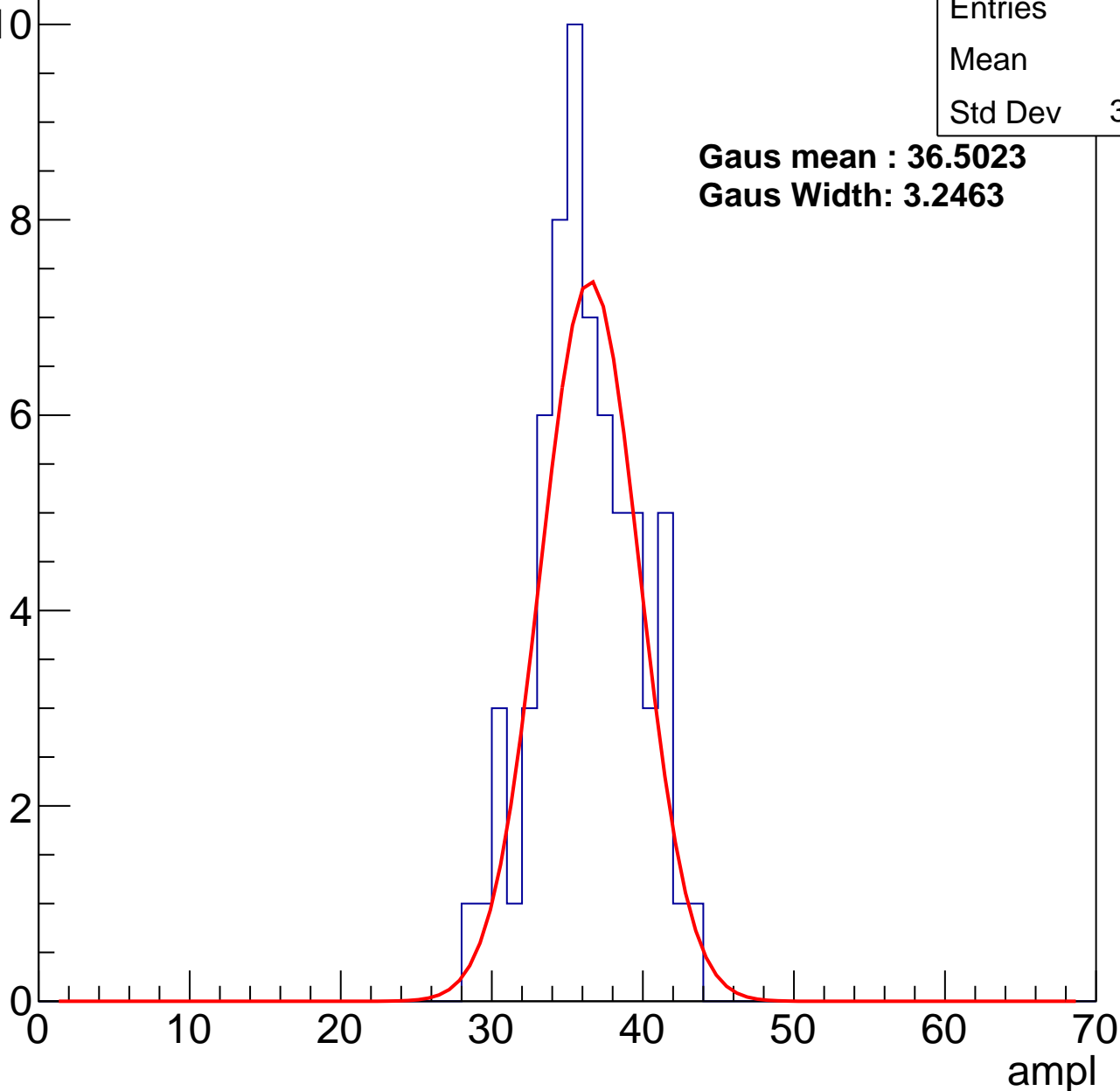
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.8
Std Dev	3.295

Gaus mean : 36.5023

Gaus Width: 3.2463



B1L102S, U11-ch32, adc2

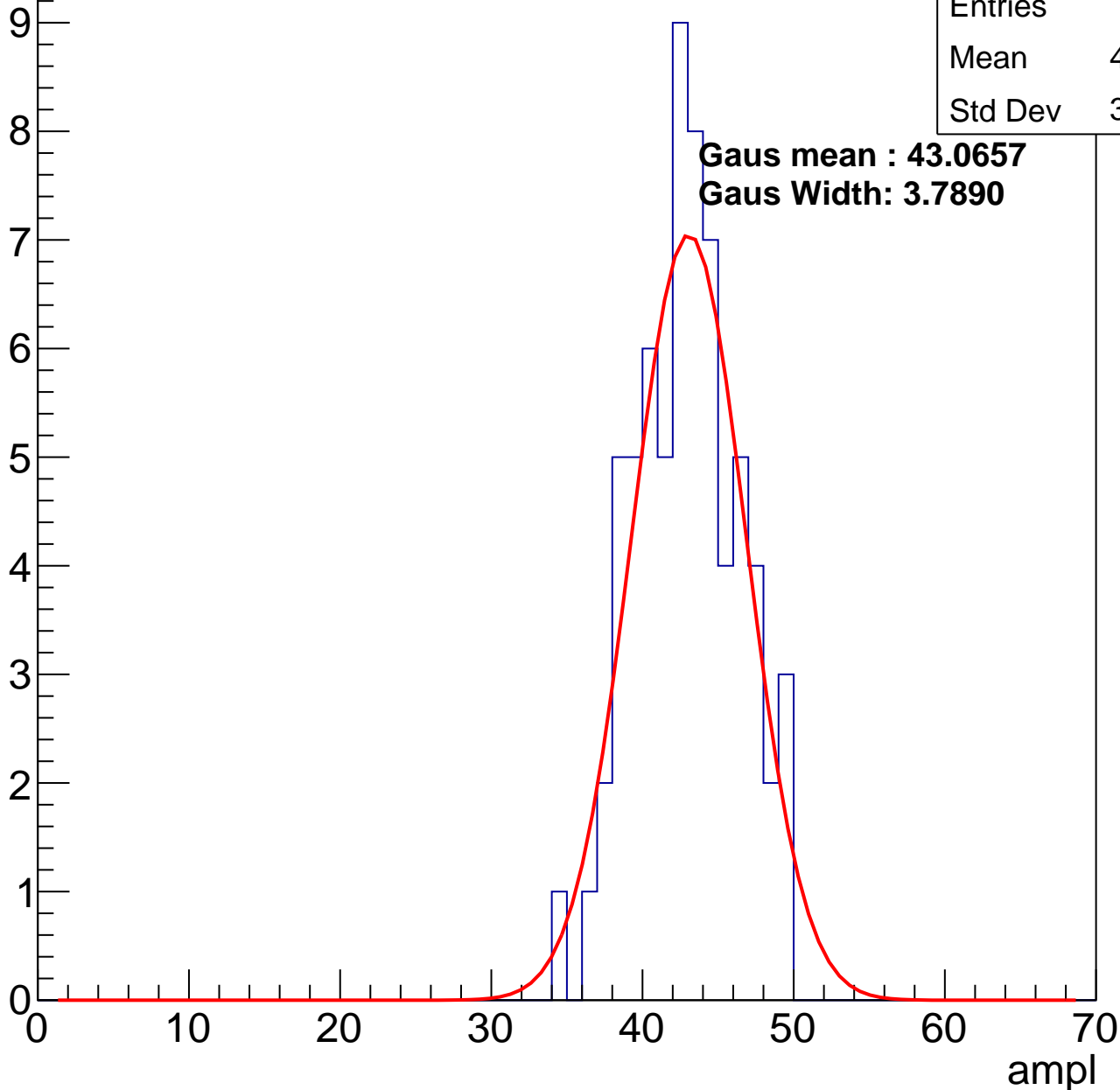
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.46
Std Dev	3.369

Gaus mean : 43.0657

Gaus Width: 3.7890

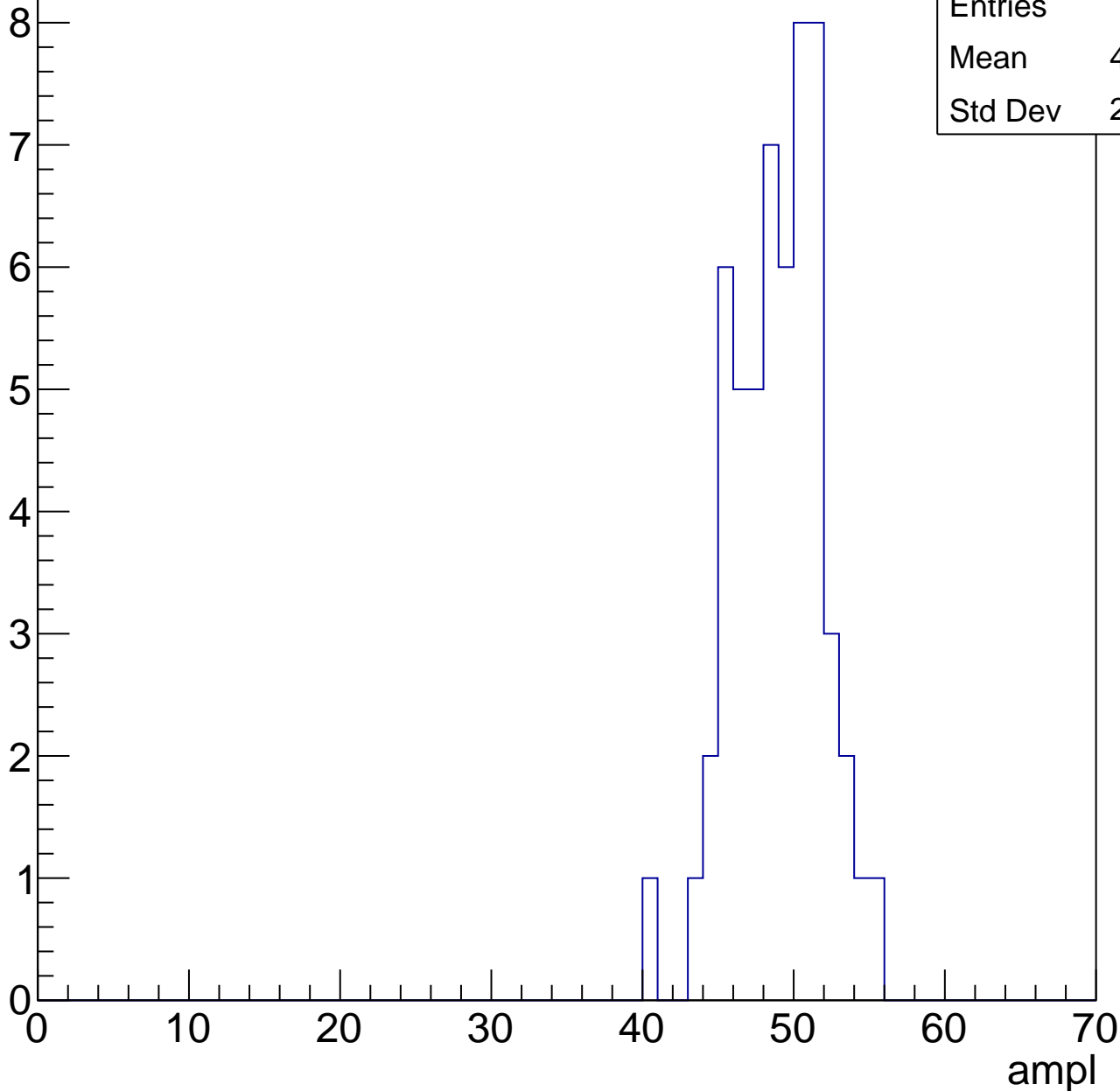


B1L102S, U11-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.48
Std Dev	2.928

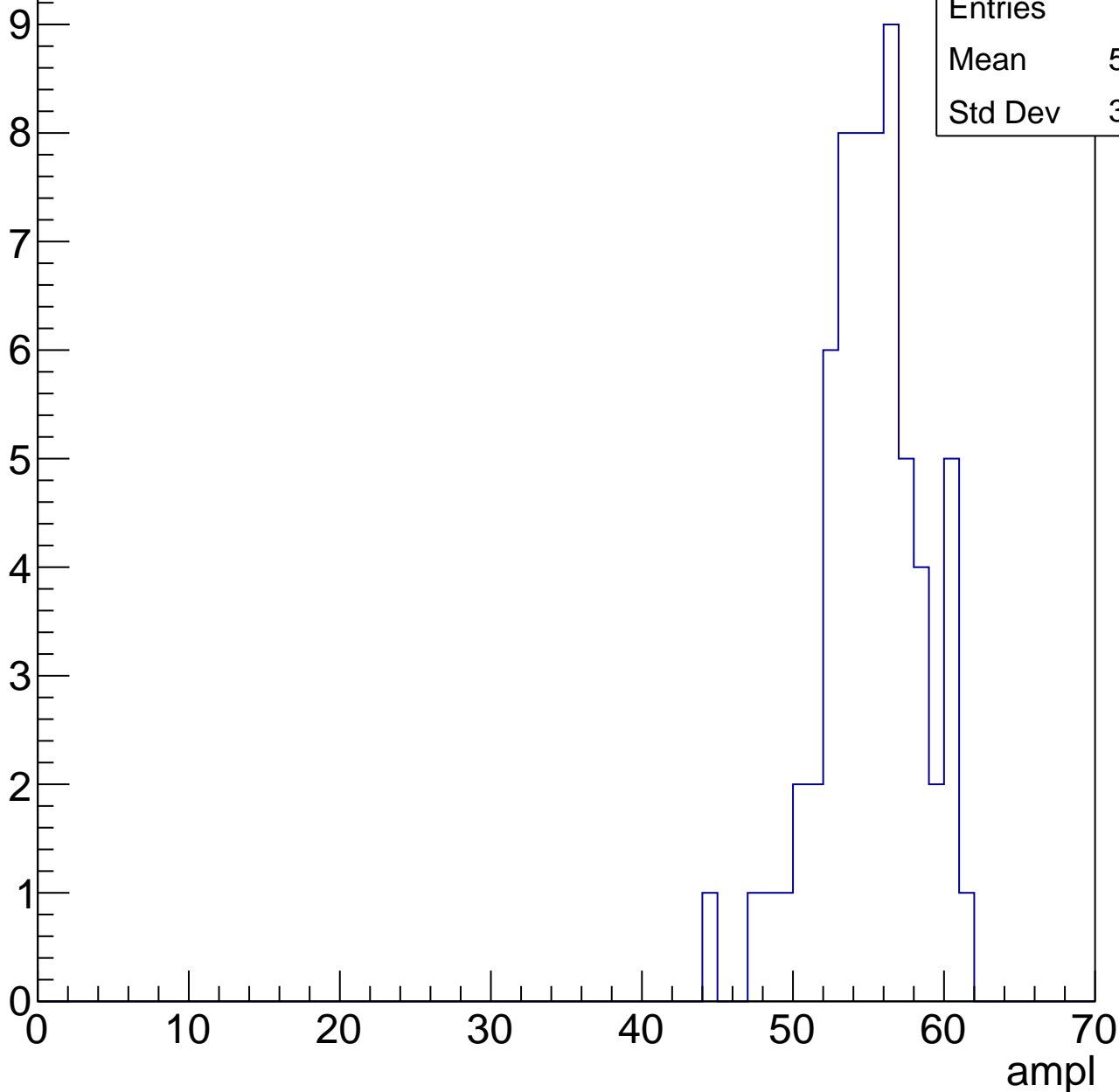


B1L102S, U11-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

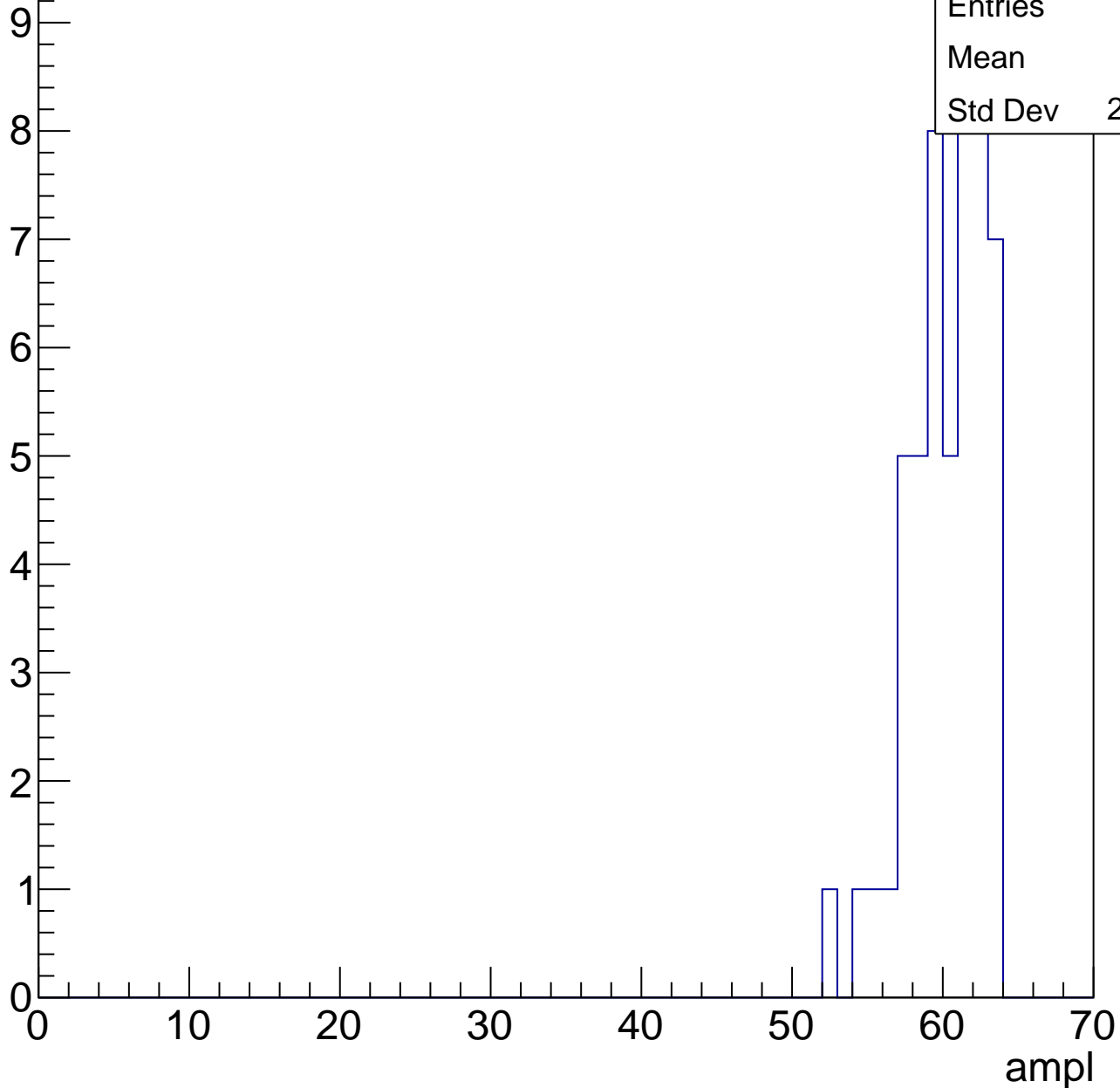
Entries	64
Mean	54.66
Std Dev	3.299



B1L102S, U11-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

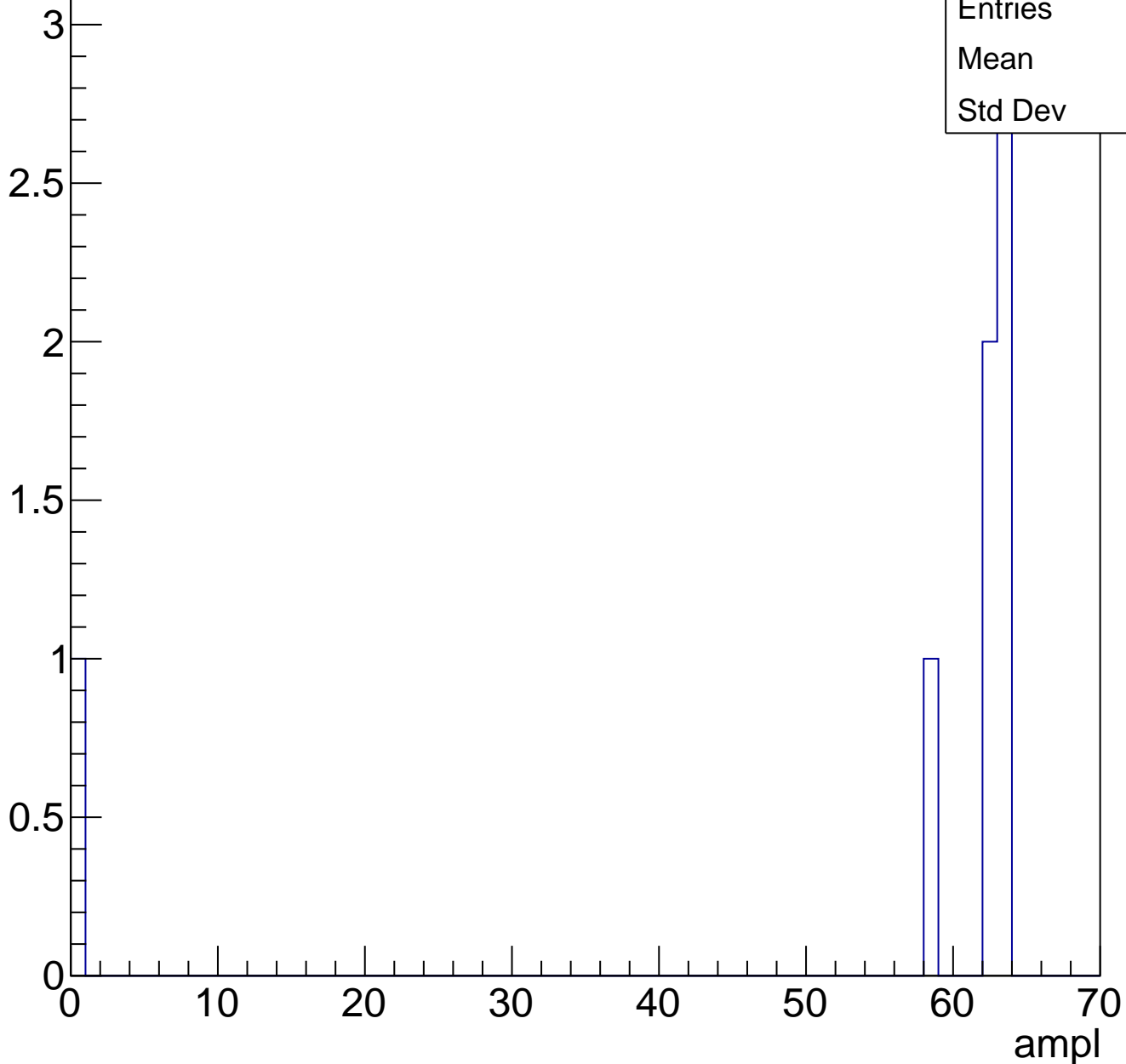
Entry



B1L102S, U11-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch33, adc0

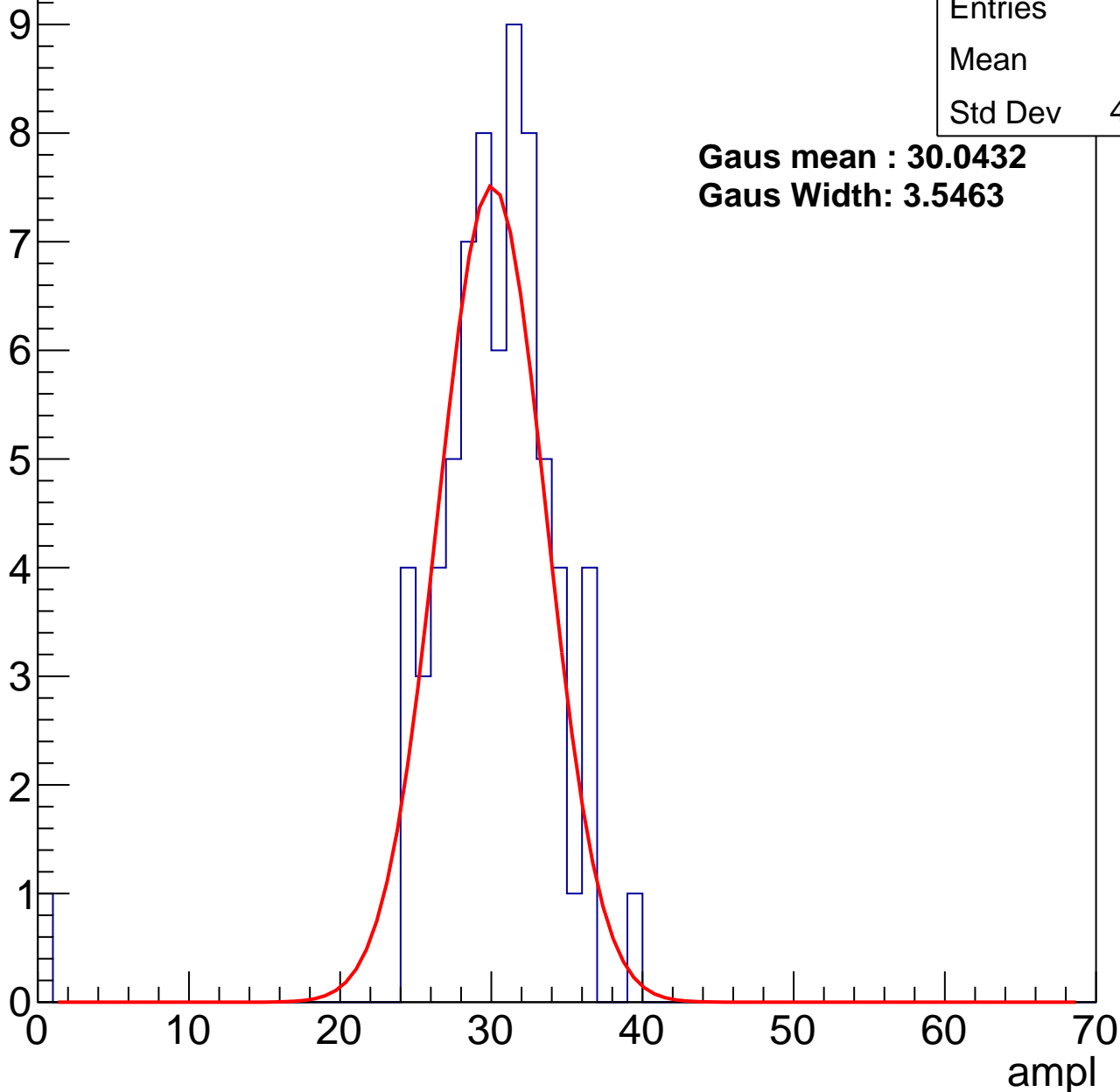
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.6
Std Dev	4.859

Gaus mean : 30.0432

Gaus Width: 3.5463



B1L102S, U11-ch33, adc1

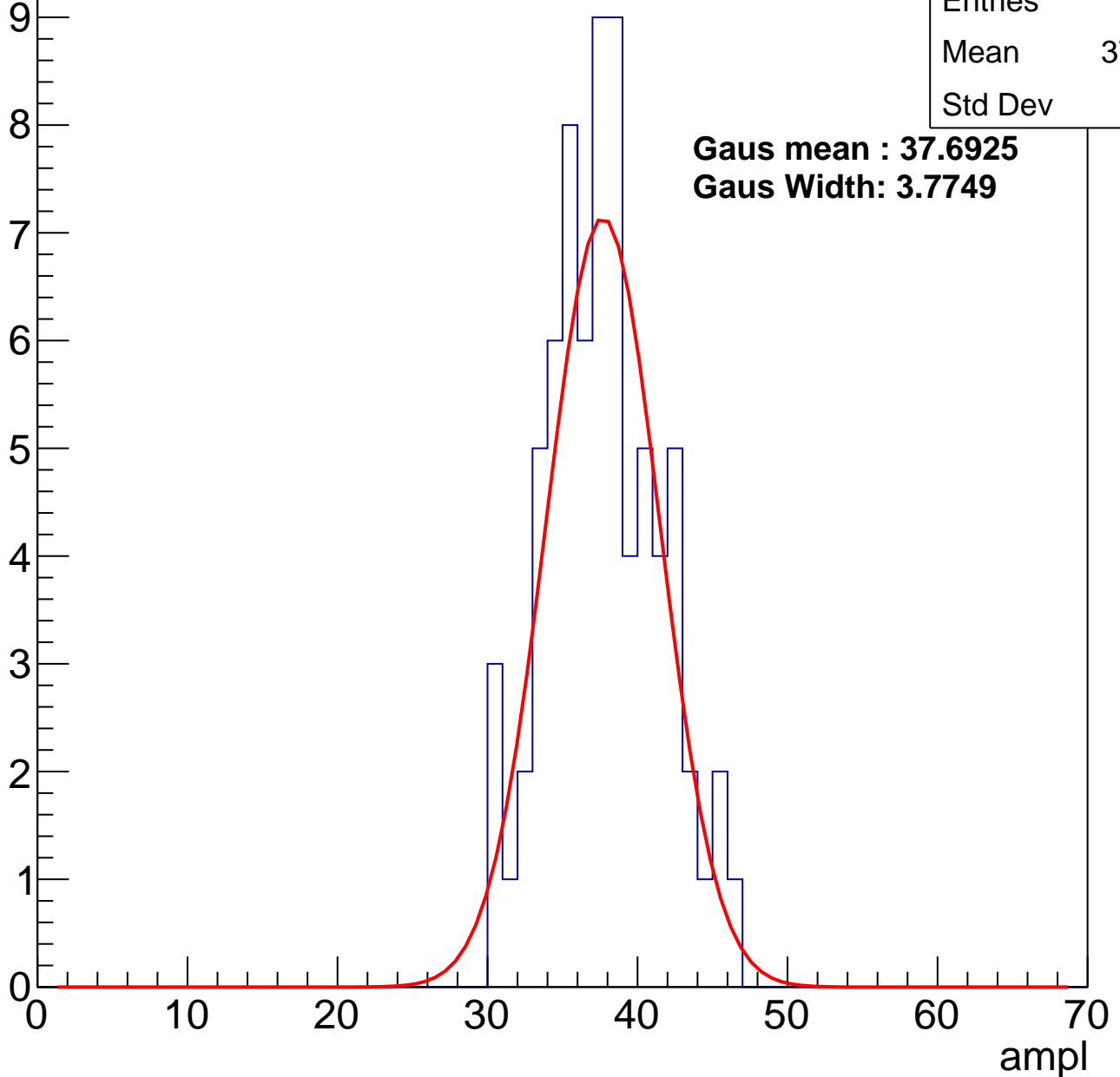
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.27
Std Dev	3.68

Gaus mean : 37.6925

Gaus Width: 3.7749



B1L102S, U11-ch33, adc2

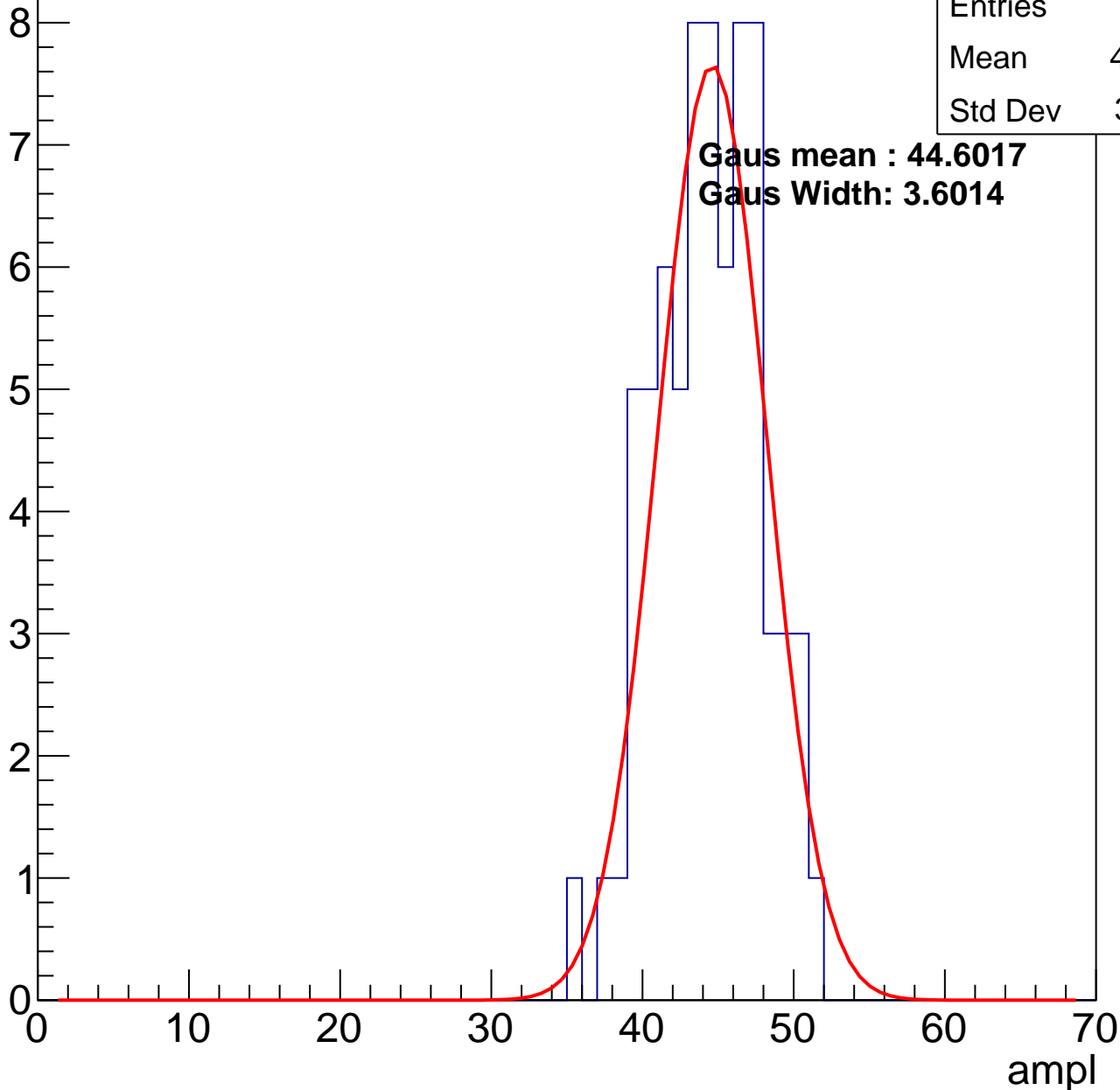
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.93
Std Dev	3.421

Gaus mean : 44.6017

Gaus Width: 3.6014

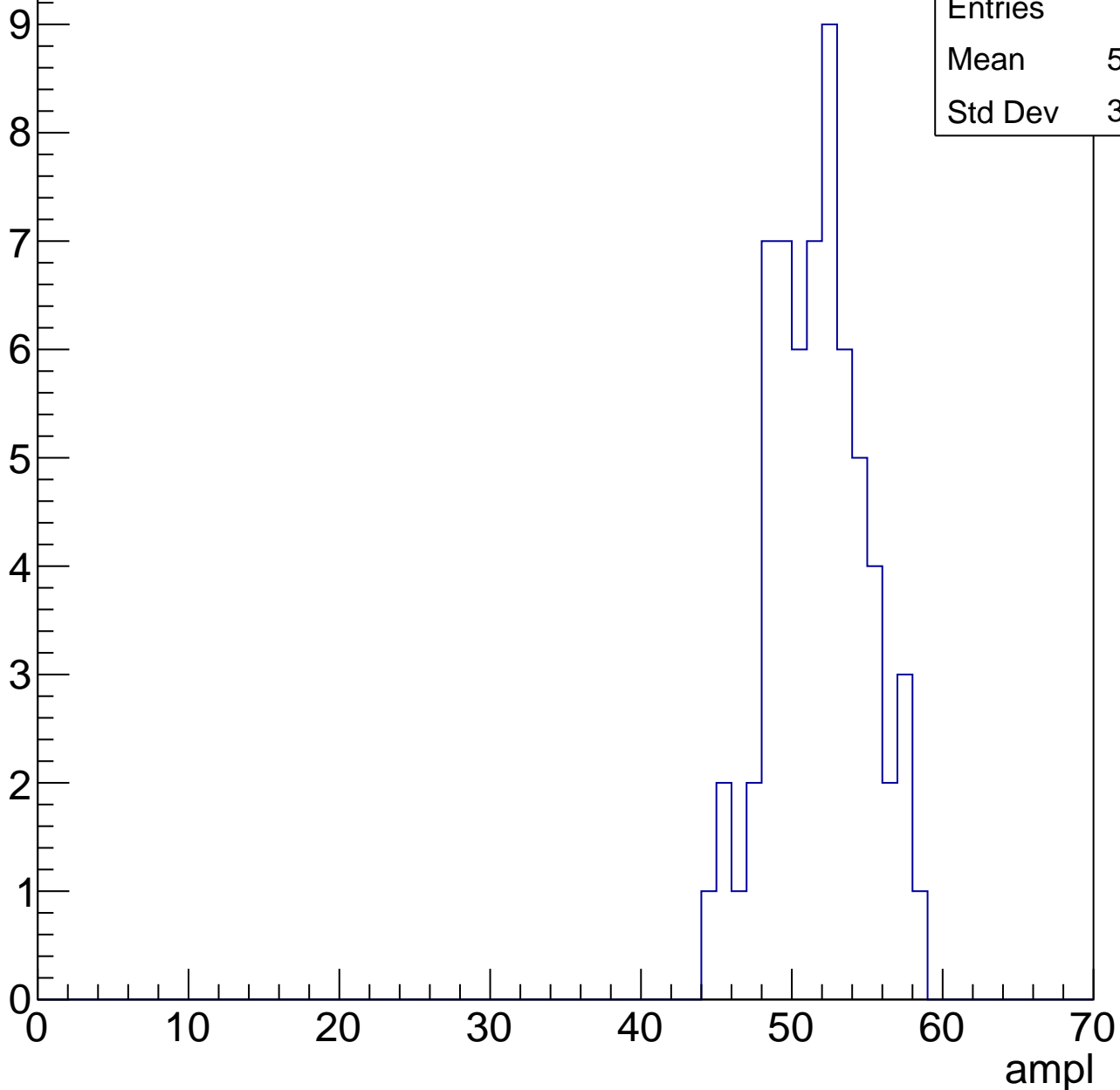


B1L102S, U11-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	51.22
Std Dev	3.139



B1L102S, U11-ch33, adc4

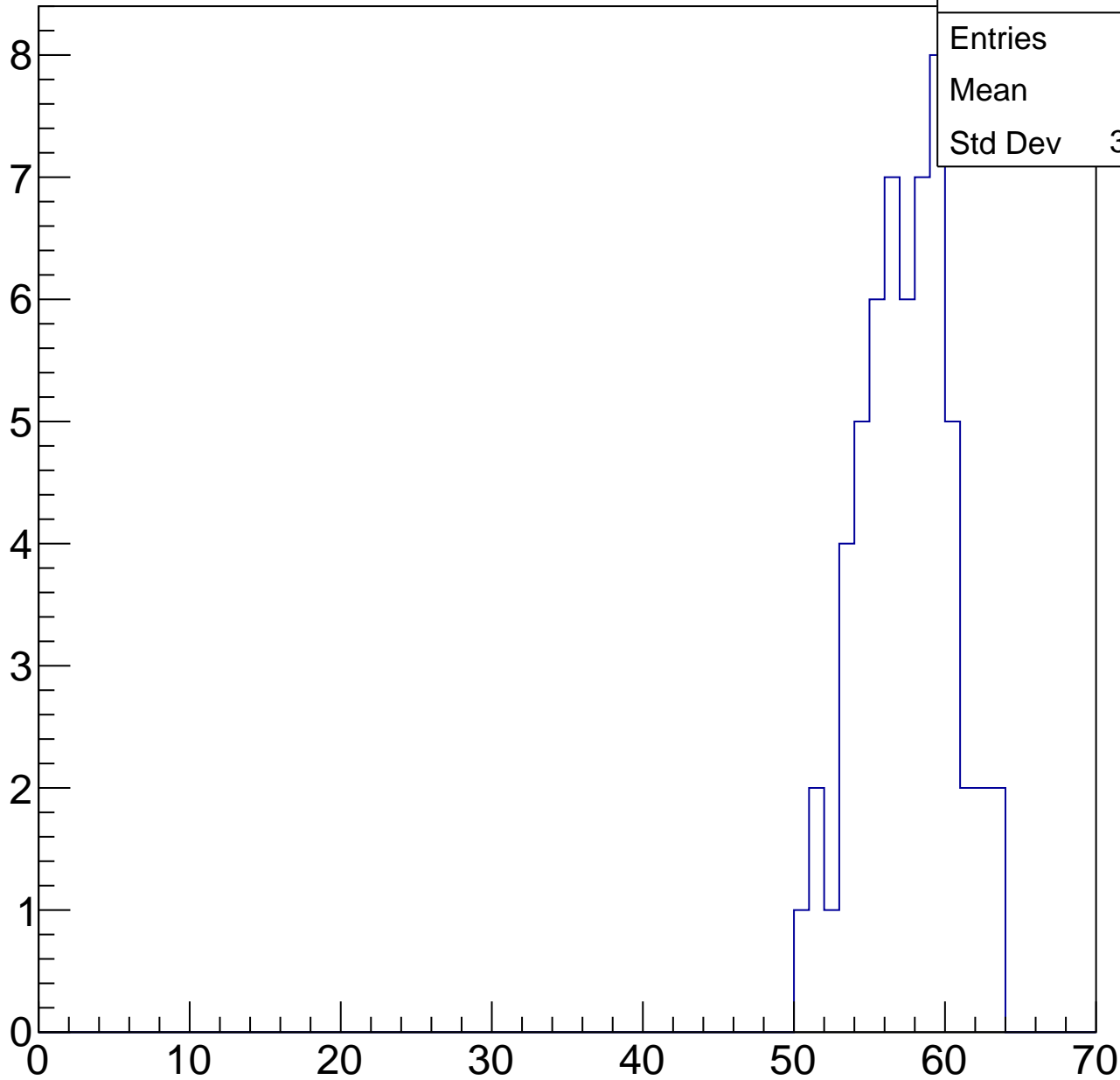
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	58
Mean	56.9
Std Dev	3.004

ampl

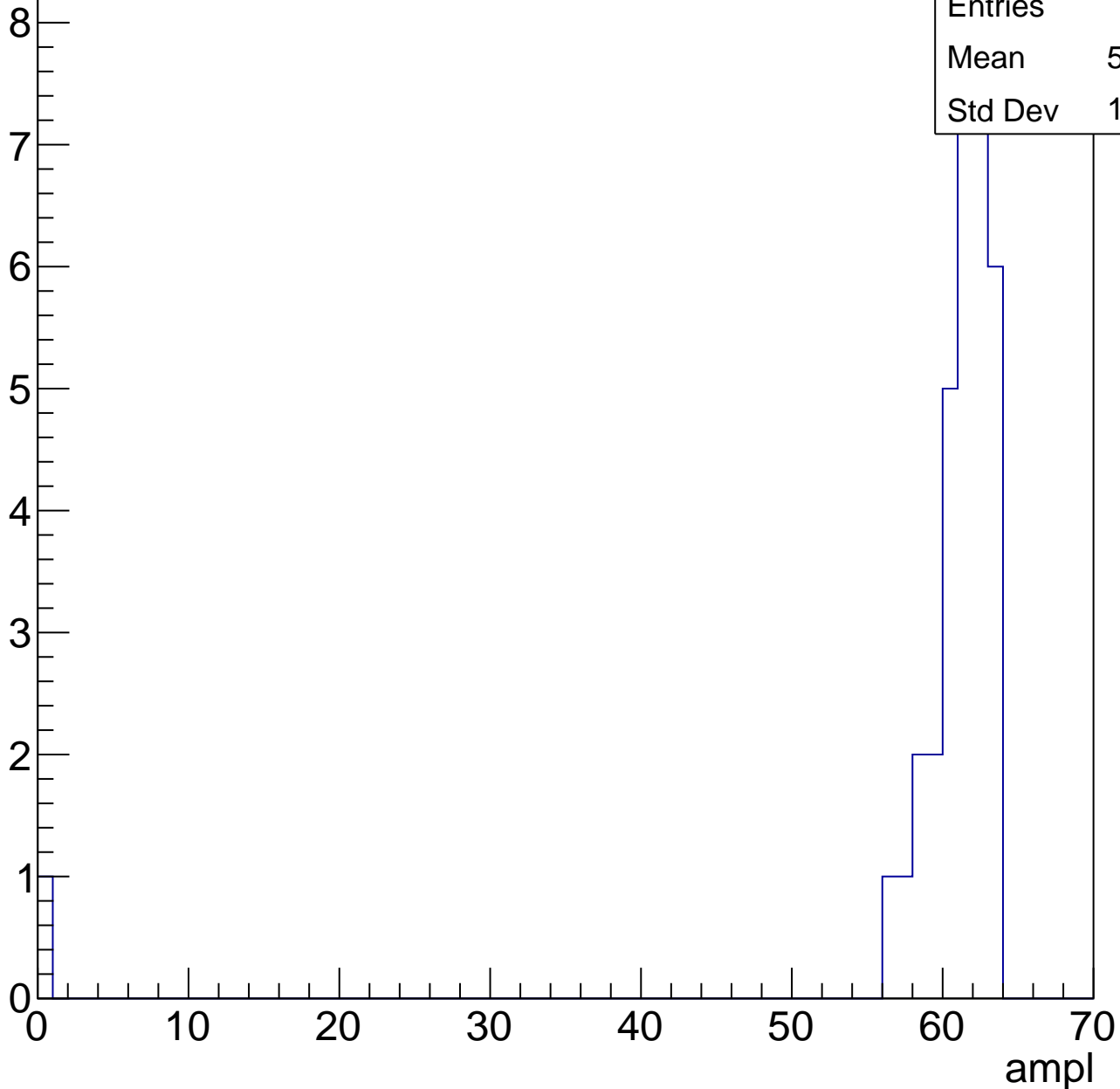


B1L102S, U11-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59.09
Std Dev	10.43



B1L102S, U11-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch34, adc0

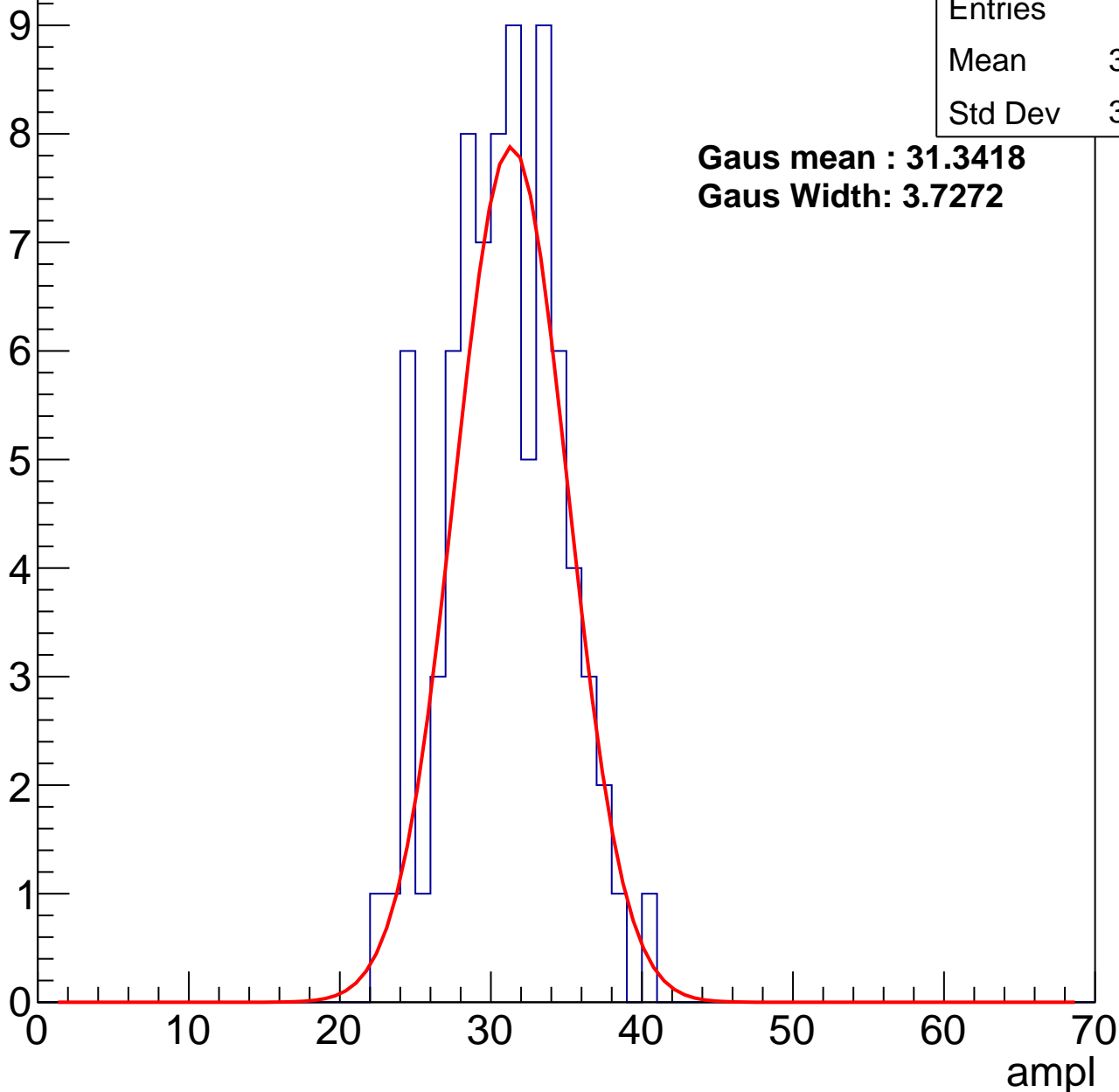
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	30.38
Std Dev	3.783

Gaus mean : 31.3418

Gaus Width: 3.7272



B1L102S, U11-ch34, adc1

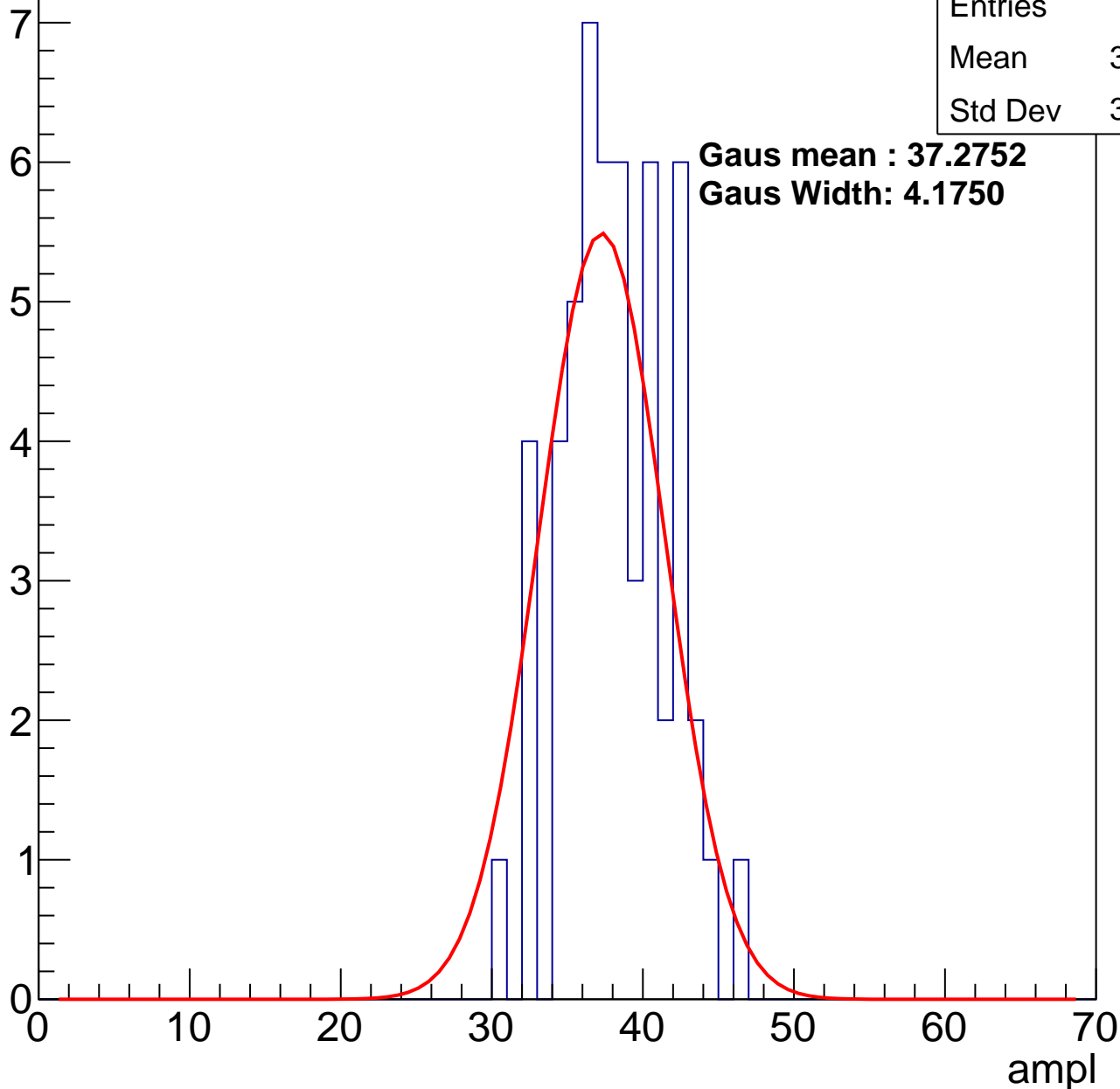
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.74
Std Dev	3.438

Gaus mean : 37.2752

Gaus Width: 4.1750



B1L102S, U11-ch34, adc2

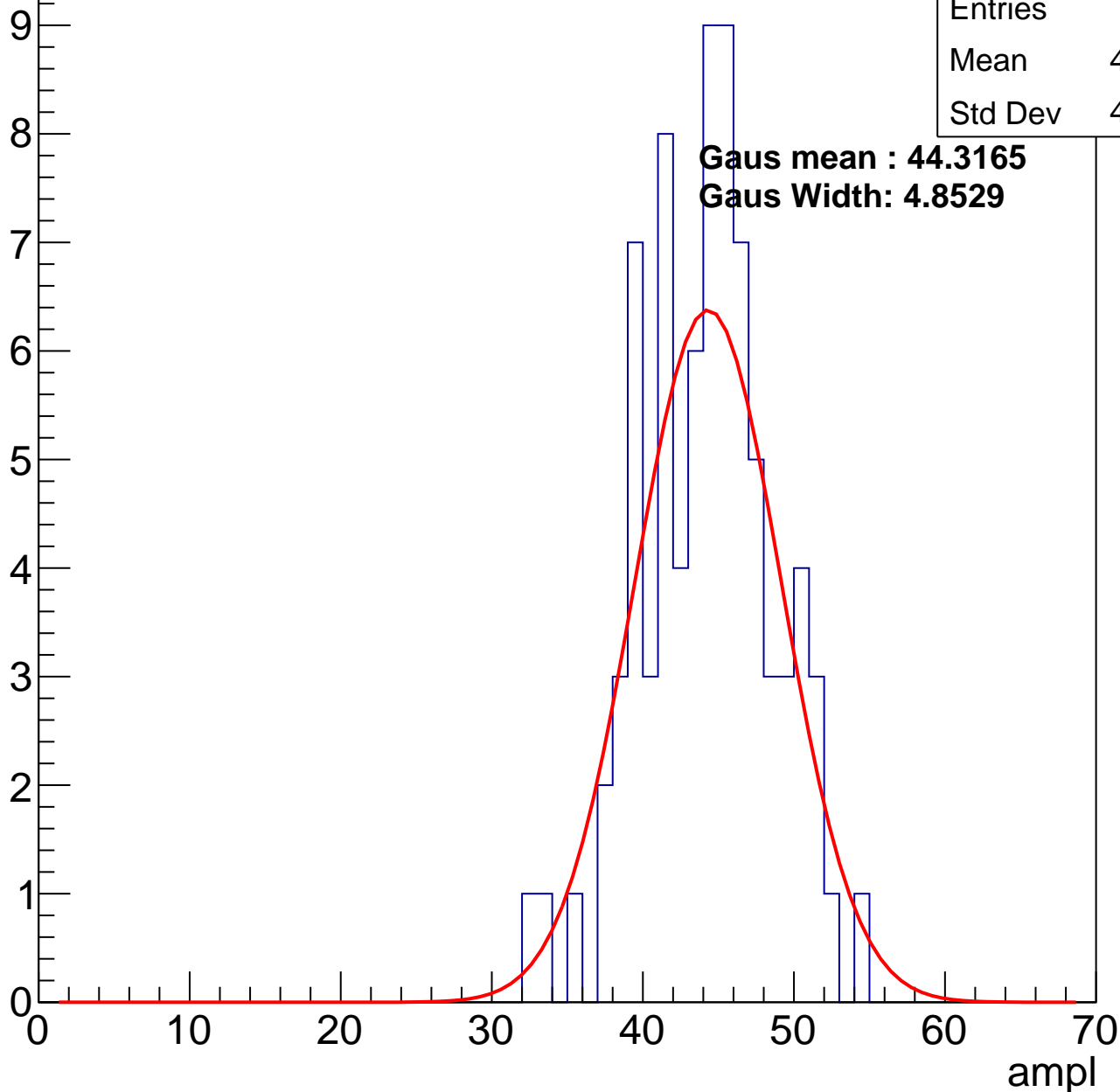
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	43.74
Std Dev	4.337

Gaus mean : 44.3165

Gaus Width: 4.8529

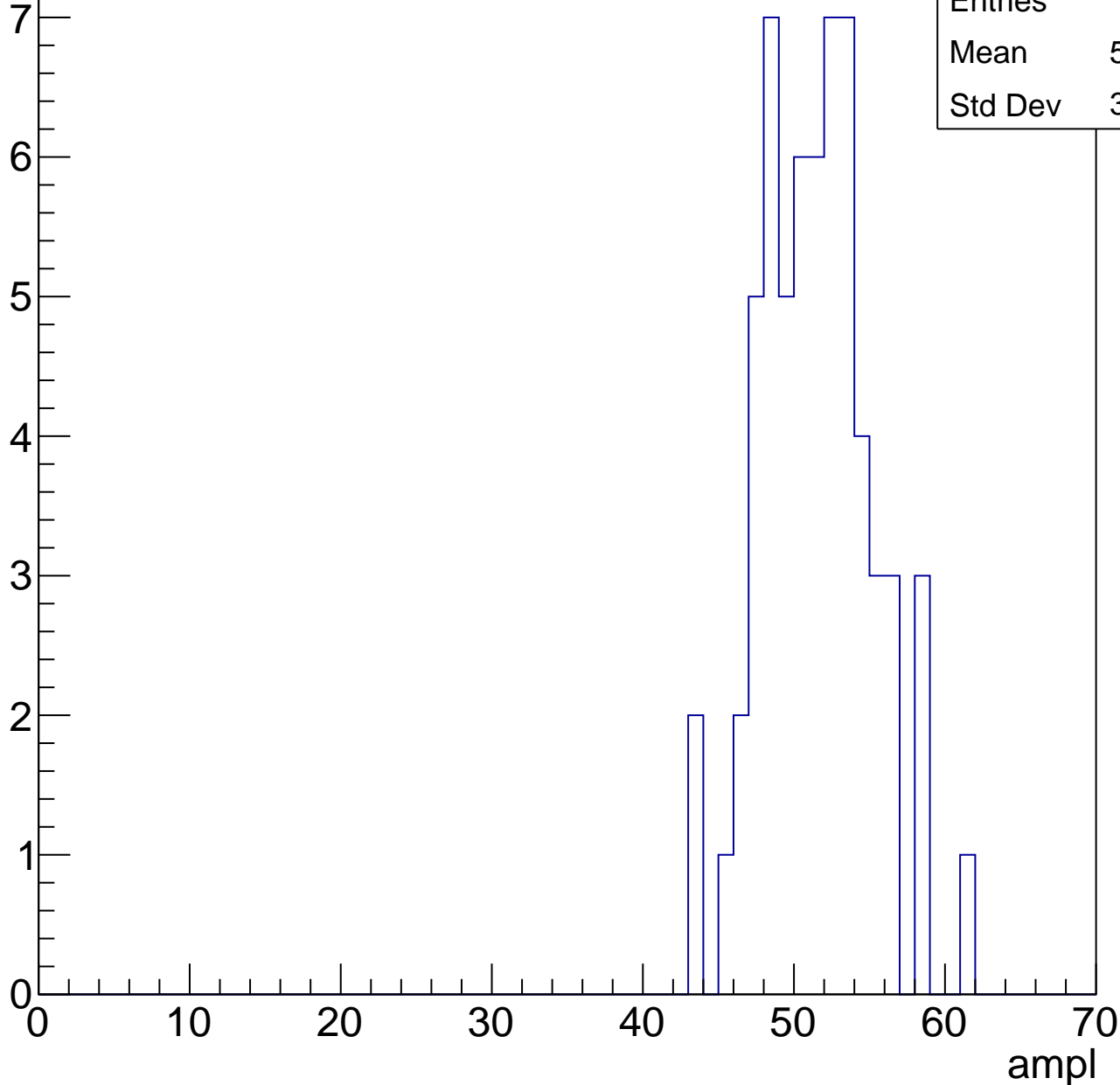


B1L102S, U11-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

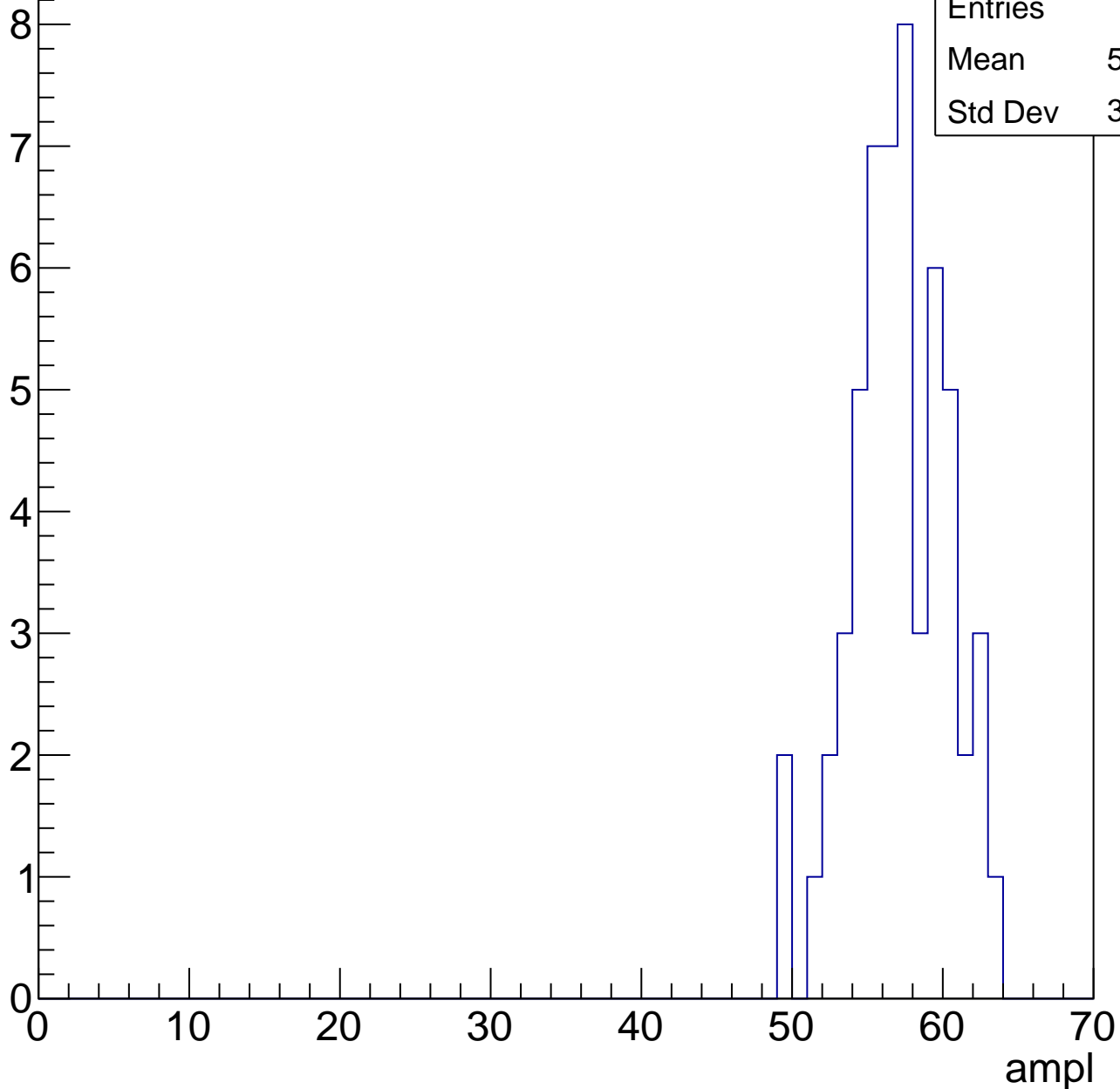
Entries	62
Mean	51.03
Std Dev	3.645



B1L102S, U11-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

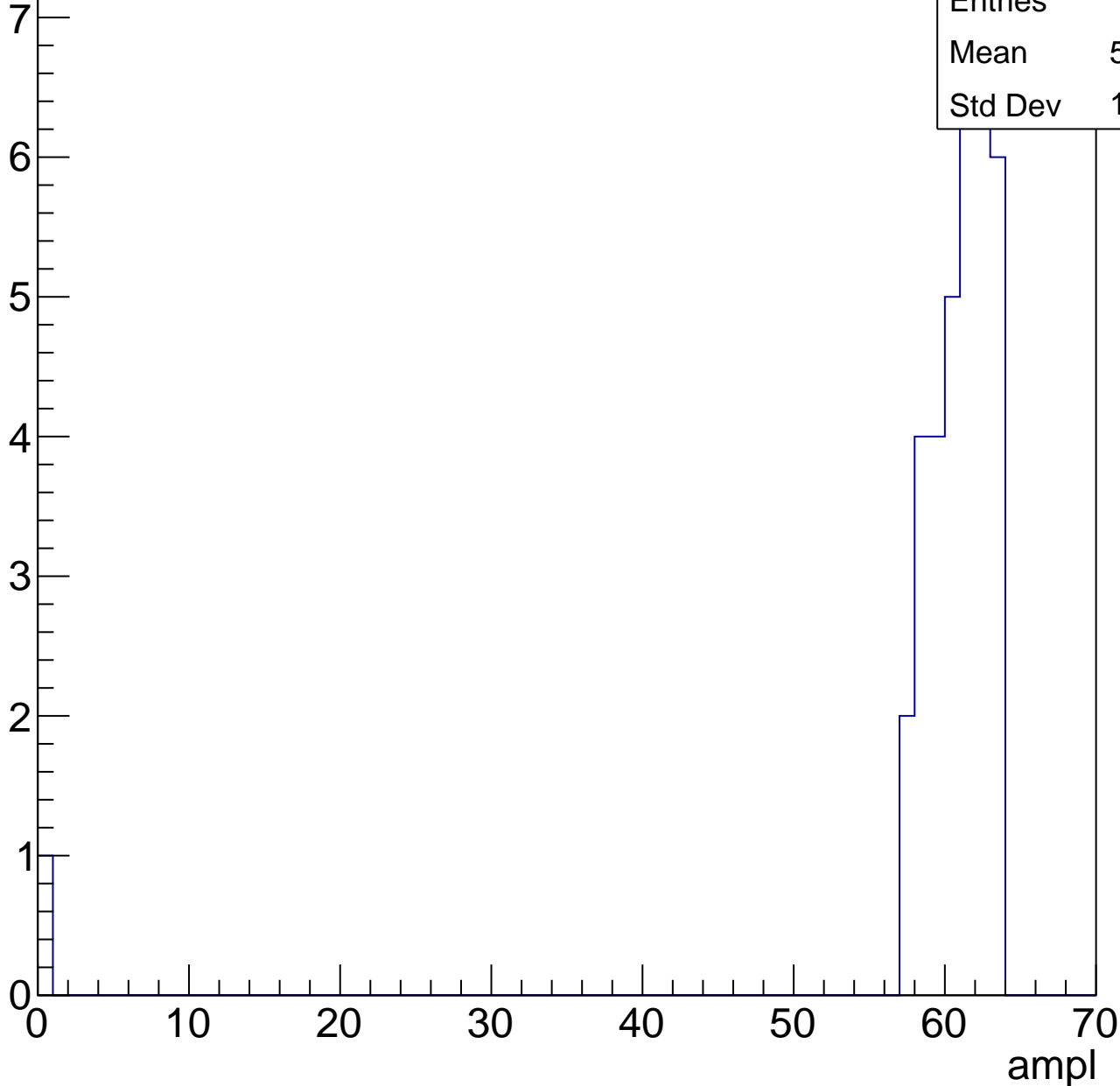


B1L102S, U11-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.92
Std Dev	10.12



B1L102S, U11-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch35, adc0

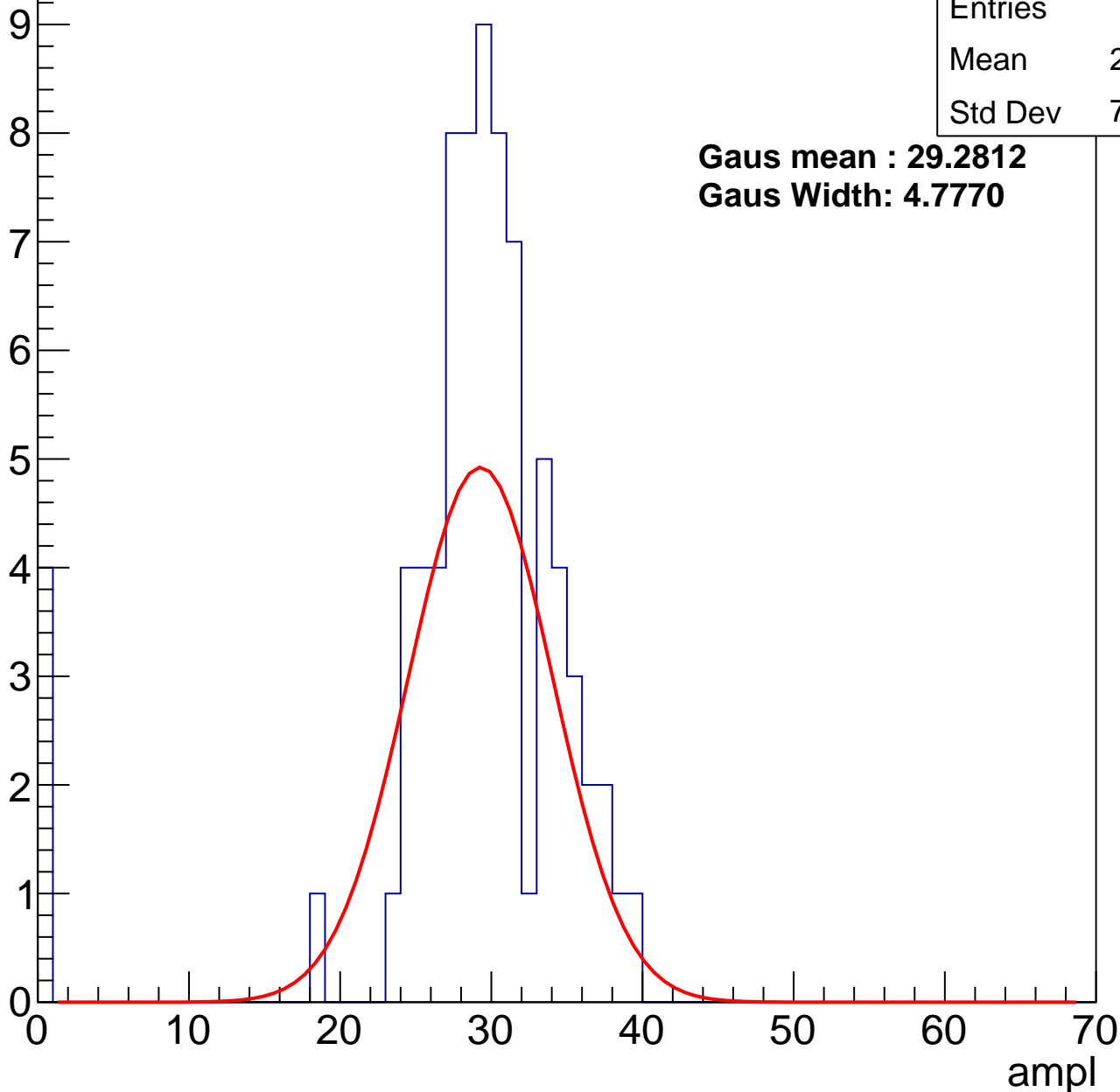
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.05
Std Dev	7.587

Gaus mean : 29.2812

Gaus Width: 4.7770



B1L102S, U11-ch35, adc1

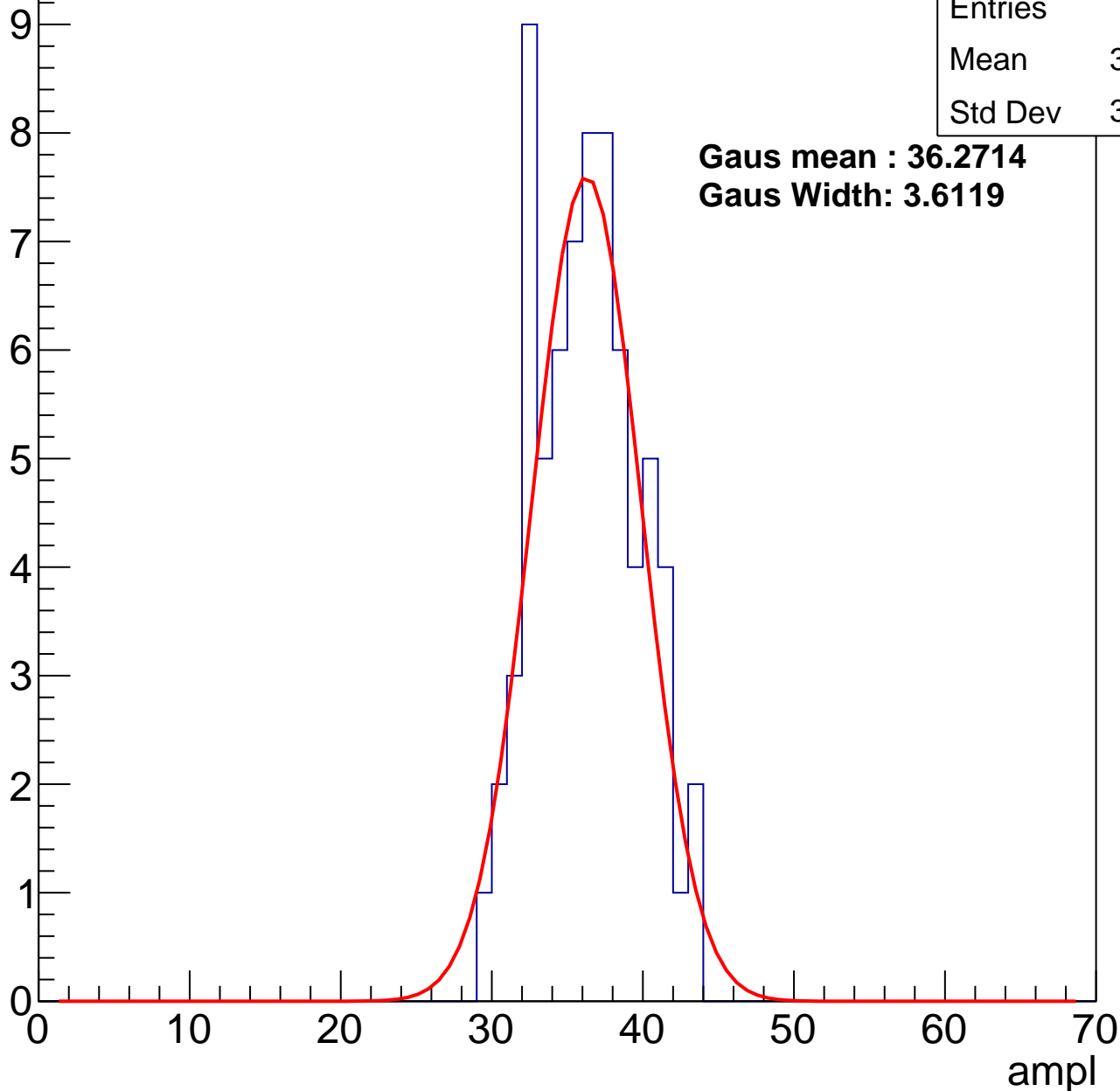
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.83
Std Dev	3.348

Gaus mean : 36.2714

Gaus Width: 3.6119



B1L102S, U11-ch35, adc2

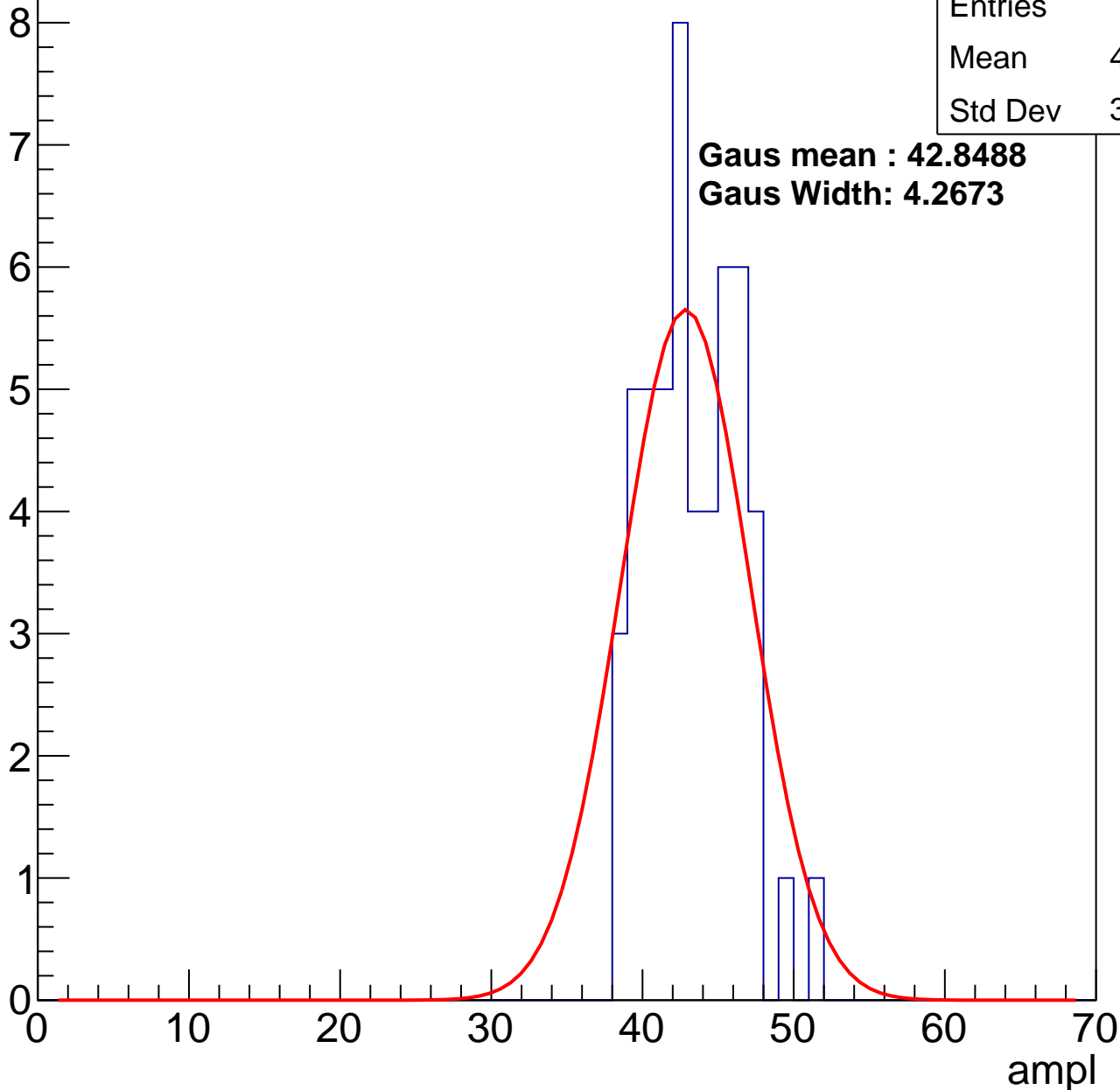
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.92
Std Dev	3.018

Gaus mean : 42.8488

Gaus Width: 4.2673



B1L102S, U11-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	48.62
Std Dev	3.459

Entry

10

8

6

4

2

0

0

10

20

30

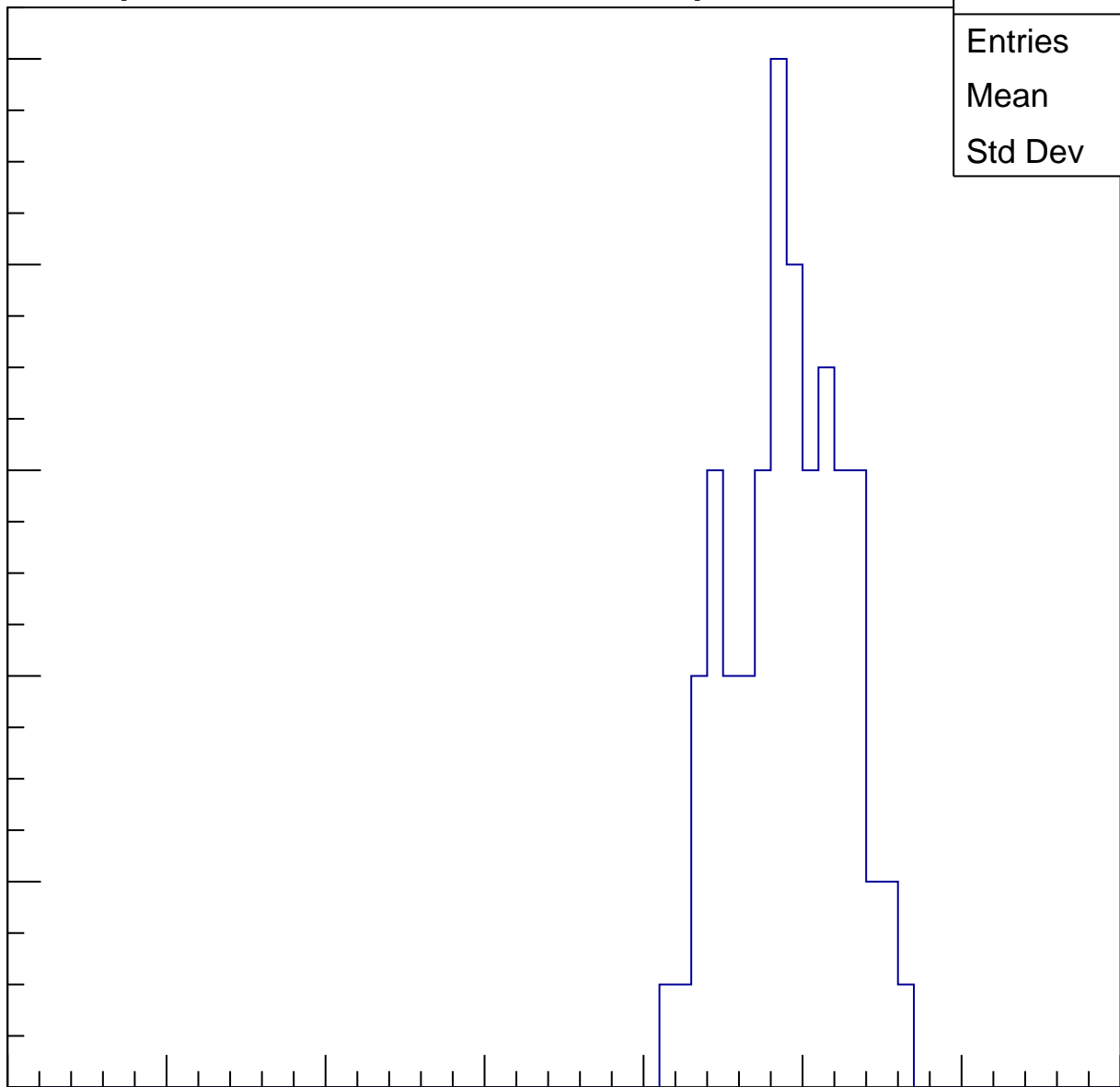
40

50

60

70

ampl

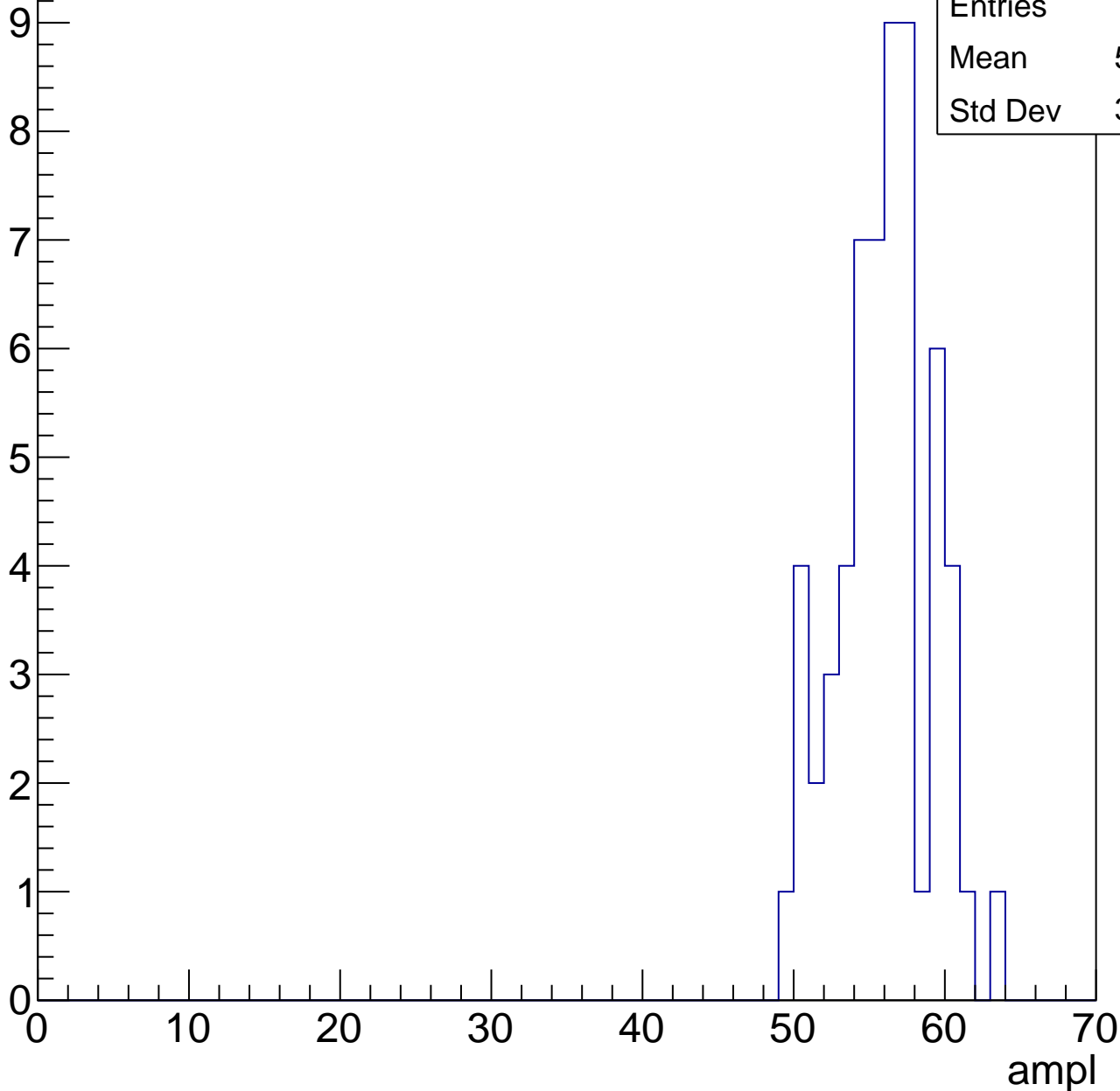


B1L102S, U11-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.51
Std Dev	3.061

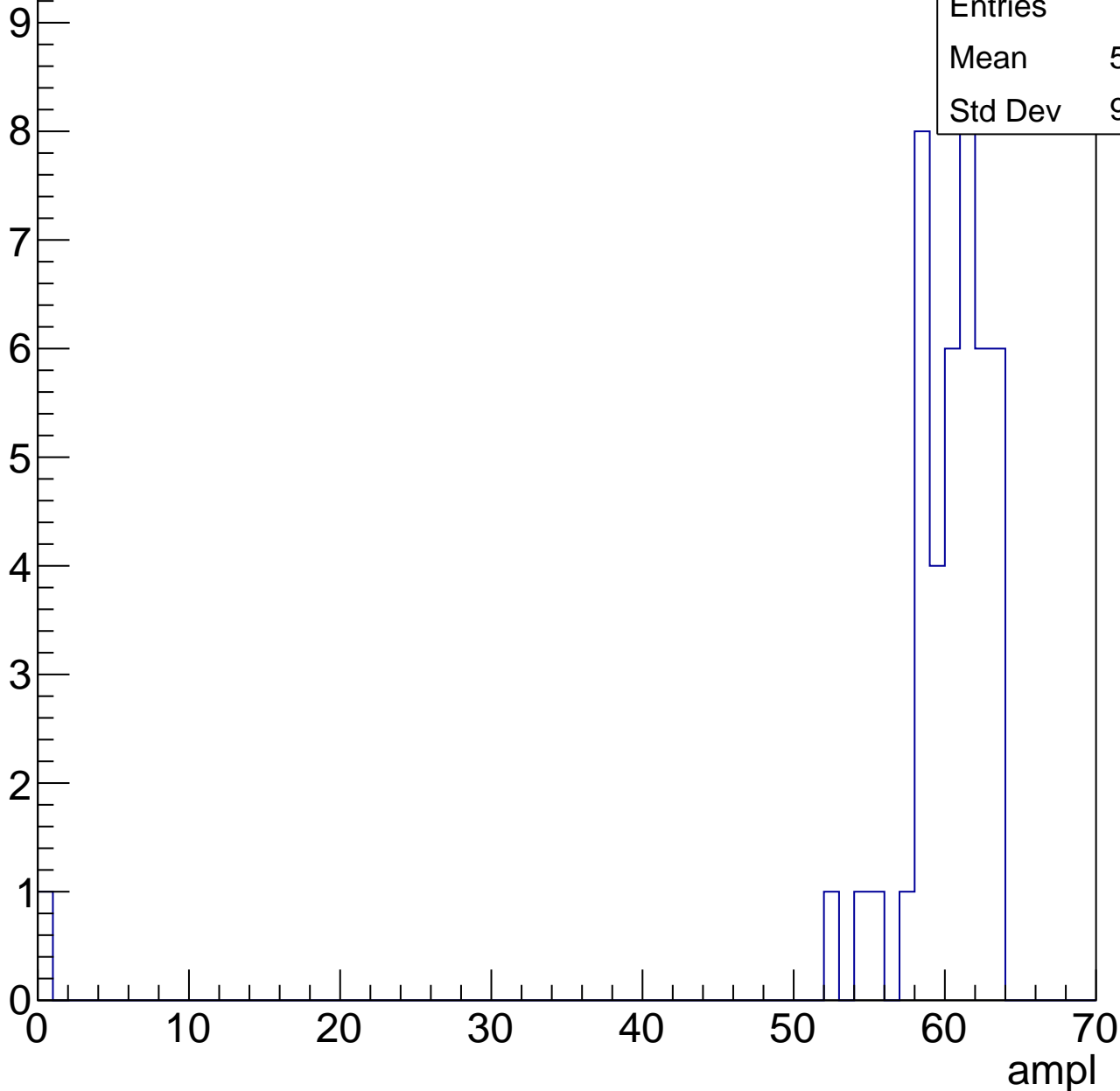


B1L102S, U11-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

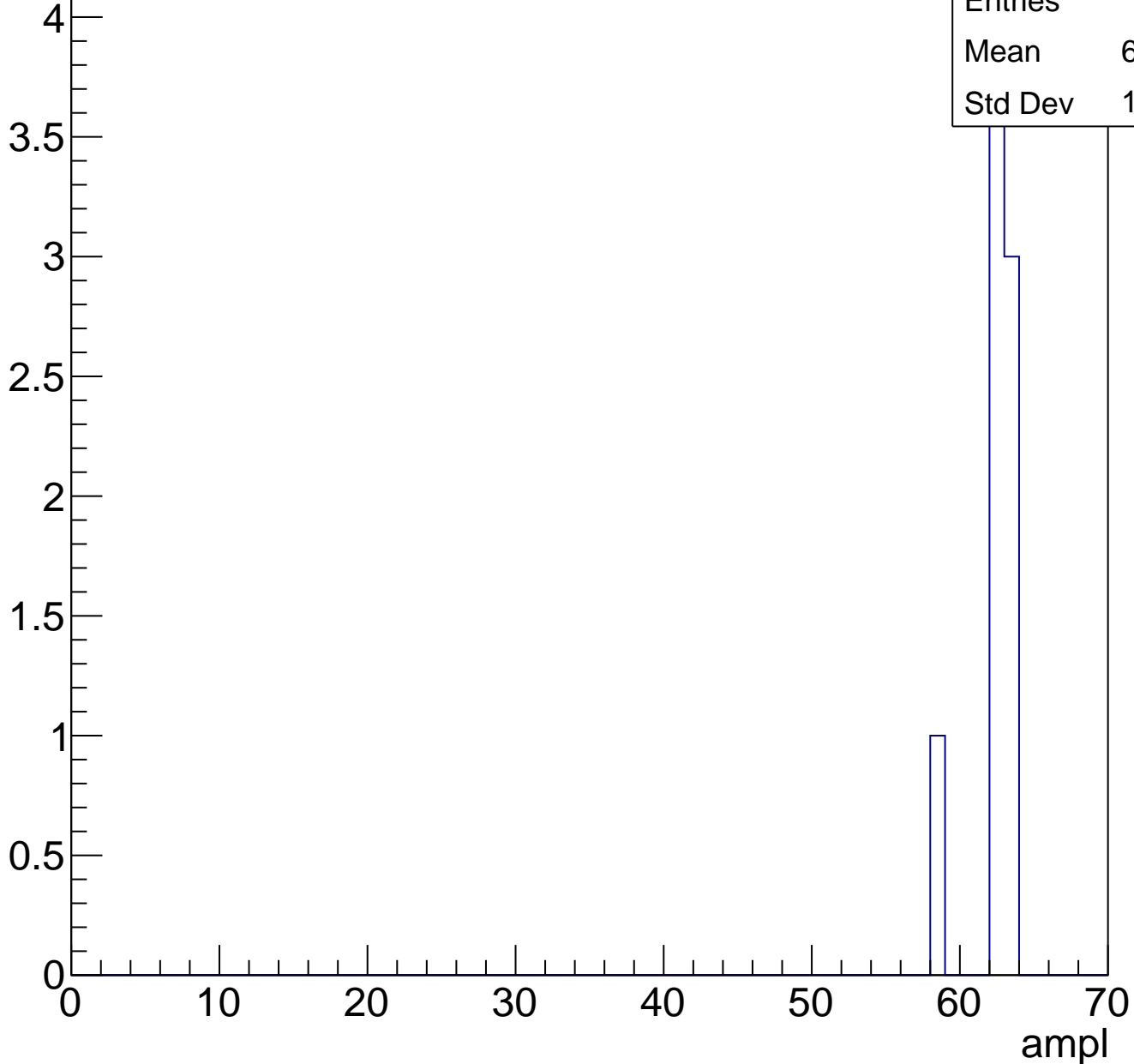
Entries	44
Mean	58.57
Std Dev	9.253



B1L102S, U11-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U11-ch36, adc0

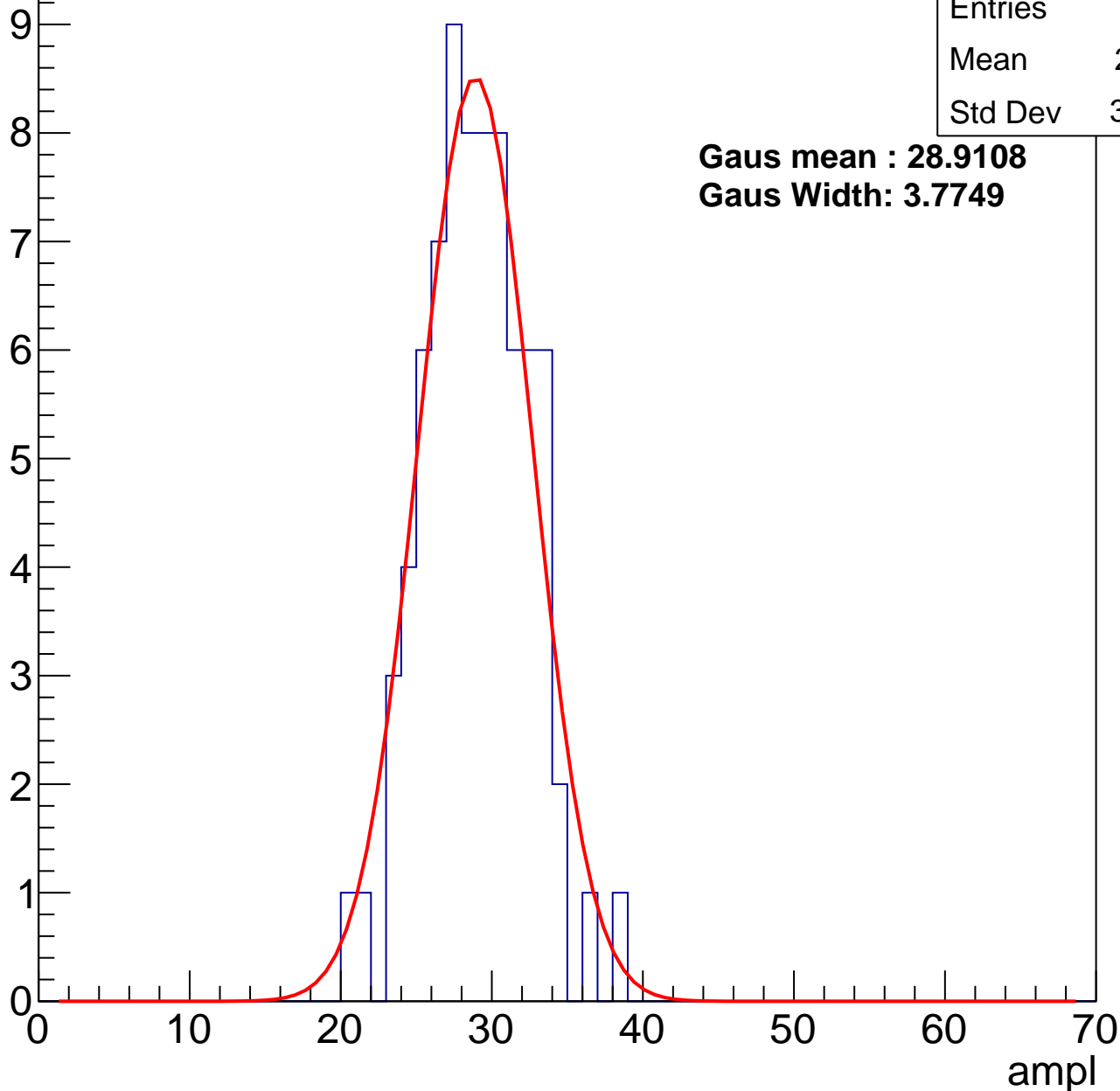
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.51
Std Dev	3.417

Gaus mean : 28.9108

Gaus Width: 3.7749



B1L102S, U11-ch36, adc1

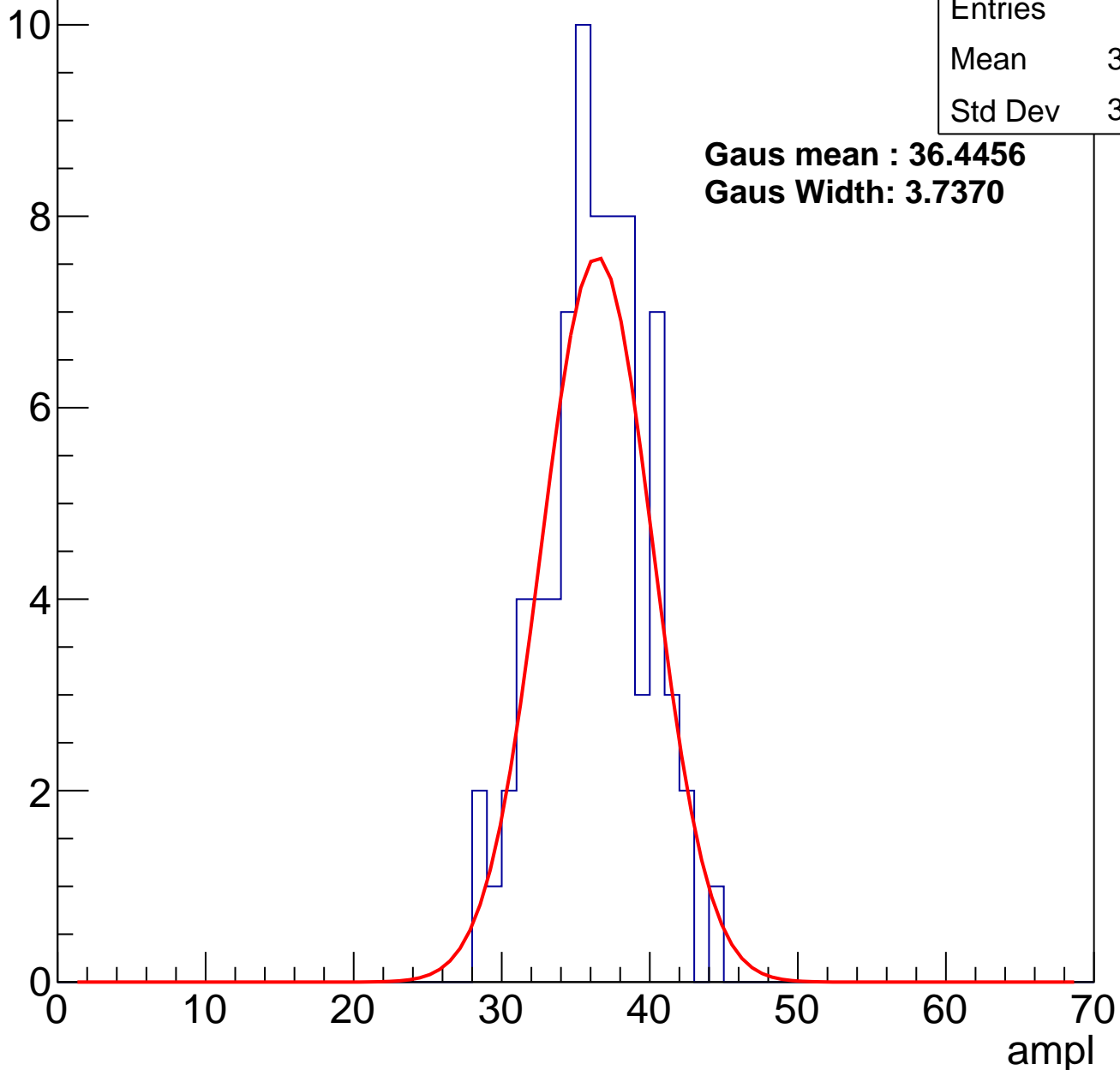
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	35.85
Std Dev	3.439

Gaus mean : 36.4456

Gaus Width: 3.7370

Entry



B1L102S, U11-ch36, adc2

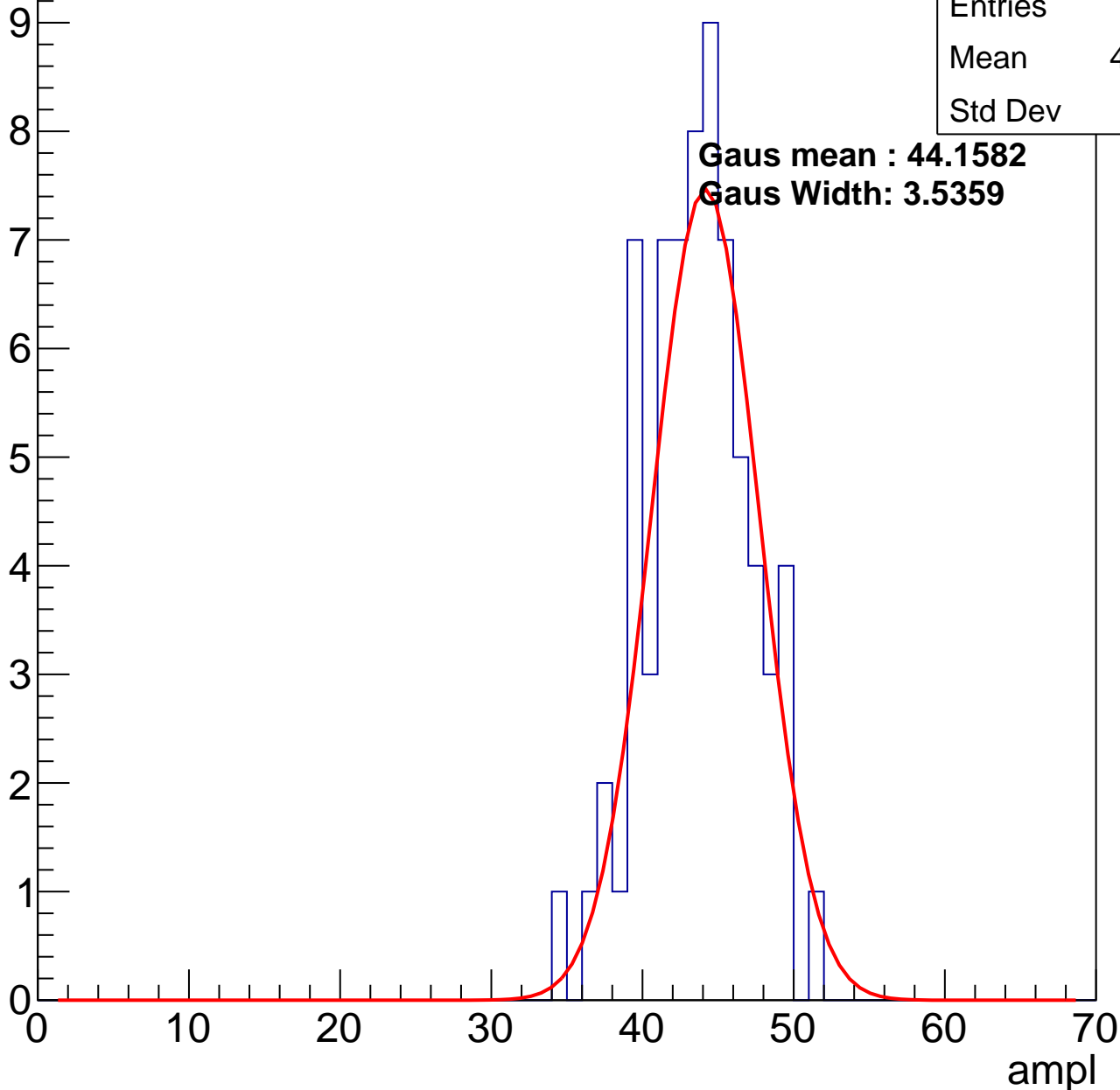
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.14
Std Dev	3.44

Gaus mean : 44.1582

Gaus Width: 3.5359

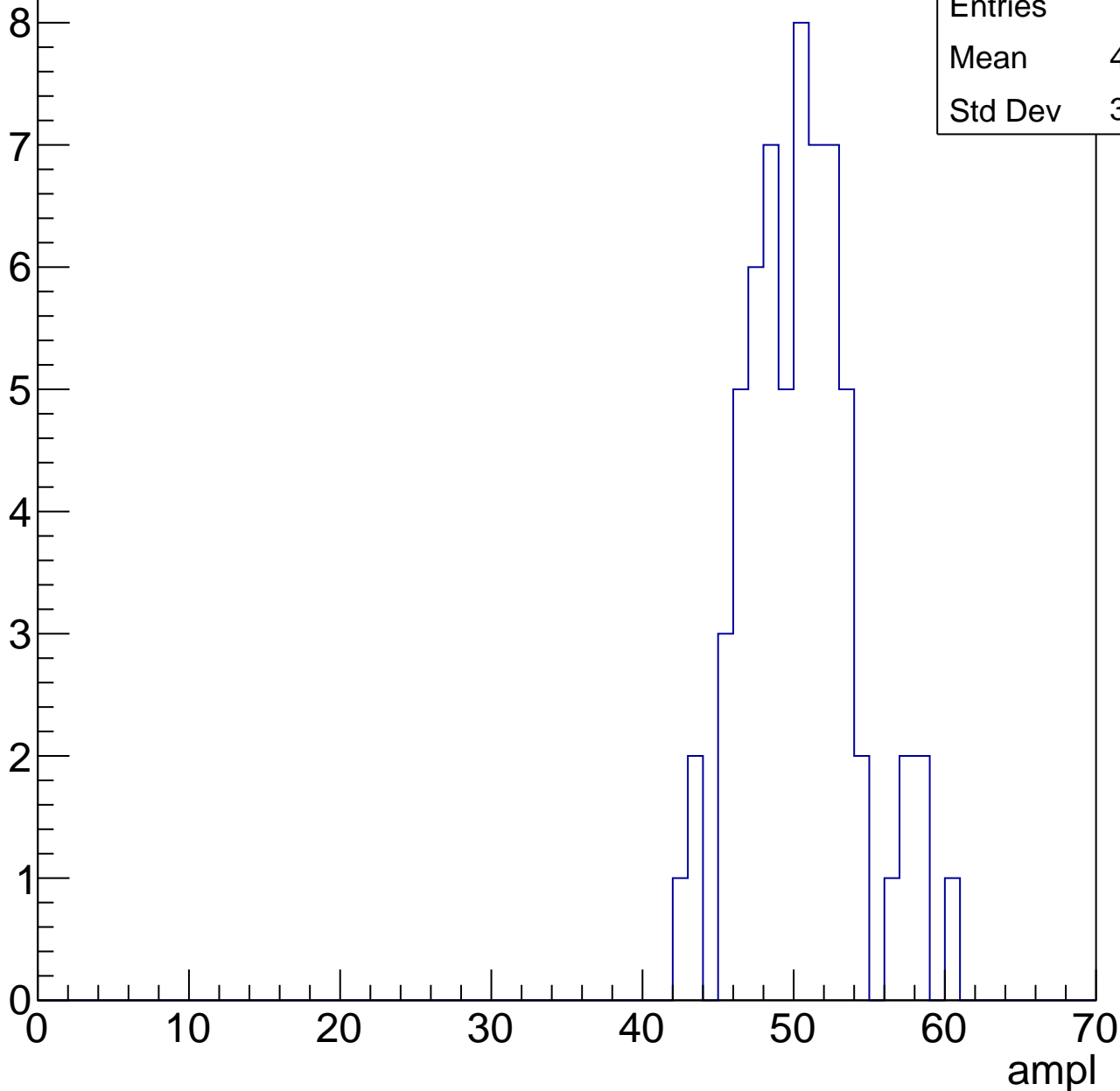


B1L102S, U11-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.94
Std Dev	3.703

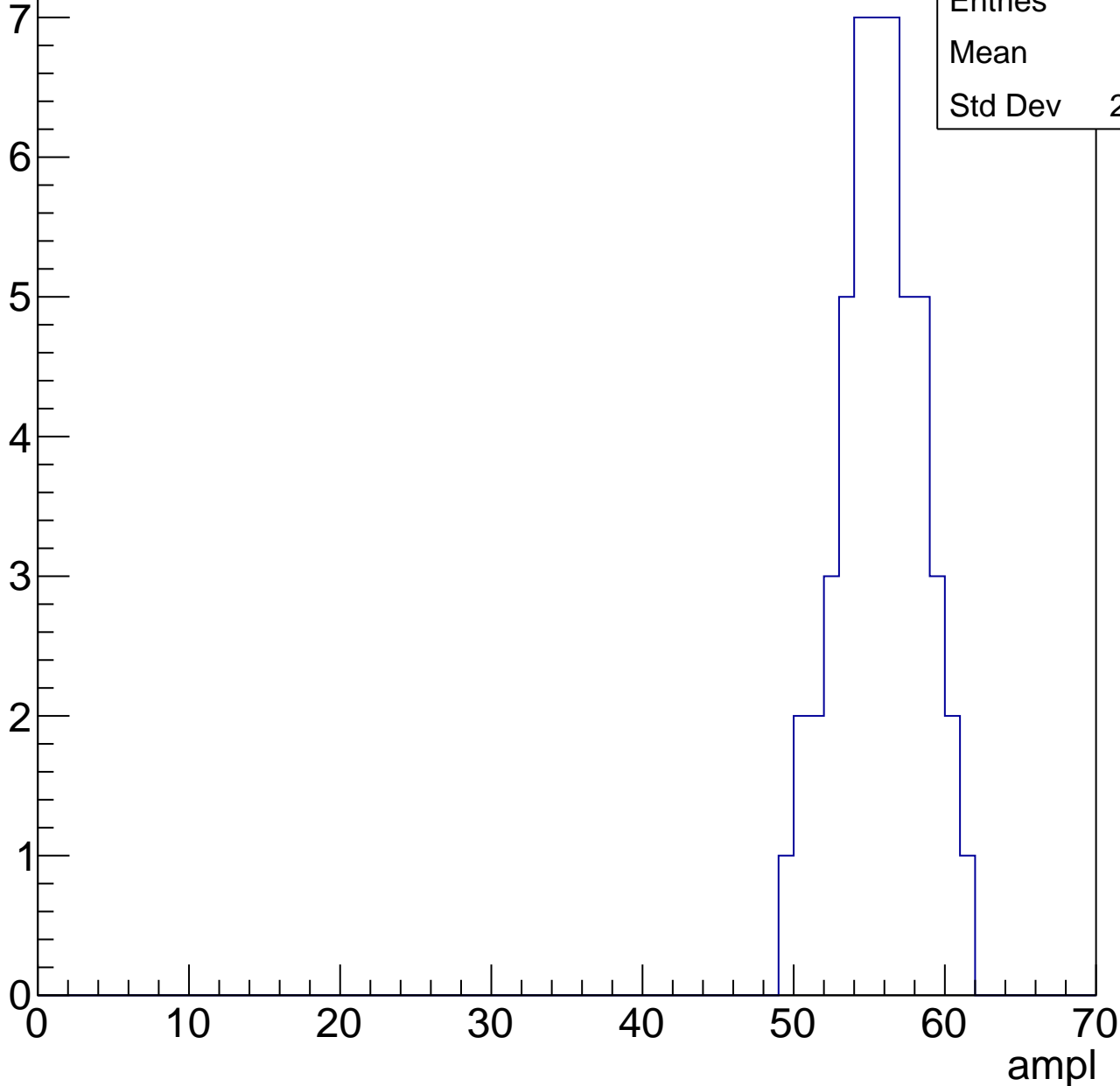


B1L102S, U11-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.2
Std Dev	2.742

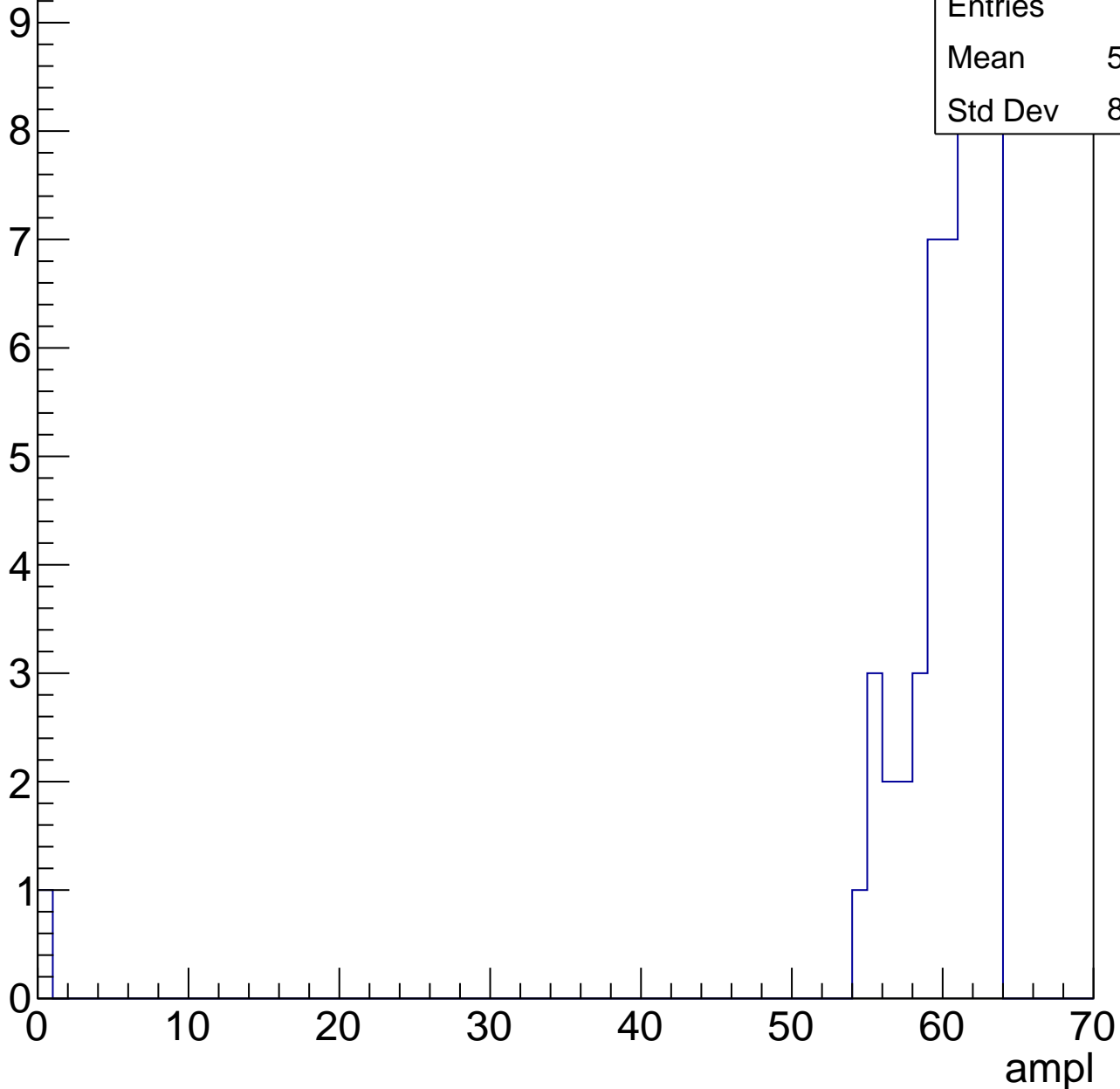


B1L102S, U11-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

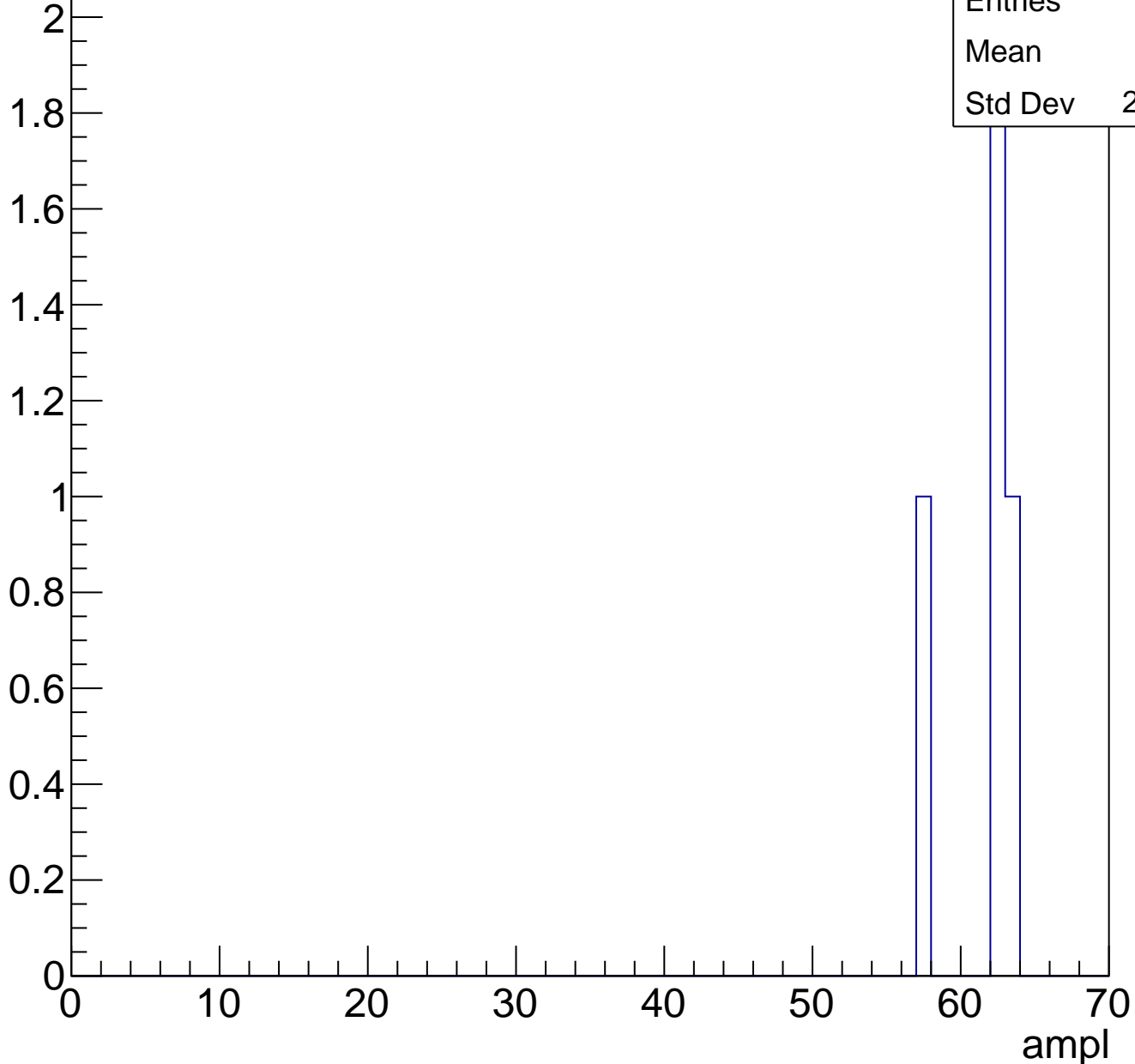
Entries	52
Mean	58.92
Std Dev	8.595



B1L102S, U11-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch37, adc0

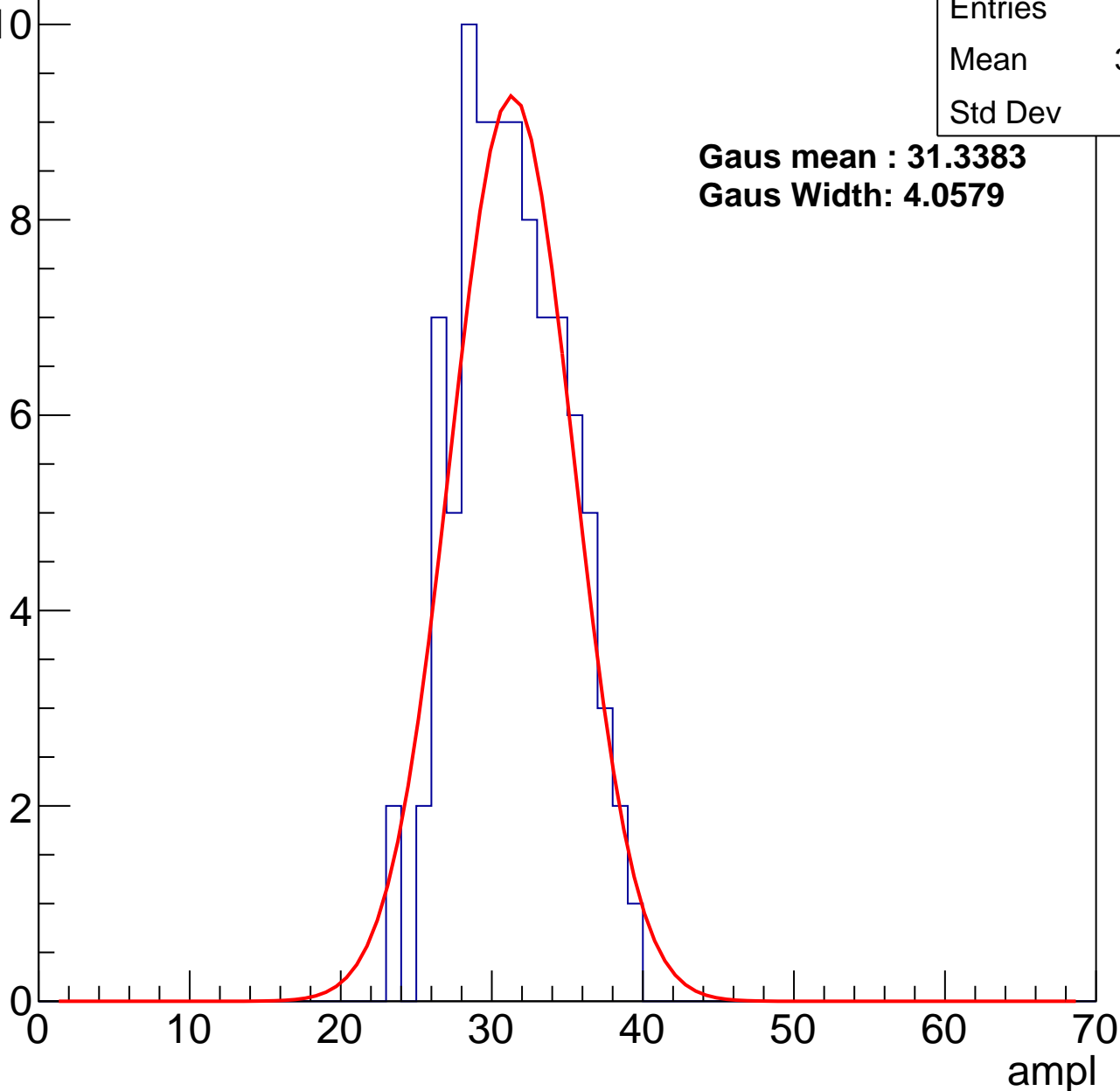
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	92
Mean	30.91
Std Dev	3.58

Gaus mean : 31.3383

Gaus Width: 4.0579



B1L102S, U11-ch37, adc1

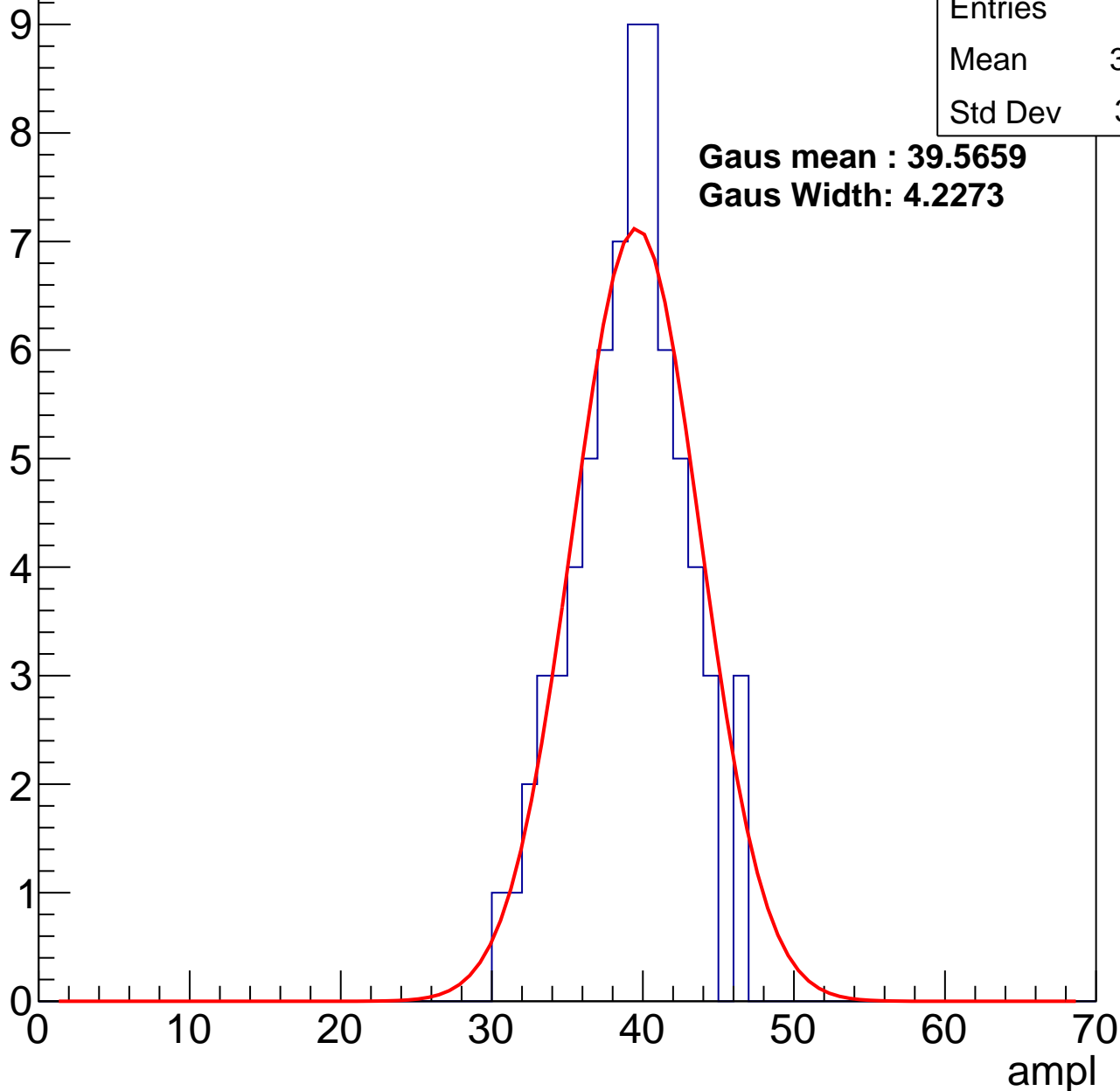
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	38.63
Std Dev	3.601

Gaus mean : 39.5659

Gaus Width: 4.2273



B1L102S, U11-ch37, adc2

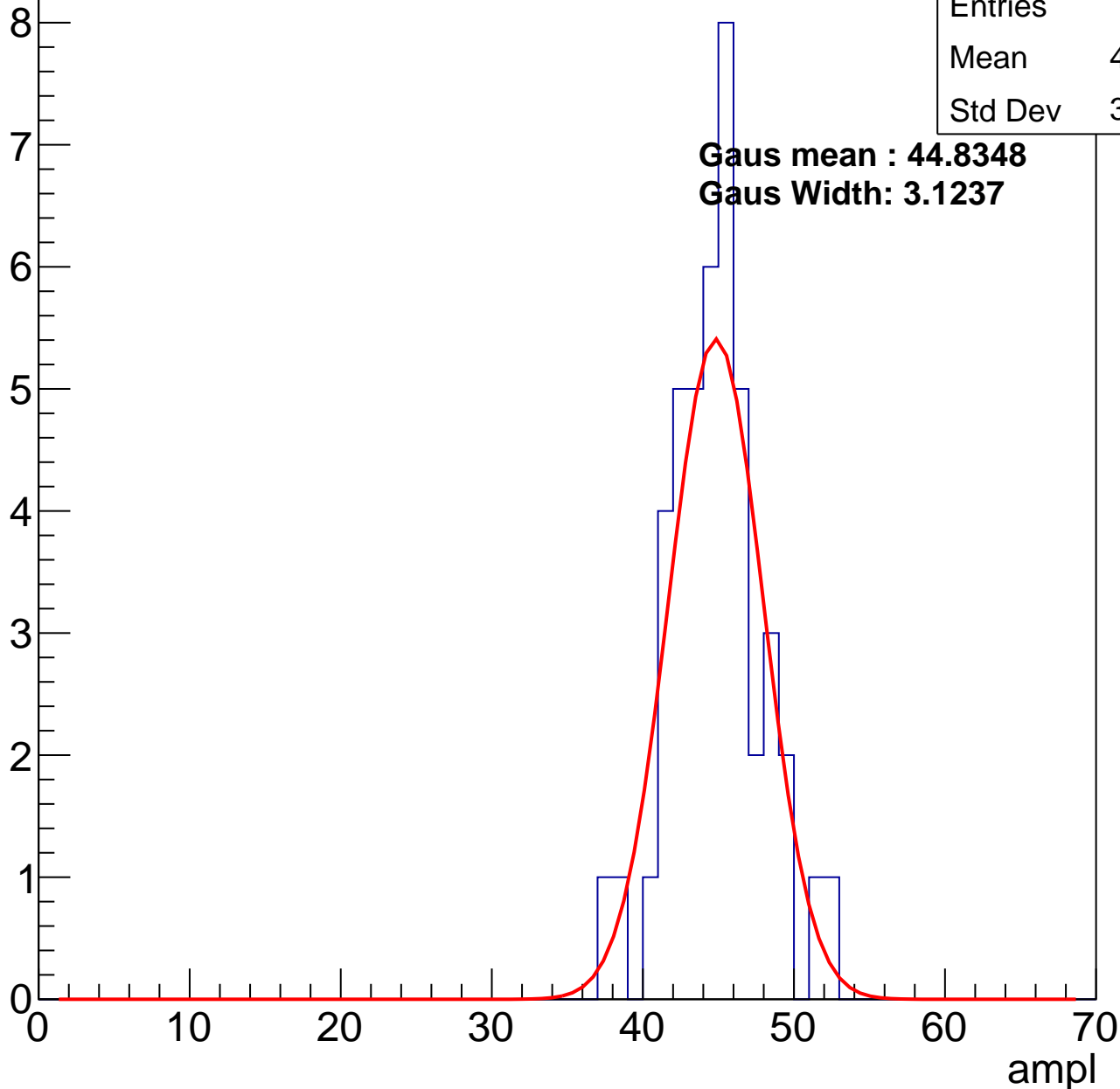
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	44.38
Std Dev	3.028

Gaus mean : 44.8348

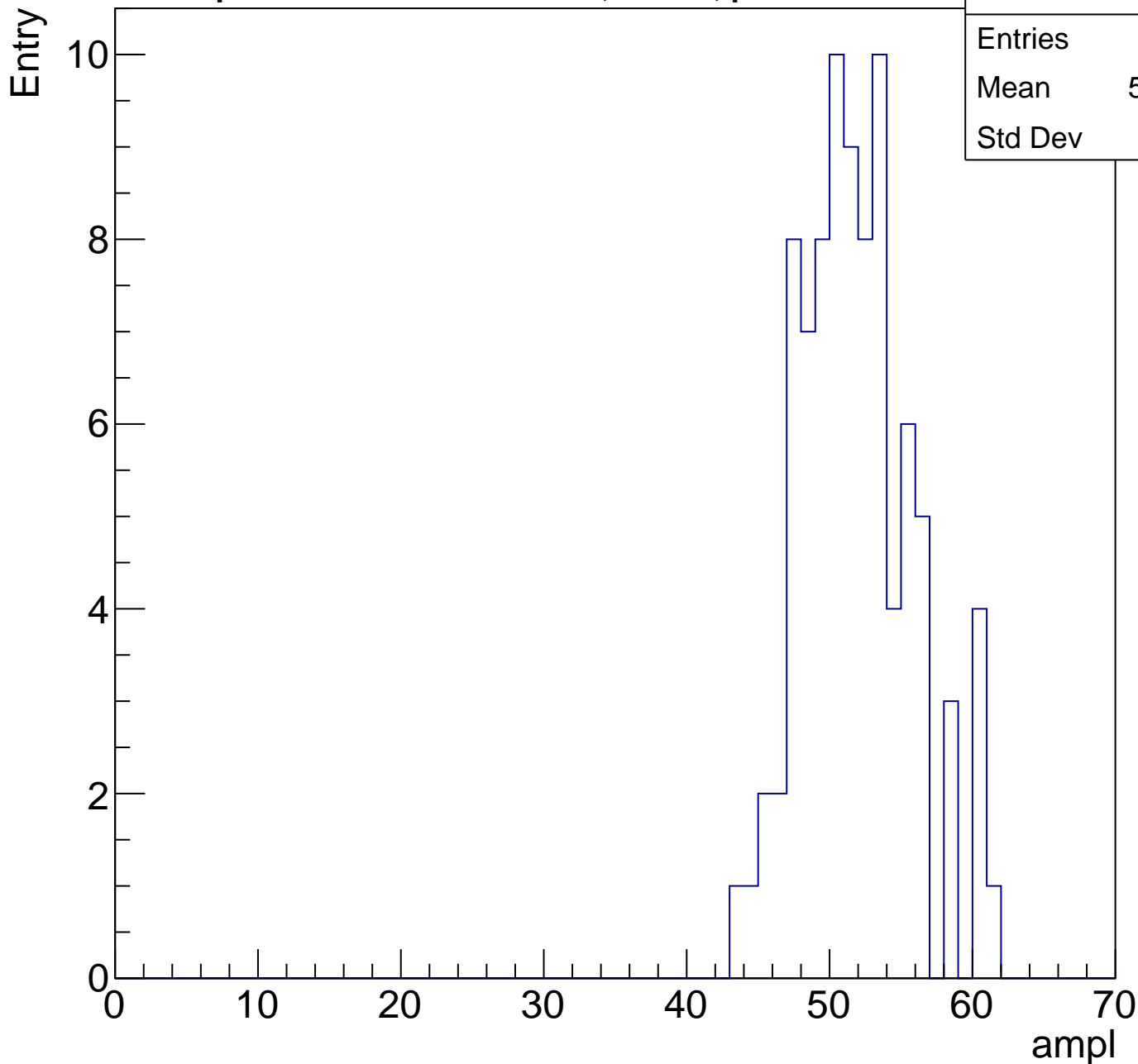
Gaus Width: 3.1237



B1L102S, U11-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	51.45
Std Dev	3.86

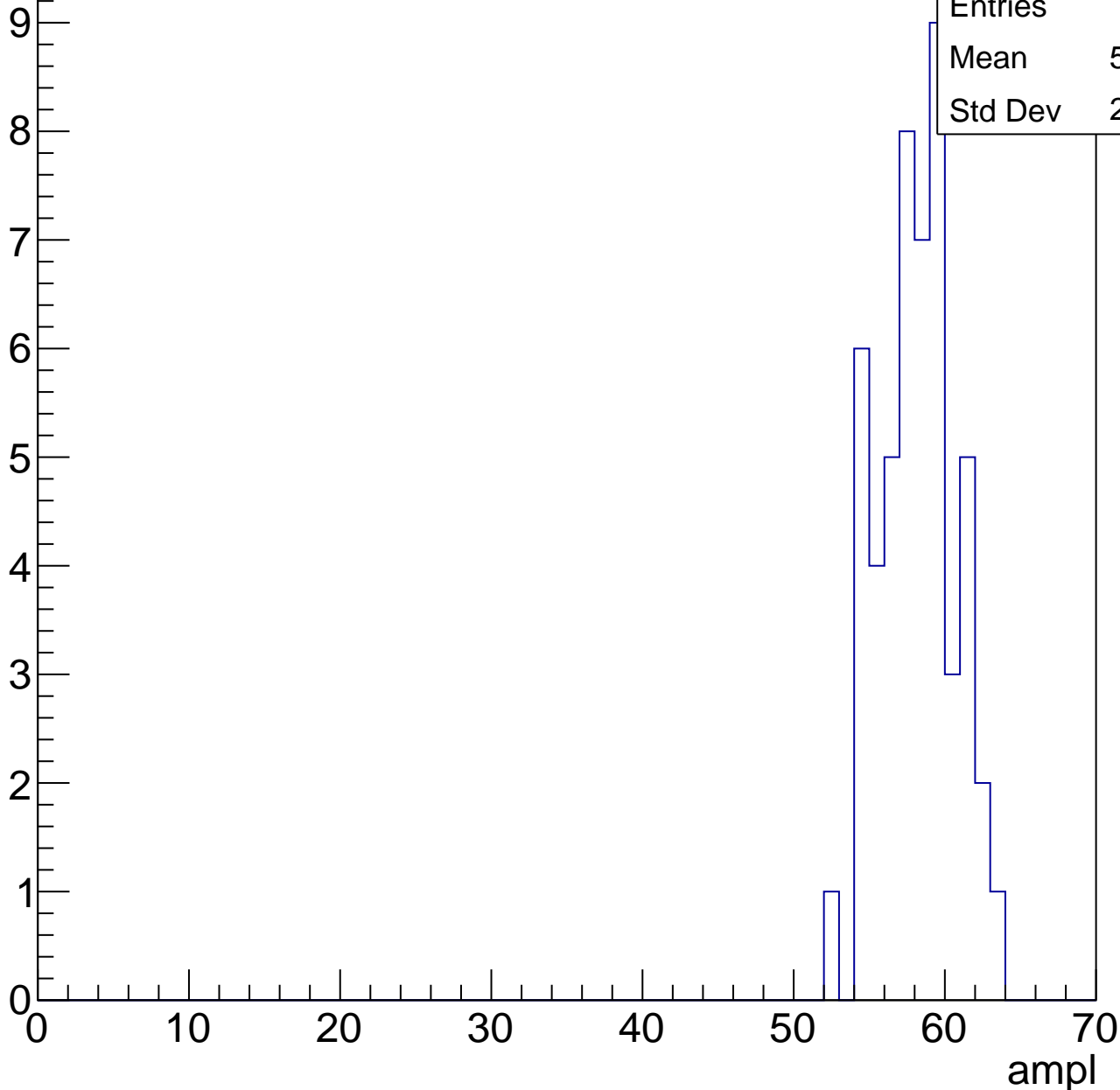


B1L102S, U11-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.67
Std Dev	2.479

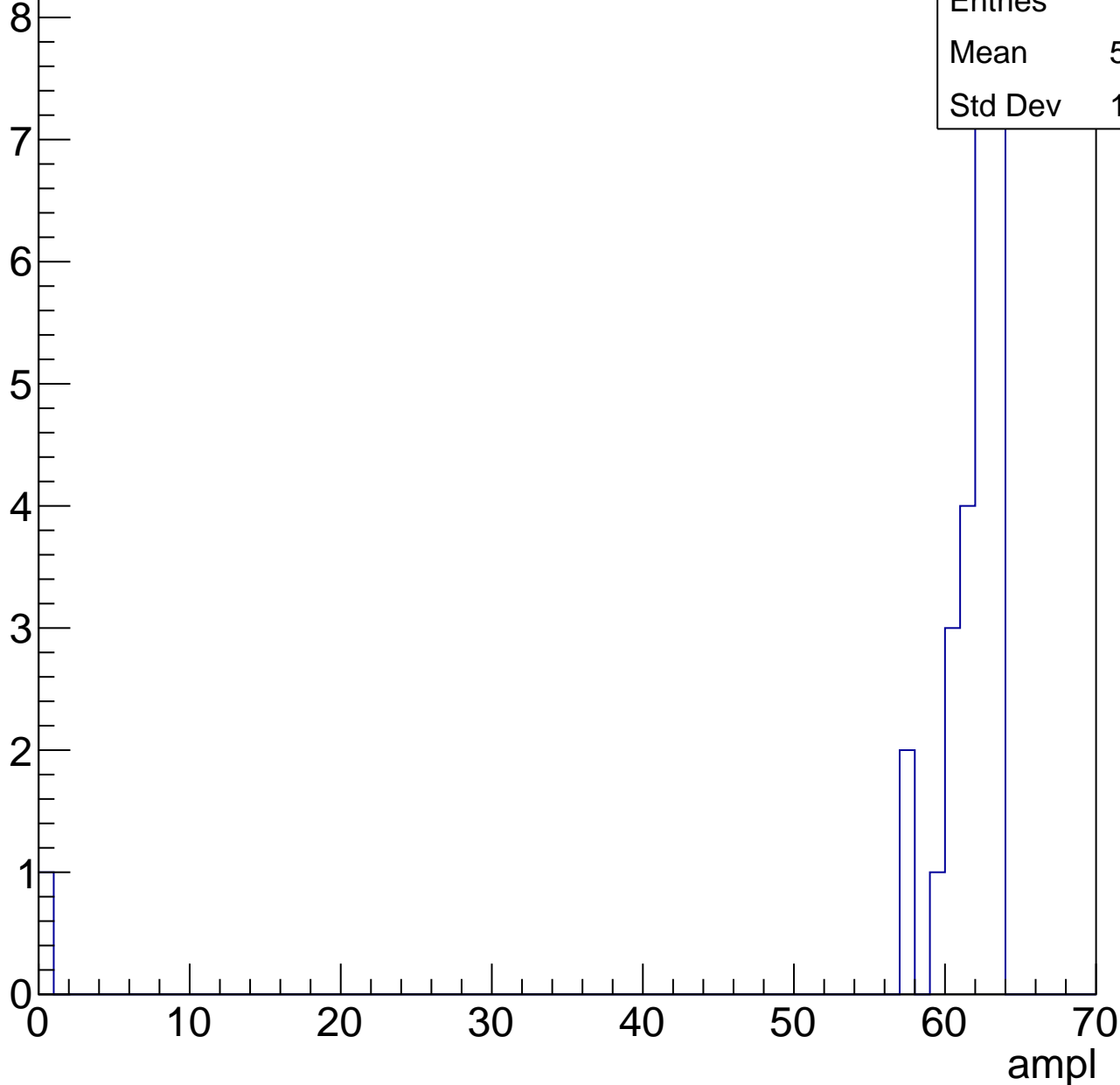


B1L102S, U11-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	27
Mean	59.15
Std Dev	11.72



B1L102S, U11-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch38, adc0

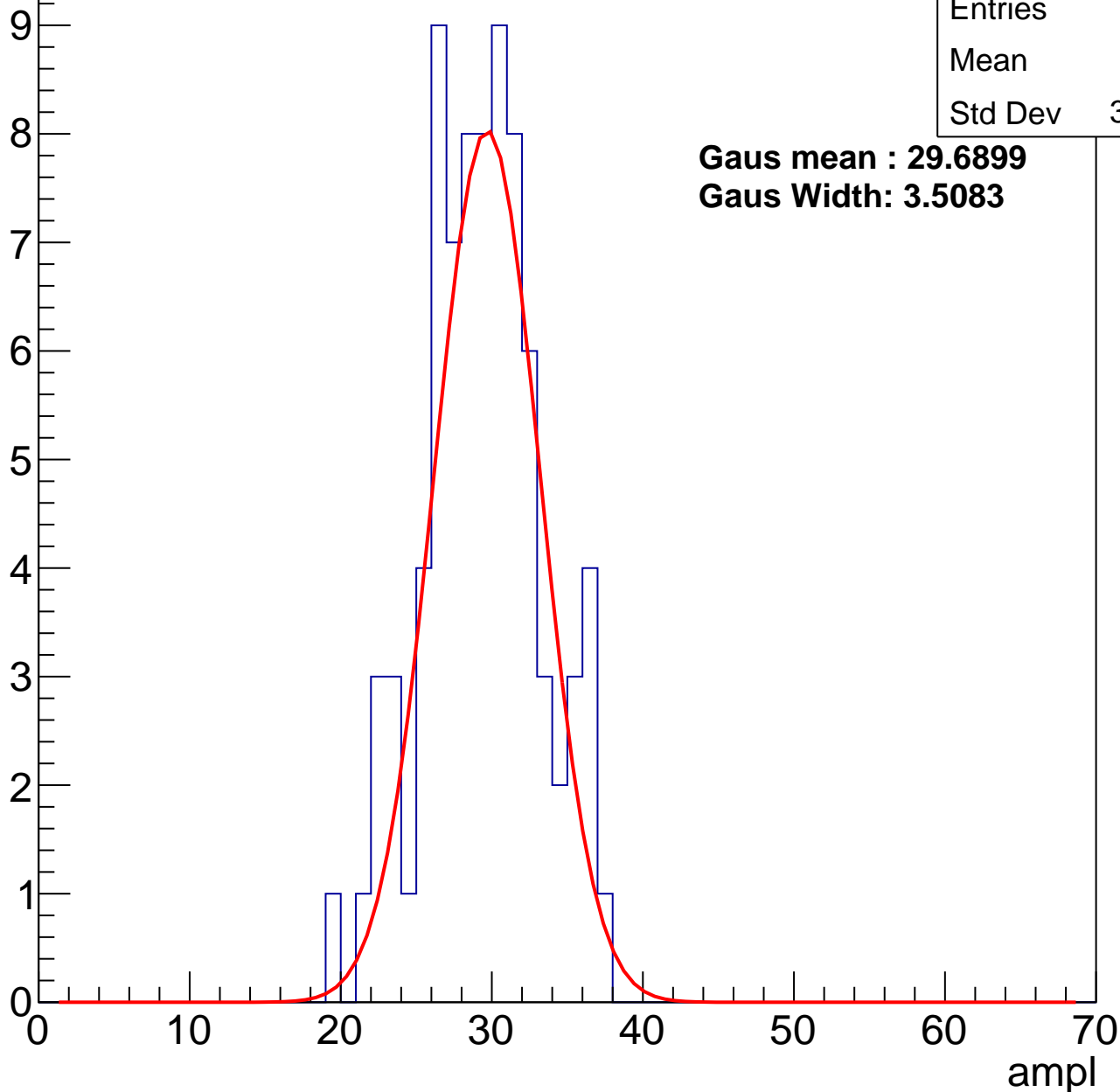
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.9
Std Dev	3.825

Gaus mean : 29.6899

Gaus Width: 3.5083



B1L102S, U11-ch38, adc1

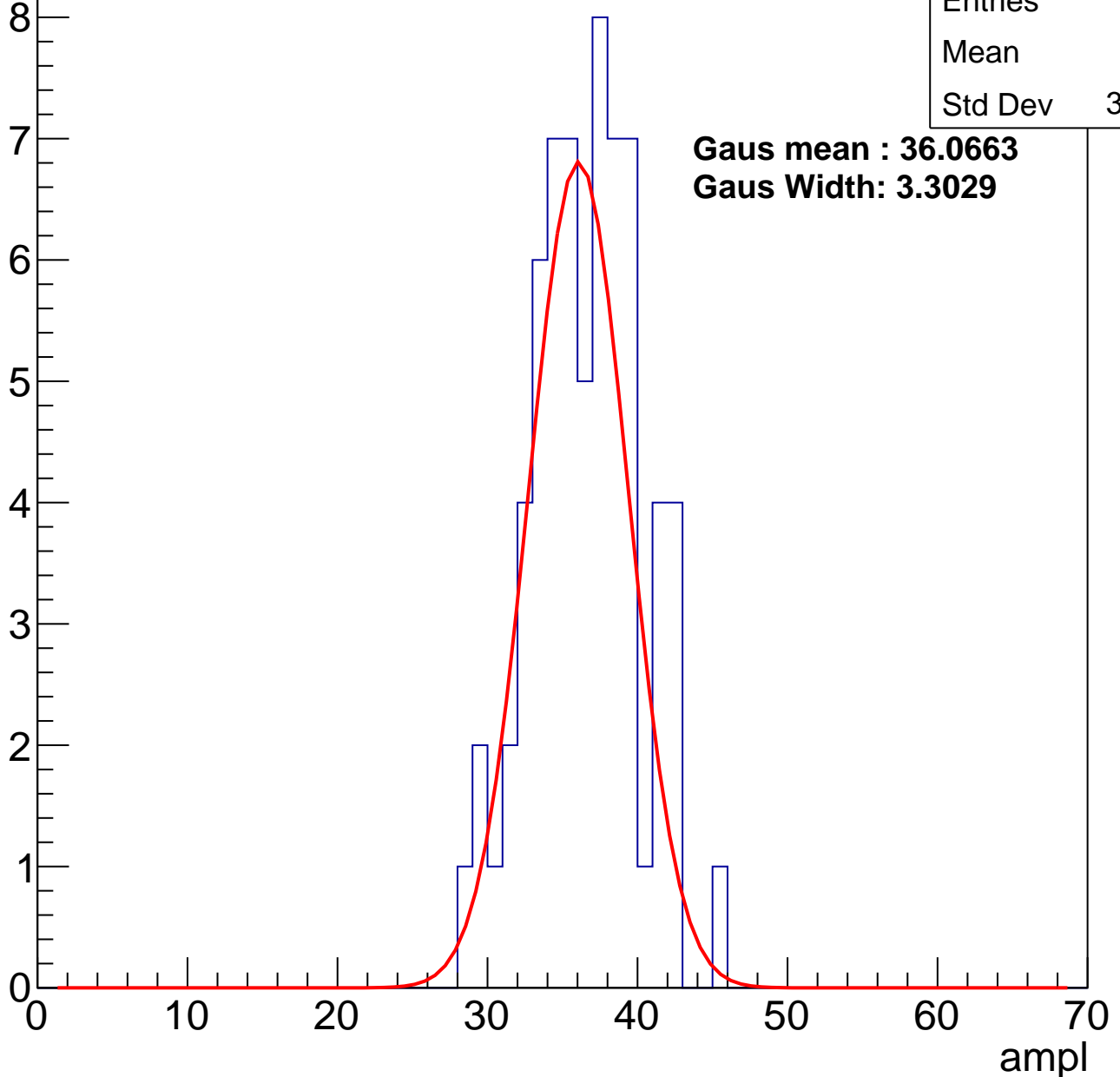
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.1
Std Dev	3.541

Gaus mean : 36.0663

Gaus Width: 3.3029



B1L102S, U11-ch38, adc2

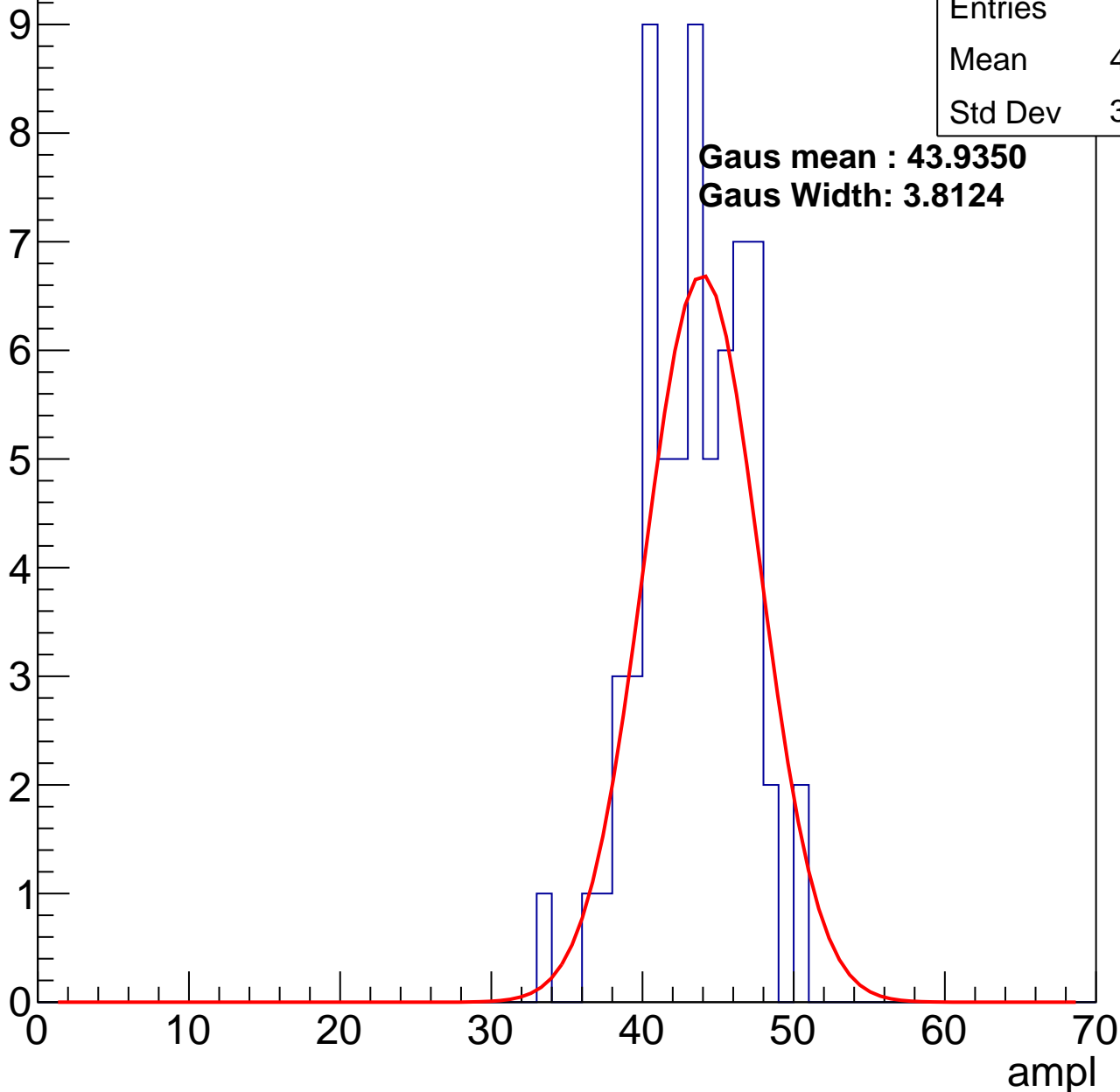
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.97
Std Dev	3.407

Gaus mean : 43.9350

Gaus Width: 3.8124

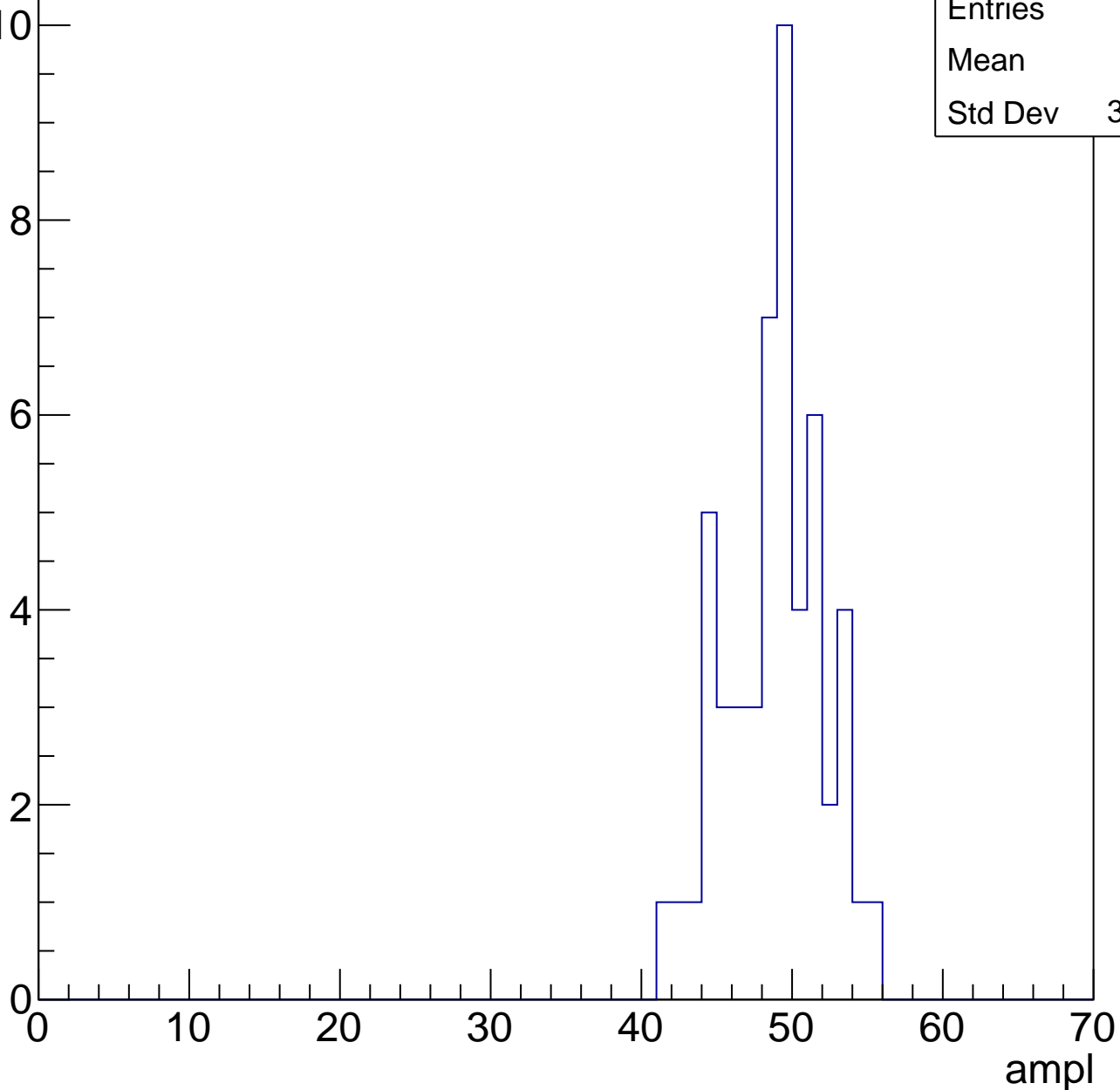


B1L102S, U11-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	48.4
Std Dev	3.164

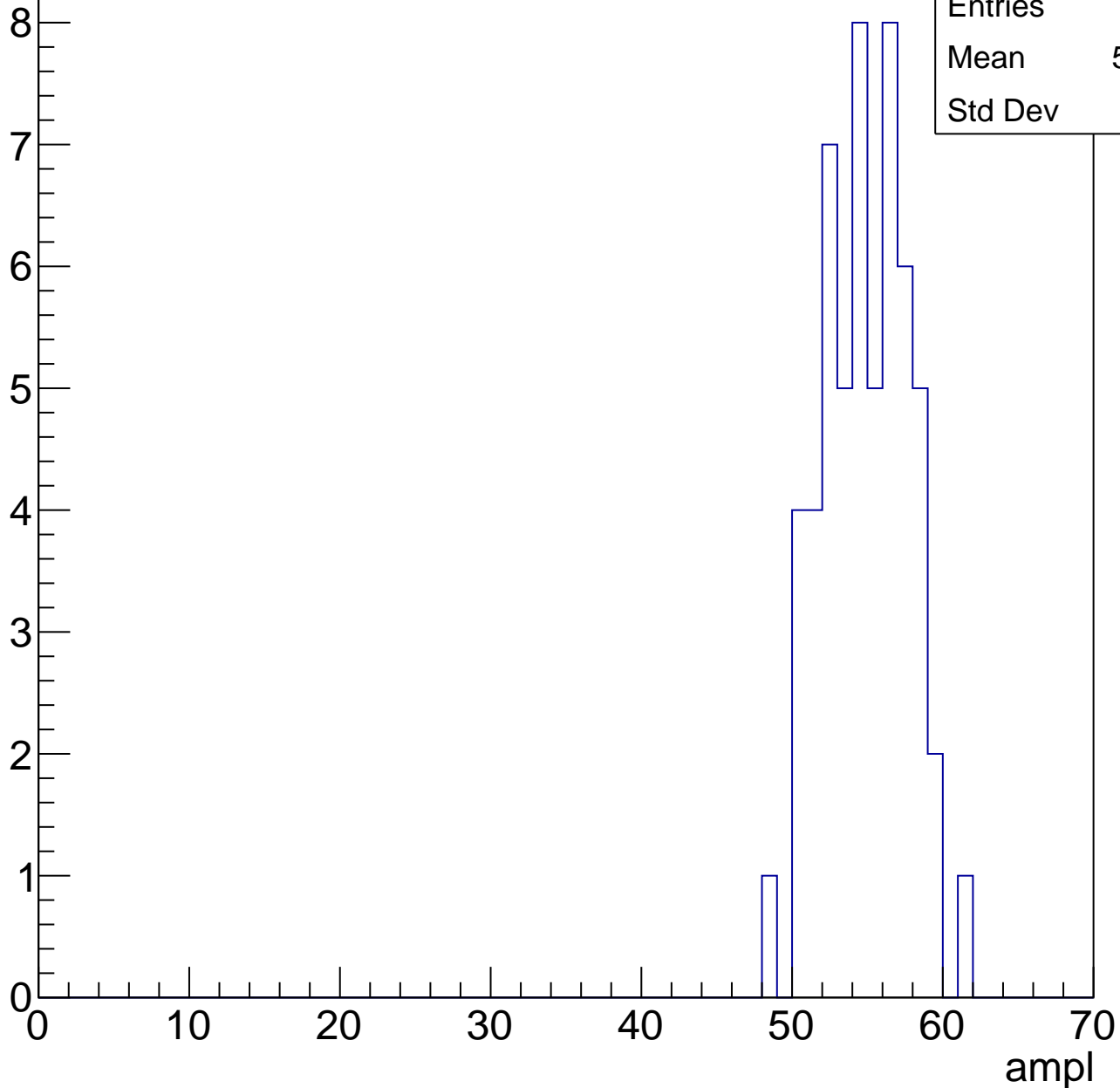


B1L102S, U11-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.41
Std Dev	2.77

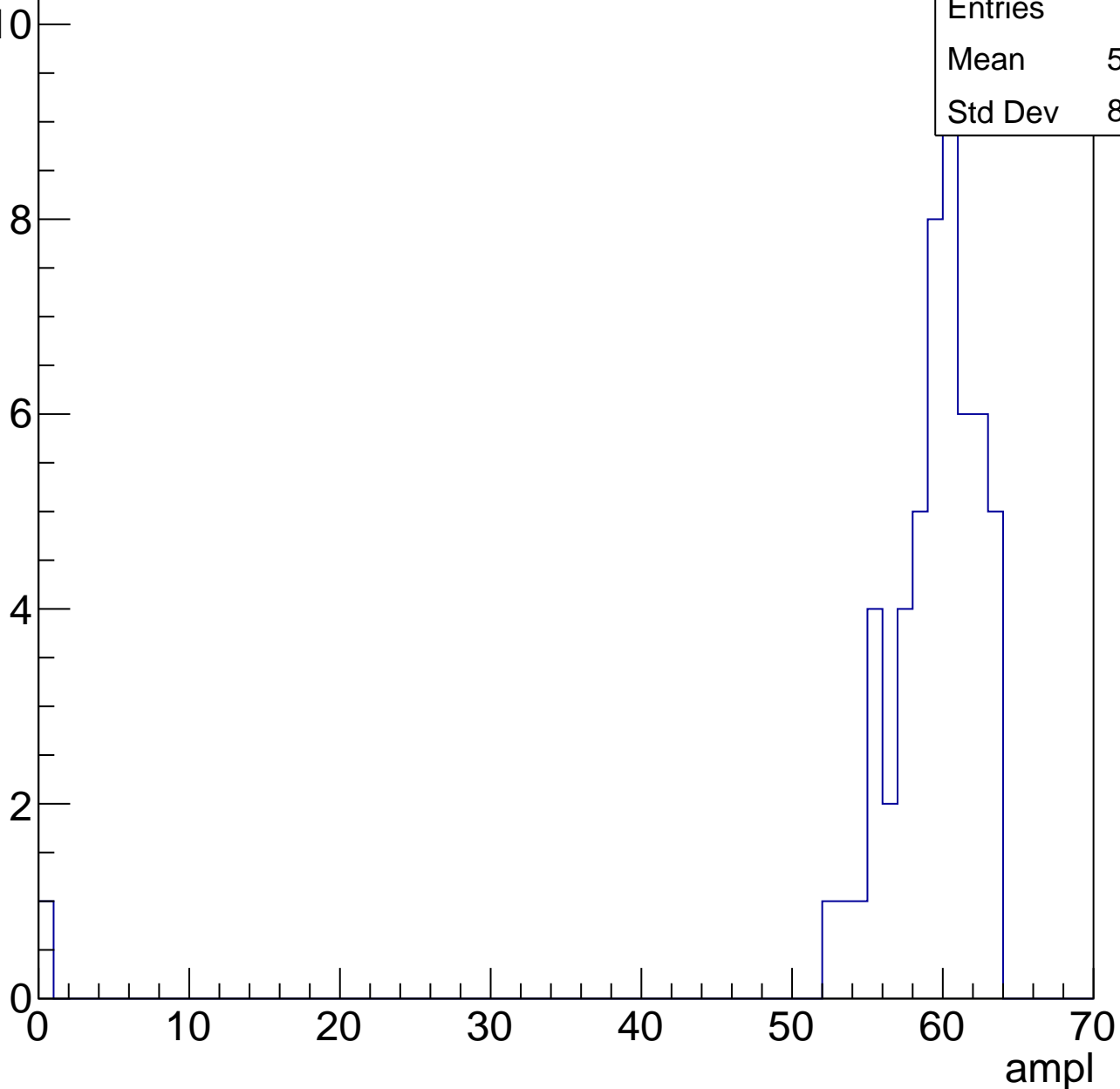


B1L102S, U11-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.04
Std Dev	8.402

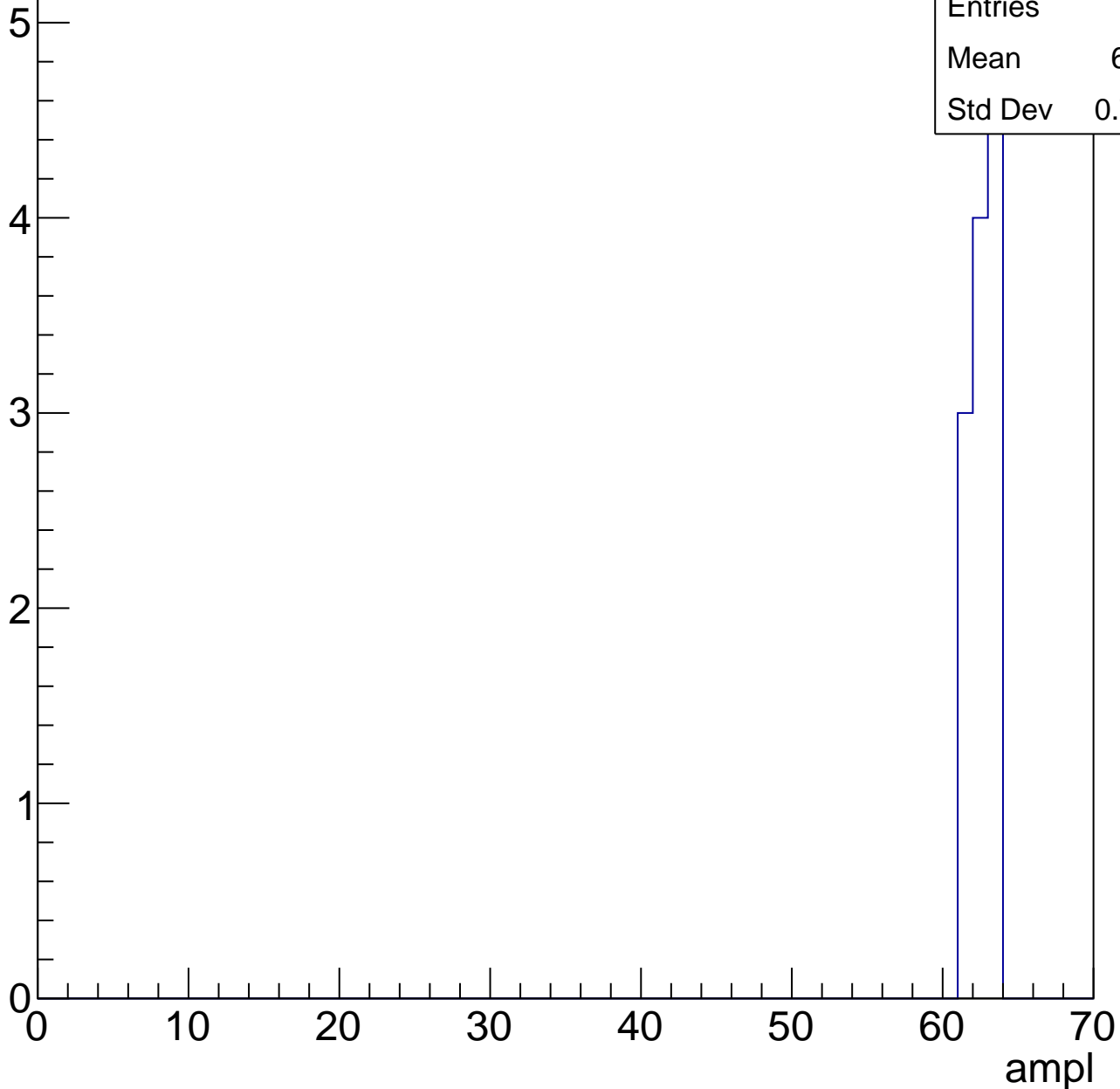


B1L102S, U11-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62.17
Std Dev	0.7993



B1L102S, U11-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch39, adc0

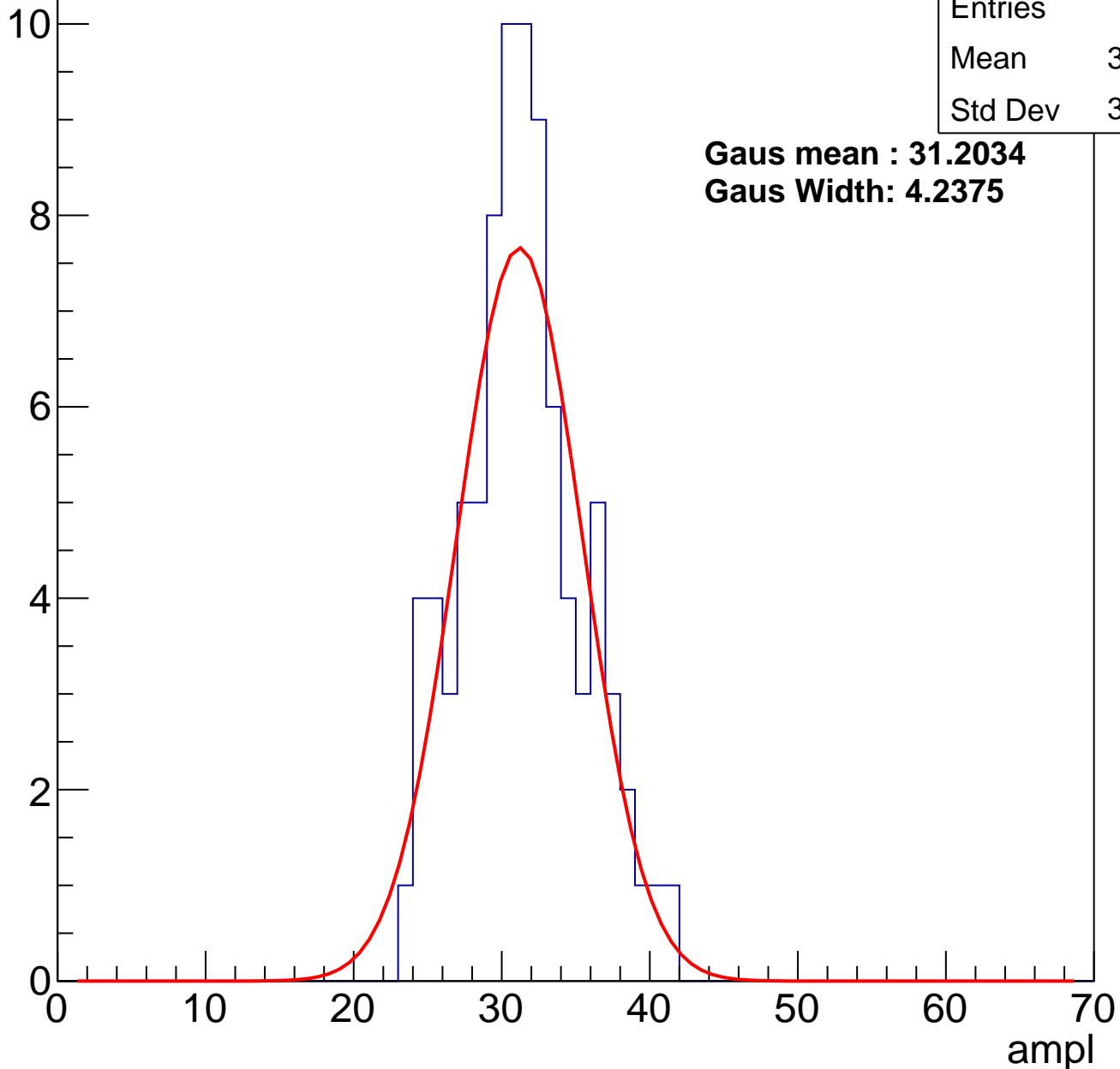
calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	30.92
Std Dev	3.968

Gaus mean : 31.2034

Gaus Width: 4.2375

Entry



B1L102S, U11-ch39, adc1

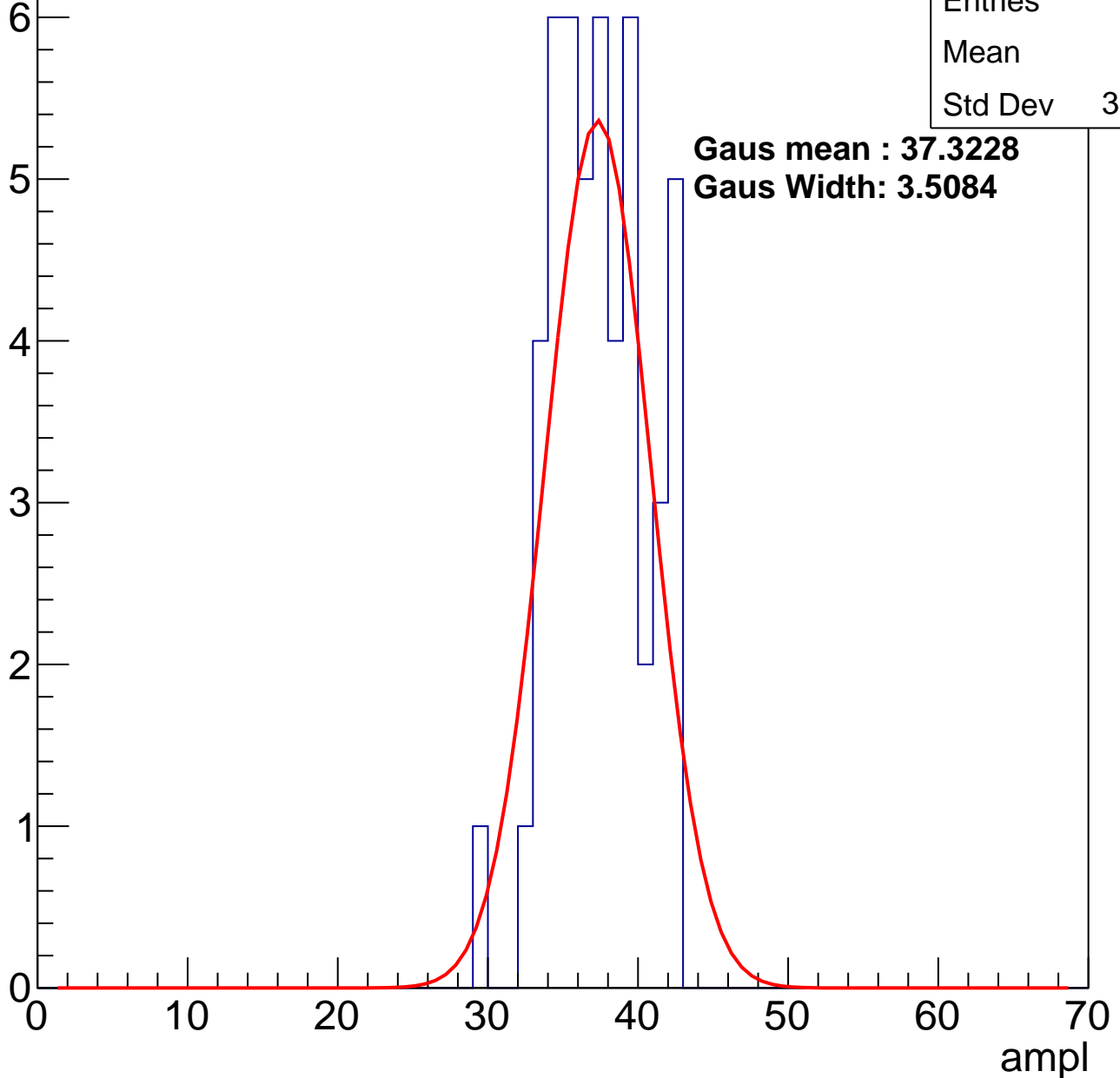
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	36.9
Std Dev	3.046

Gaus mean : 37.3228

Gaus Width: 3.5084



B1L102S, U11-ch39, adc2

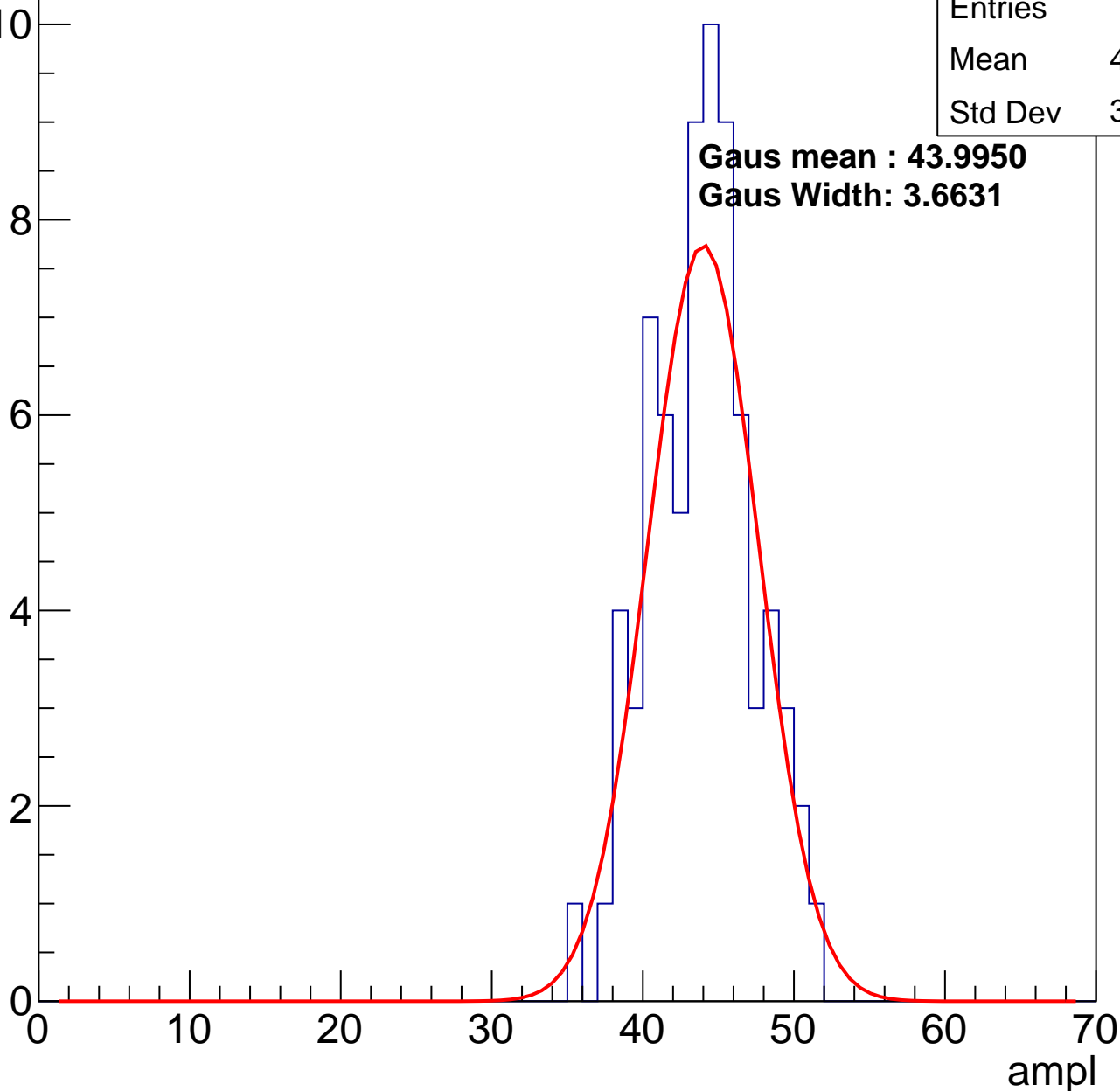
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.46
Std Dev	3.374

Gaus mean : 43.9950

Gaus Width: 3.6631

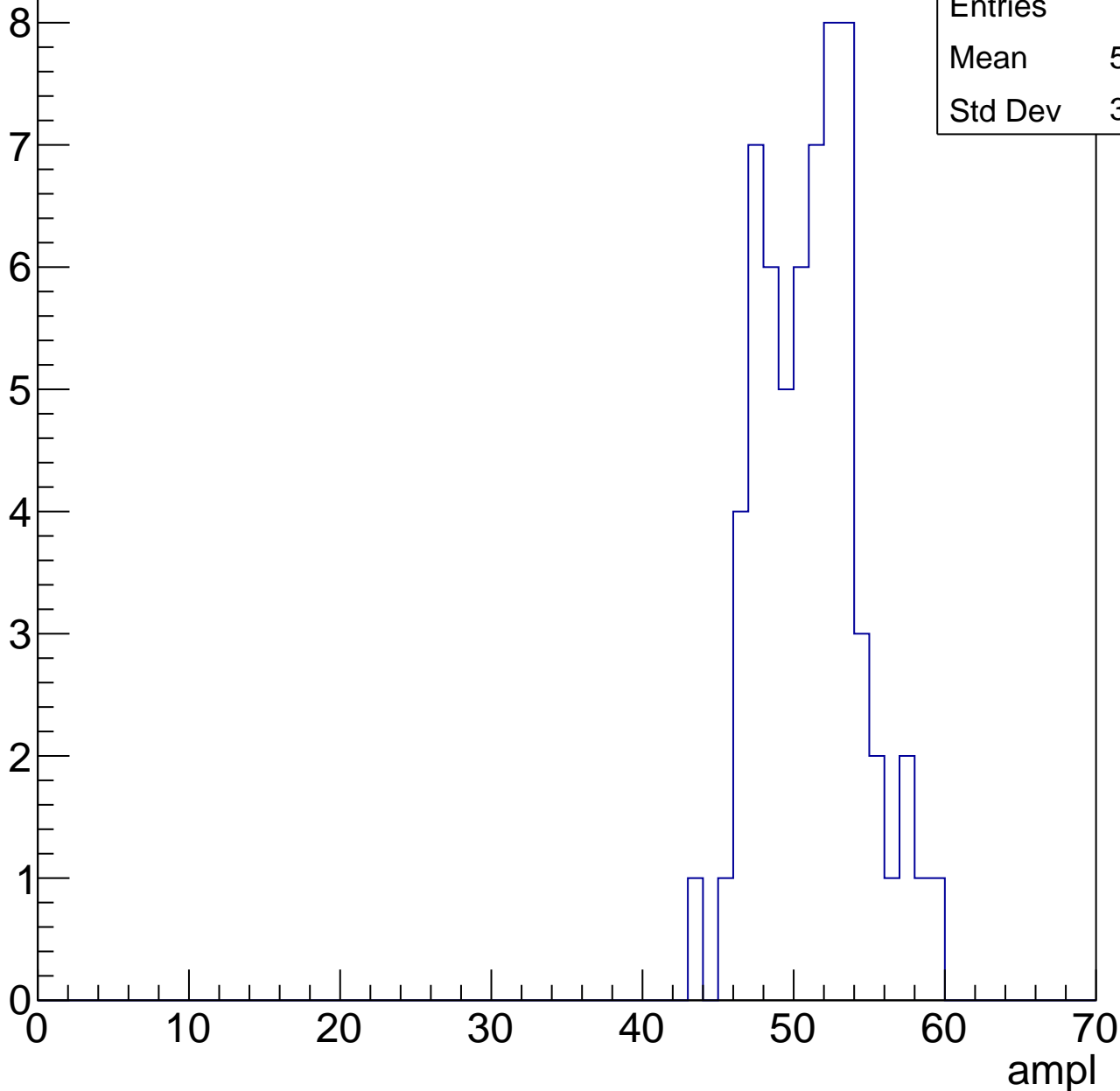


B1L102S, U11-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.63
Std Dev	3.316

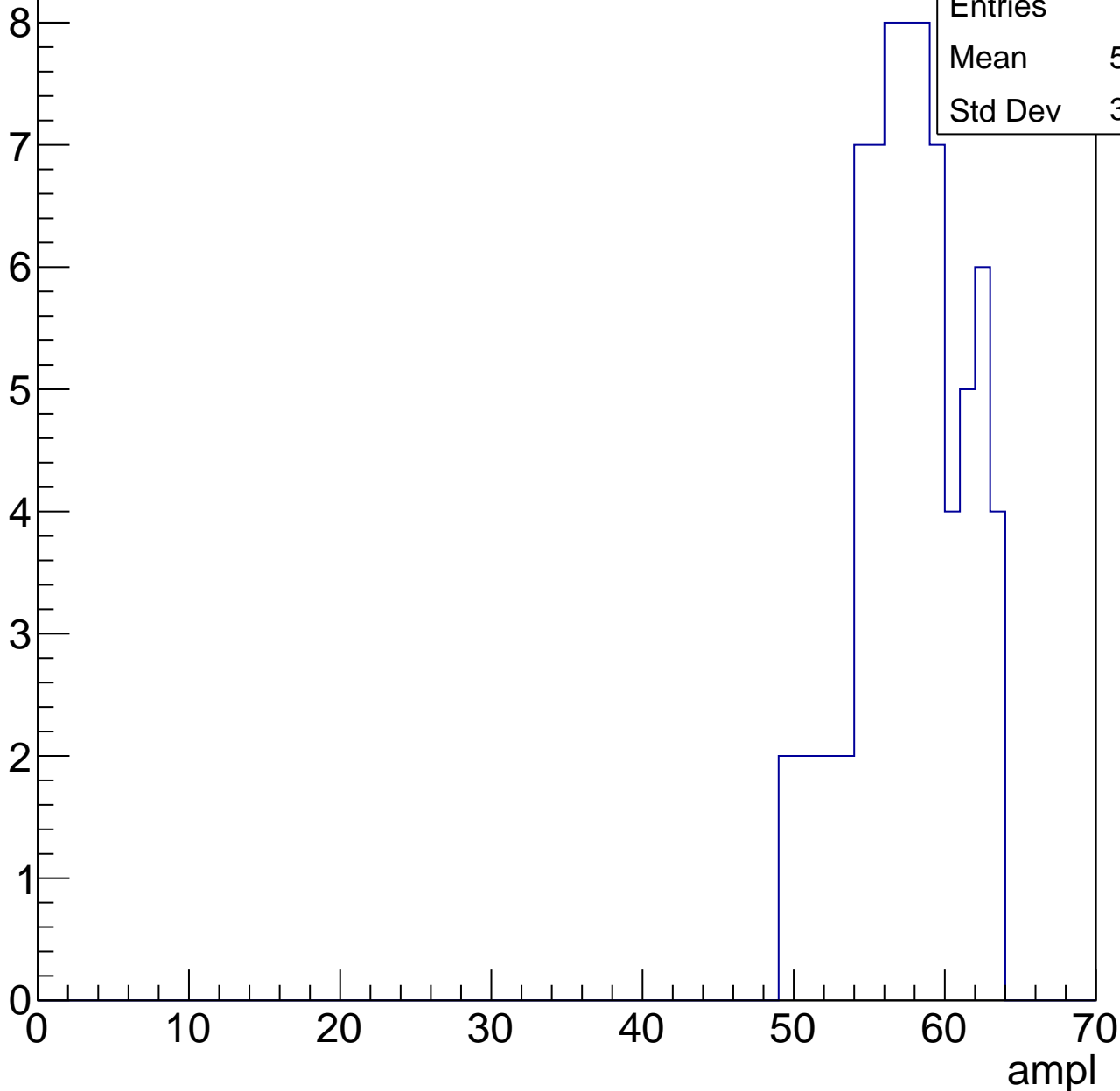


B1L102S, U11-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

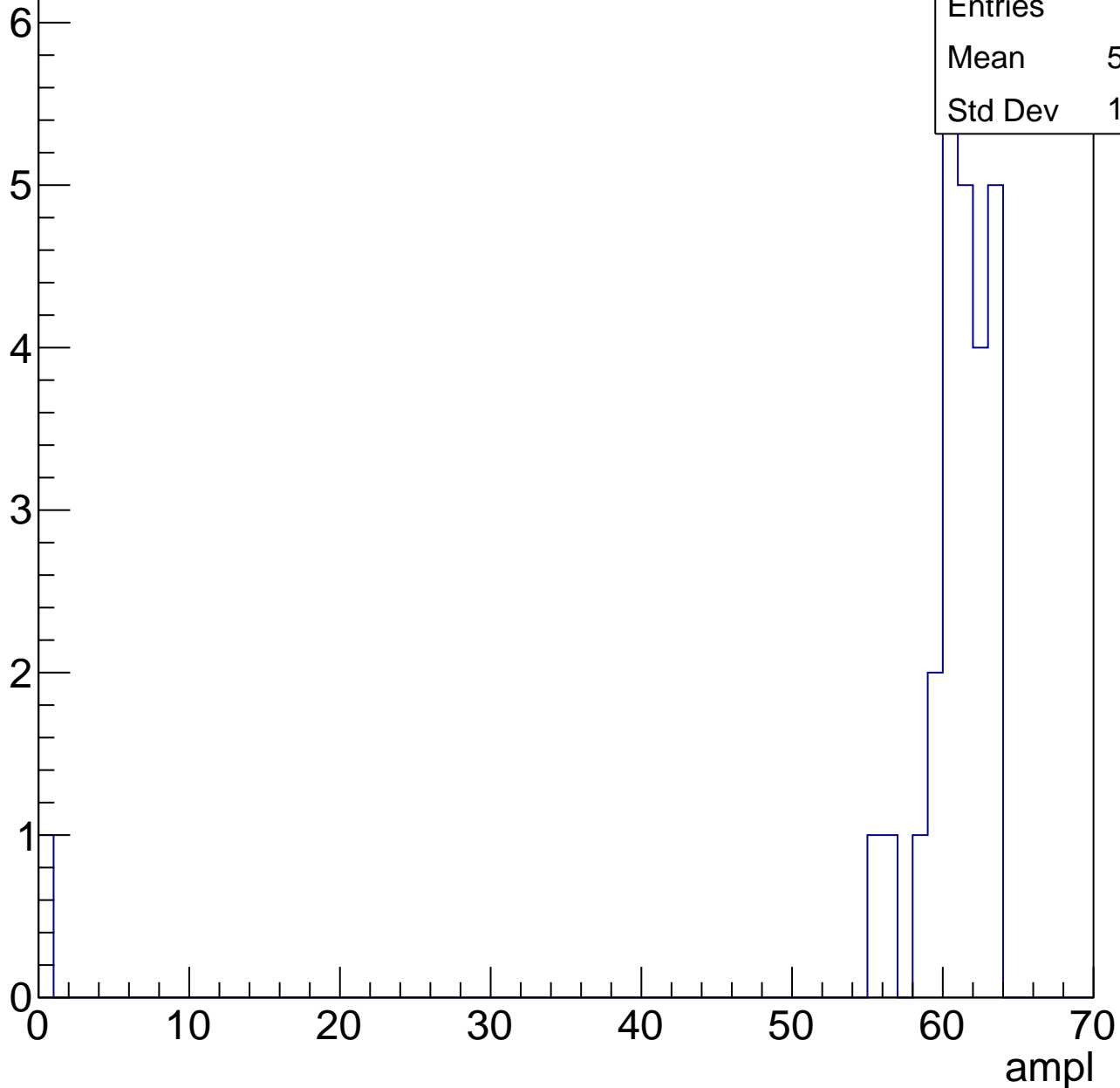
Entries	74
Mean	57.07
Std Dev	3.535



B1L102S, U11-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U11-ch40, adc0

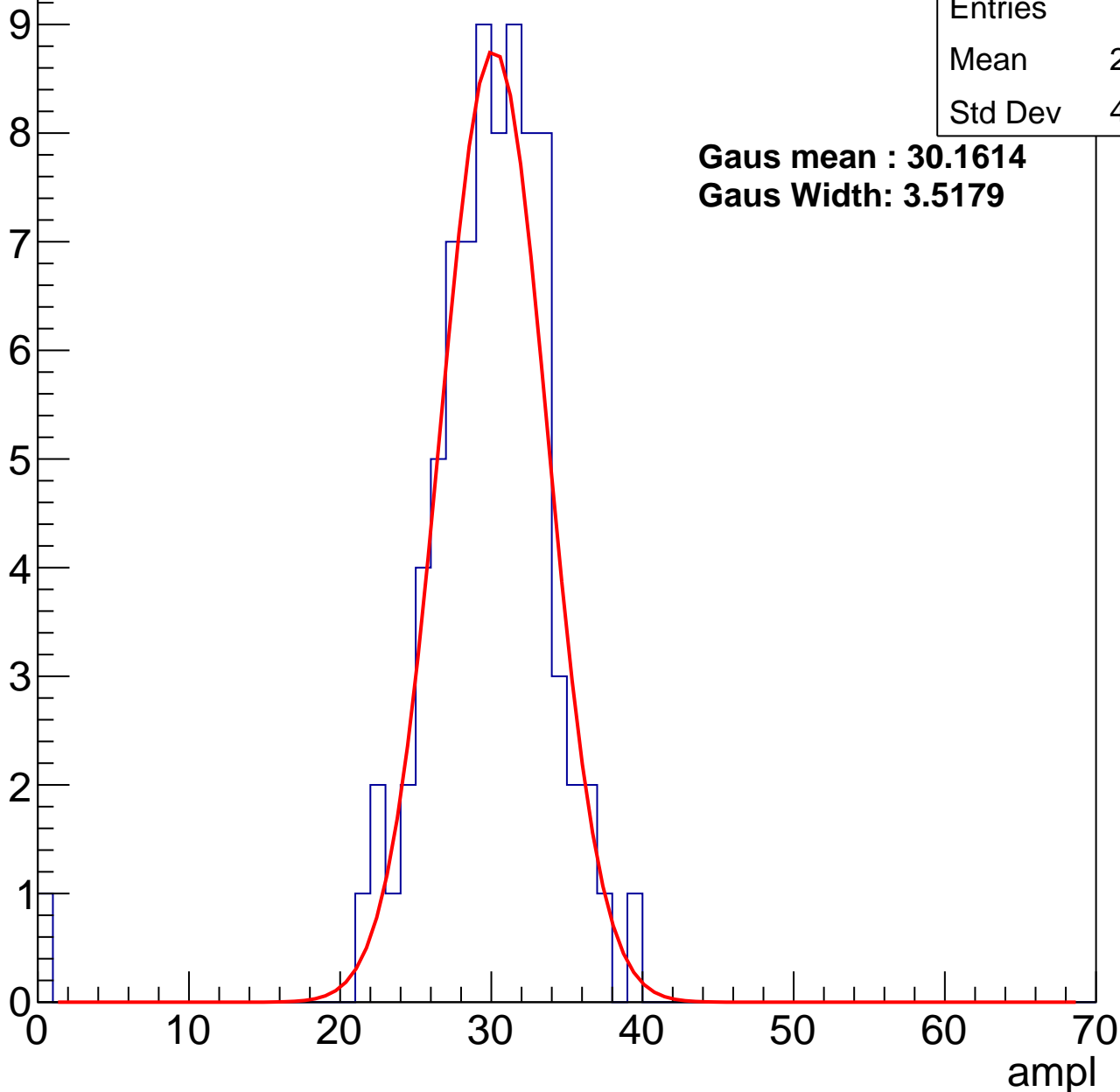
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.27
Std Dev	4.818

Gaus mean : 30.1614

Gaus Width: 3.5179



B1L102S, U11-ch40, adc1

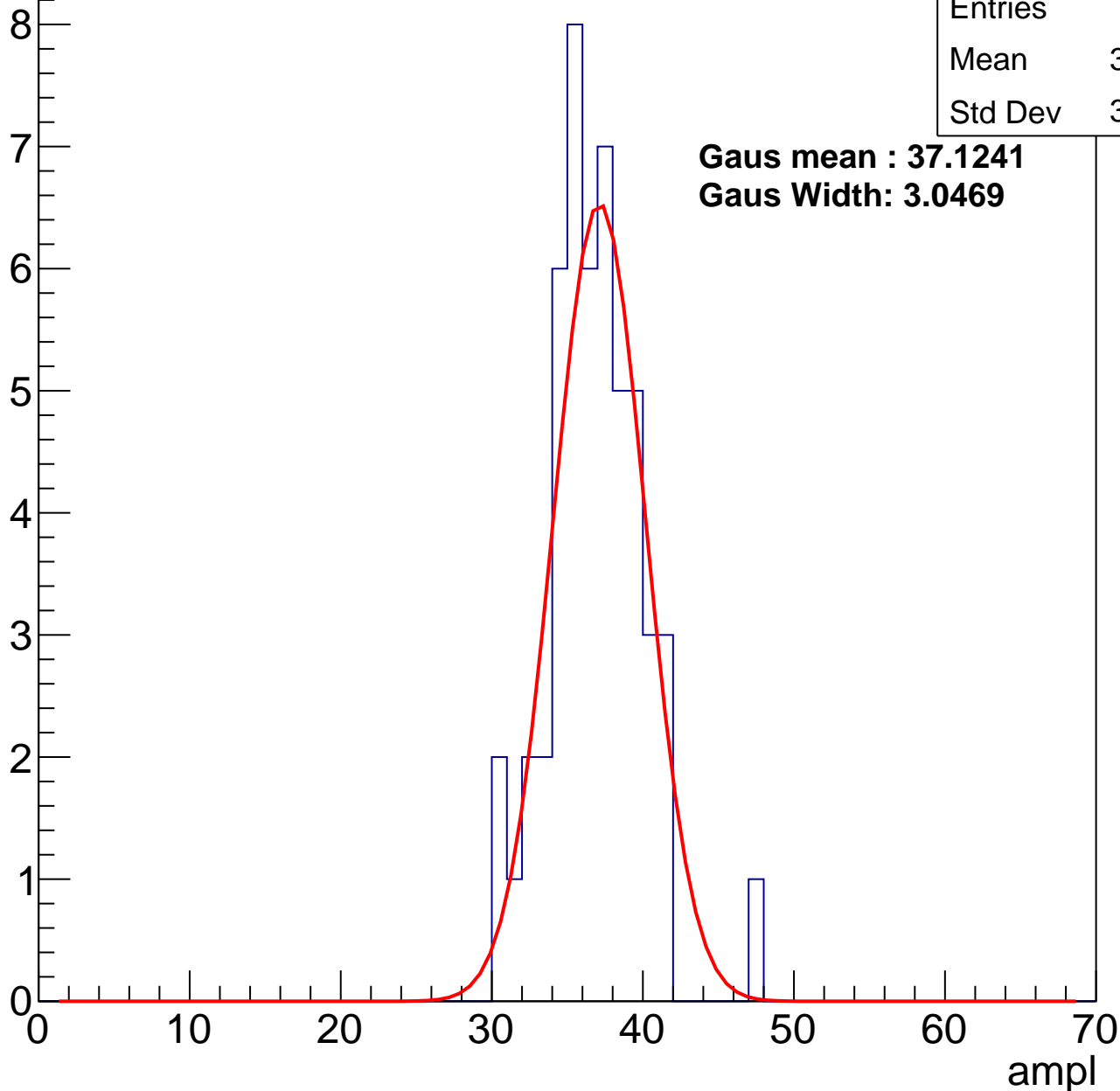
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	36.37
Std Dev	3.093

Gaus mean : 37.1241

Gaus Width: 3.0469



B1L102S, U11-ch40, adc2

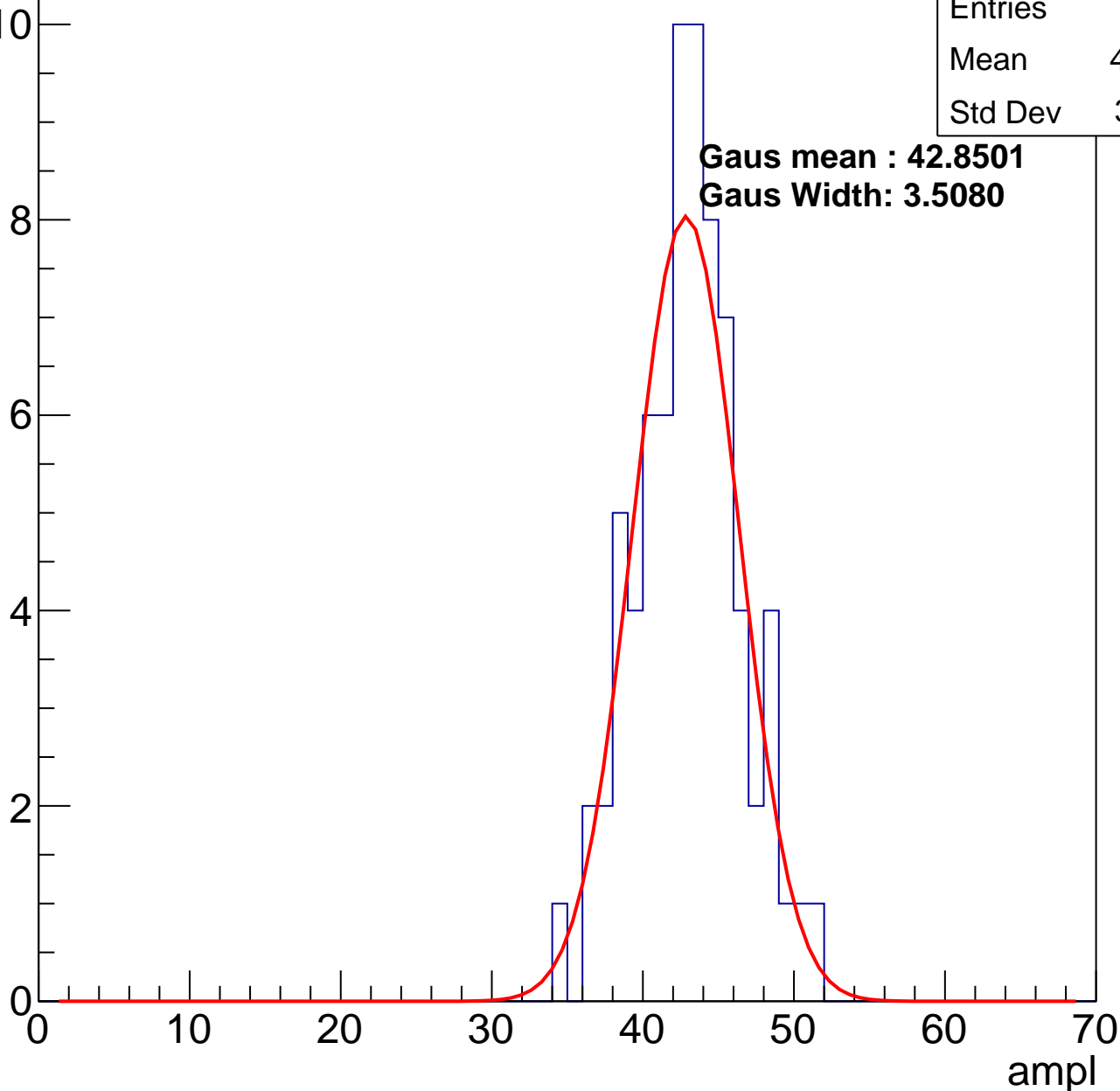
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.55
Std Dev	3.421

Gaus mean : 42.8501

Gaus Width: 3.5080

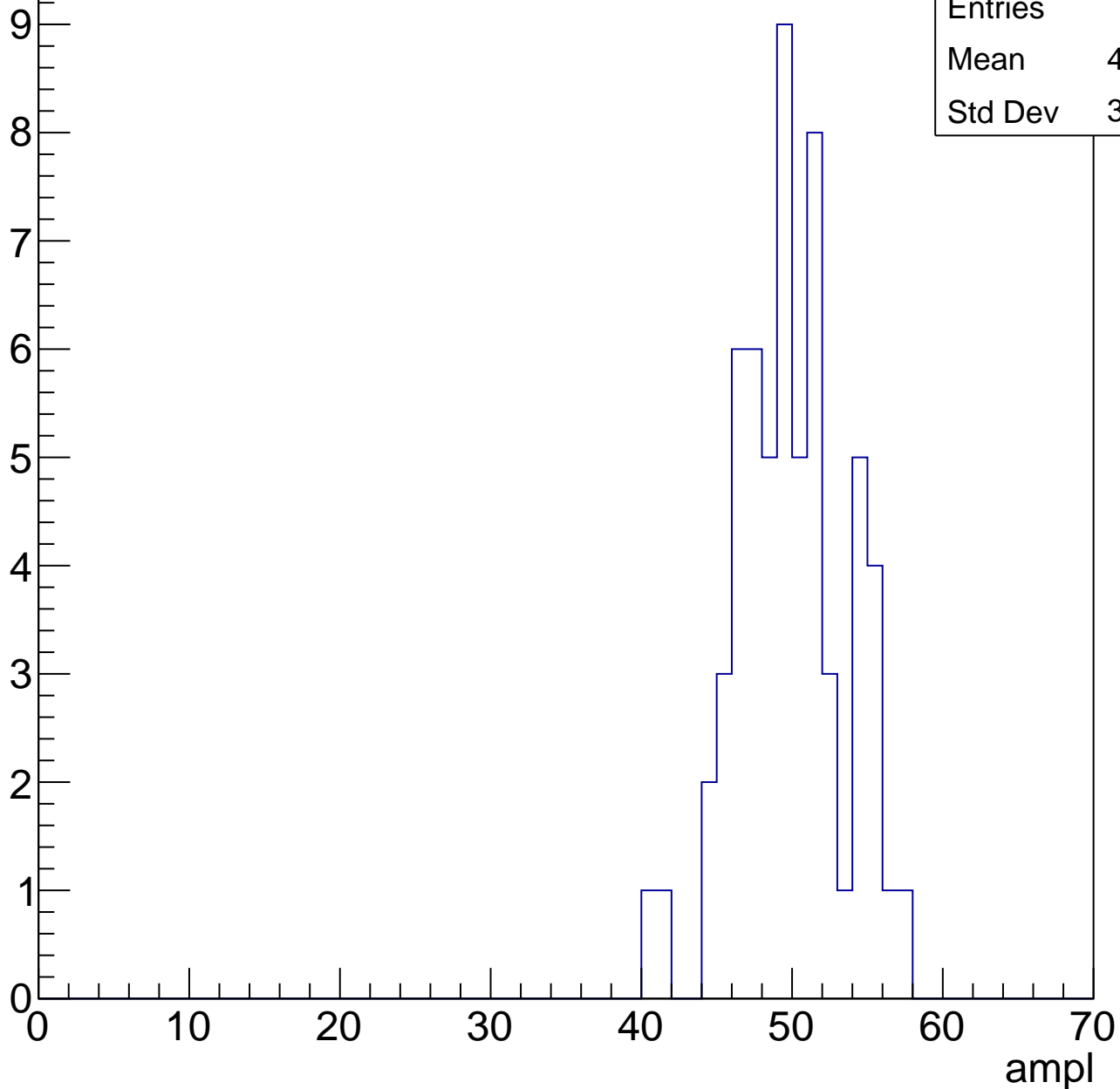


B1L102S, U11-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.39
Std Dev	3.573



B1L102S, U11-ch40, adc4

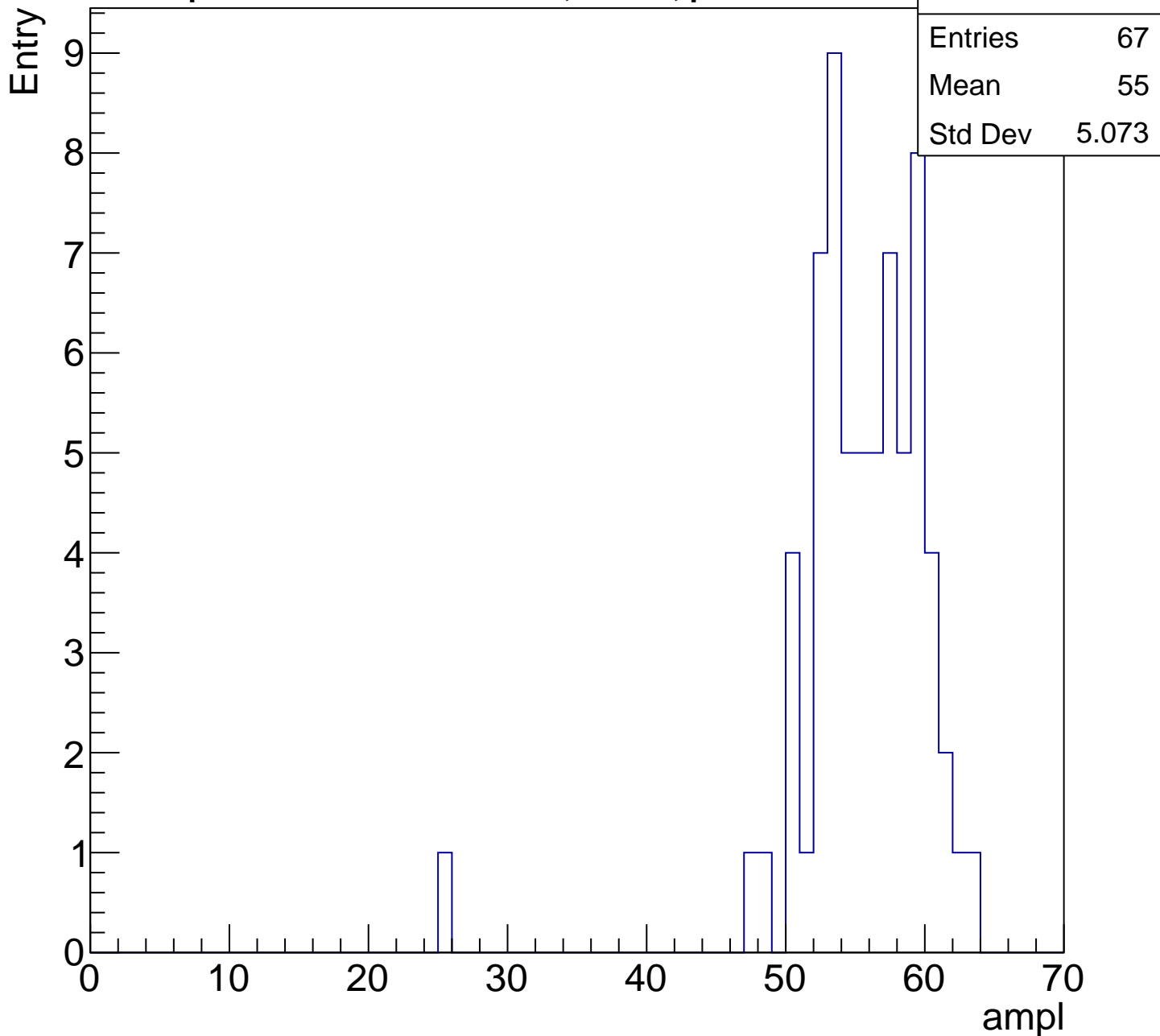
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	55
Std Dev	5.073

ampl

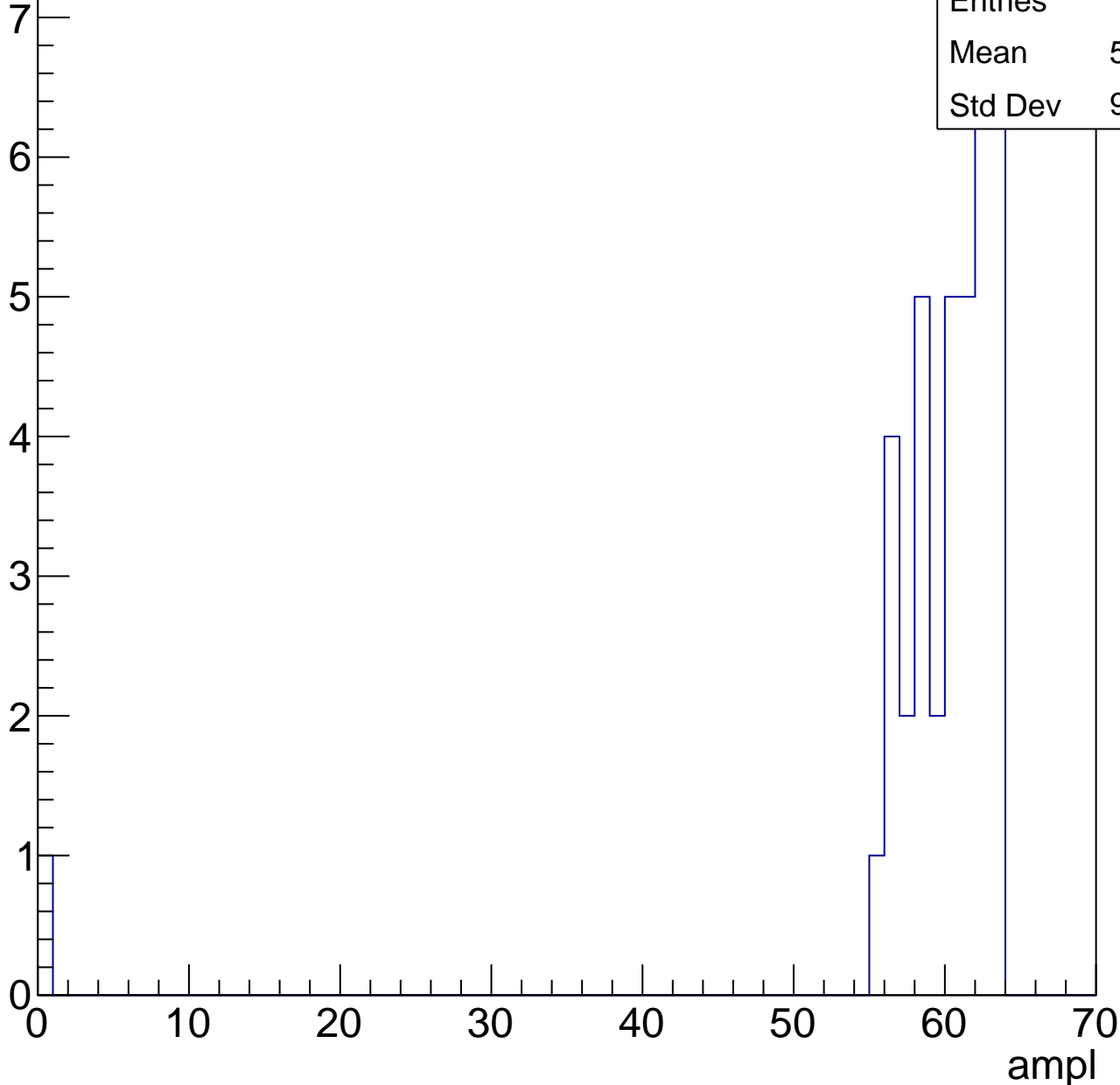


B1L102S, U11-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

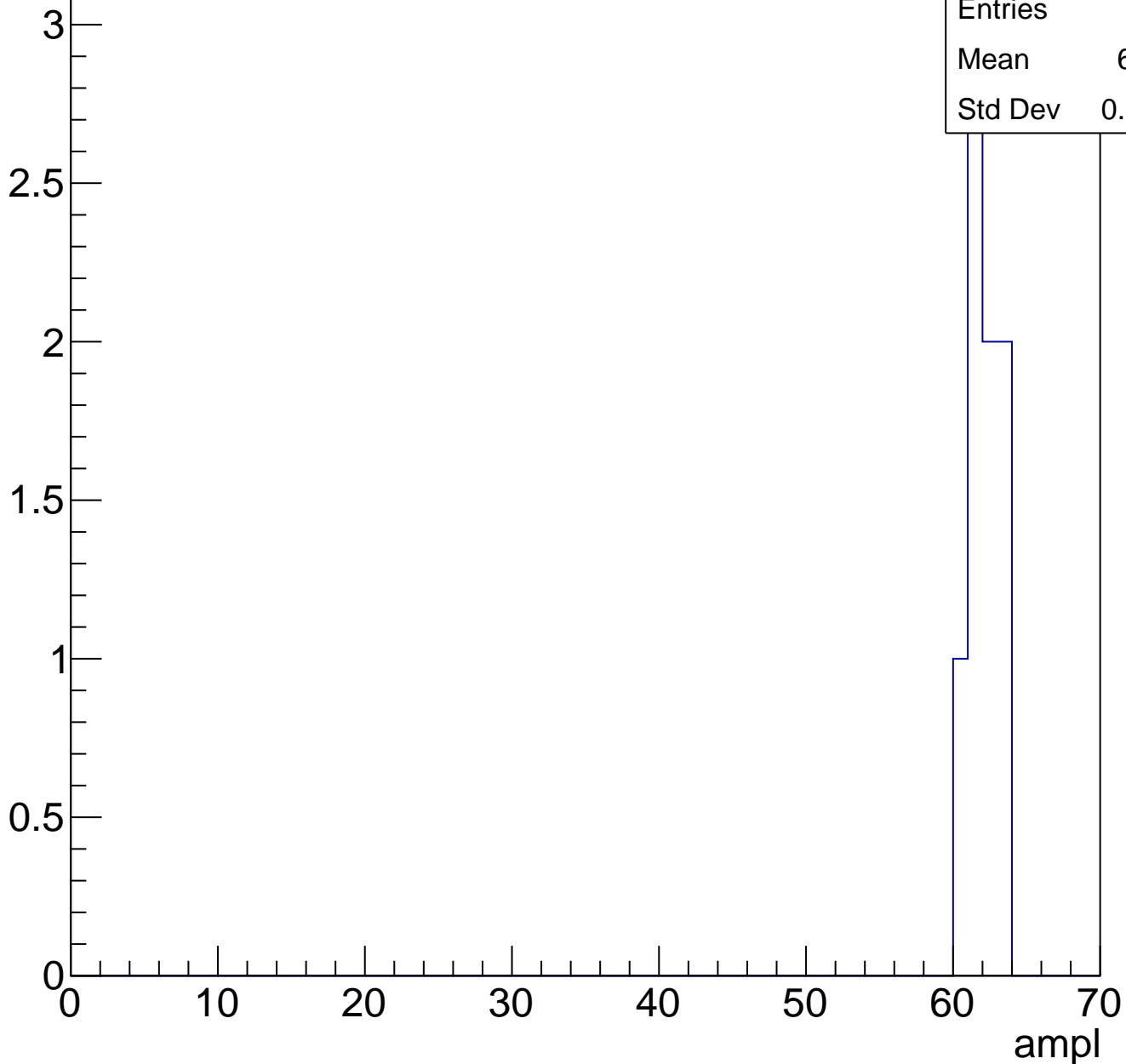
Entries	39
Mean	58.49
Std Dev	9.787



B1L102S, U11-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch41, adc0

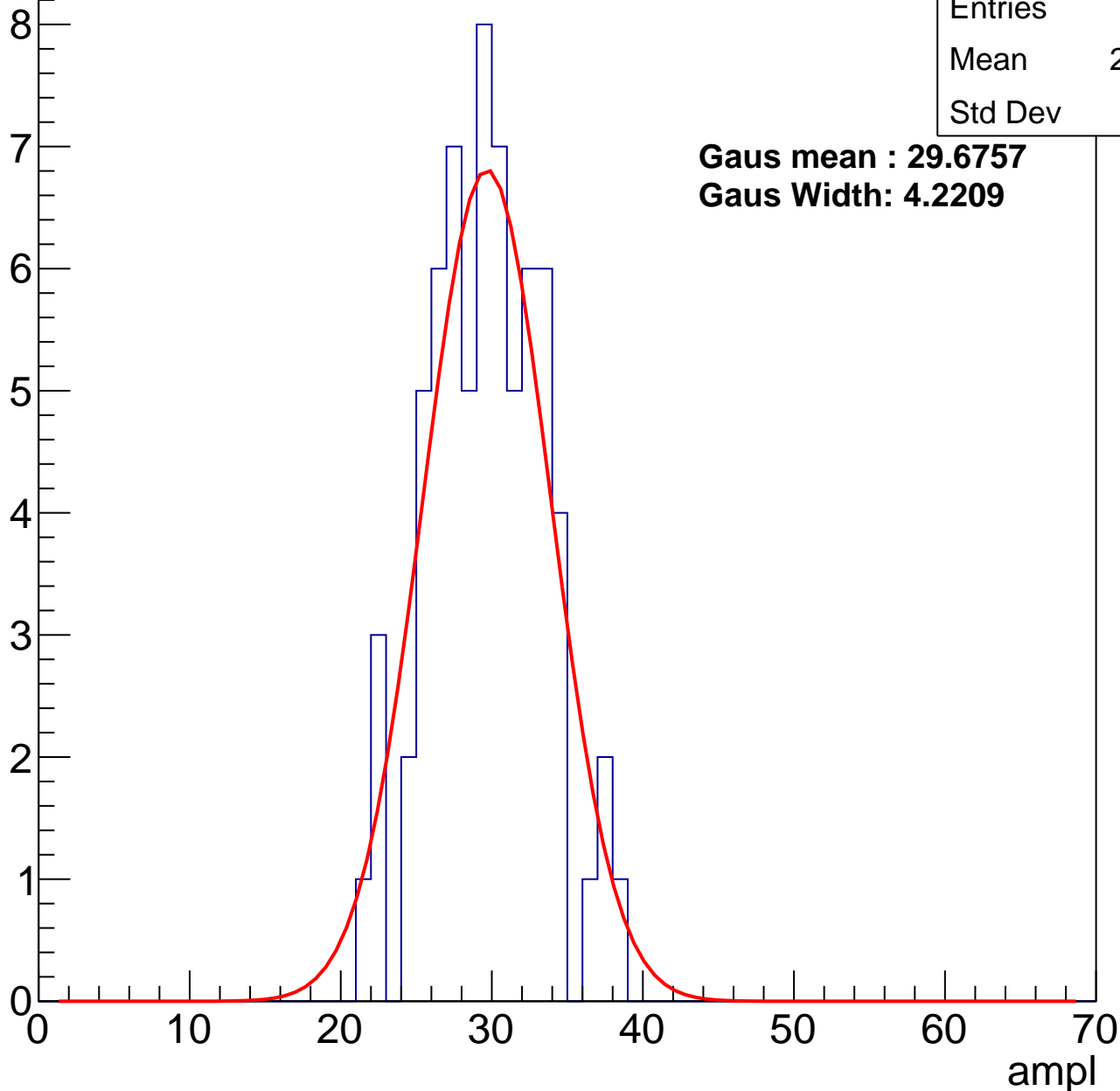
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.22
Std Dev	3.73

Gaus mean : 29.6757

Gaus Width: 4.2209



B1L102S, U11-ch41, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	34.83
Std Dev	3.209

Gaus mean : 35.5095

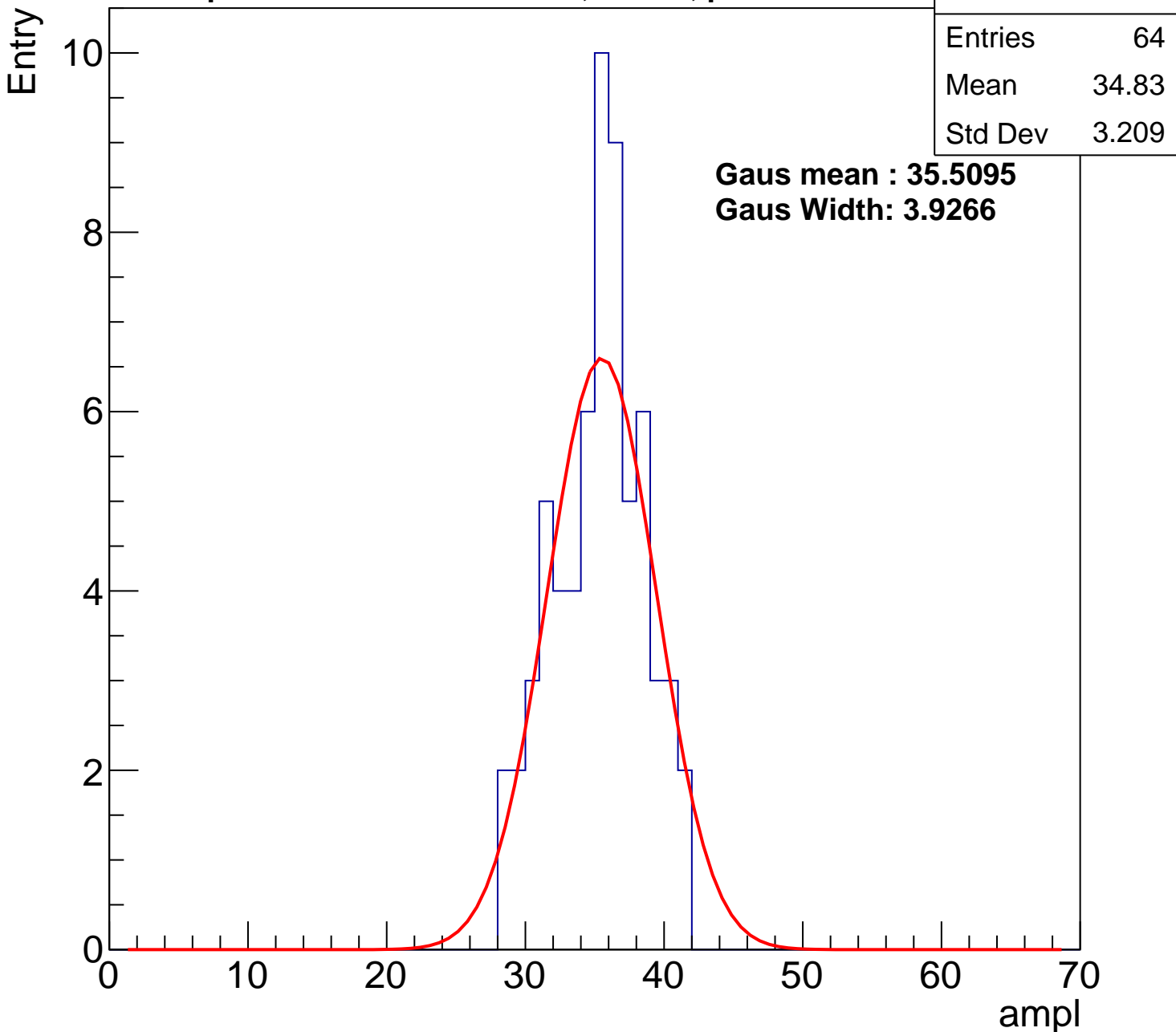
Gaus Width: 3.9266

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch41, adc2

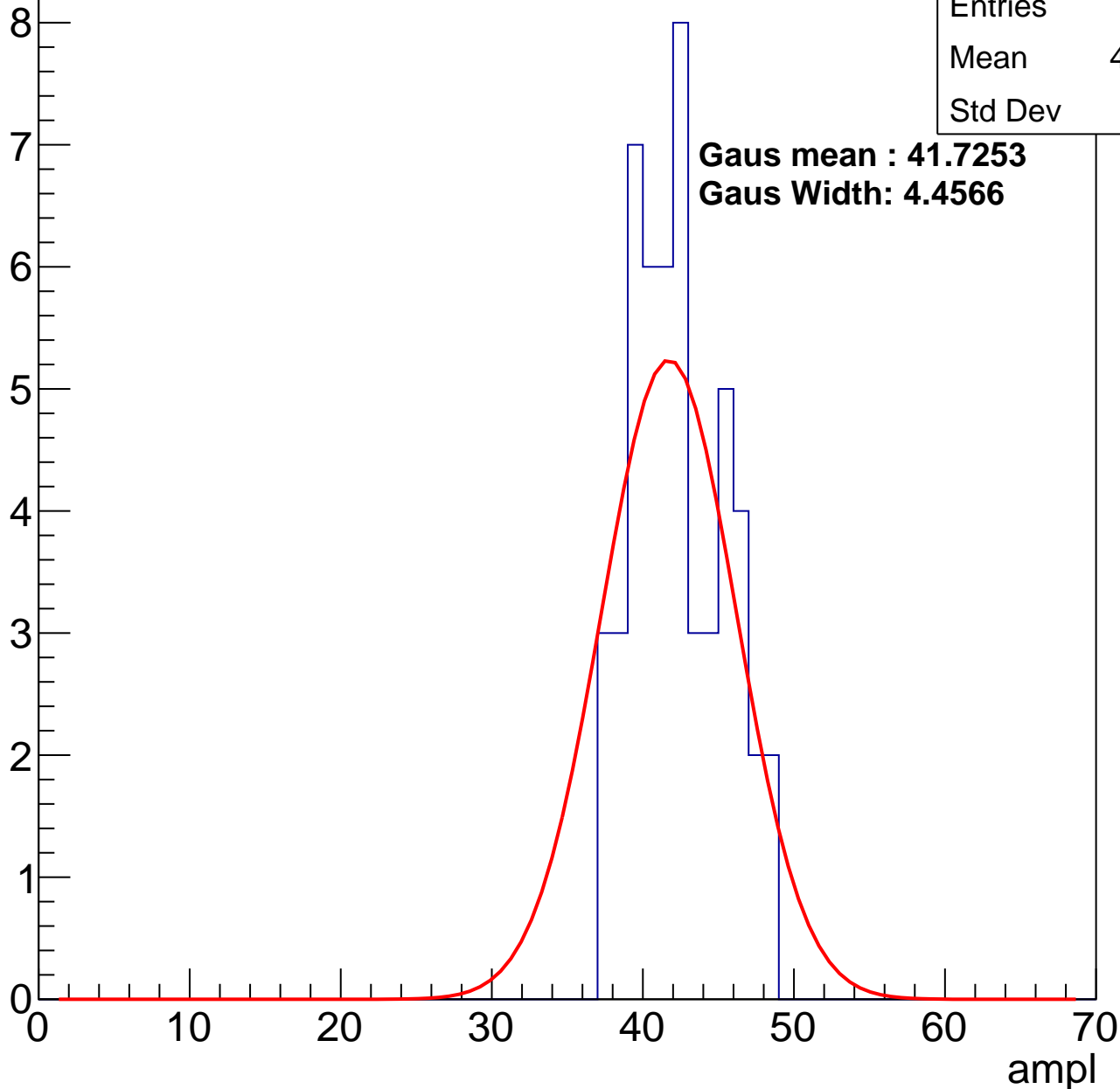
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	41.92
Std Dev	2.98

Gaus mean : 41.7253

Gaus Width: 4.4566



B1L102S, U11-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

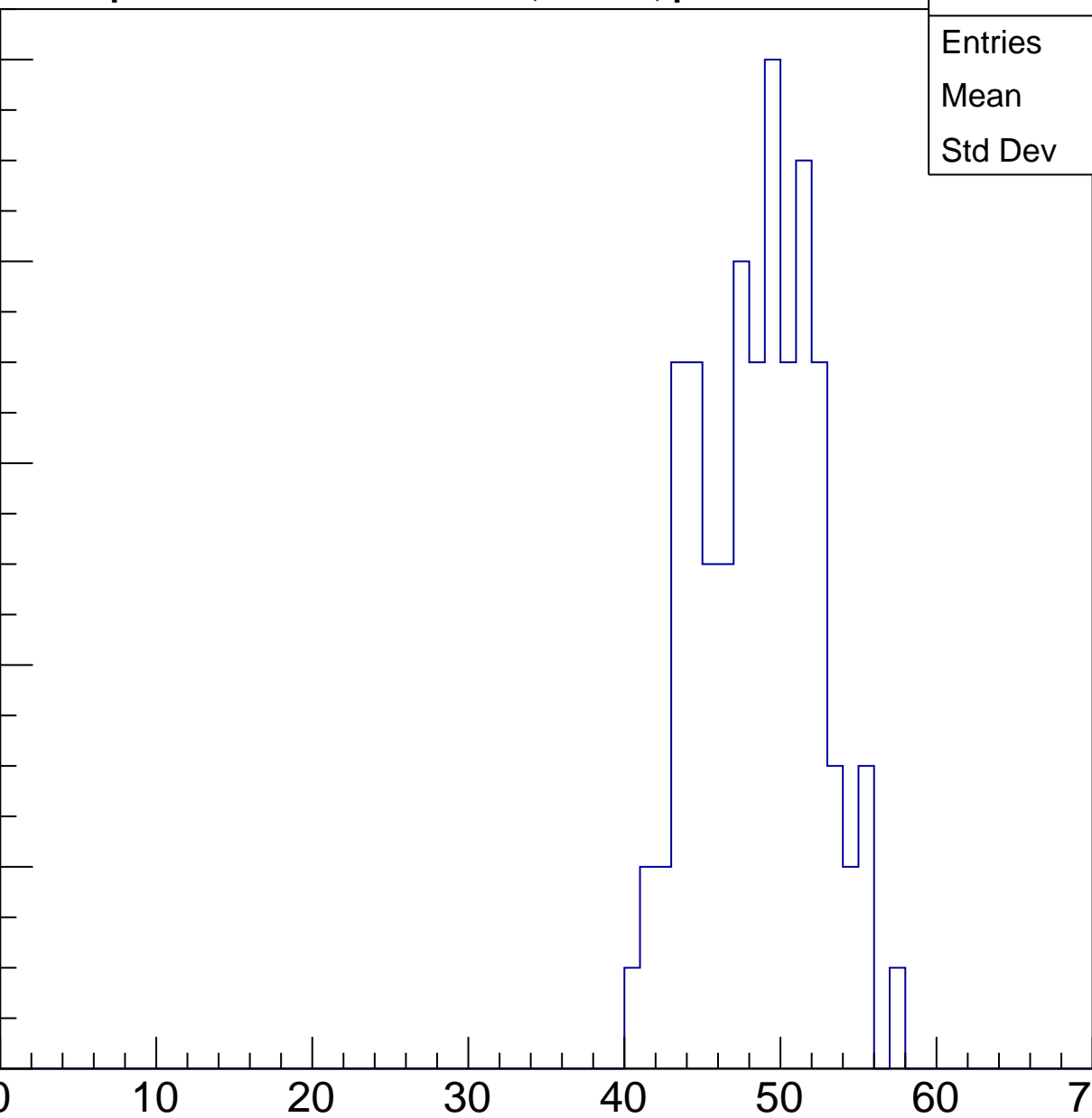
Entries	86
Mean	48.07
Std Dev	3.735

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

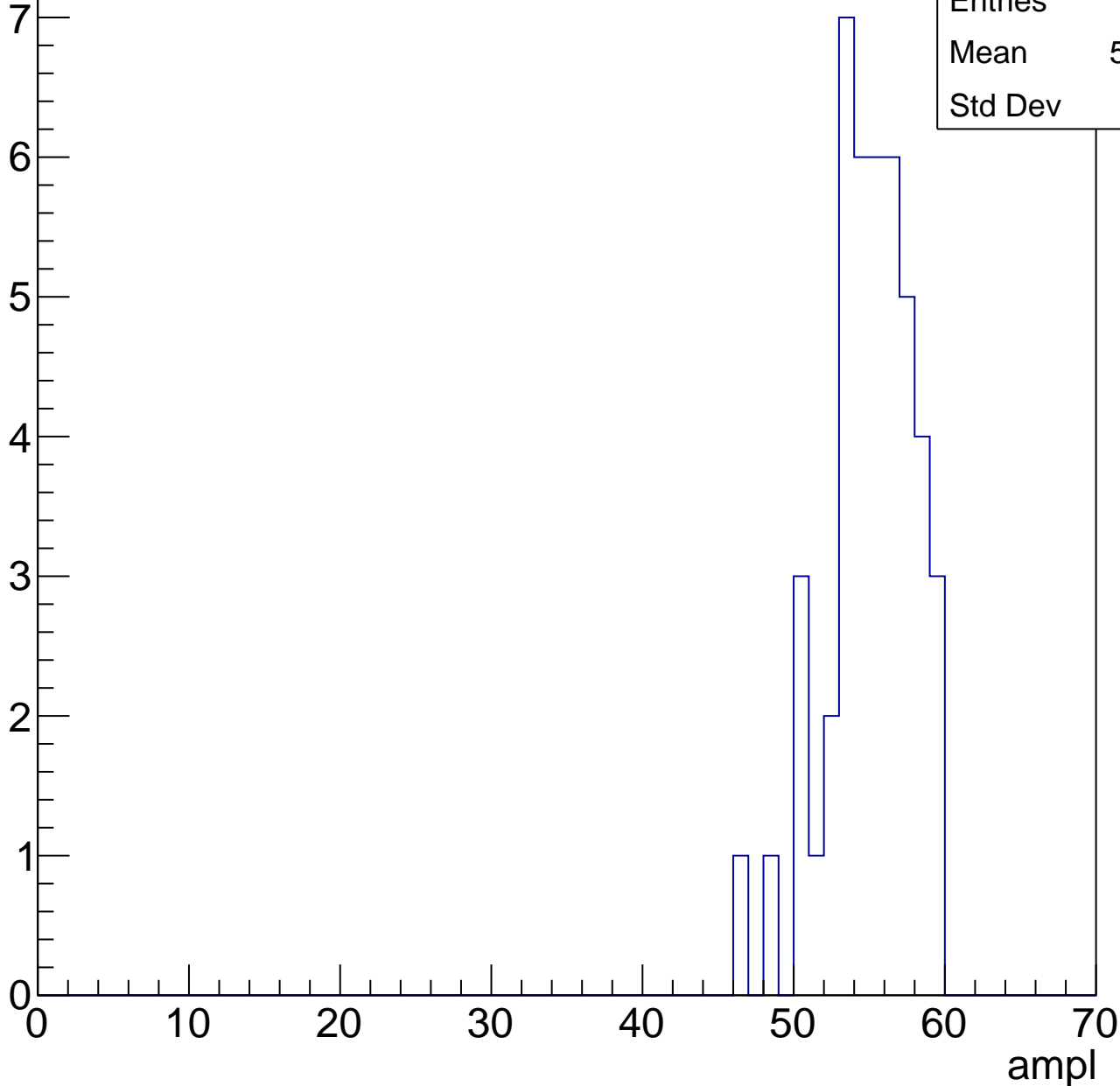


B1L102S, U11-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	54.53
Std Dev	2.88

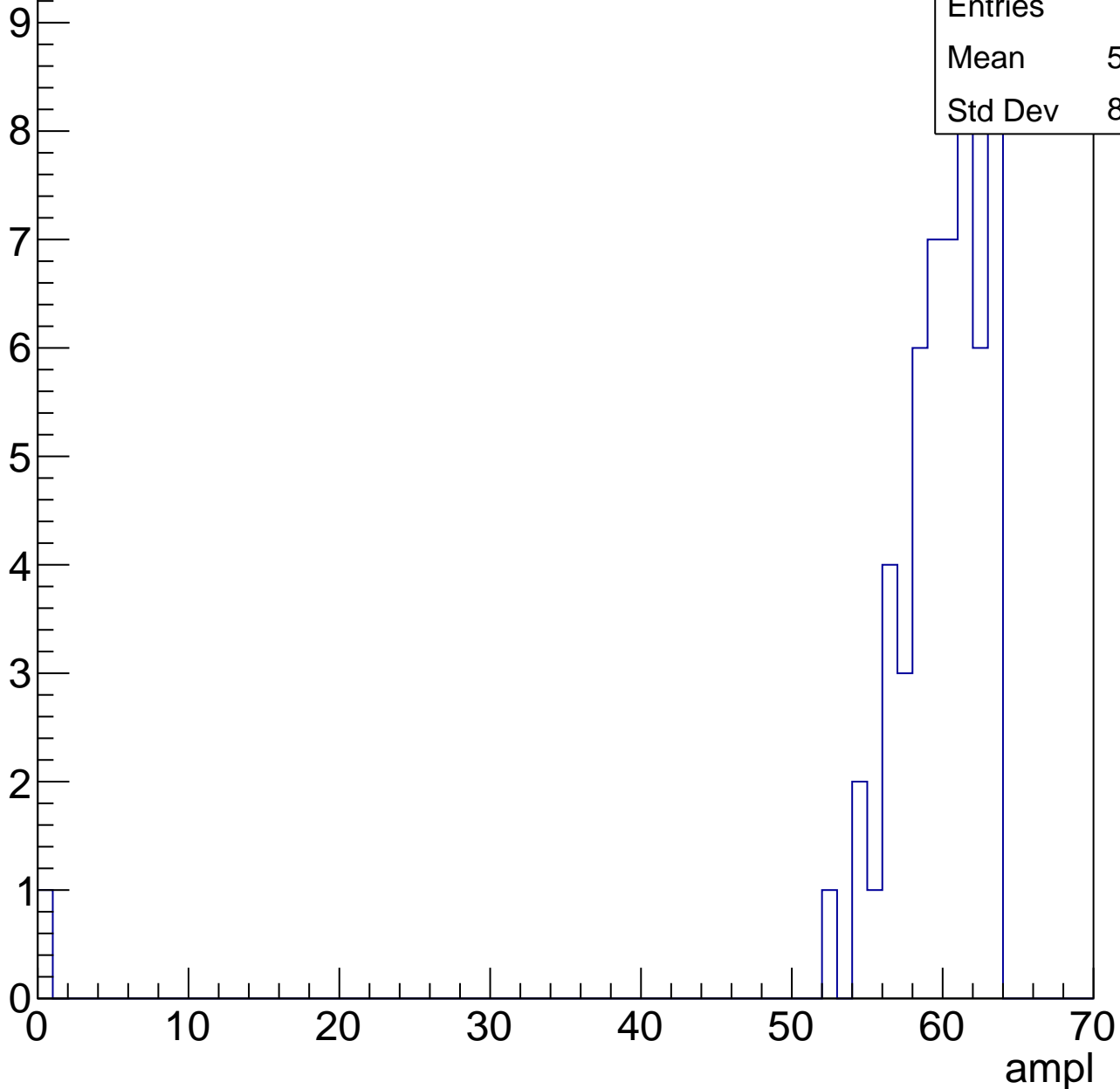


B1L102S, U11-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

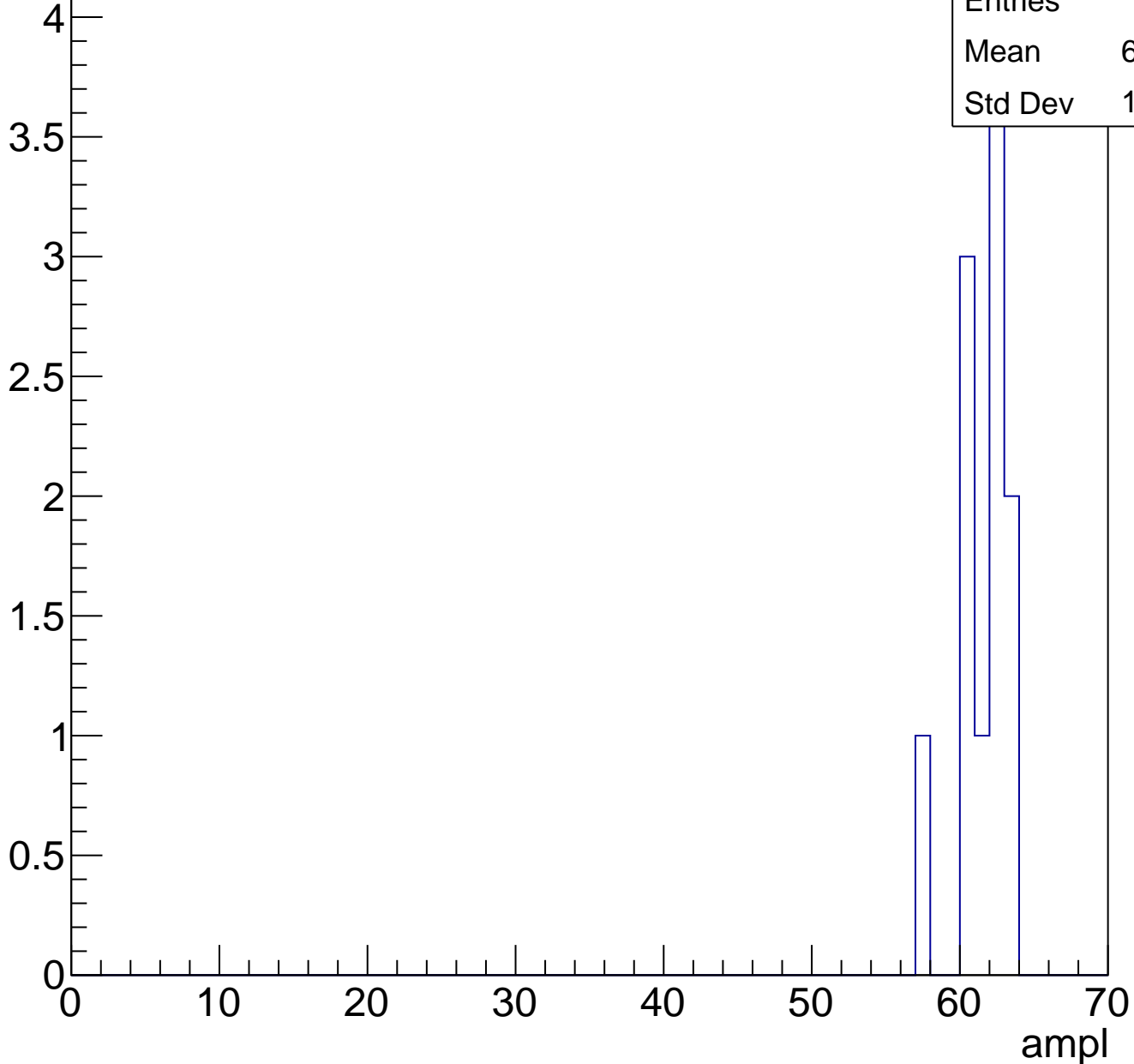
Entries	55
Mean	58.47
Std Dev	8.377



B1L102S, U11-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	11
Mean	61.09
Std Dev	1.676

B1L102S, U11-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch42, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	28.4
Std Dev	3.409

Gaus mean : 28.6266

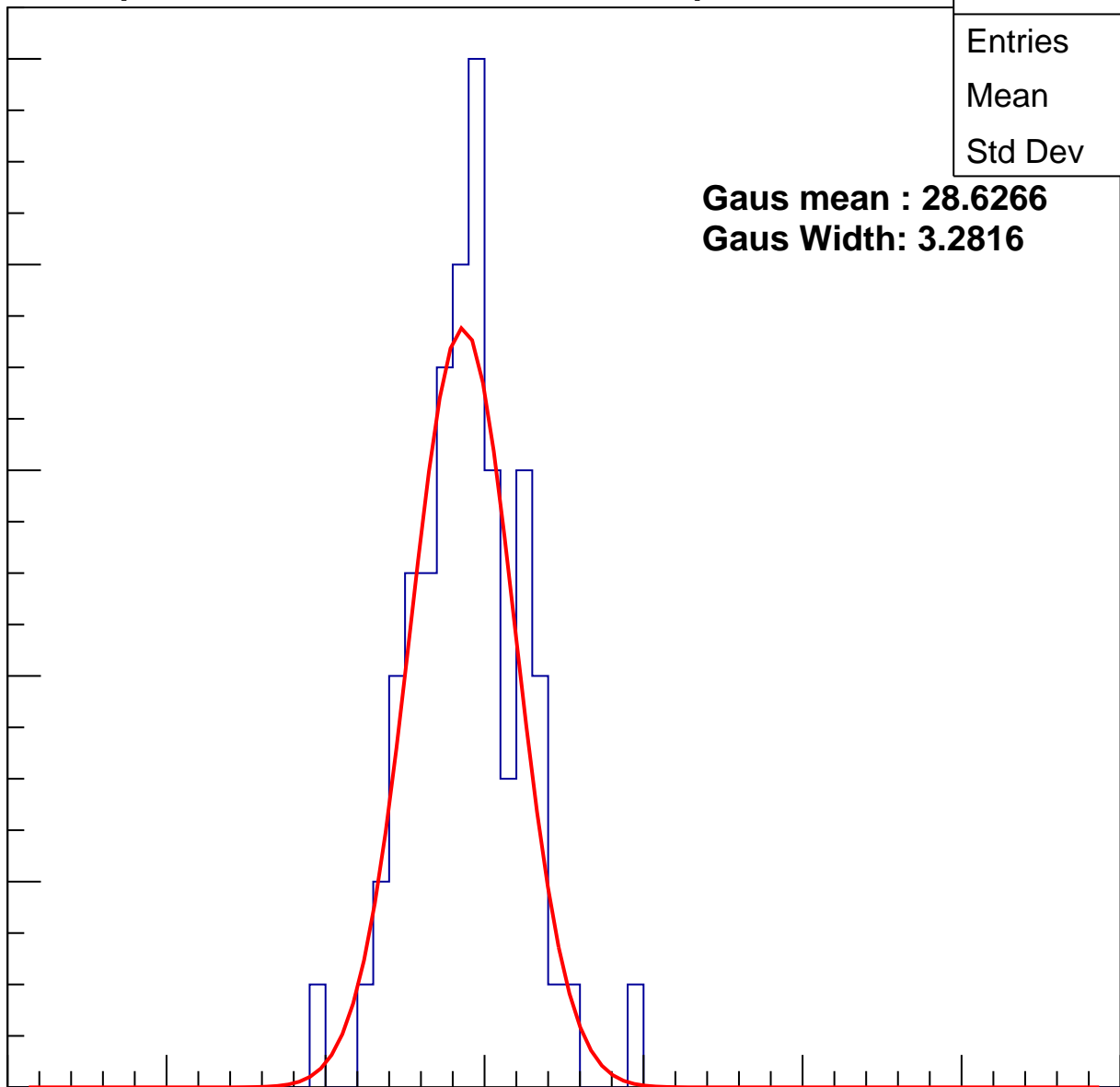
Gaus Width: 3.2816

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch42, adc1

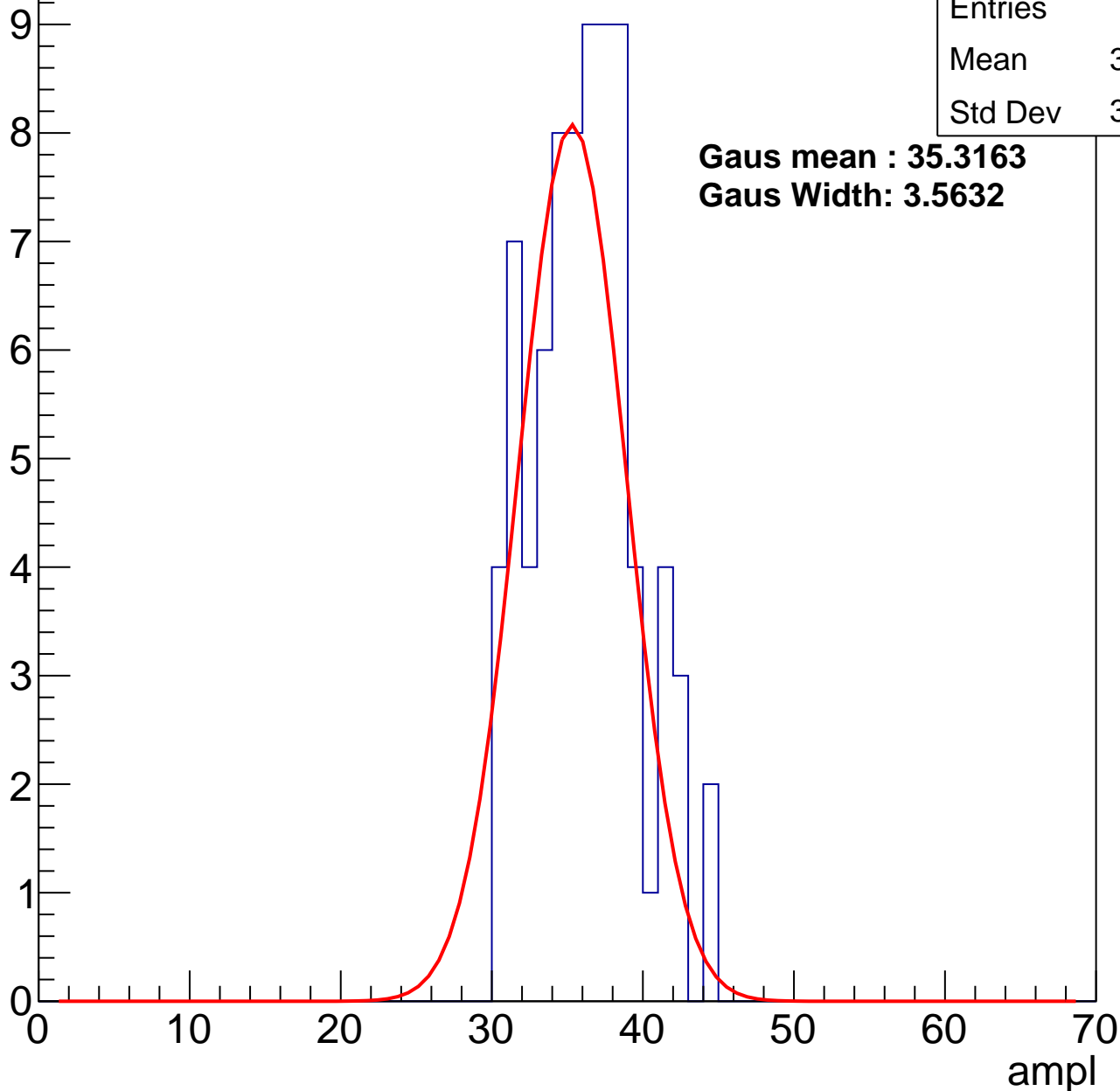
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.74
Std Dev	3.406

Gaus mean : 35.3163

Gaus Width: 3.5632



B1L102S, U11-ch42, adc2

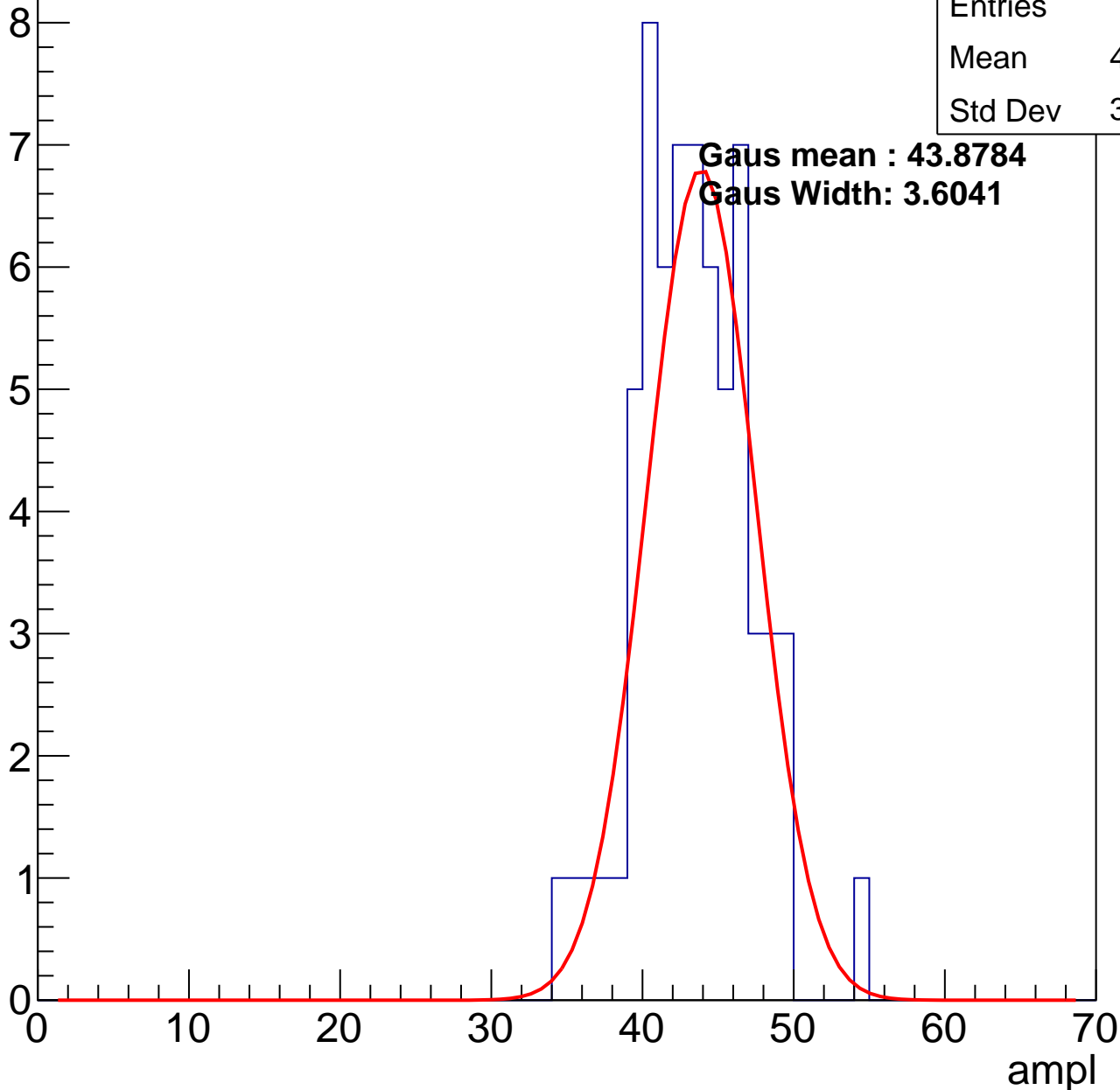
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.92
Std Dev	3.649

Gaus mean : 43.8784

Gaus Width: 3.6041

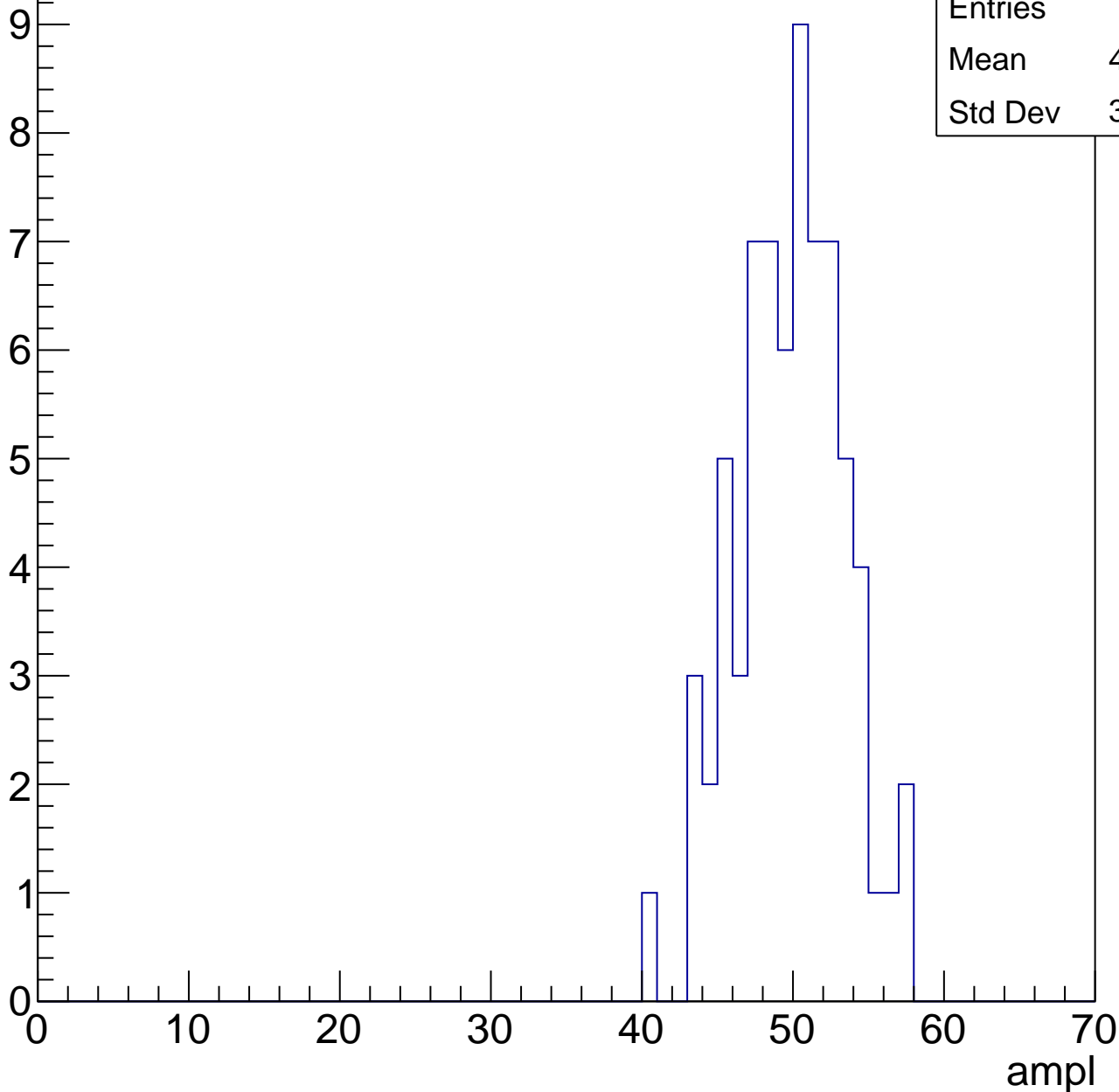


B1L102S, U11-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

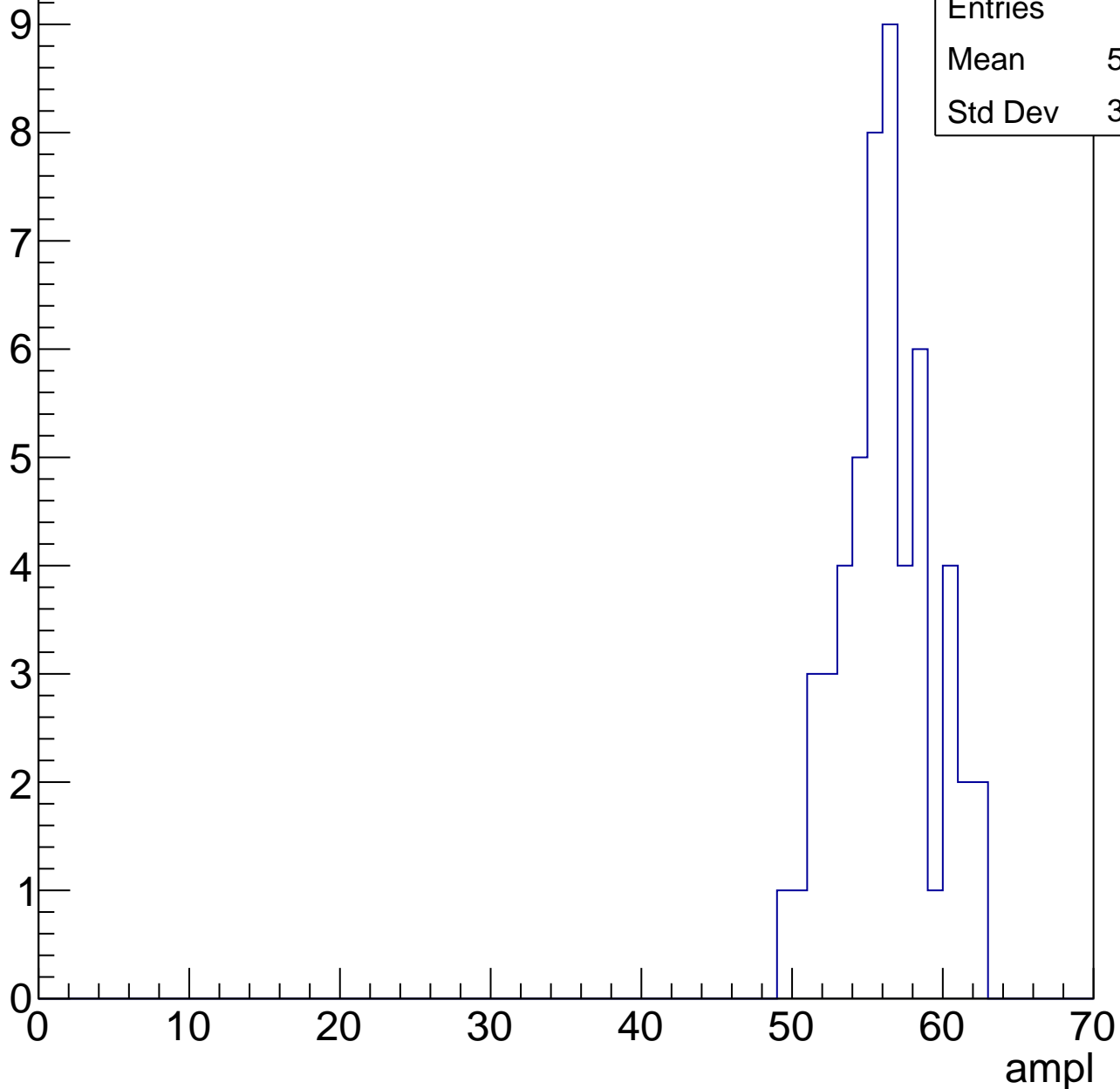
Entries	70
Mean	49.37
Std Dev	3.514



B1L102S, U11-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch42, adc5

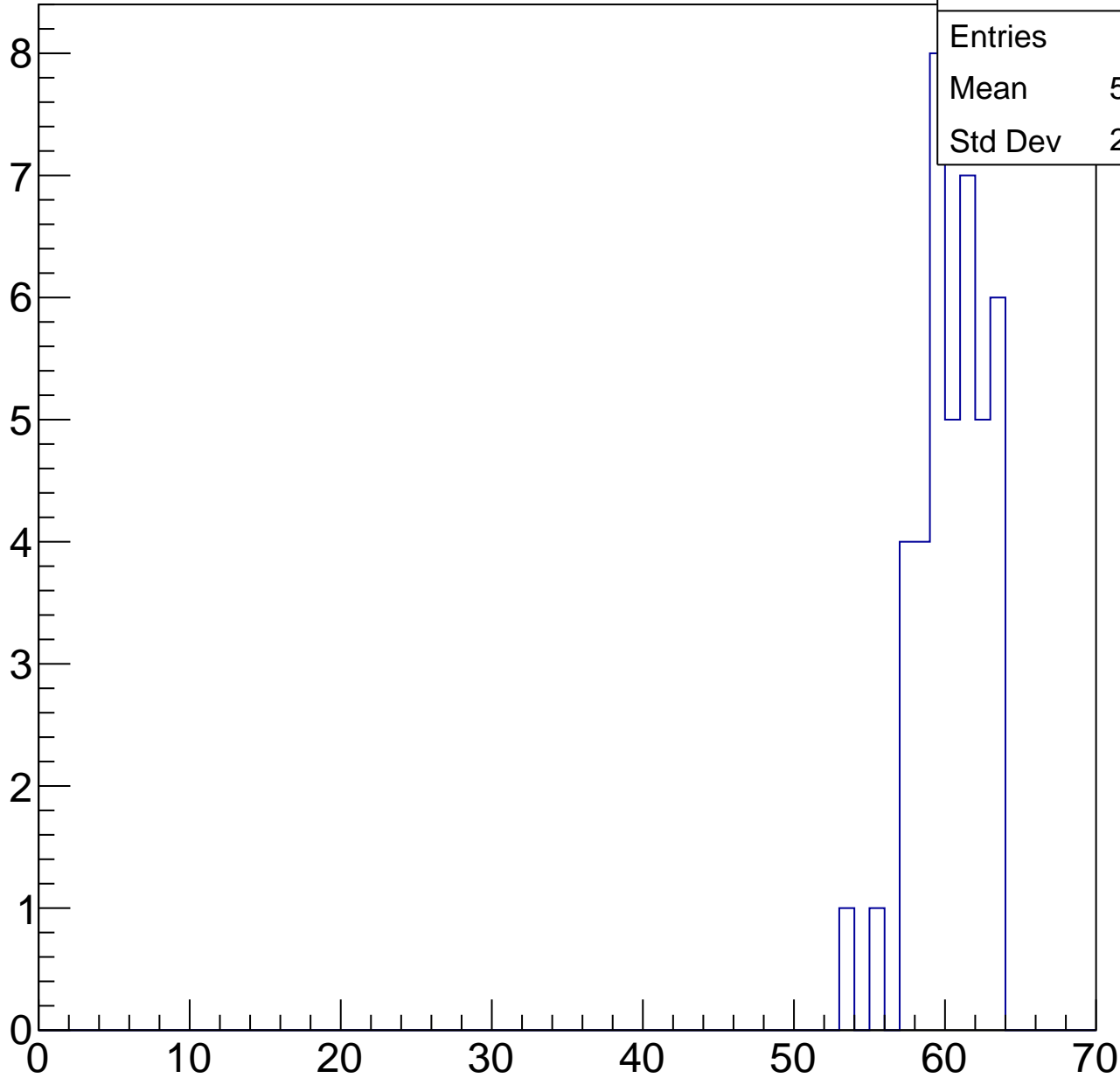
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.88
Std Dev	2.287

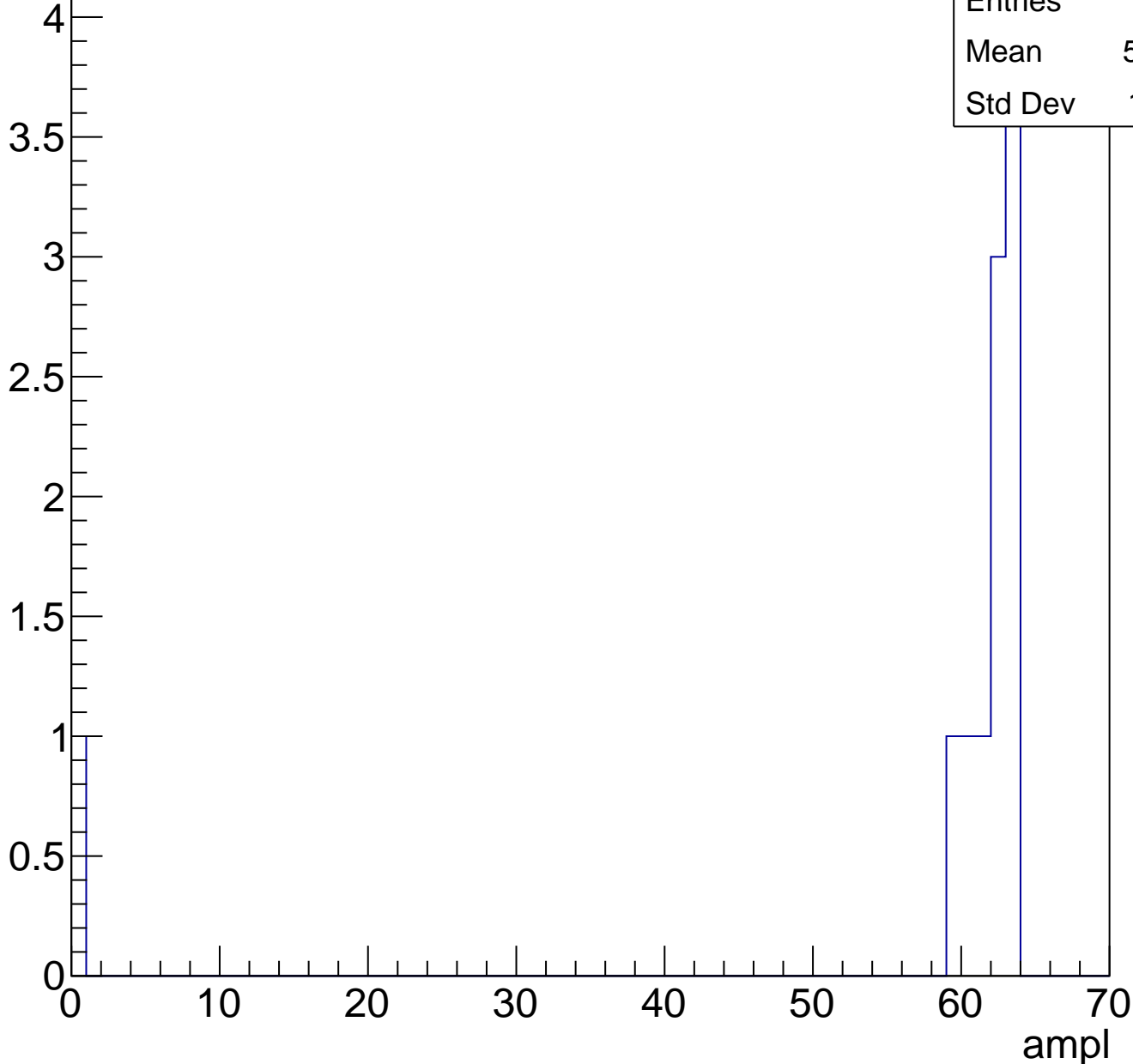
ampl



B1L102S, U11-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch43, adc0

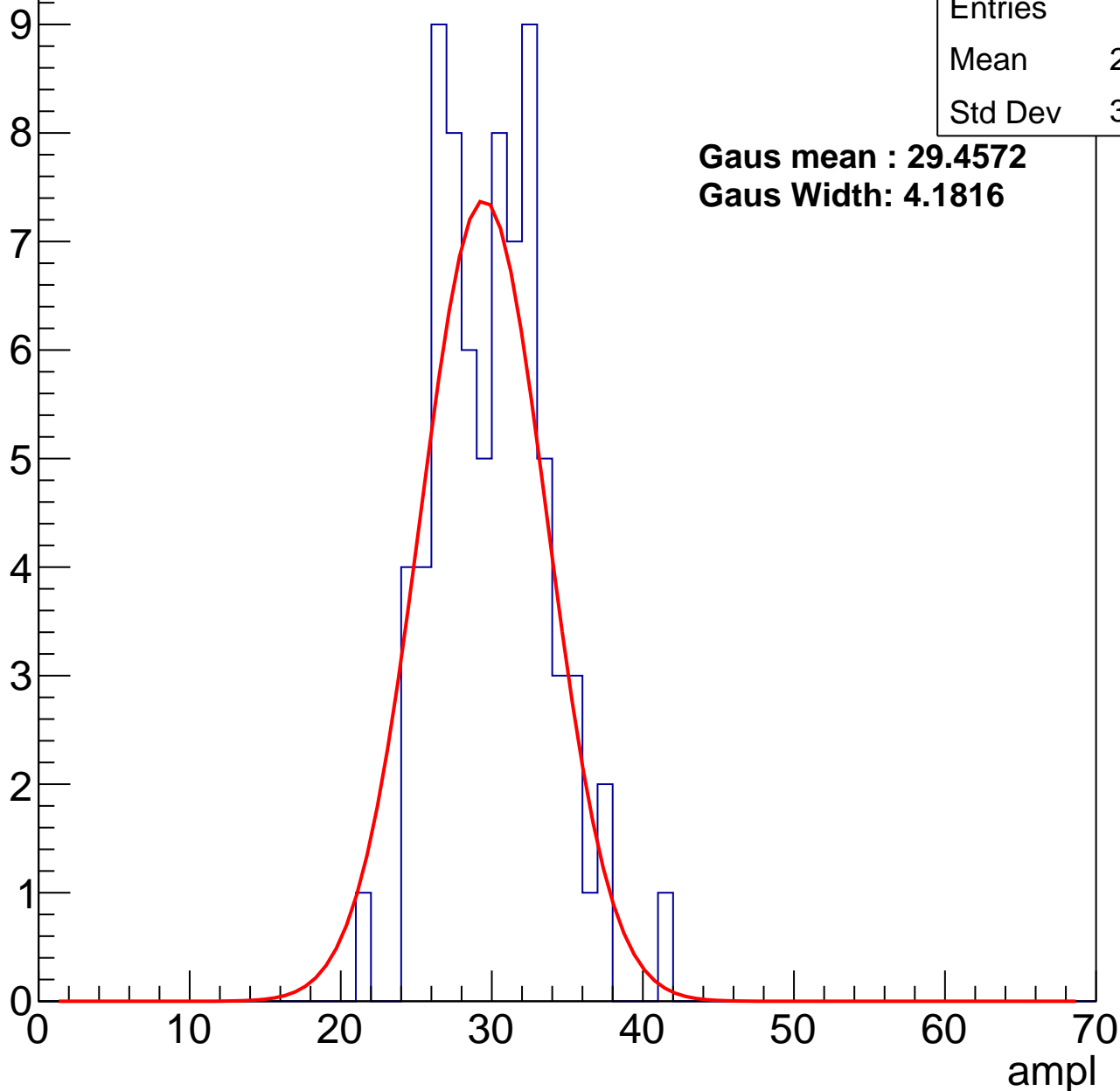
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.58
Std Dev	3.668

Gaus mean : 29.4572

Gaus Width: 4.1816



B1L102S, U11-ch43, adc1

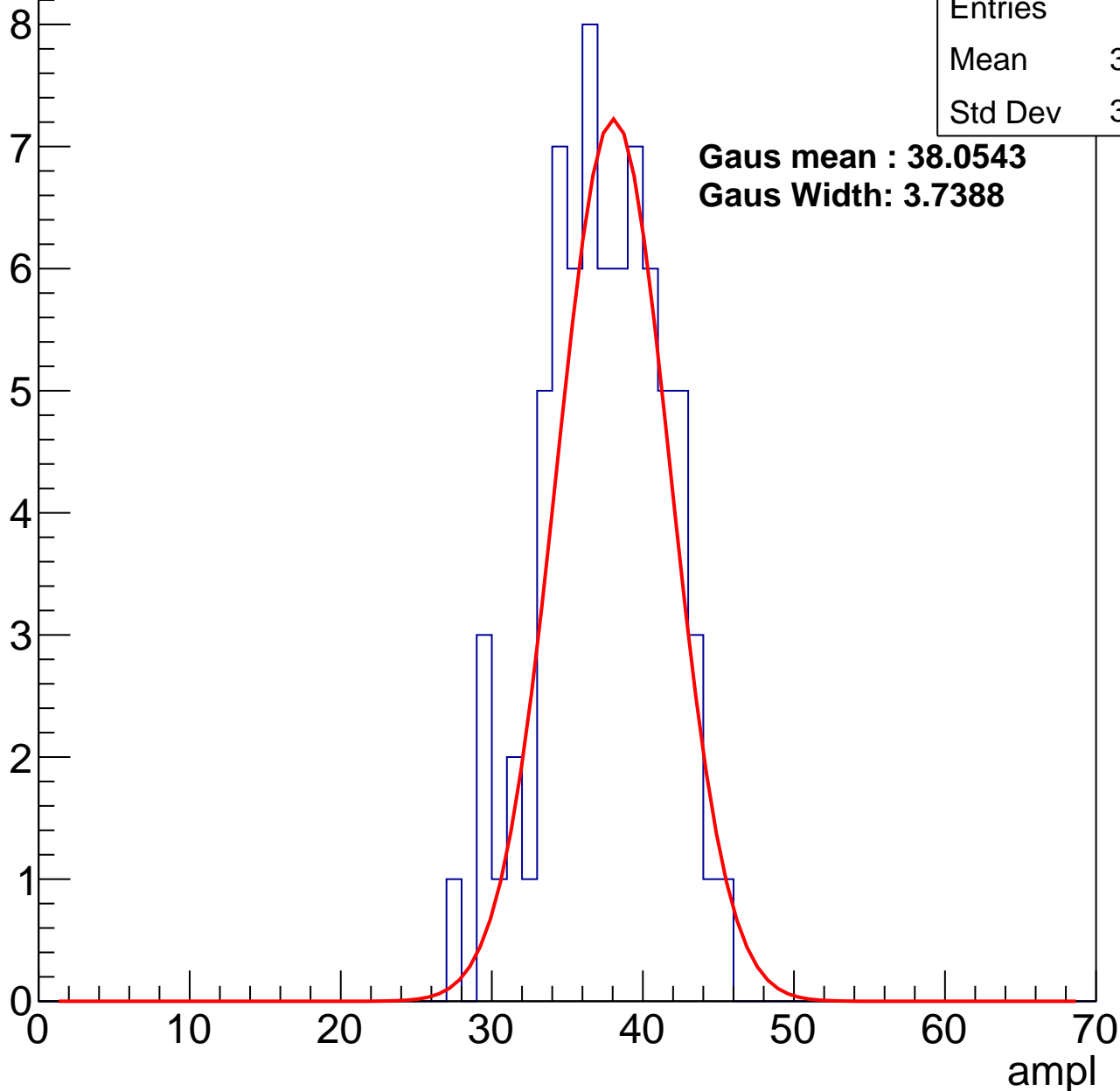
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.96
Std Dev	3.902

Gaus mean : 38.0543

Gaus Width: 3.7388



B1L102S, U11-ch43, adc2

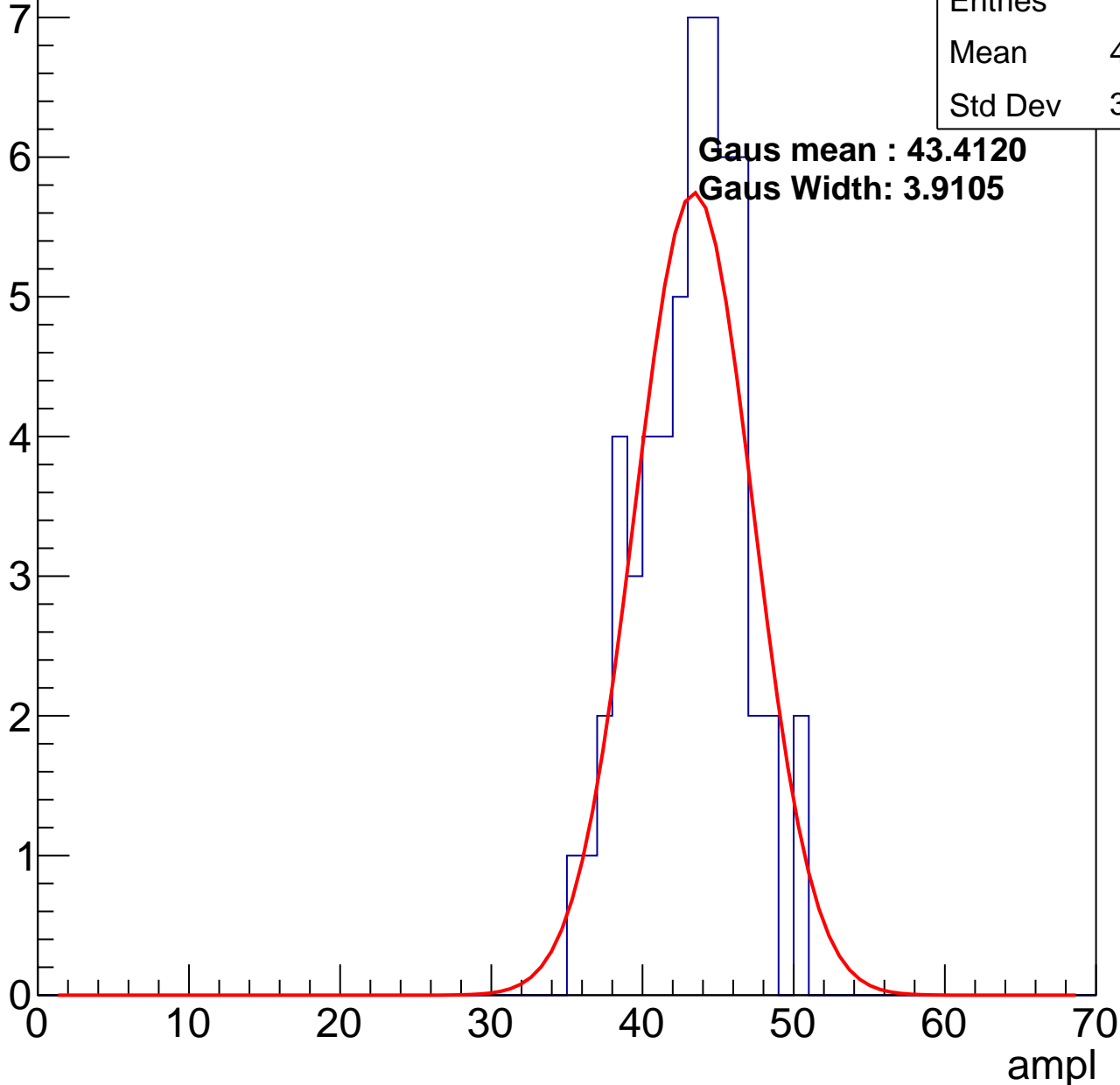
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.73
Std Dev	3.409

Gaus mean : 43.4120

Gaus Width: 3.9105

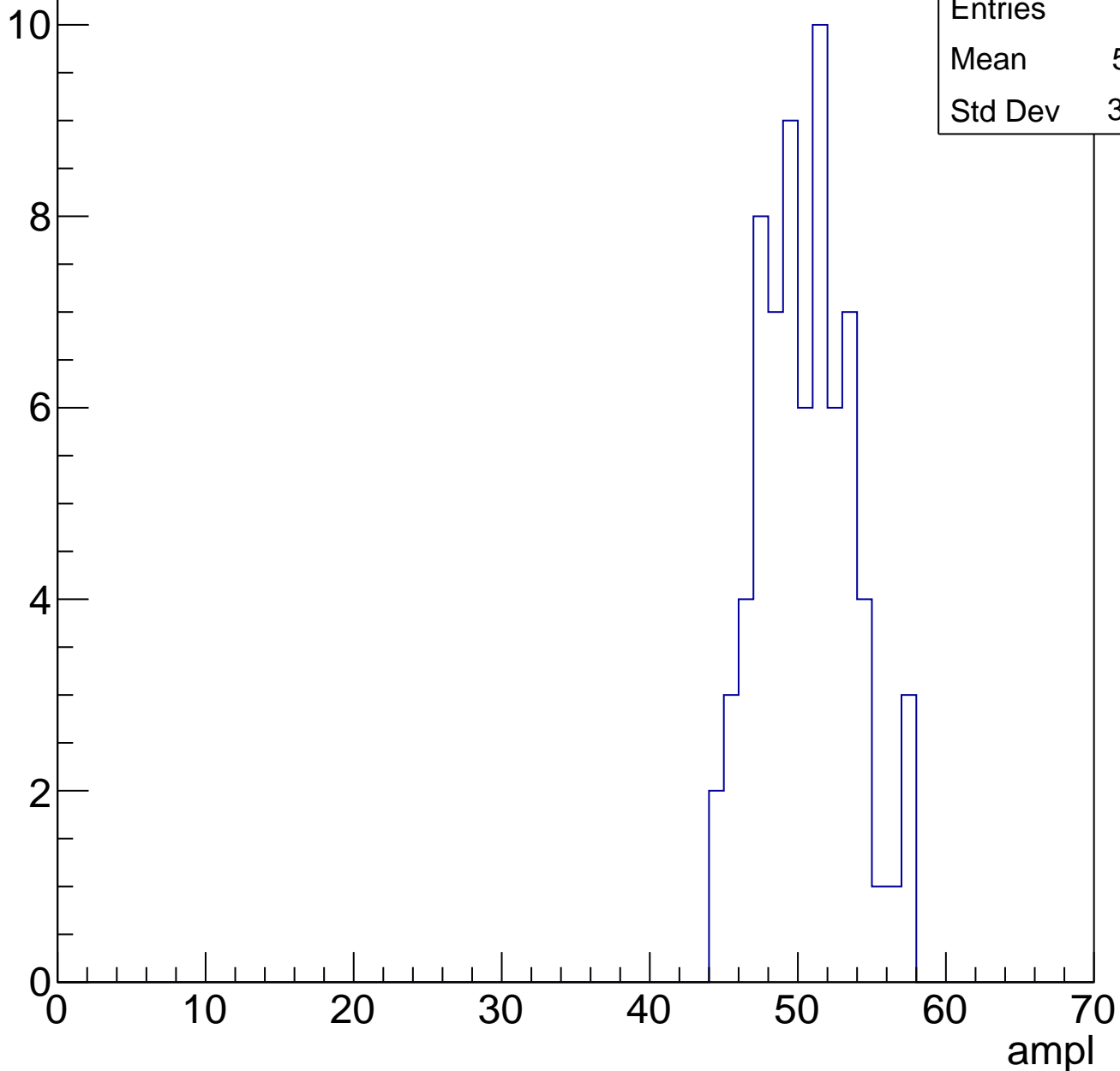


B1L102S, U11-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	50.01
Std Dev	3.115

Entry

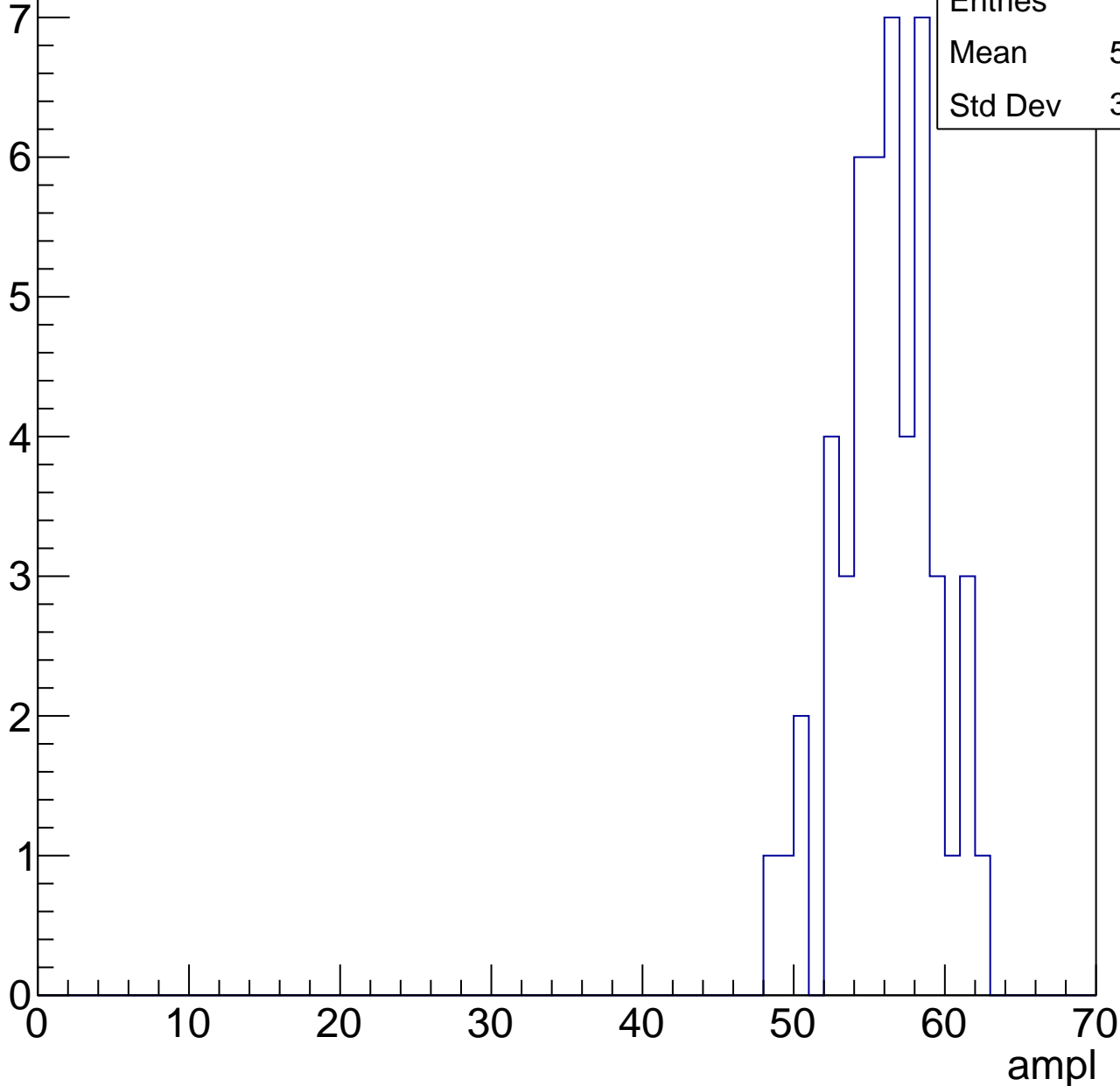


B1L102S, U11-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.63
Std Dev	3.147



B1L102S, U11-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

Entries 53

Mean 59.02

Std Dev 8.502

8

6

4

2

0

0

10

20

30

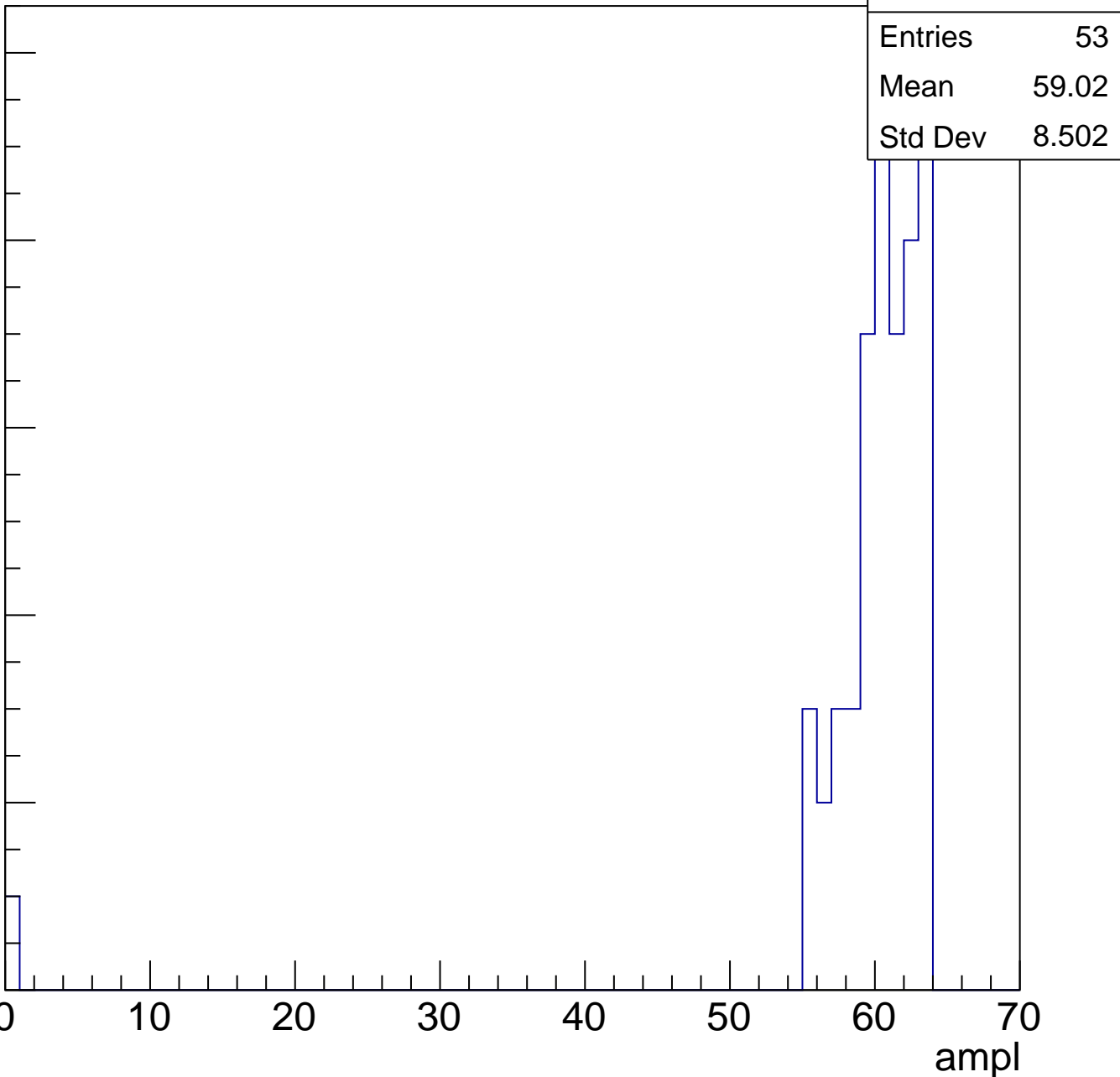
40

50

60

70

ampl



B1L102S, U11-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch44, adc0

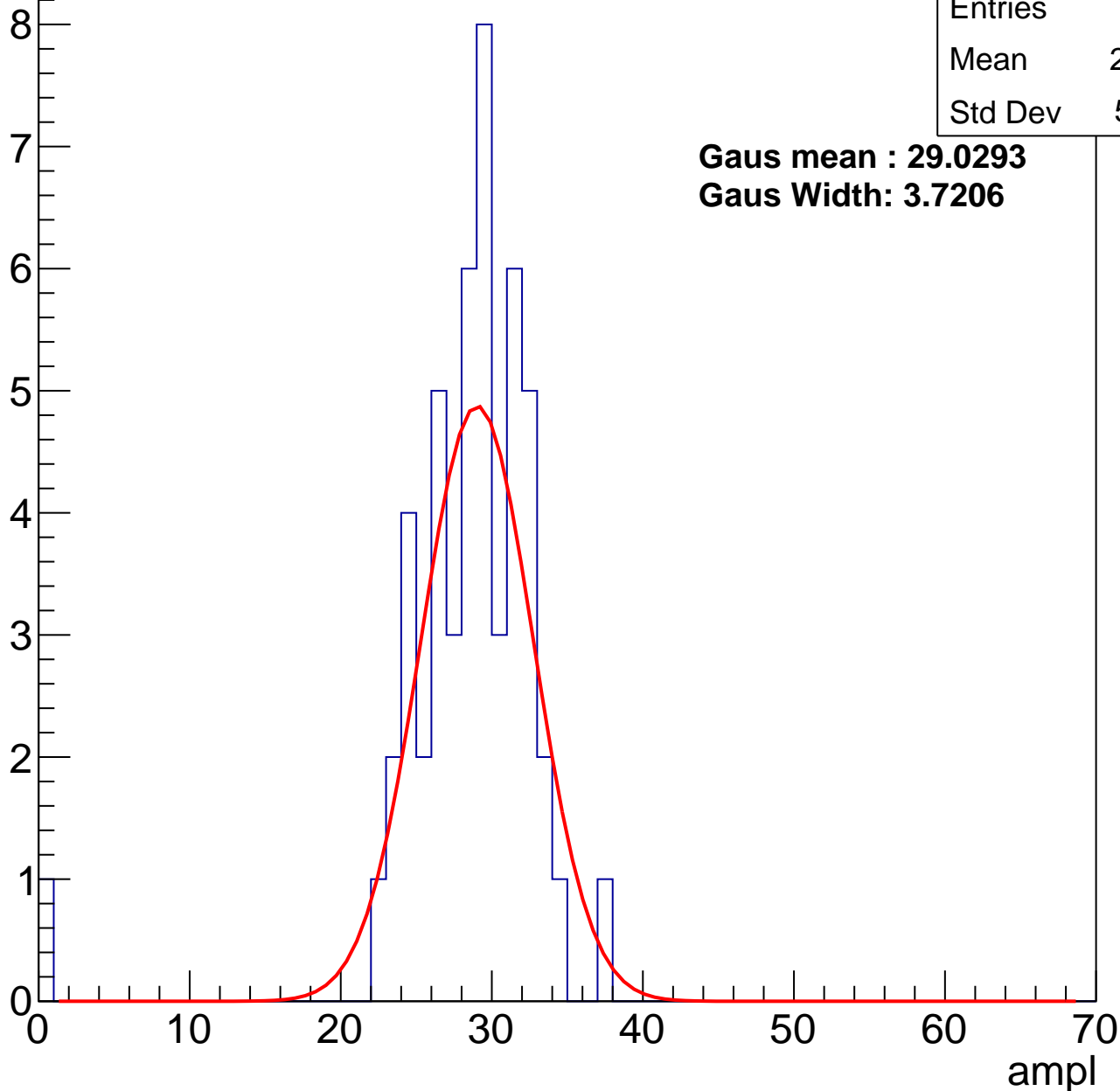
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	27.96
Std Dev	5.091

Gaus mean : 29.0293

Gaus Width: 3.7206



B1L102S, U11-ch44, adc1

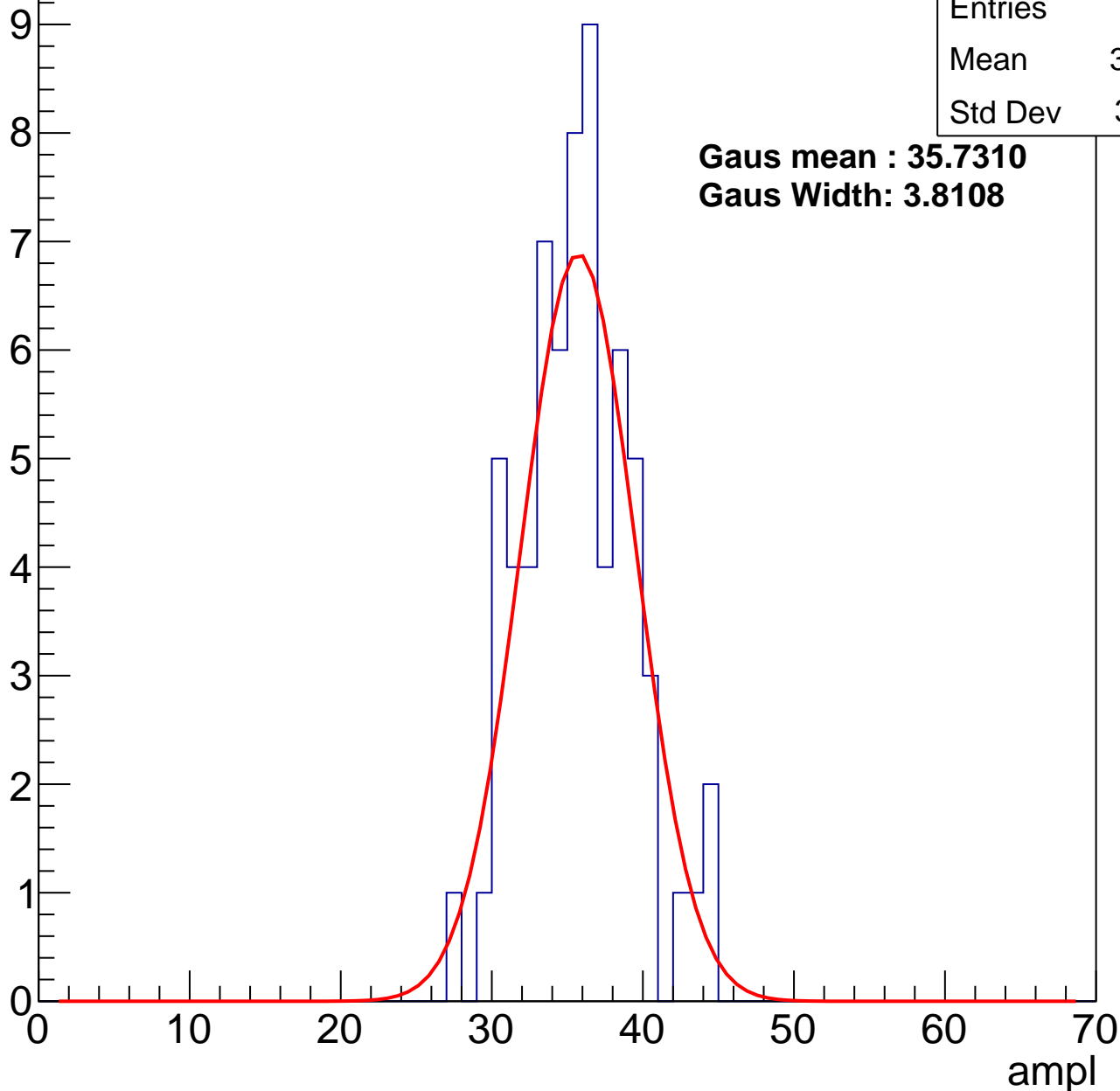
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.24
Std Dev	3.591

Gaus mean : 35.7310

Gaus Width: 3.8108



B1L102S, U11-ch44, adc2

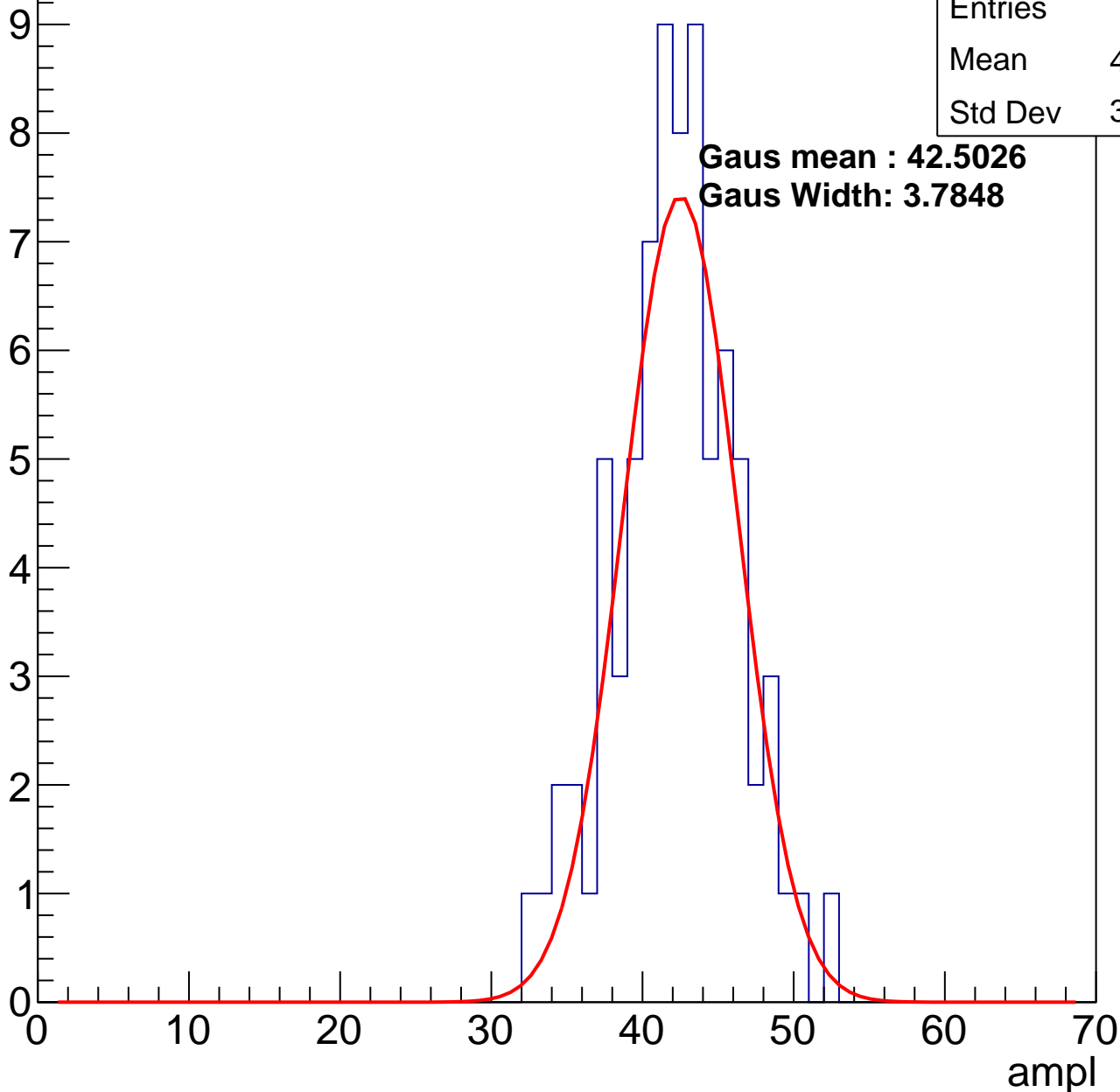
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.74
Std Dev	3.992

Gaus mean : 42.5026

Gaus Width: 3.7848

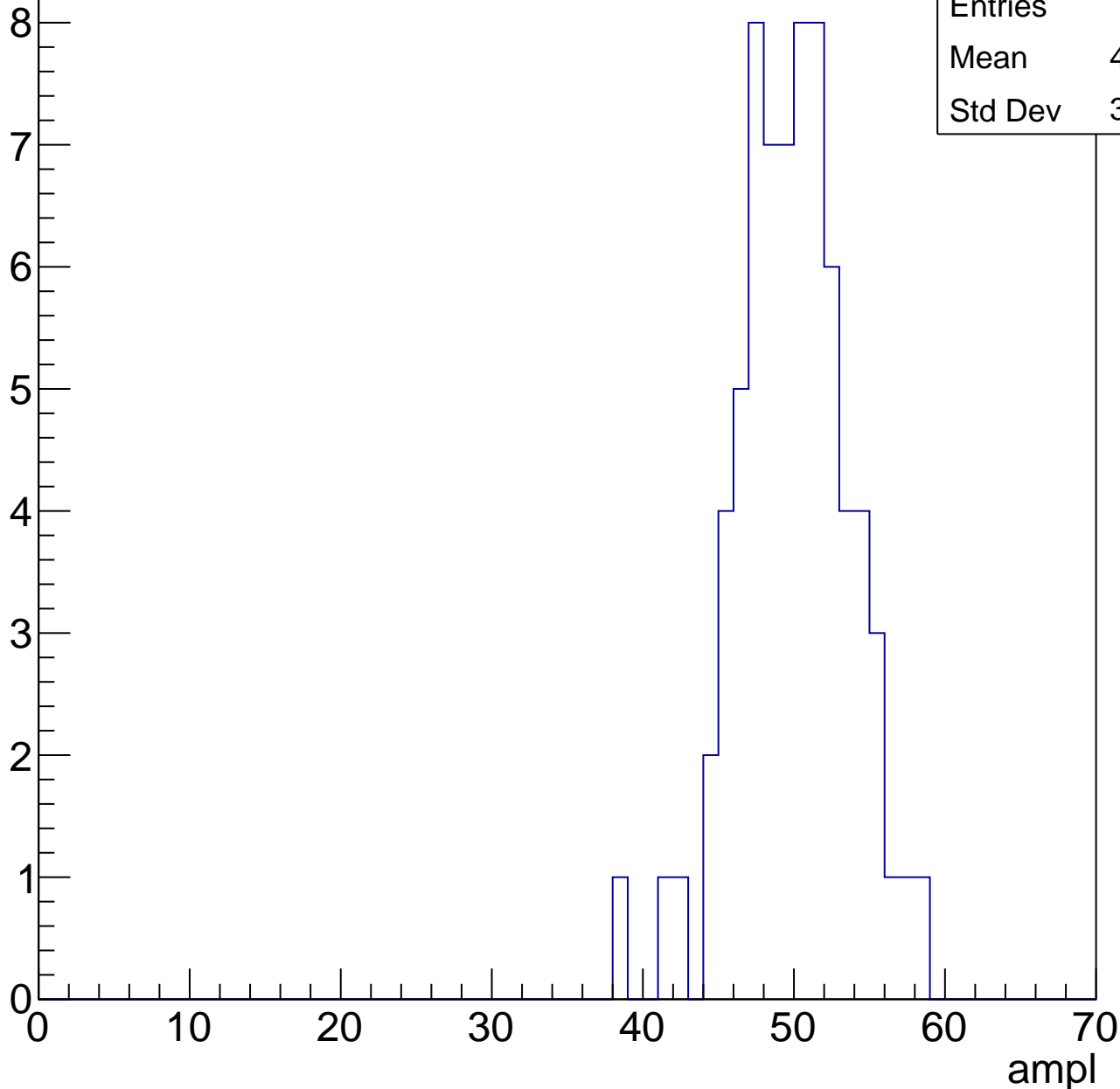


B1L102S, U11-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.42
Std Dev	3.688

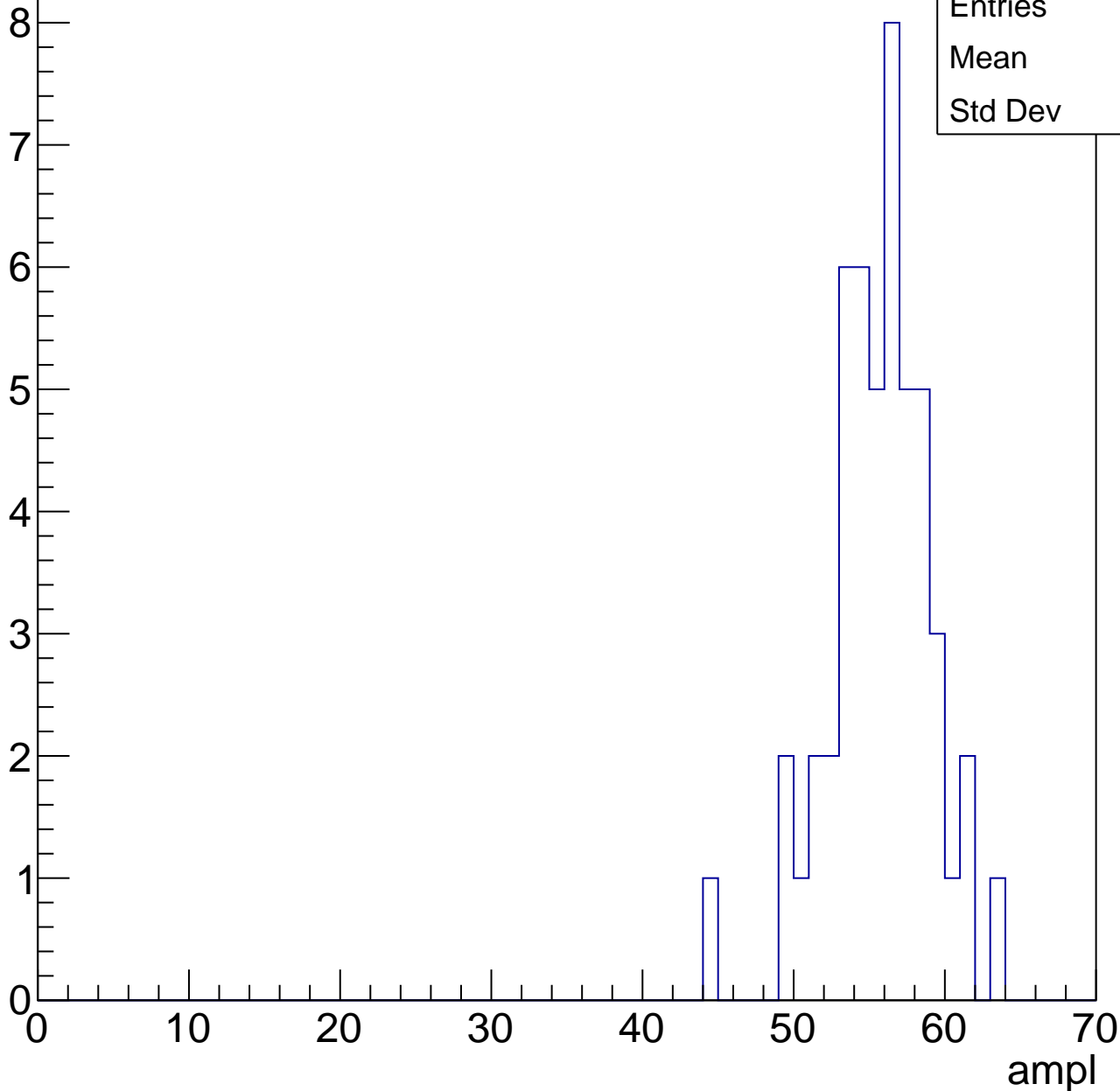


B1L102S, U11-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.2
Std Dev	3.4

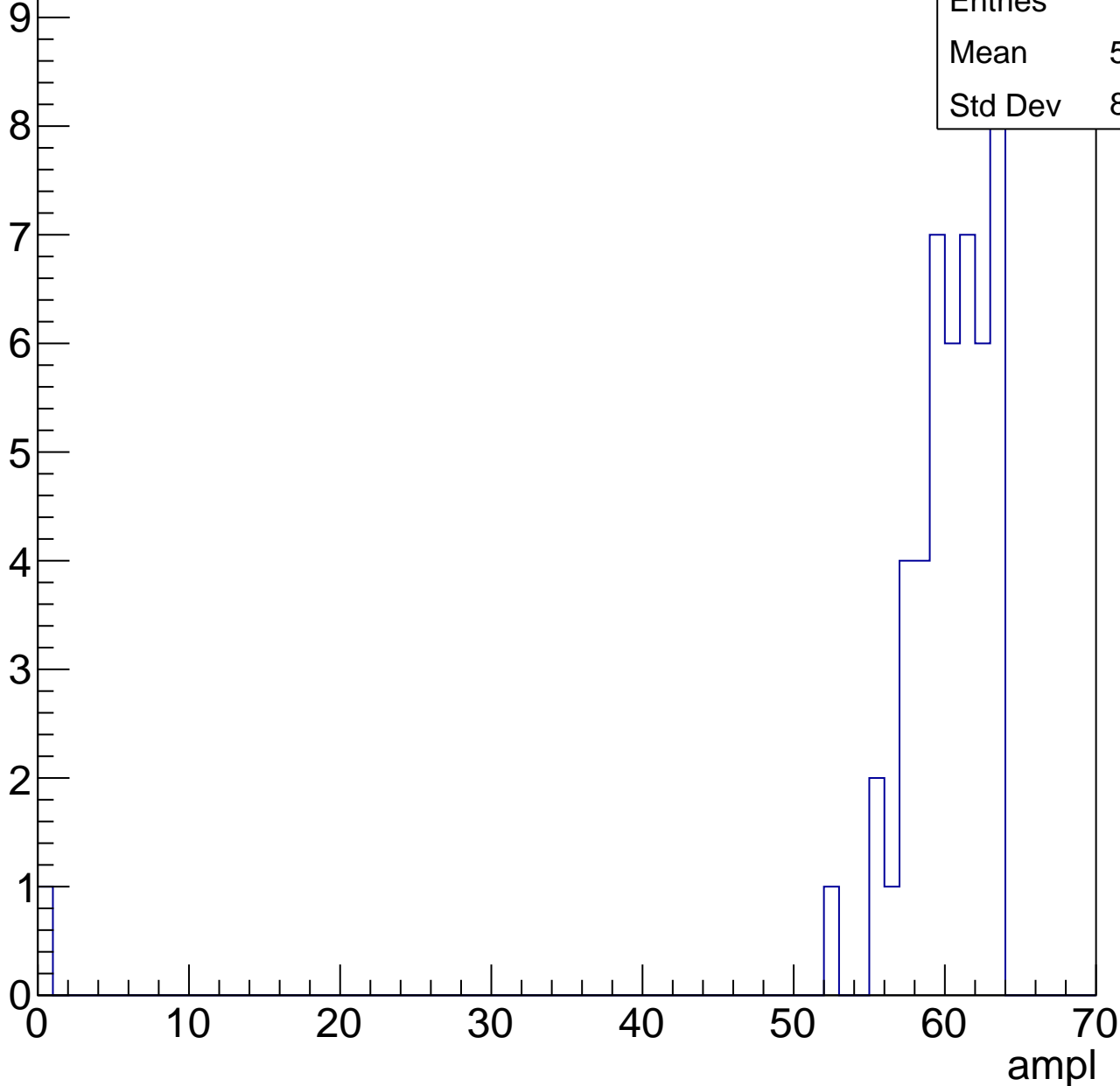


B1L102S, U11-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

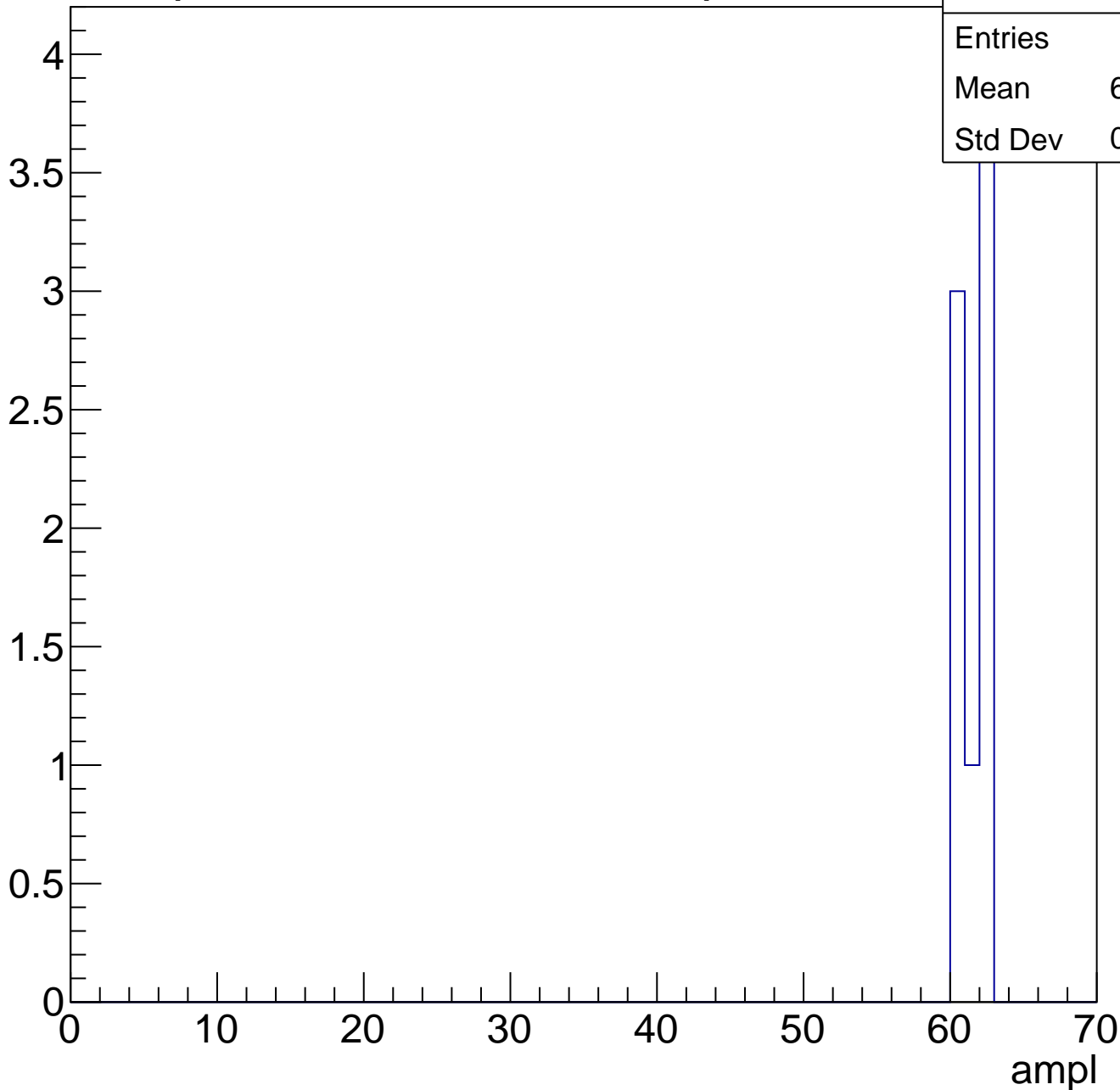
Entries	48
Mean	58.69
Std Dev	8.919



B1L102S, U11-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L102S, U11-ch45, adc0

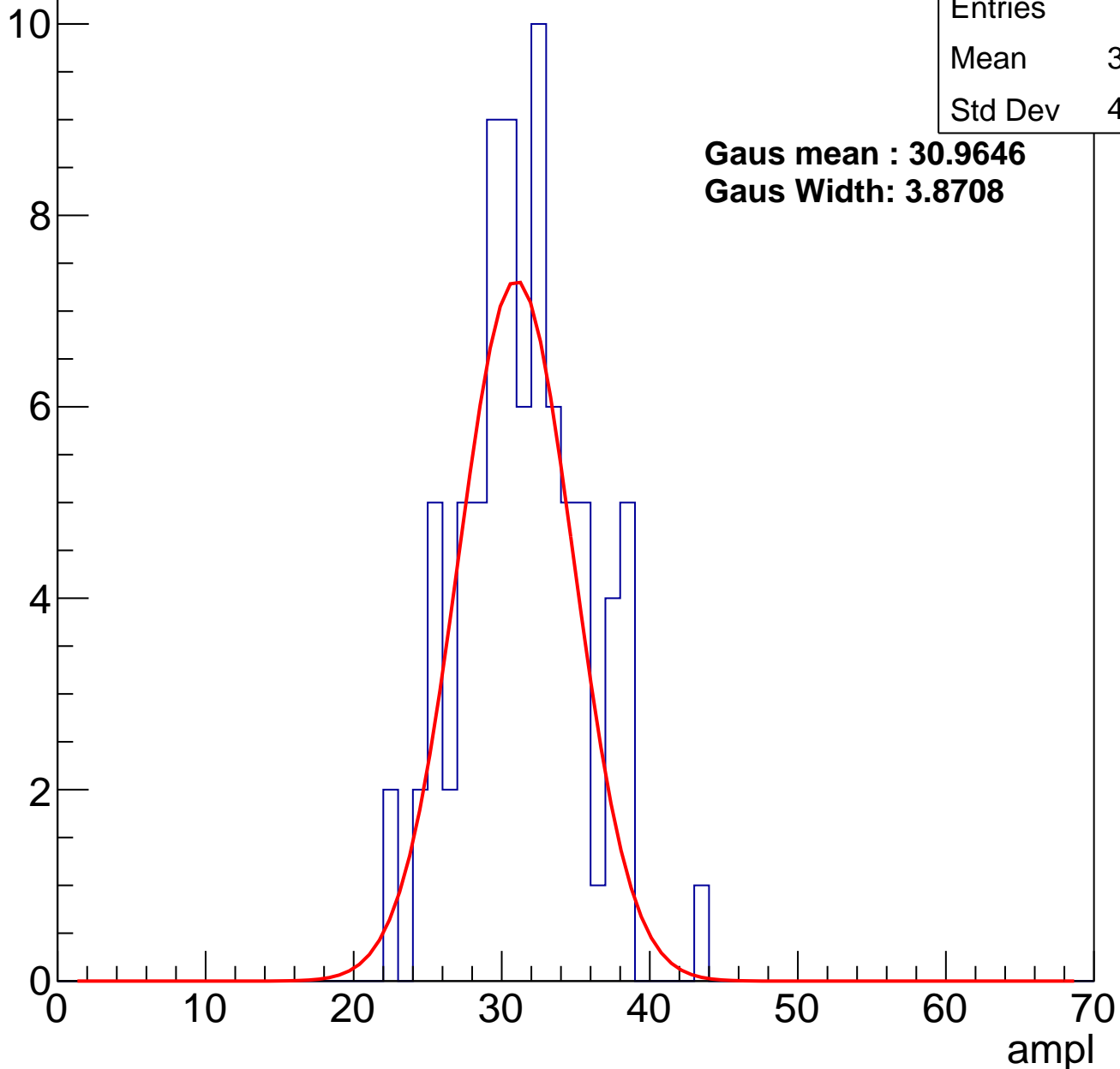
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	30.99
Std Dev	4.098

Gaus mean : 30.9646

Gaus Width: 3.8708

Entry



B1L102S, U11-ch45, adc1

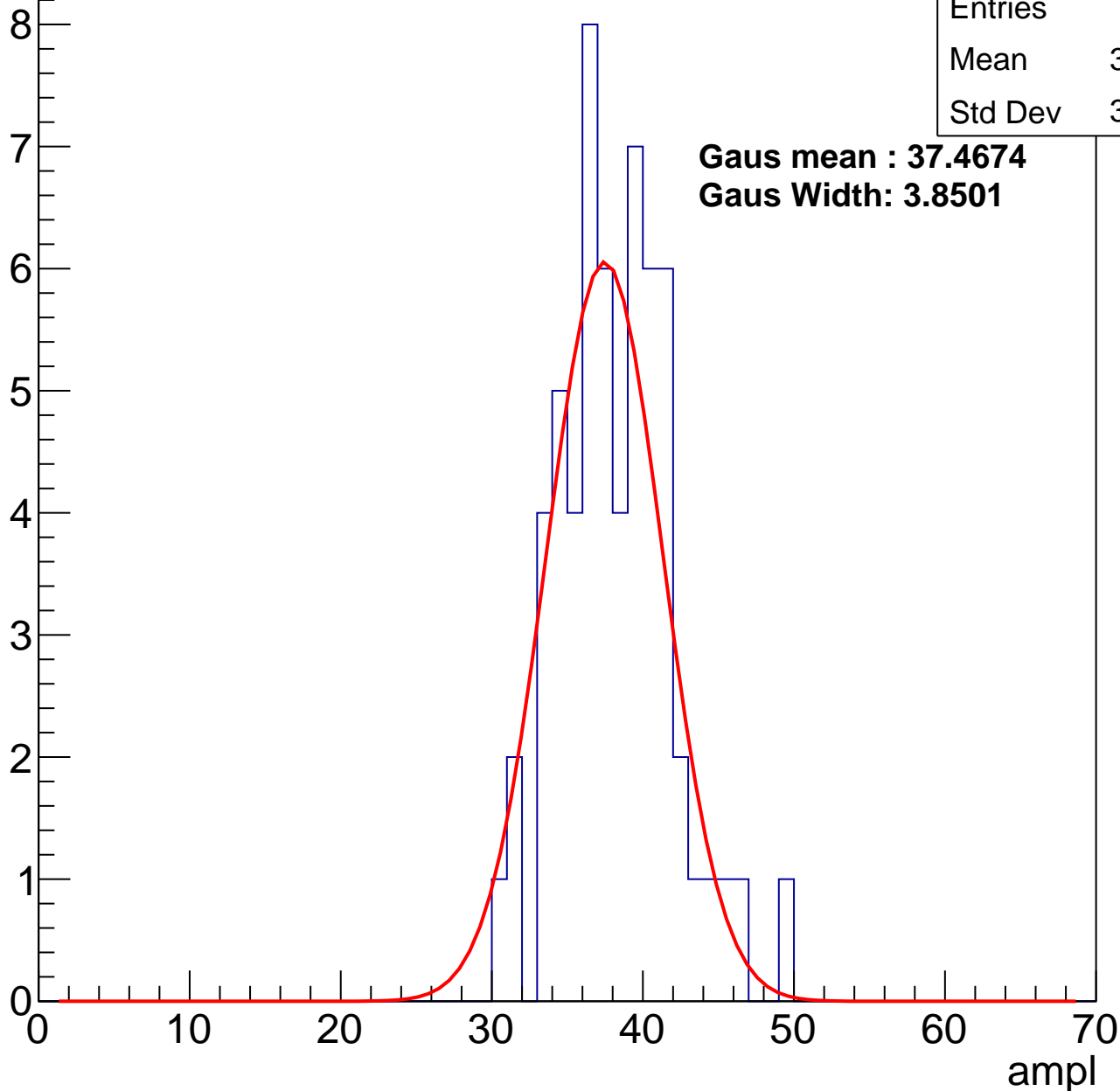
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	37.77
Std Dev	3.707

Gaus mean : 37.4674

Gaus Width: 3.8501



B1L102S, U11-ch45, adc2

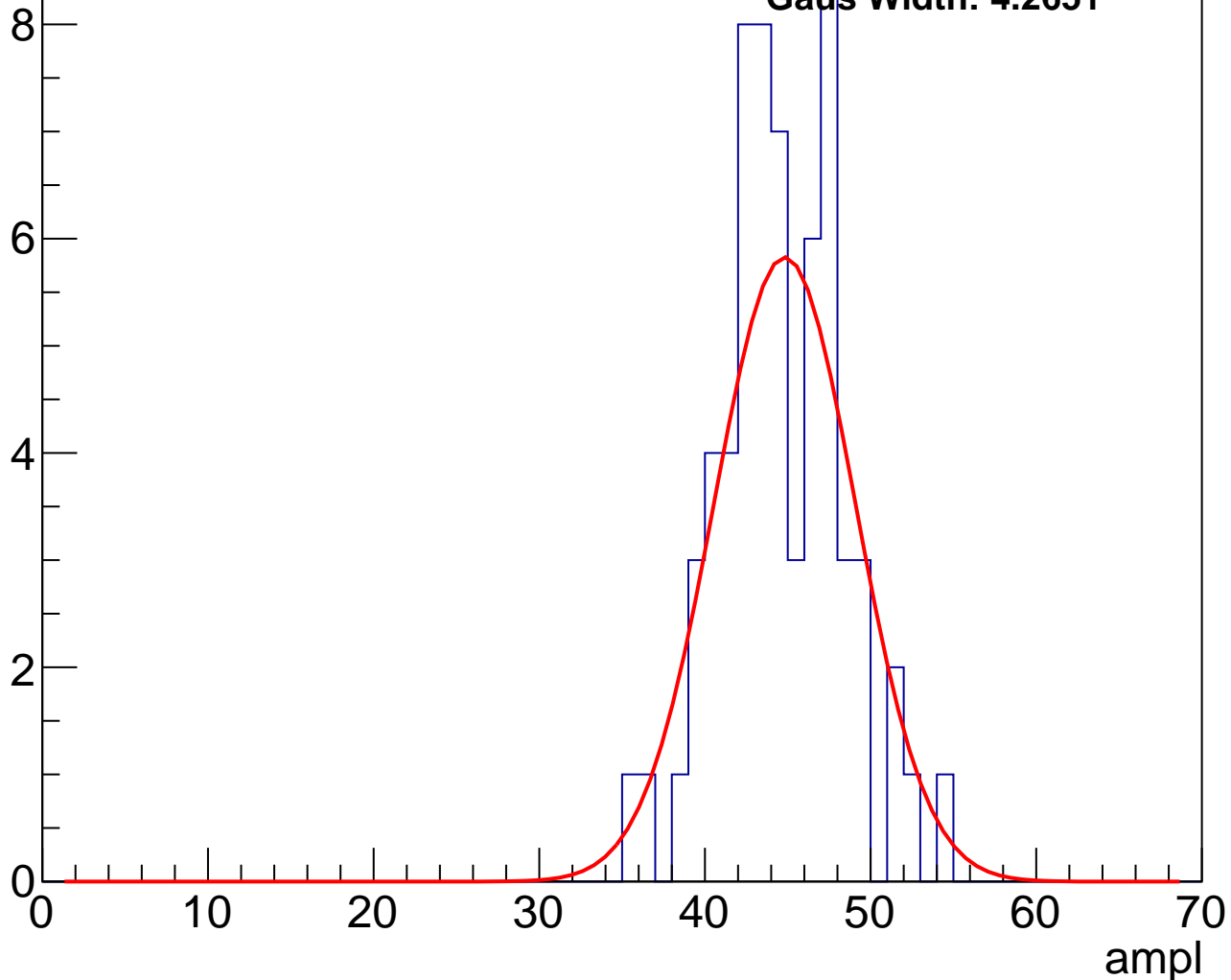
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	44.21
Std Dev	3.683

Gaus mean : 44.8159

Gaus Width: 4.2651



B1L102S, U11-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	50.69
Std Dev	3.731

Entry

10

8

6

4

2

0

0

10

20

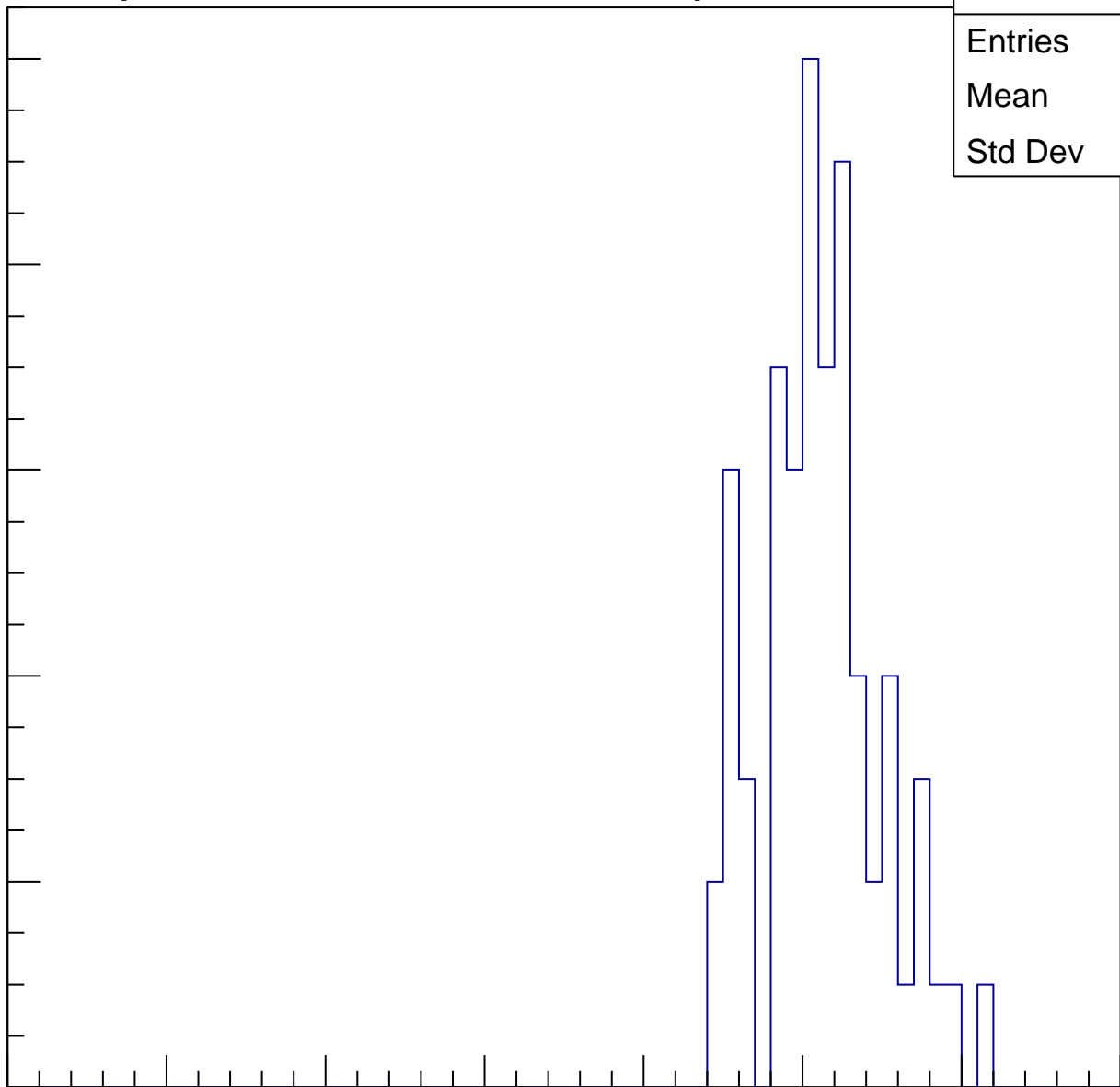
30

40

50

60

ampl

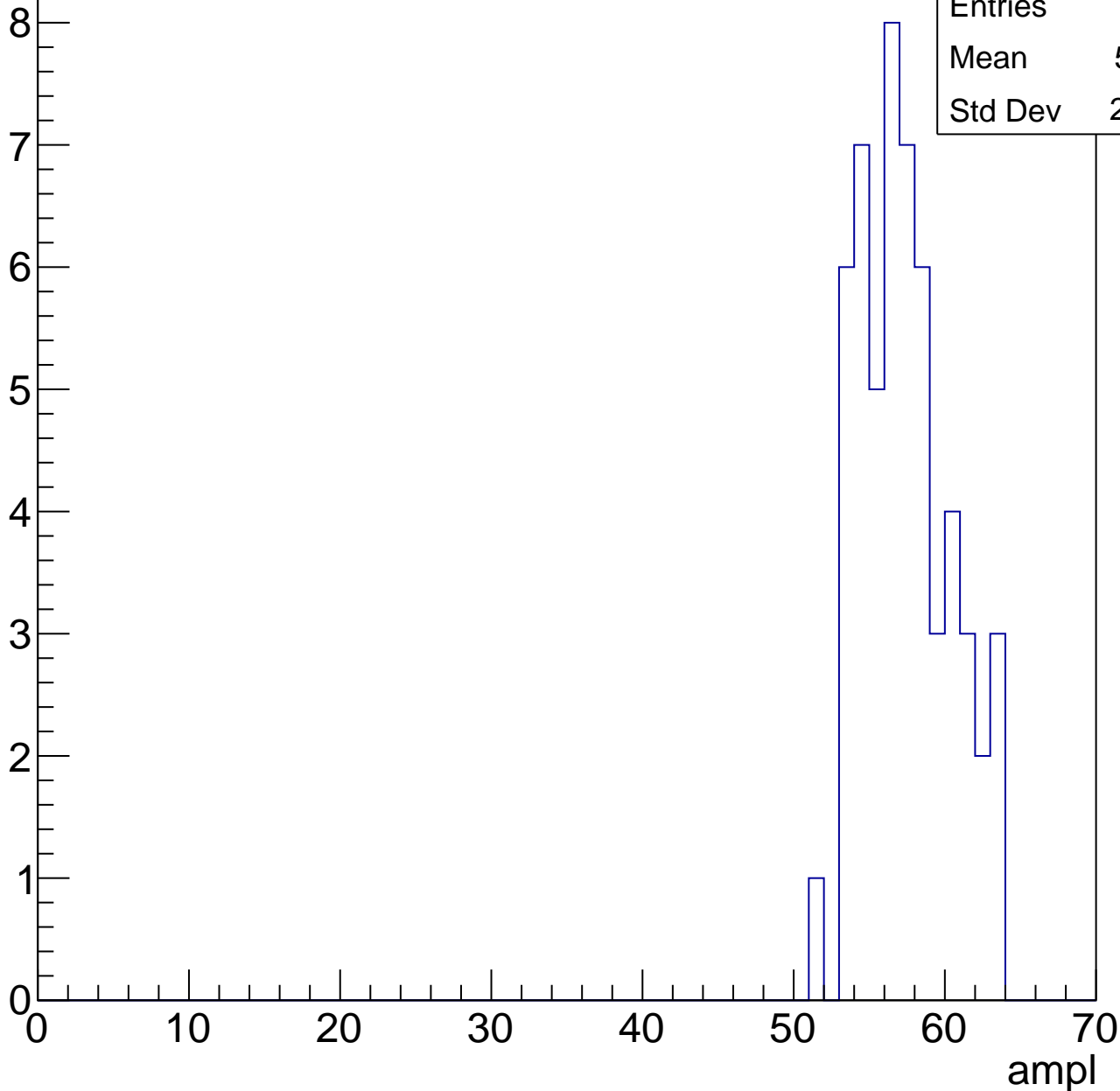


B1L102S, U11-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

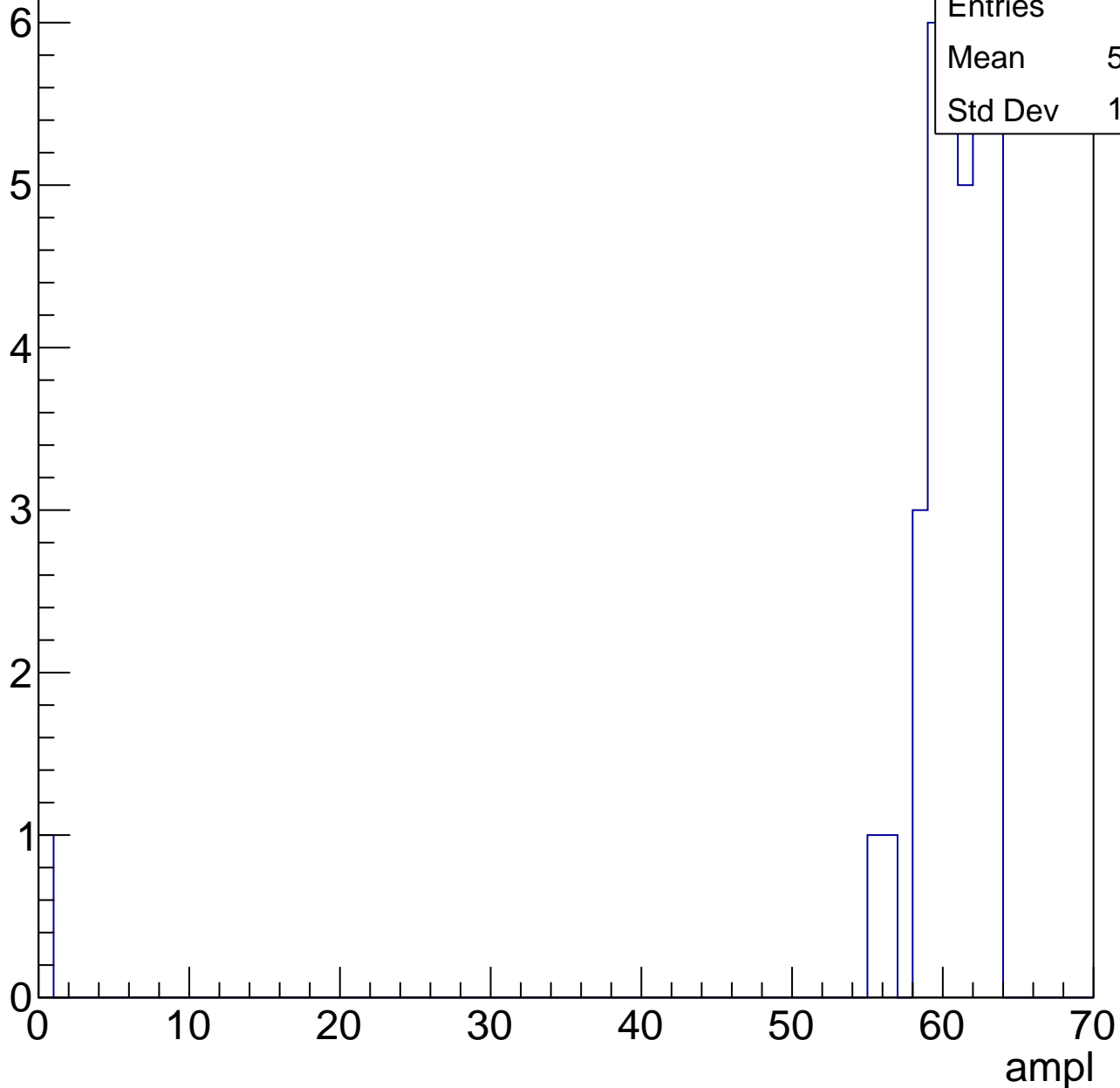
Entries	55
Mean	56.91
Std Dev	2.962



B1L102S, U11-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

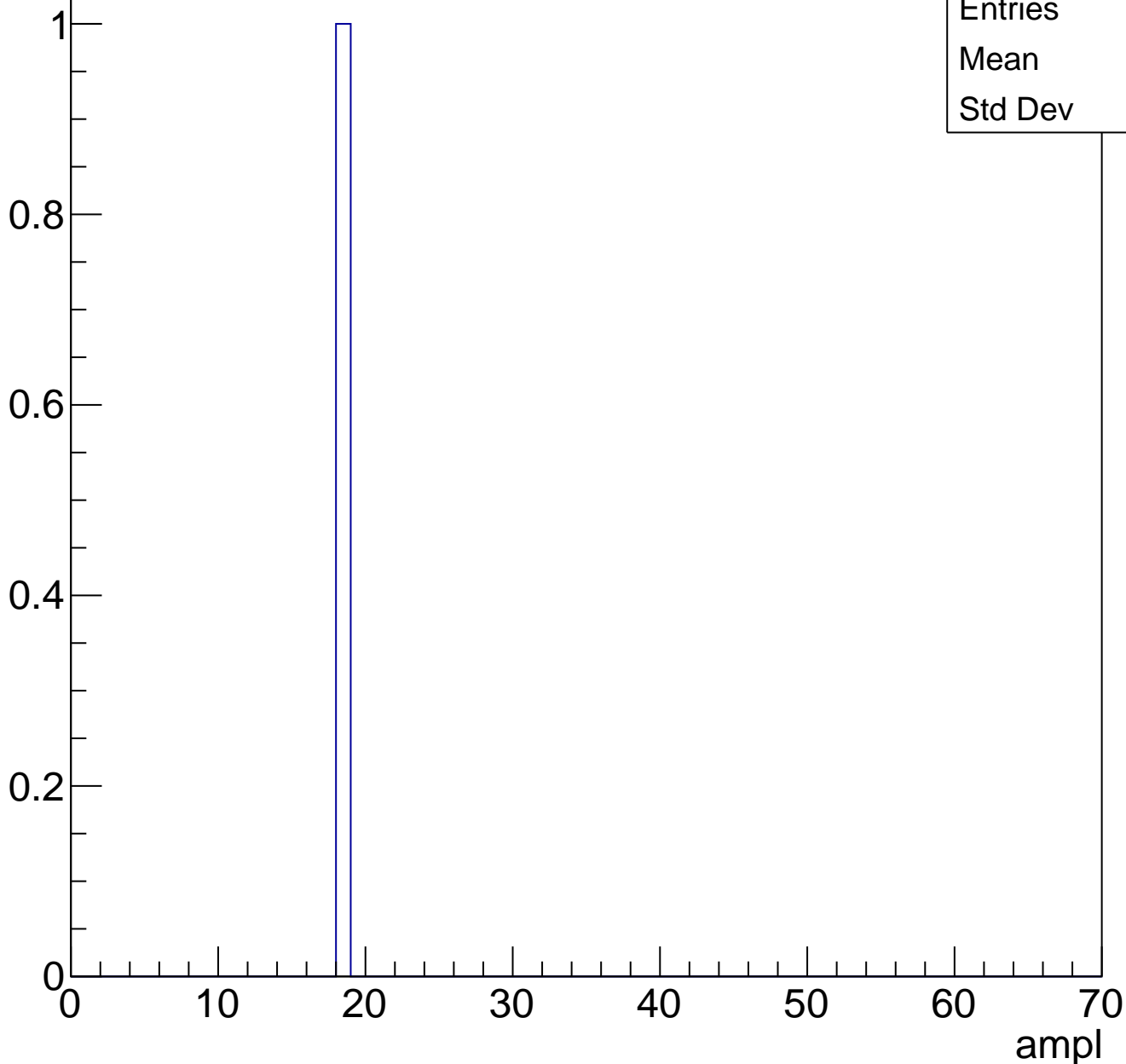
Entry



B1L102S, U11-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch46, adc0

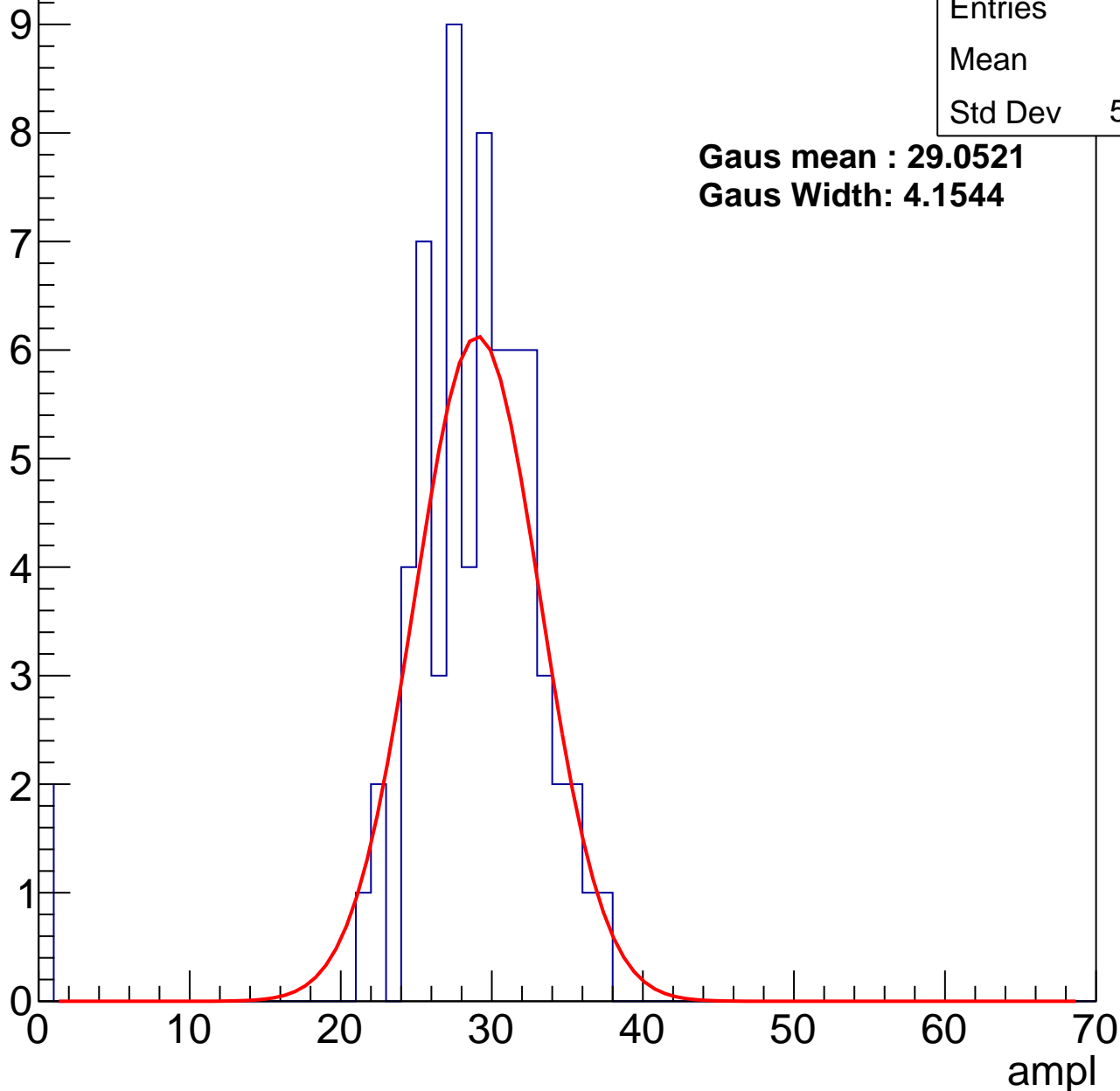
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.9
Std Dev	5.995

Gaus mean : 29.0521

Gaus Width: 4.1544



B1L102S, U11-ch46, adc1

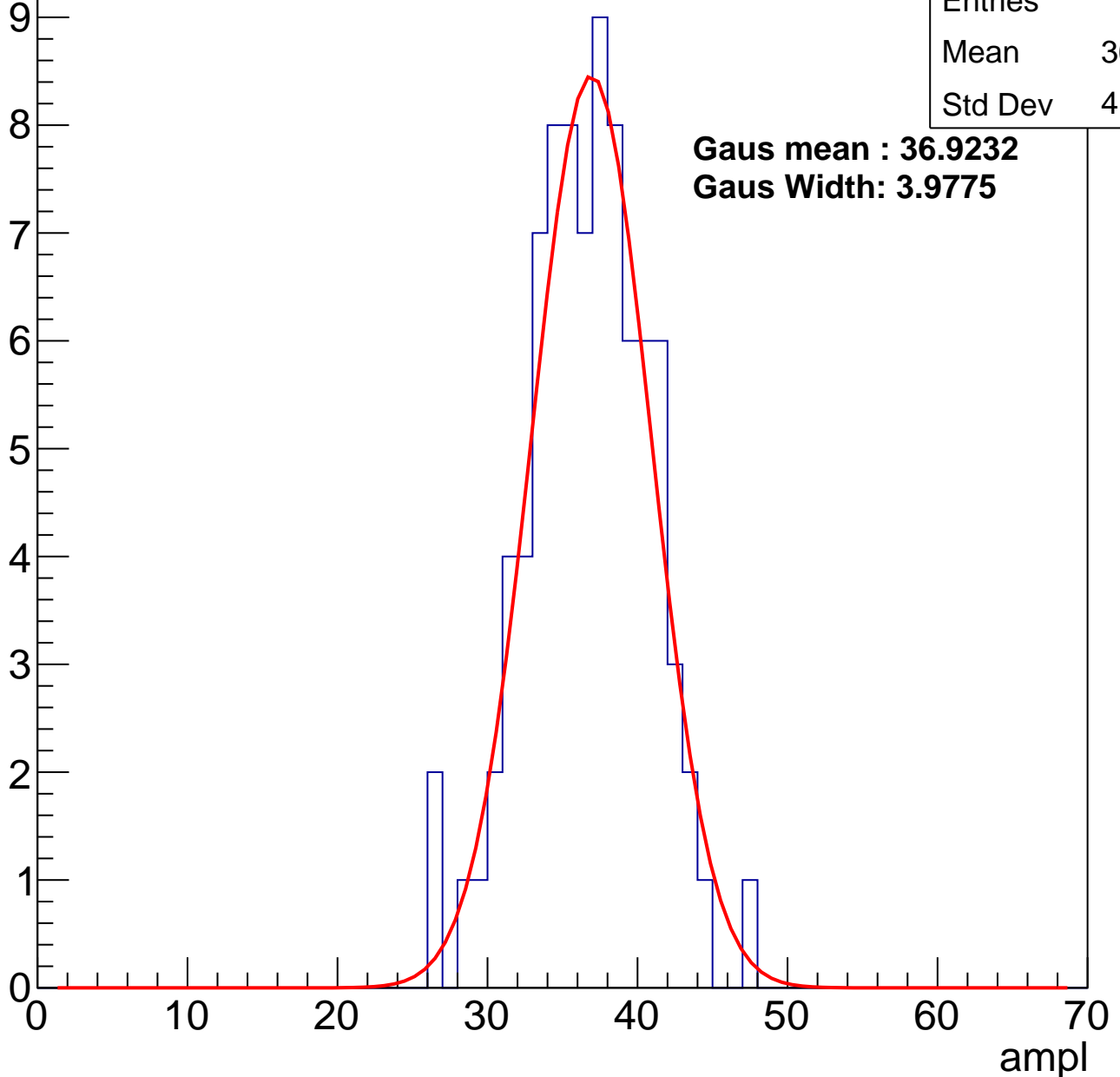
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	36.23
Std Dev	4.005

Gaus mean : 36.9232

Gaus Width: 3.9775



B1L102S, U11-ch46, adc2

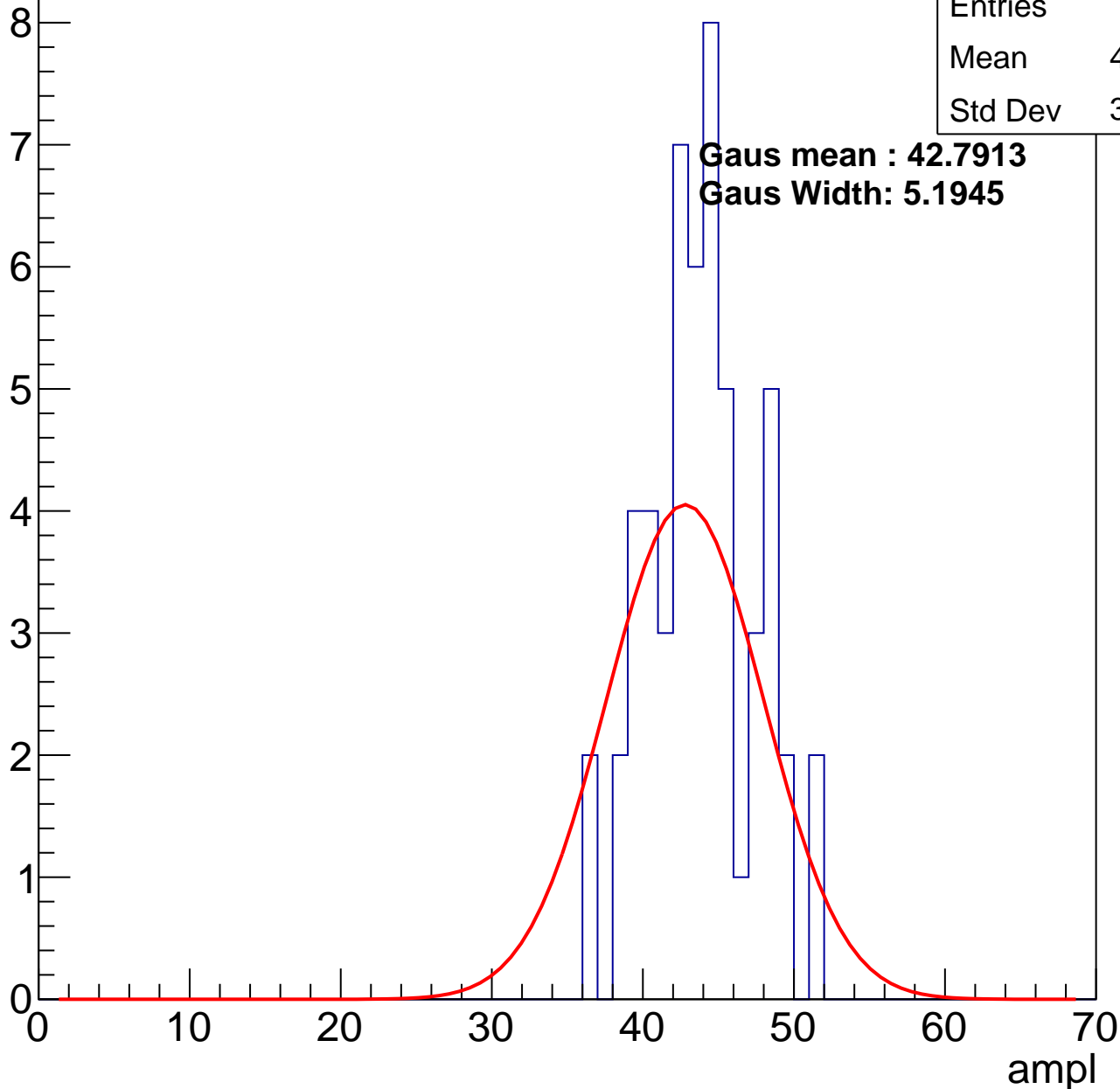
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	43.39
Std Dev	3.519

Gaus mean : 42.7913

Gaus Width: 5.1945

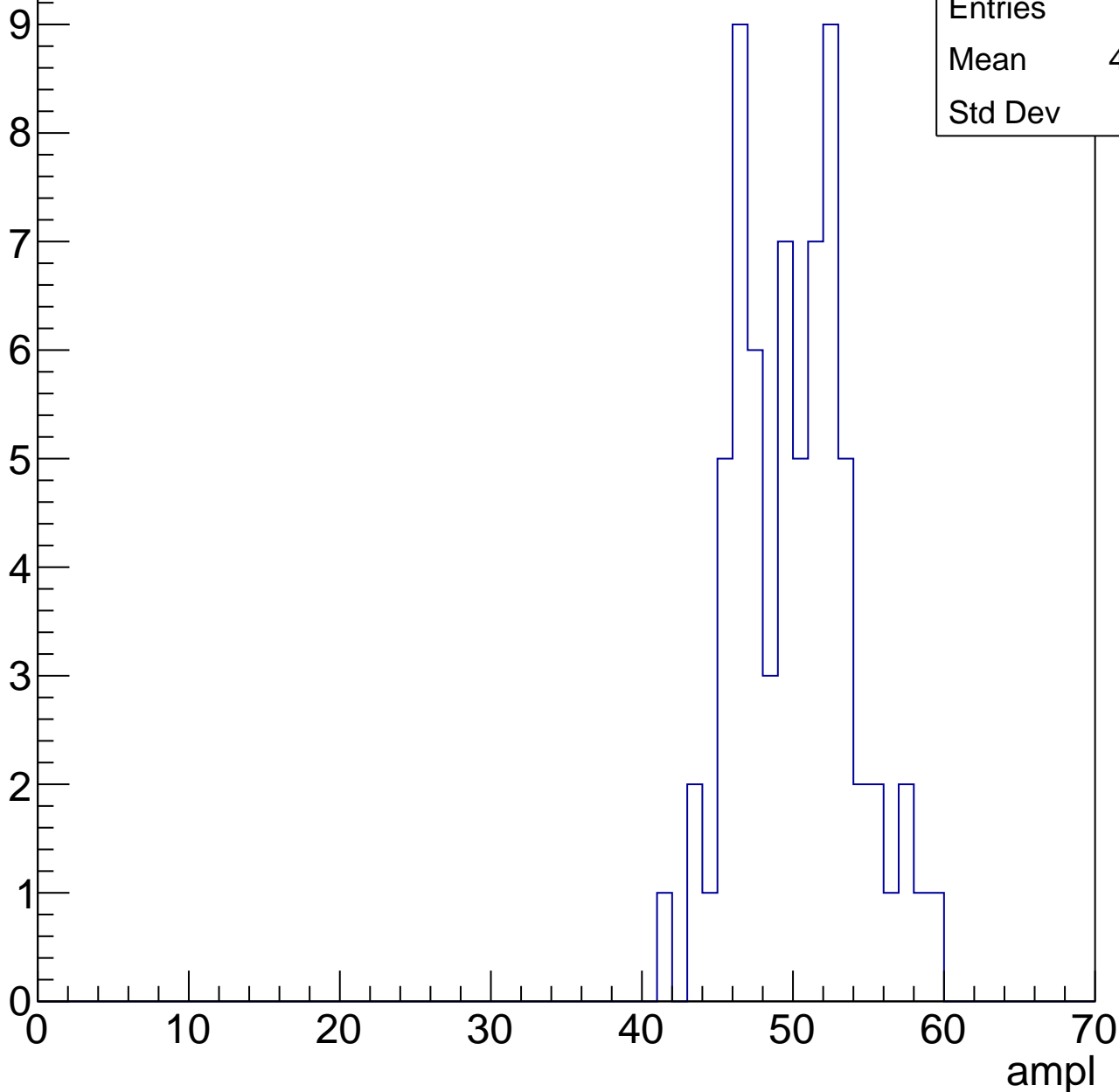


B1L102S, U11-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.62
Std Dev	3.8

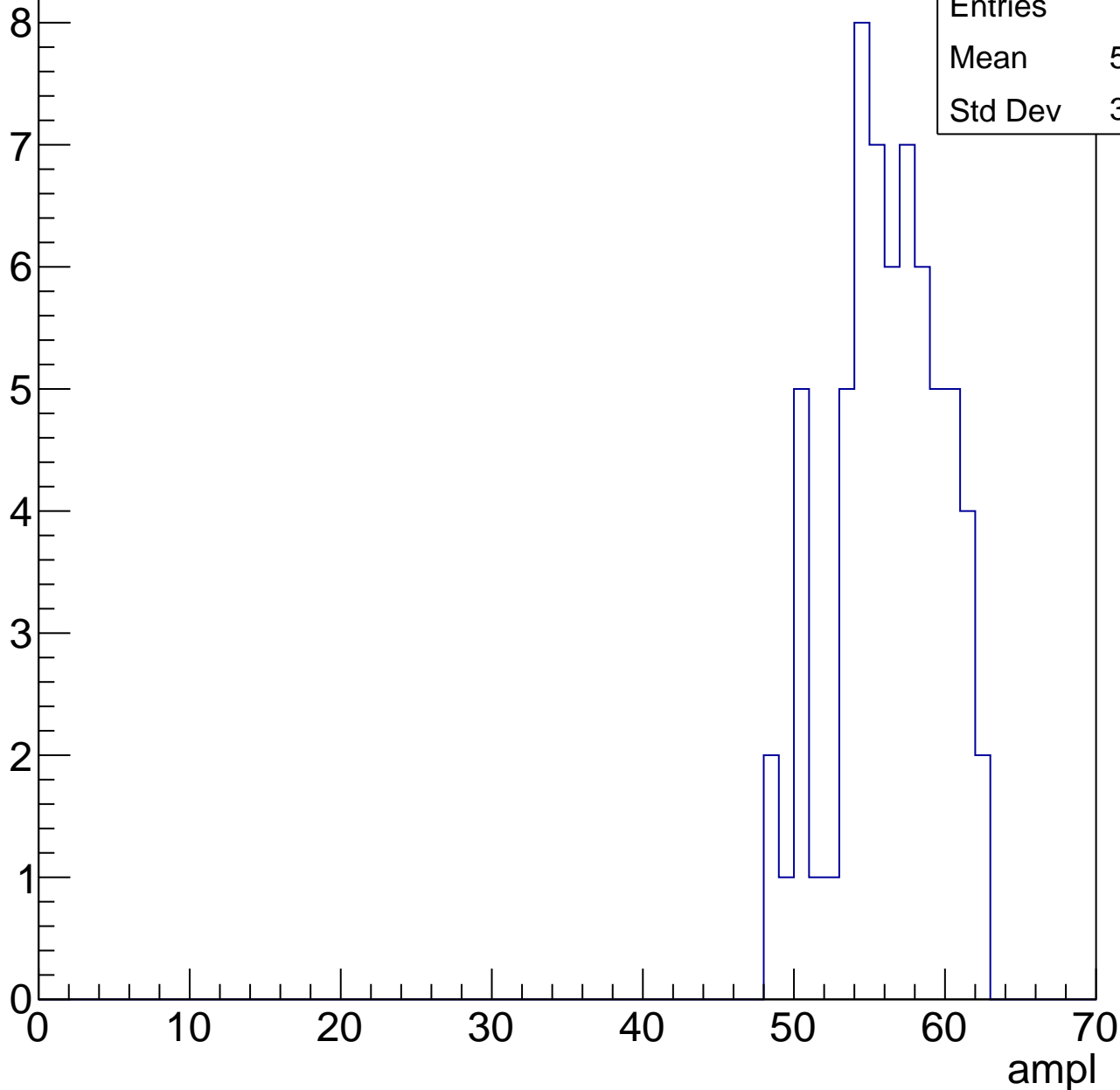


B1L102S, U11-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.78
Std Dev	3.524

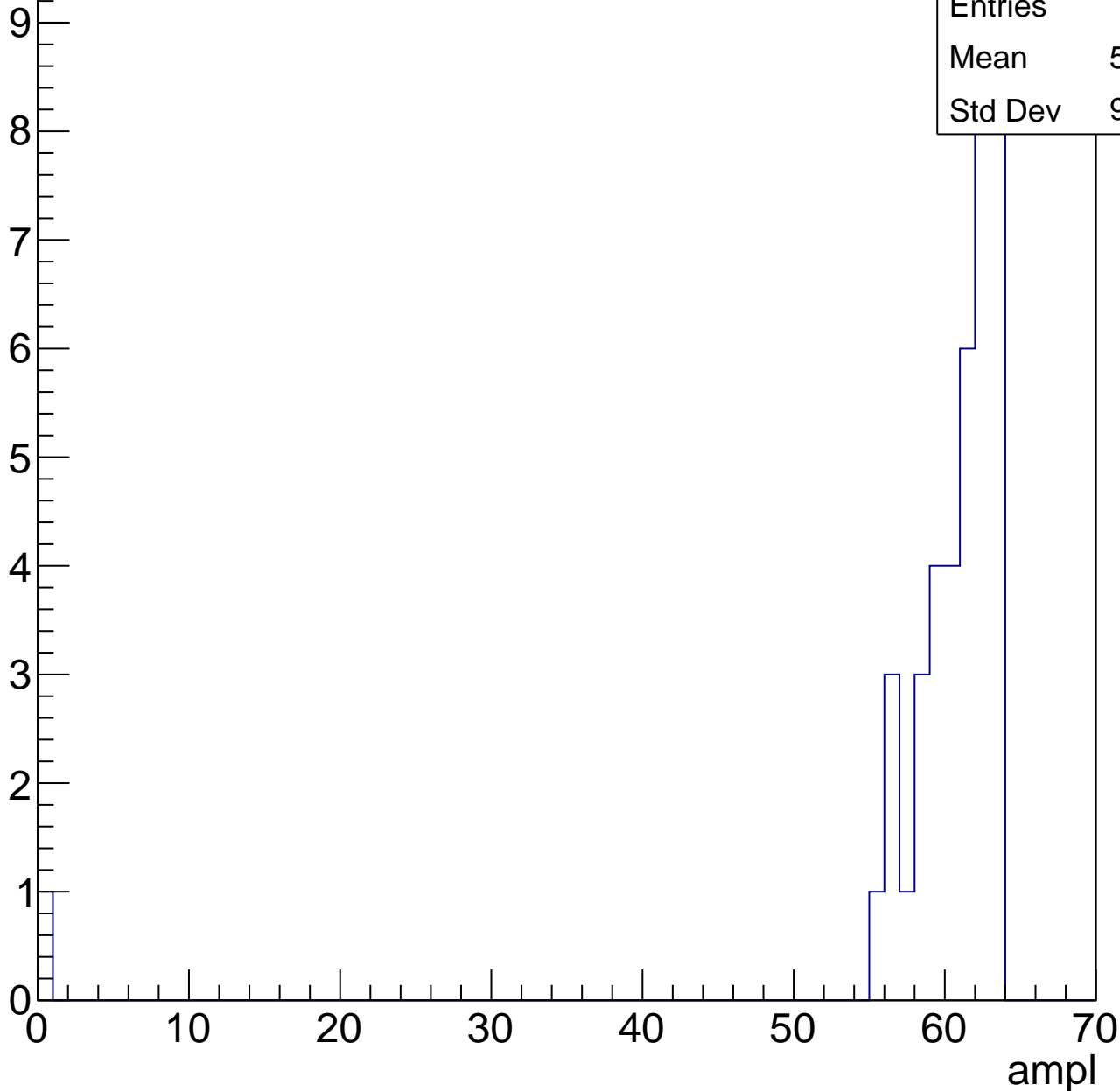


B1L102S, U11-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.98
Std Dev	9.715



B1L102S, U11-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch47, adc0

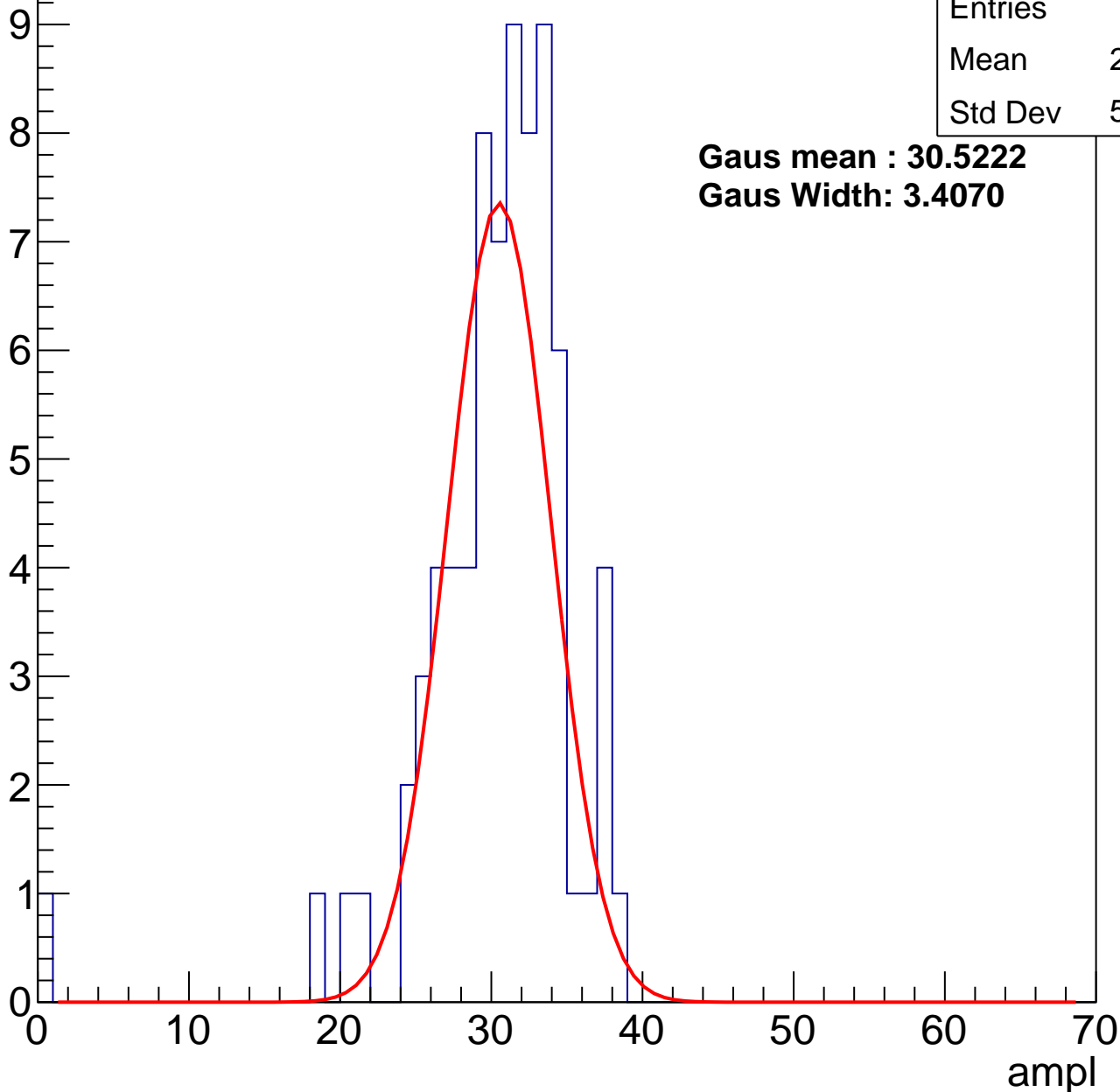
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.88
Std Dev	5.208

Gaus mean : 30.5222

Gaus Width: 3.4070



B1L102S, U11-ch47, adc1

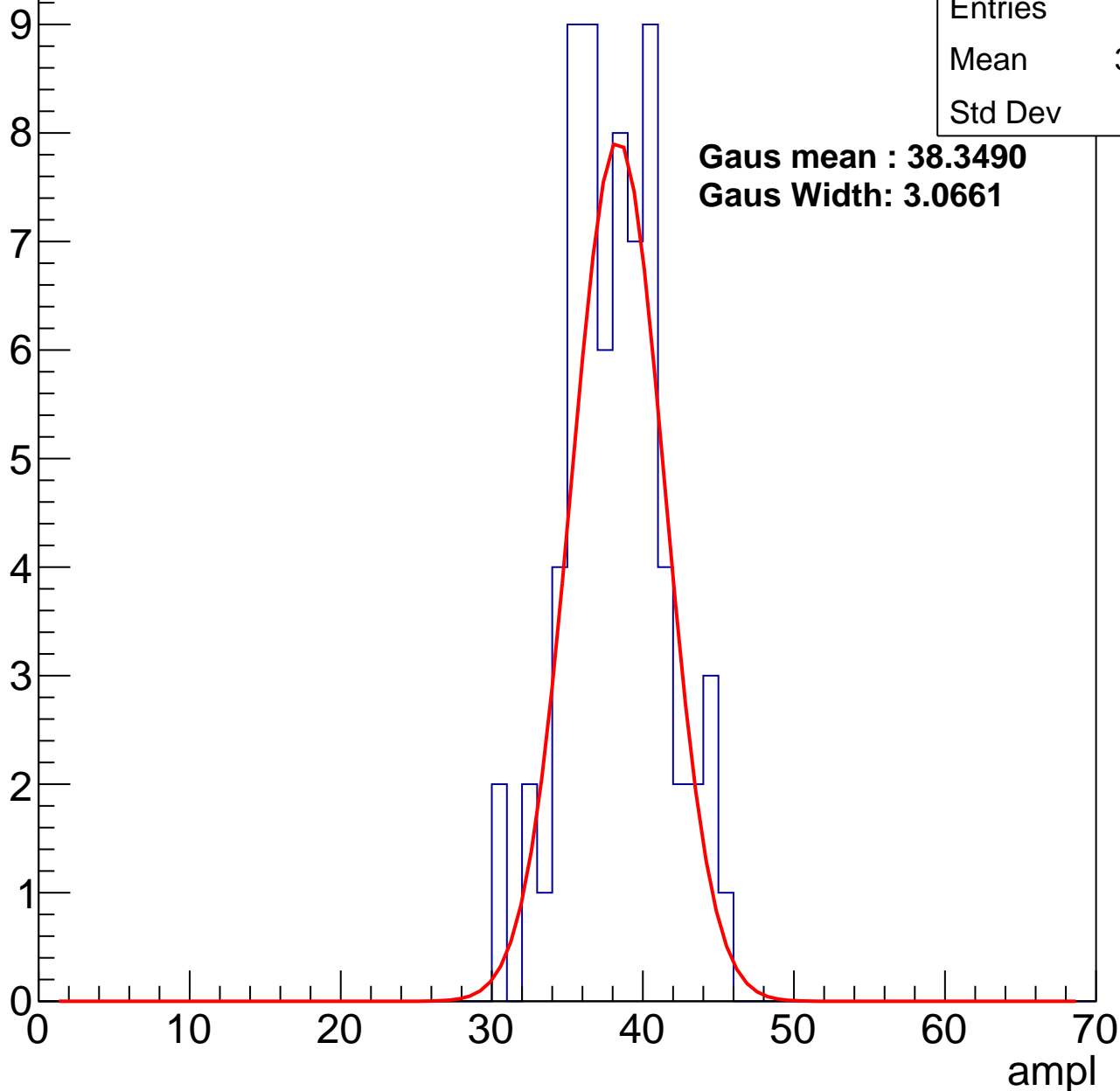
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.71
Std Dev	3.24

Gaus mean : 38.3490

Gaus Width: 3.0661



B1L102S, U11-ch47, adc2

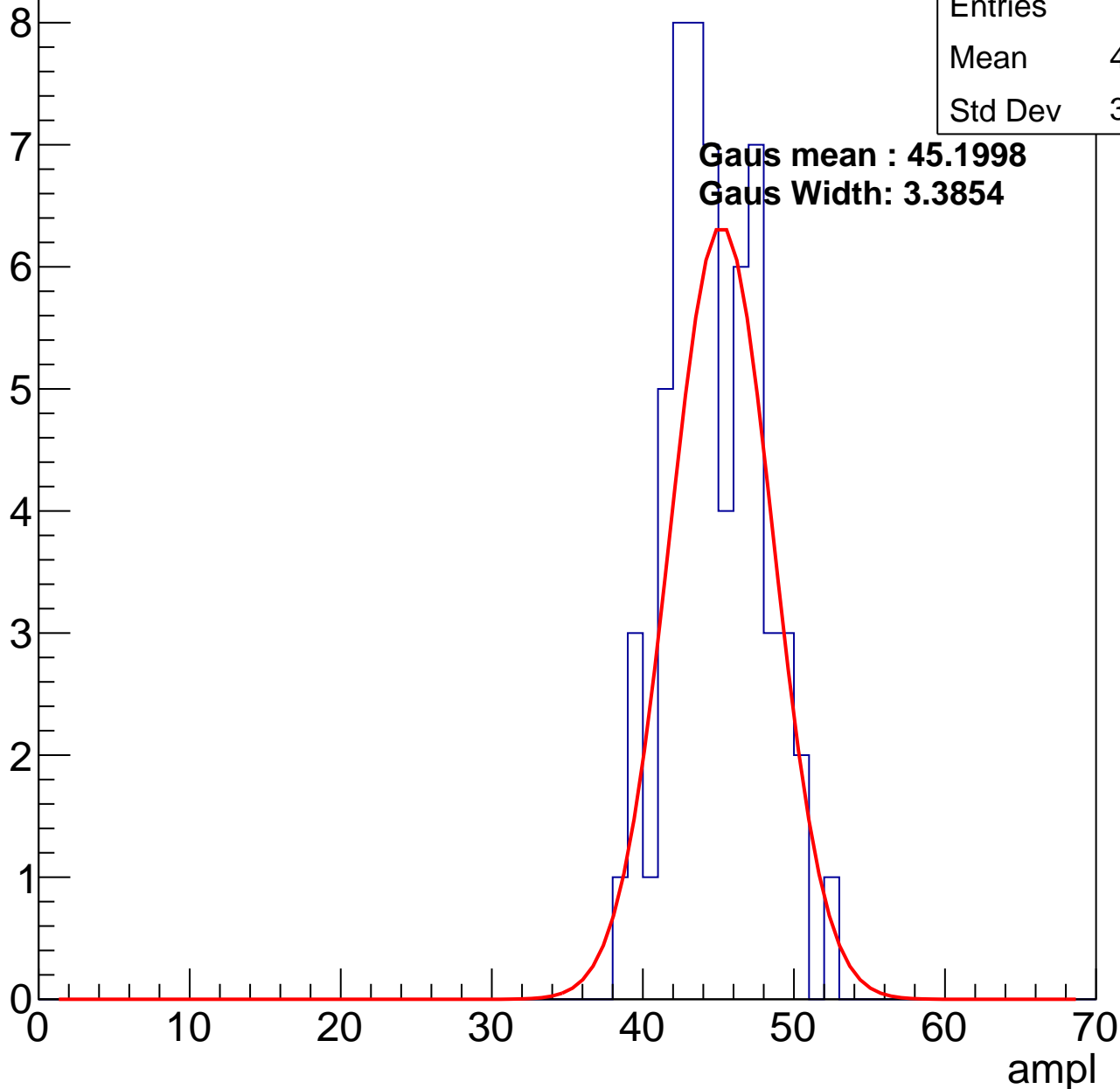
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.34
Std Dev	3.068

Gaus mean : 45.1998

Gaus Width: 3.3854



B1L102S, U11-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

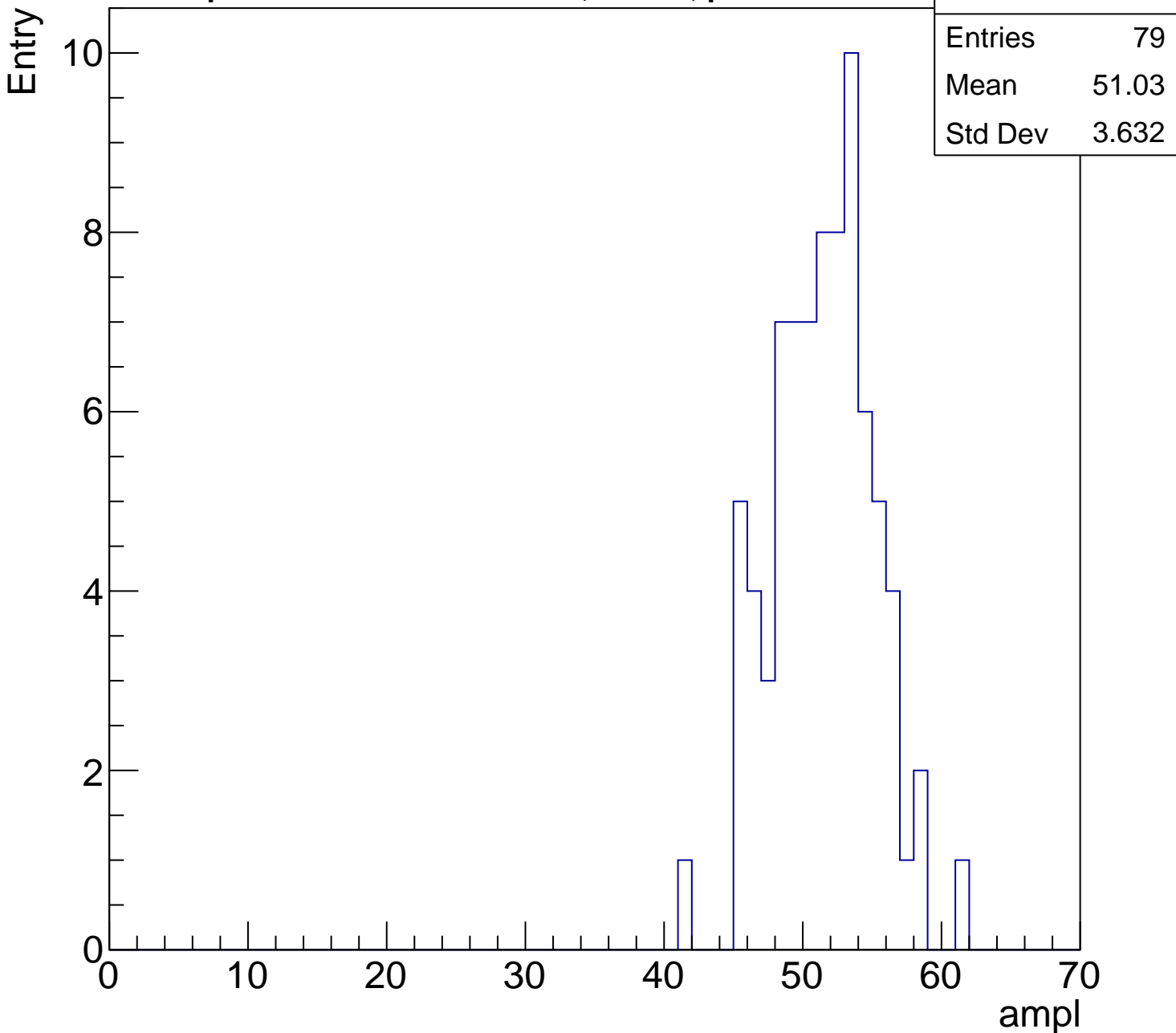
Entries	79
Mean	51.03
Std Dev	3.632

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

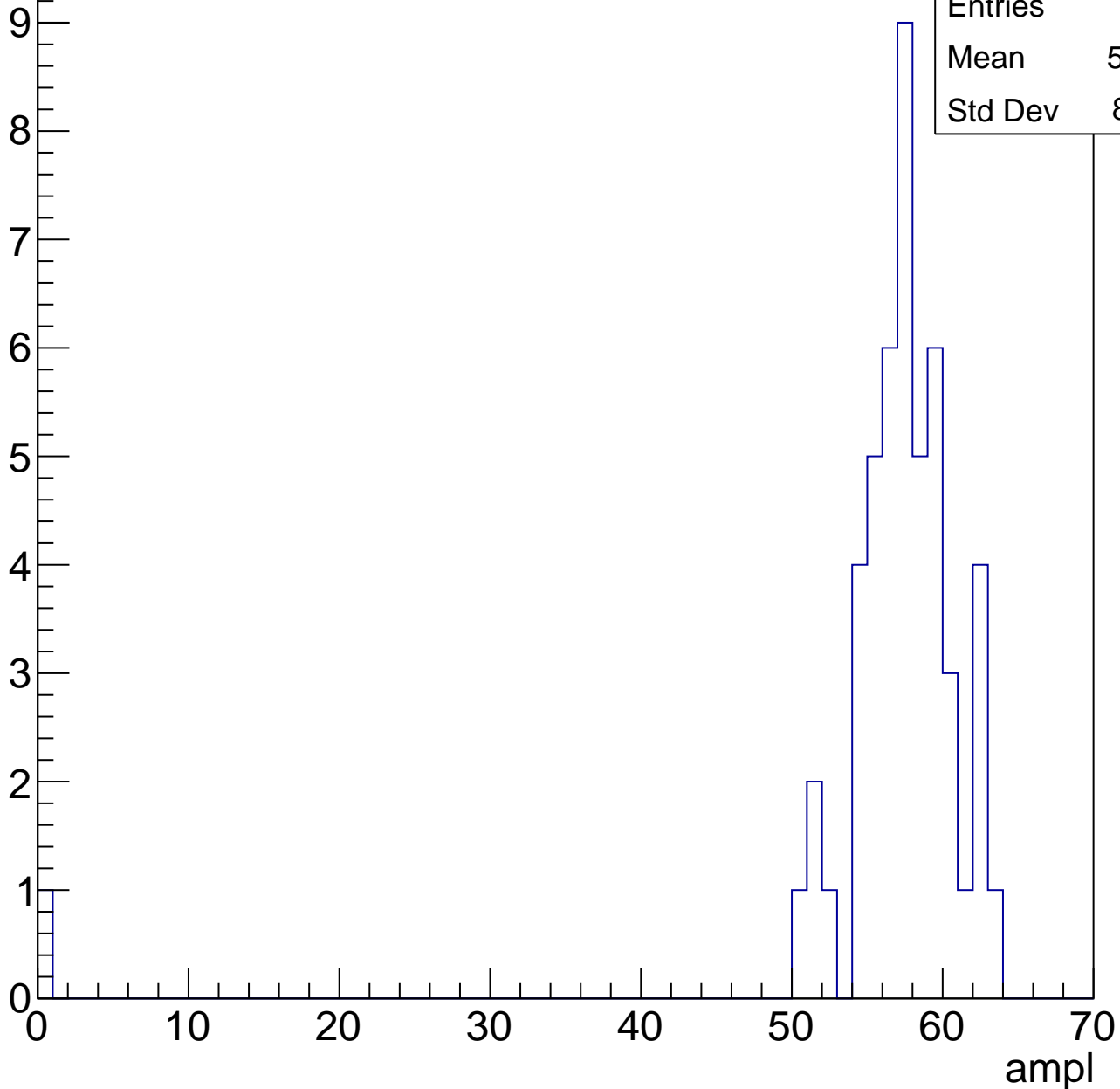


B1L102S, U11-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

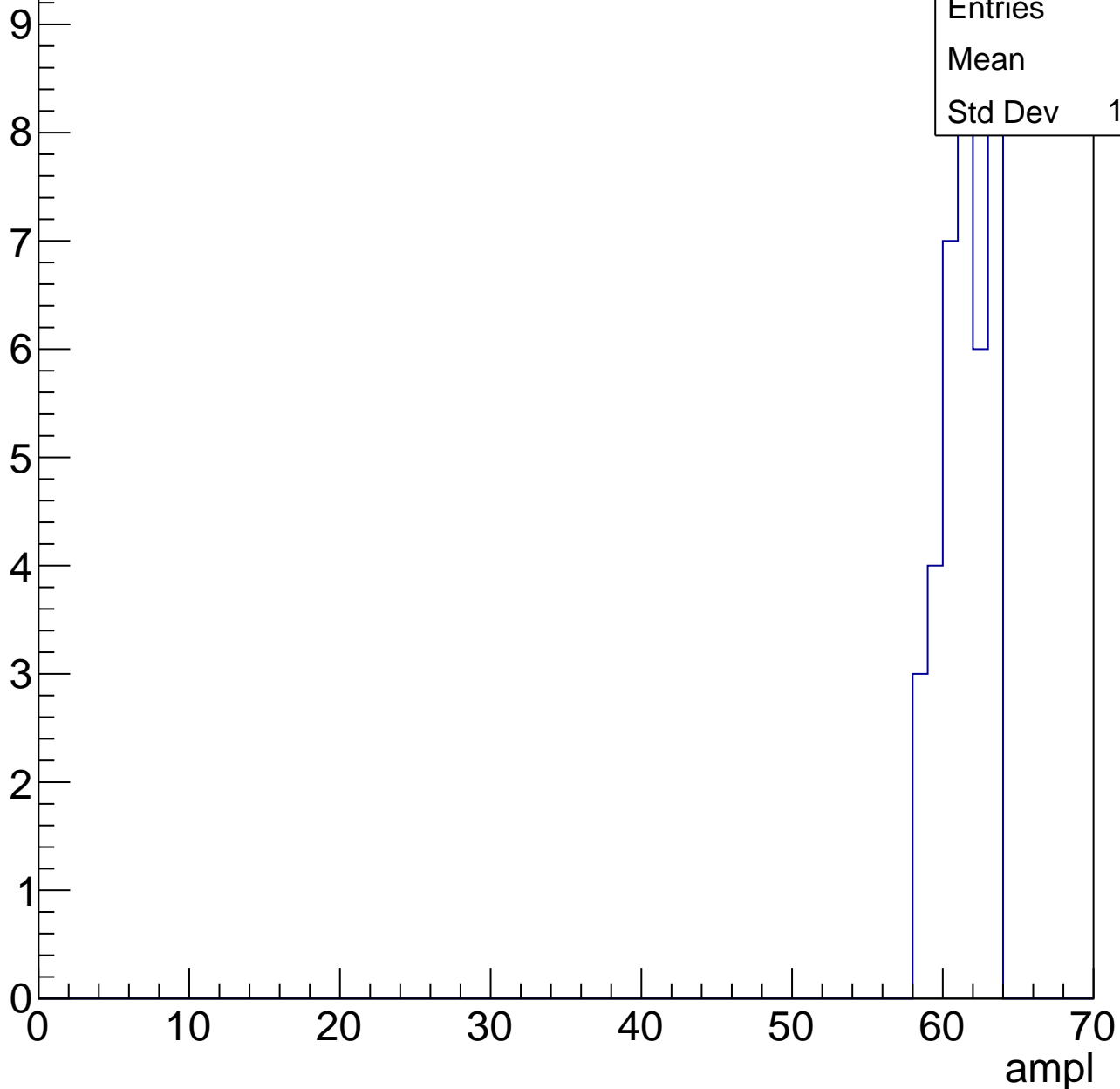
Entries	49
Mean	55.92
Std Dev	8.581



B1L102S, U11-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch48, adc0

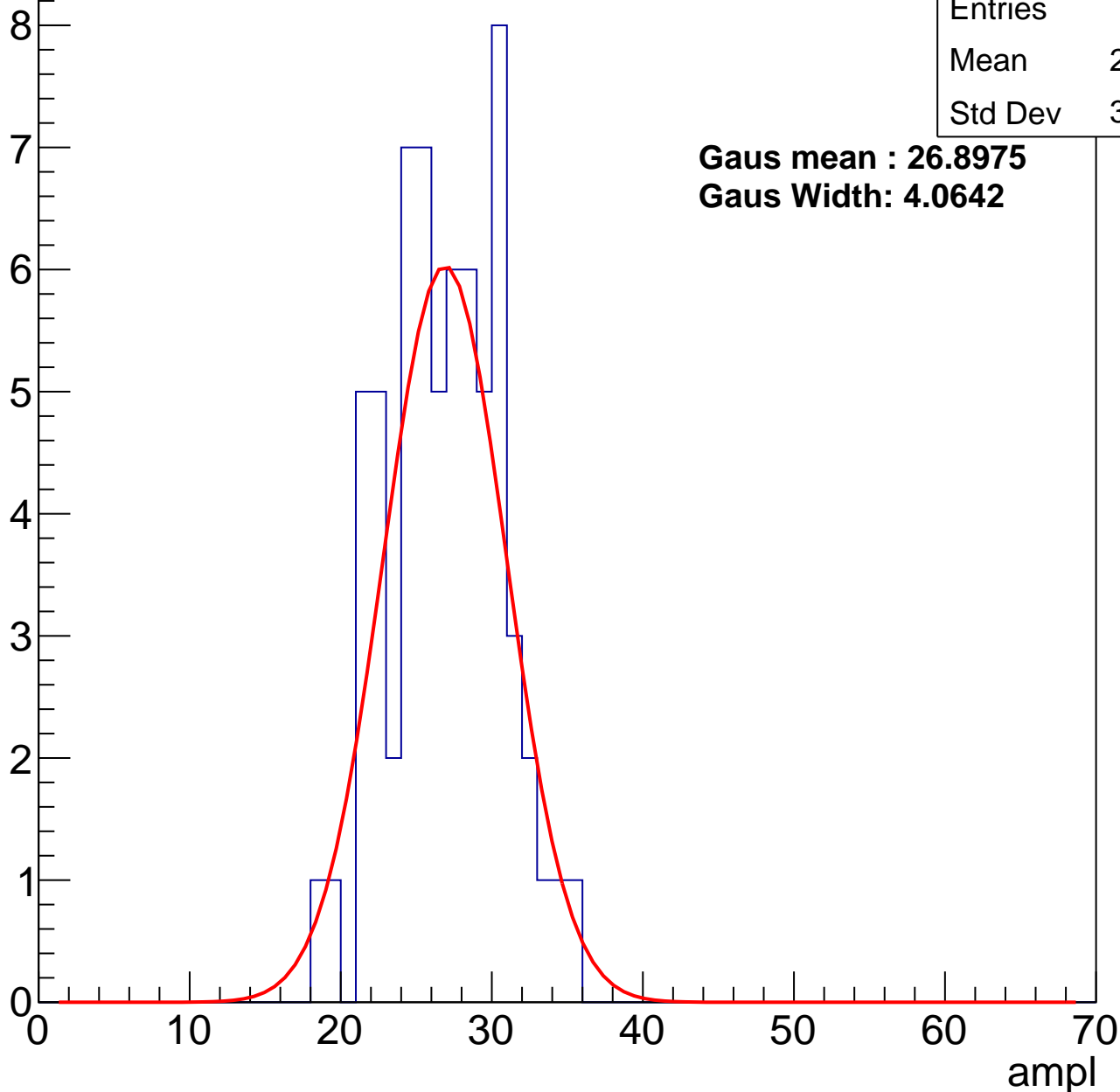
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	26.44
Std Dev	3.697

Gaus mean : 26.8975

Gaus Width: 4.0642



B1L102S, U11-ch48, adc1

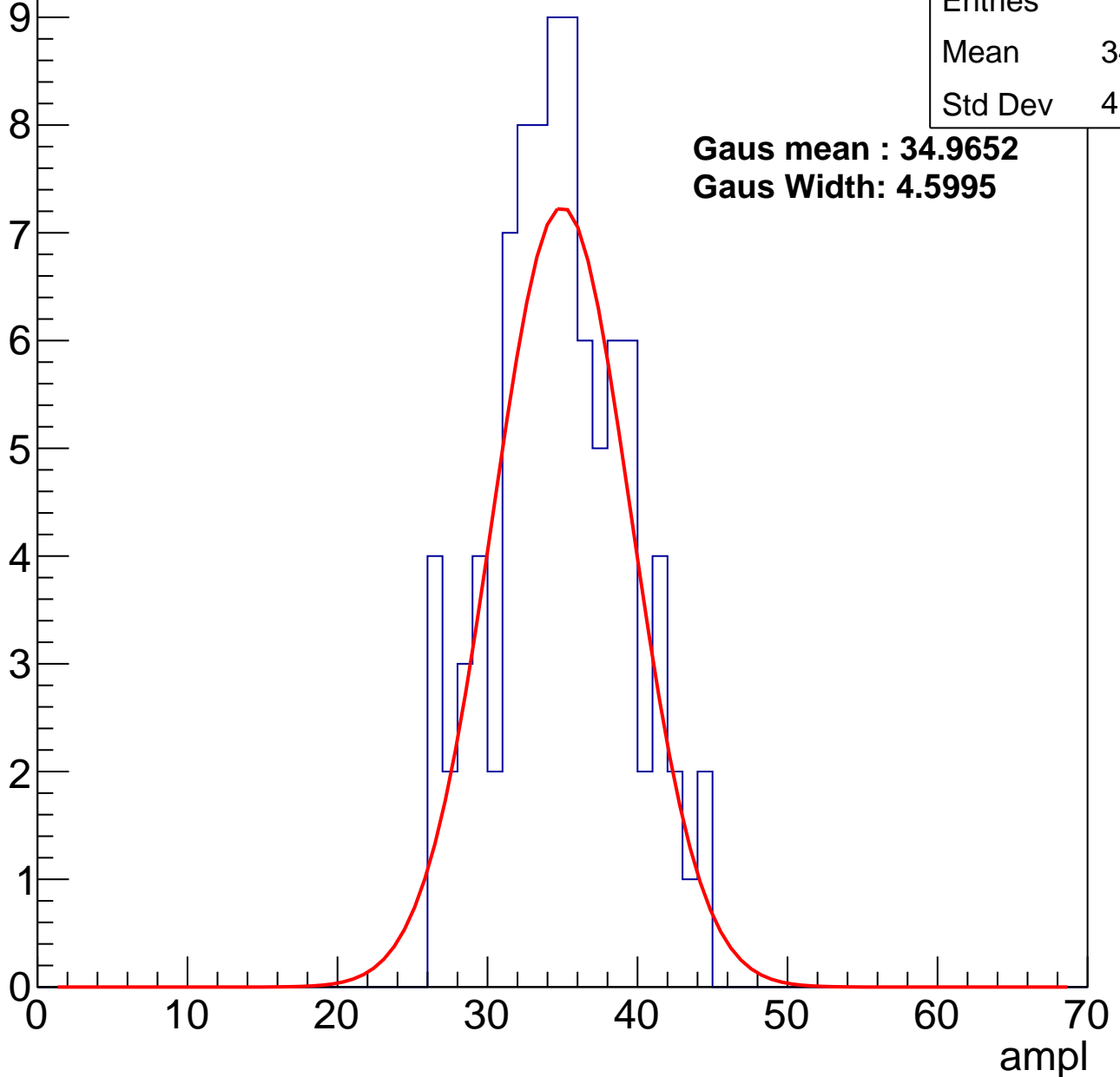
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	34.42
Std Dev	4.323

Gaus mean : 34.9652

Gaus Width: 4.5995



B1L102S, U11-ch48, adc2

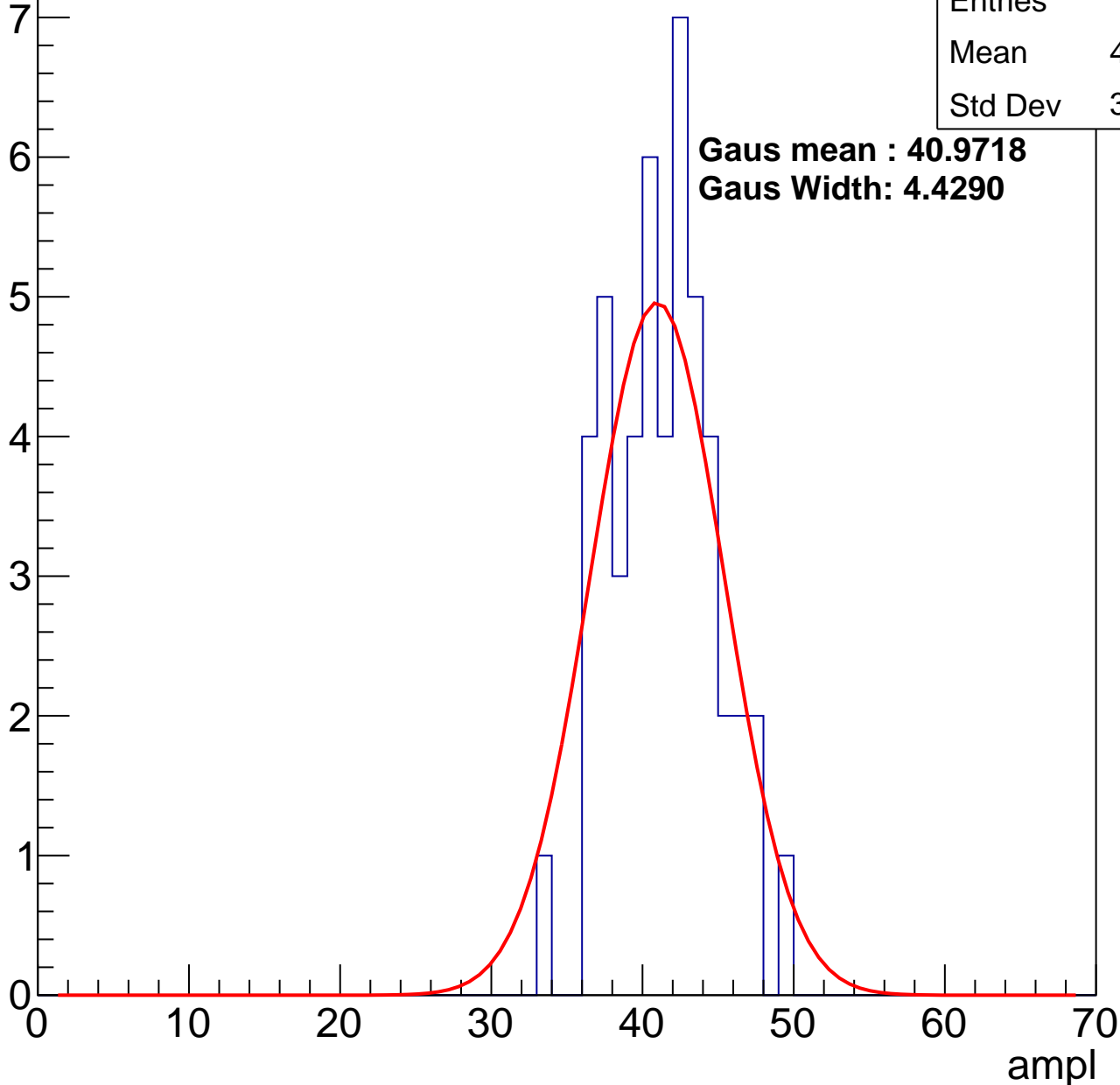
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	40.92
Std Dev	3.387

Gaus mean : 40.9718

Gaus Width: 4.4290

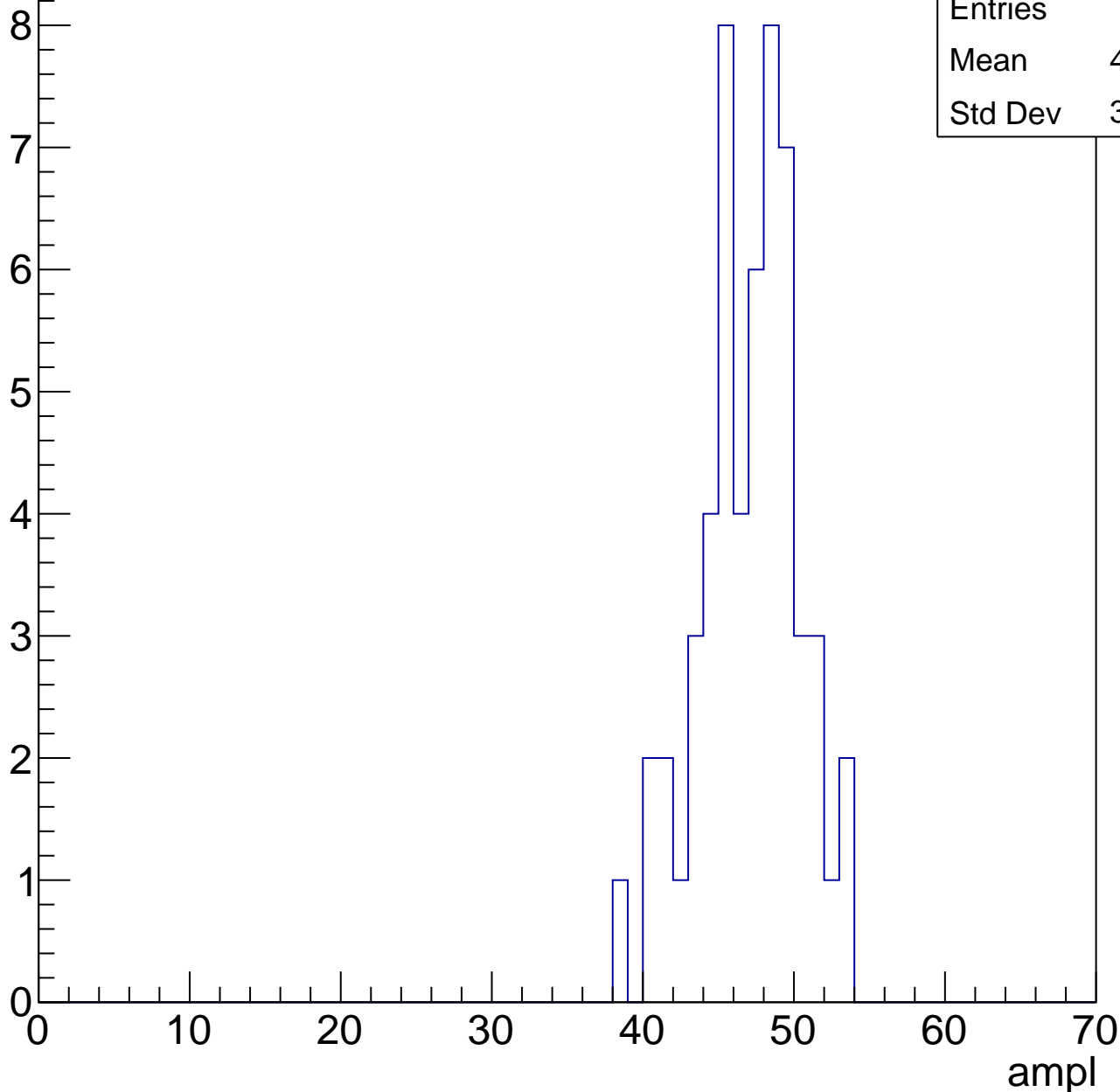


B1L102S, U11-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	46.56
Std Dev	3.285

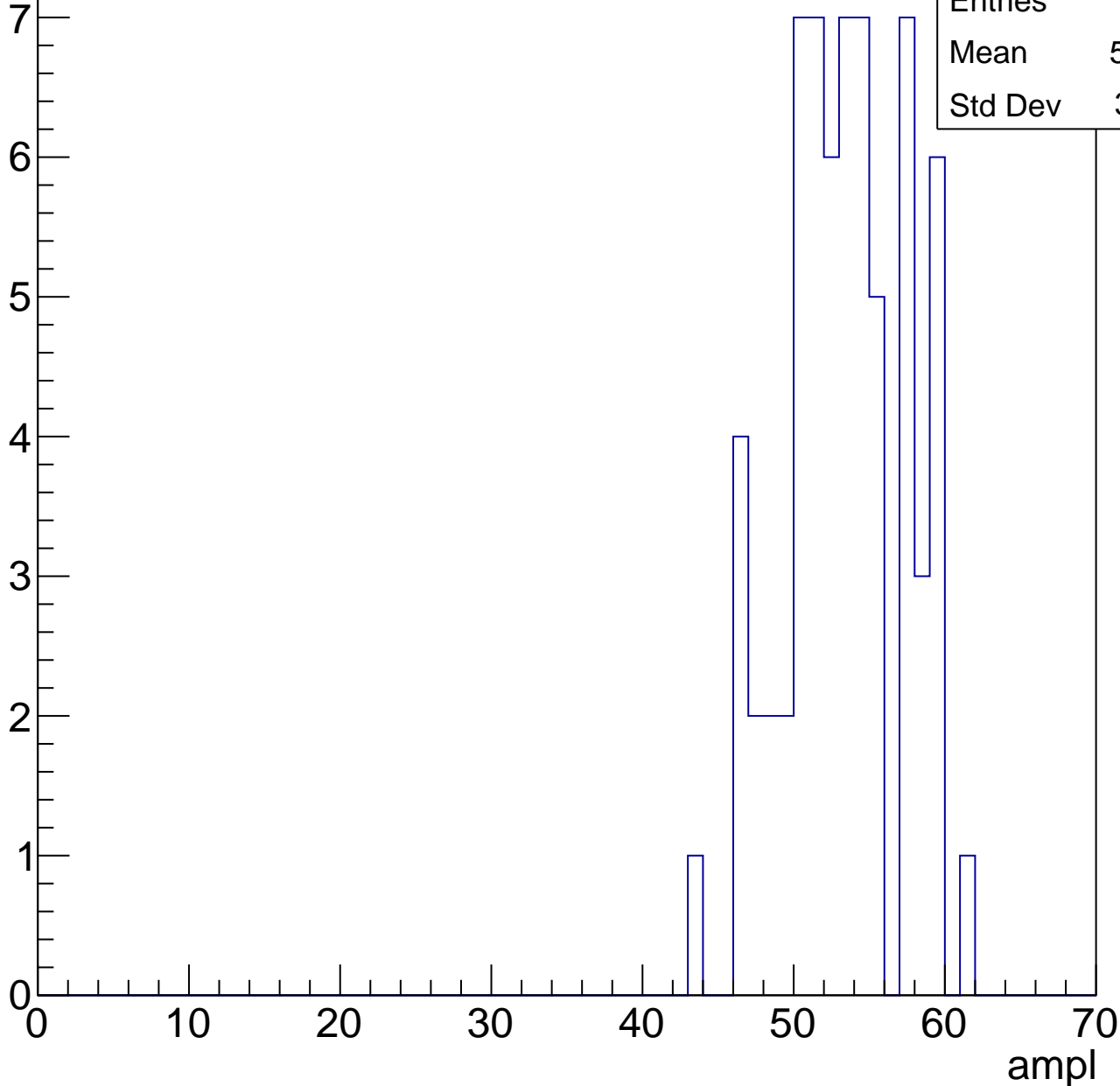


B1L102S, U11-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	52.93
Std Dev	3.941



B1L102S, U11-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	53
Mean	56.7
Std Dev	8.42

Entry

10

8

6

4

2

0

0

10

20

30

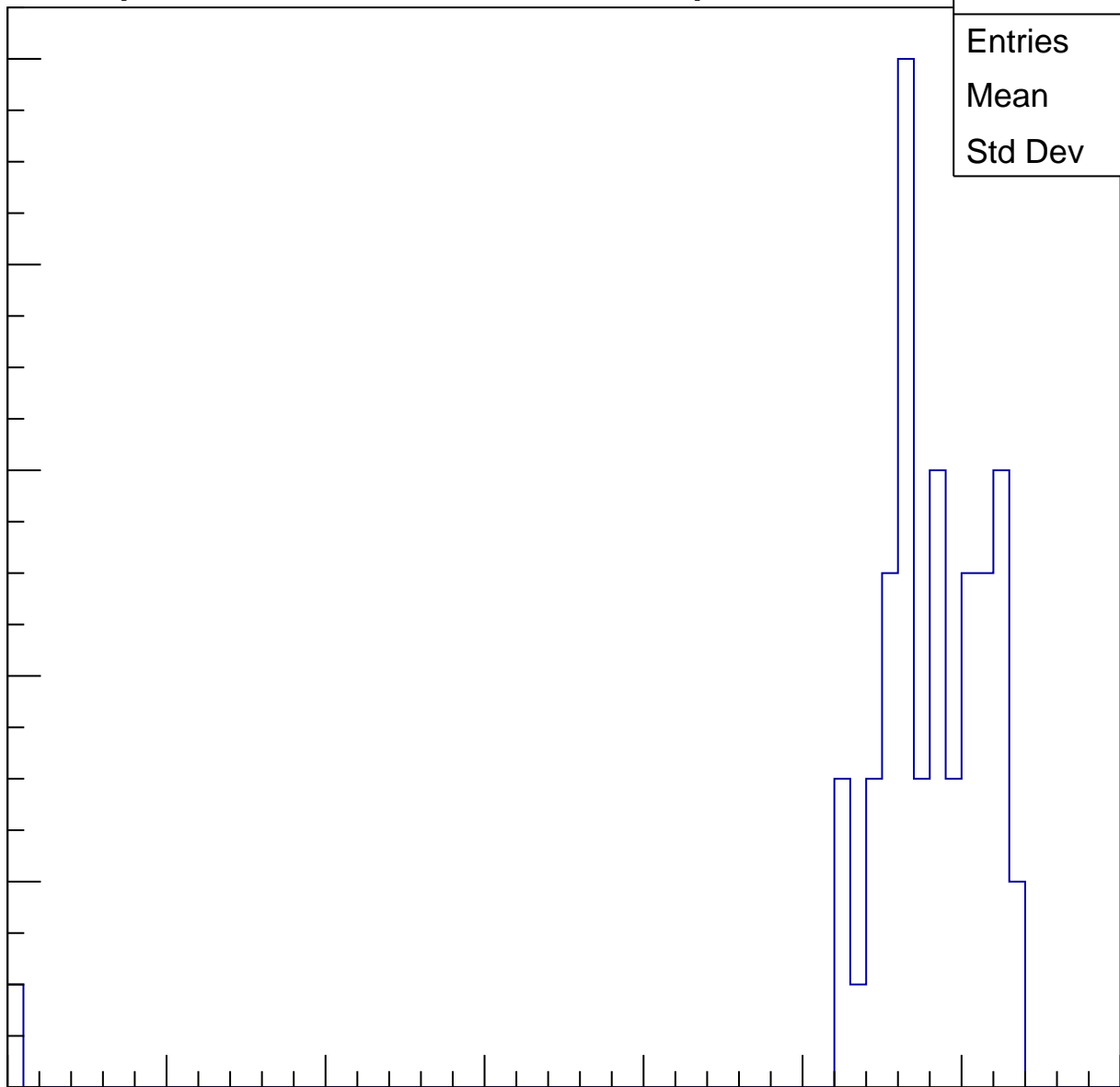
40

50

60

70

ampl

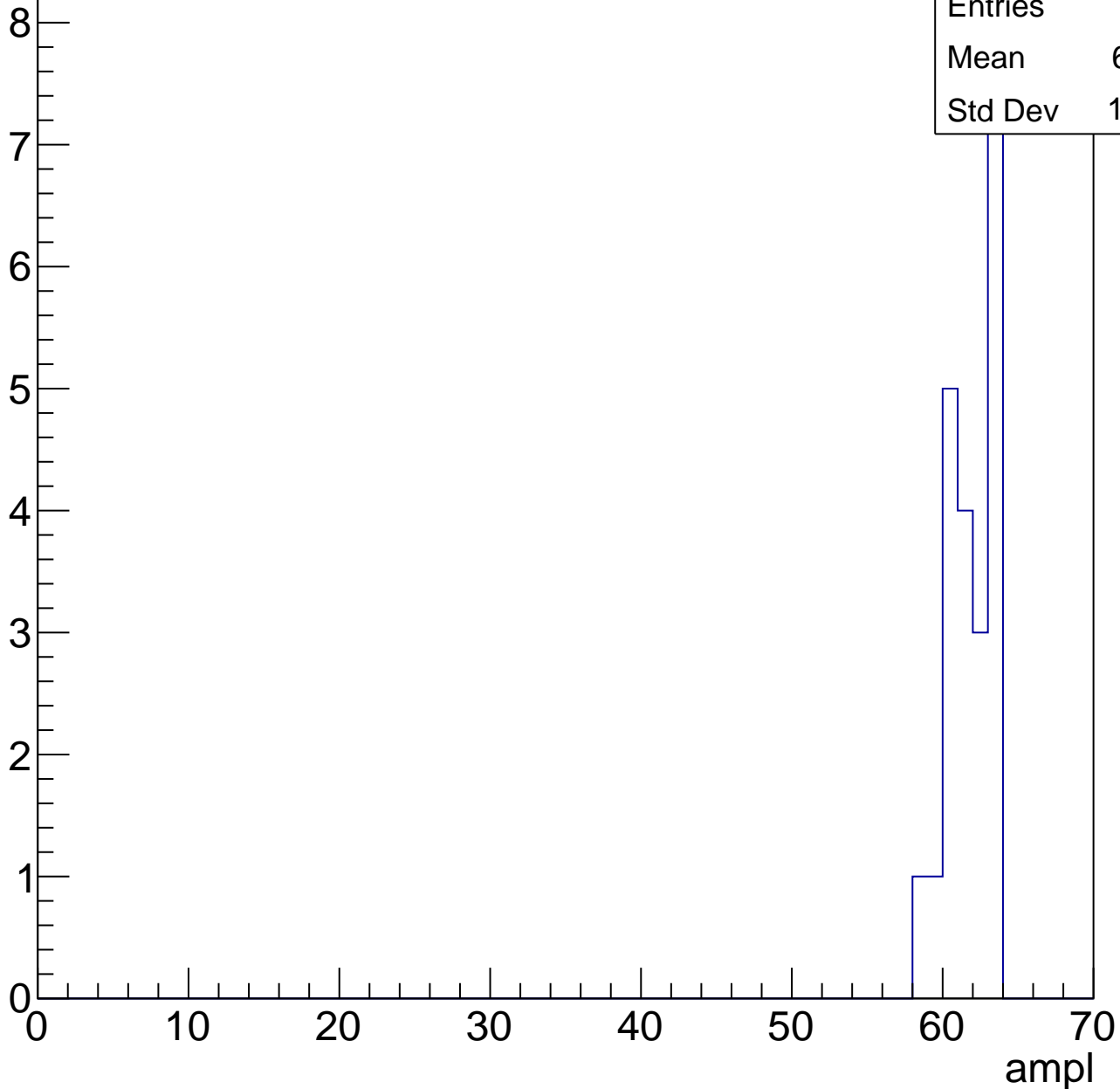


B1L102S, U11-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

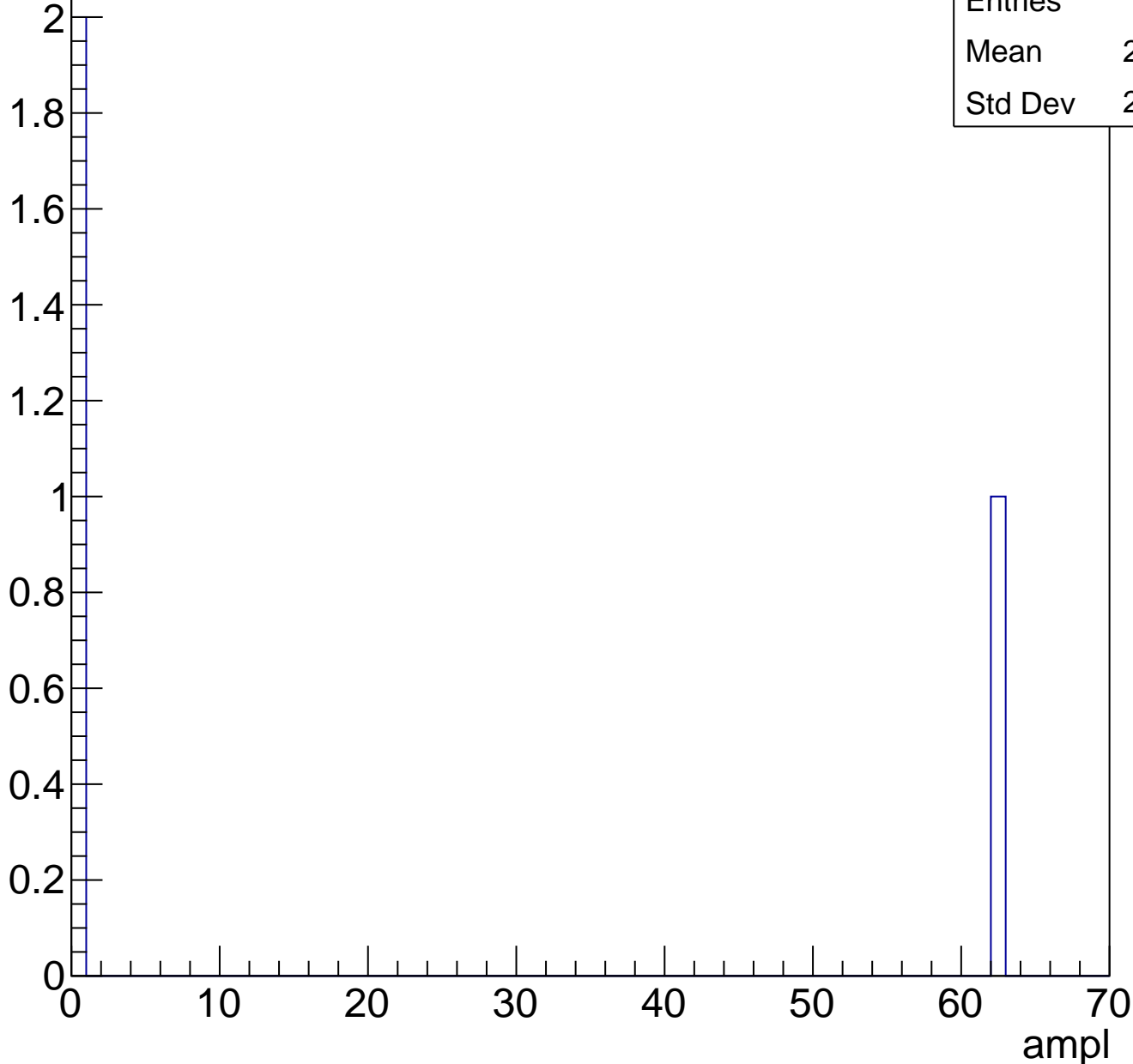
Entries	22
Mean	61.41
Std Dev	1.497



B1L102S, U11-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U11-ch49, adc0

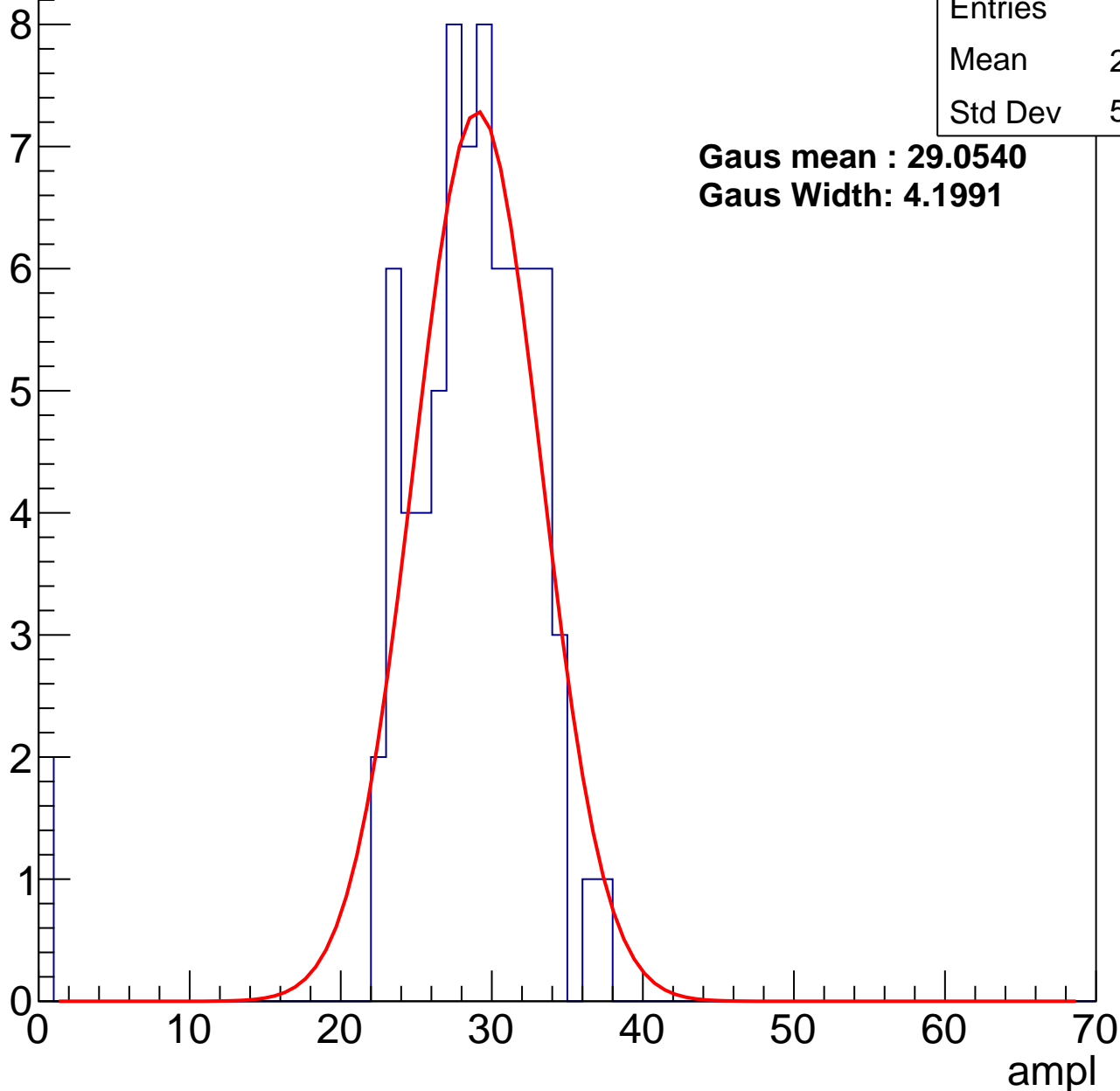
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.77
Std Dev	5.773

Gaus mean : 29.0540

Gaus Width: 4.1991



B1L102S, U11-ch49, adc1

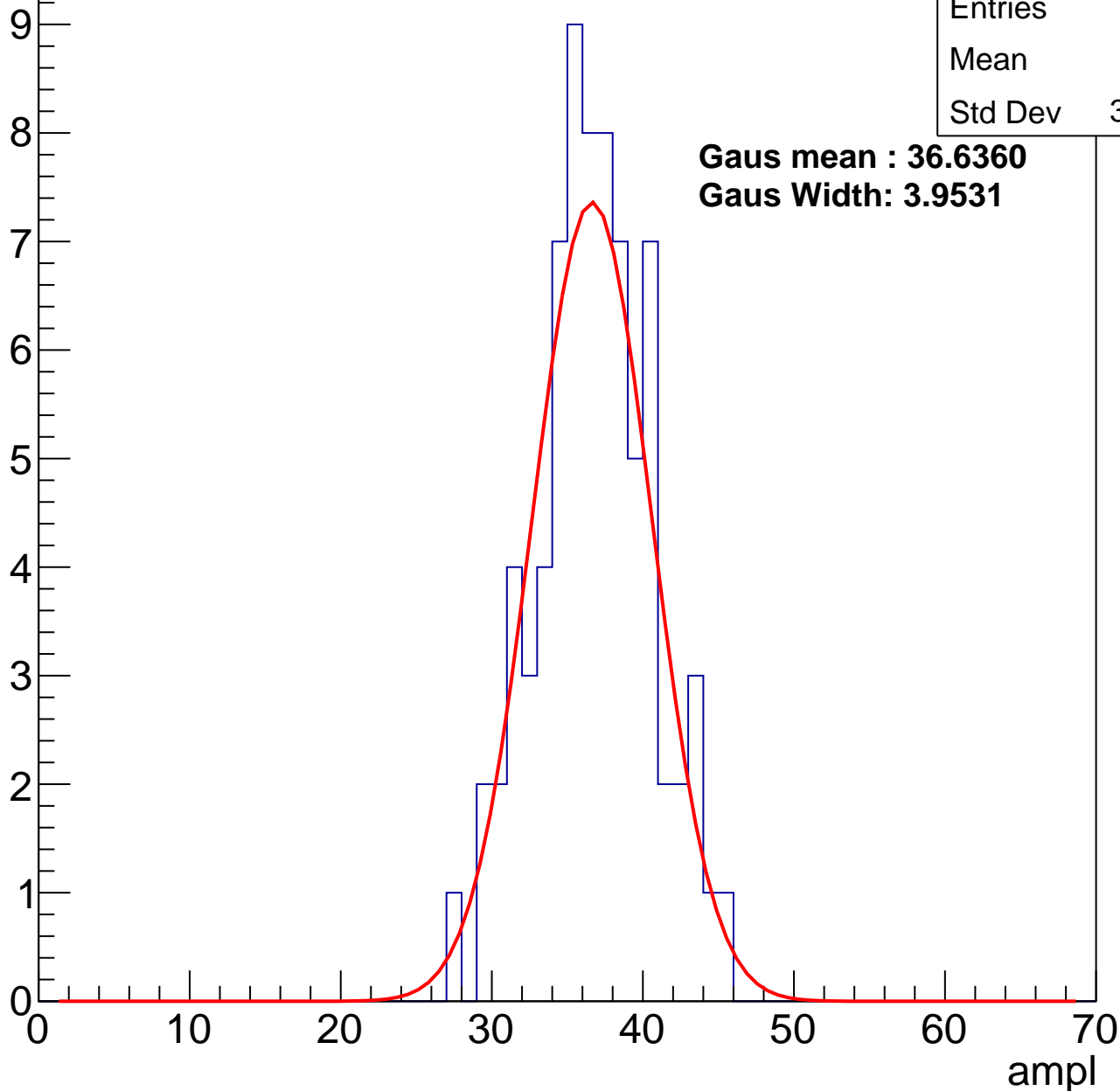
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.3
Std Dev	3.756

Gaus mean : 36.6360

Gaus Width: 3.9531



B1L102S, U11-ch49, adc2

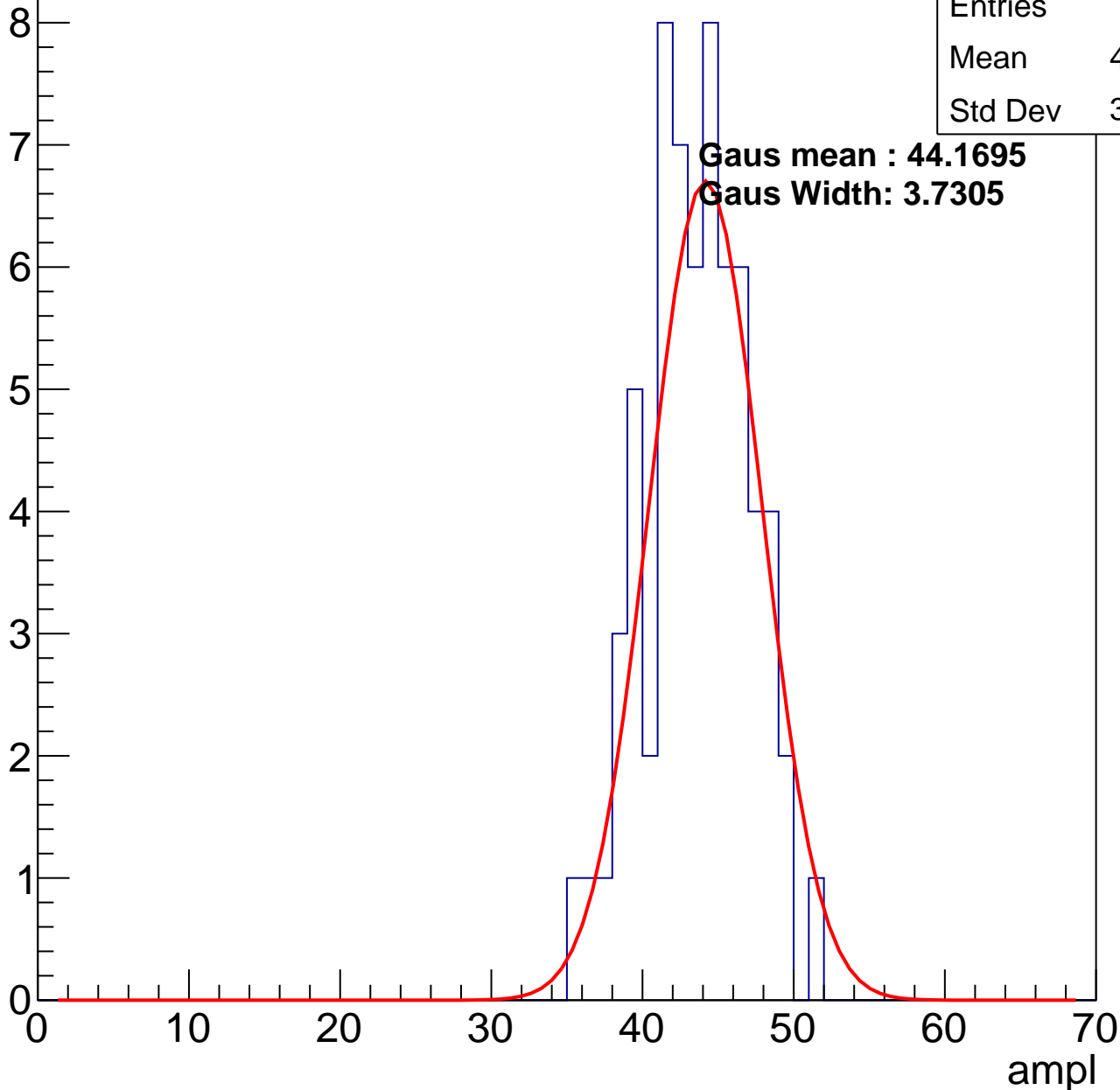
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.14
Std Dev	3.387

Gaus mean : 44.1695

Gaus Width: 3.7305

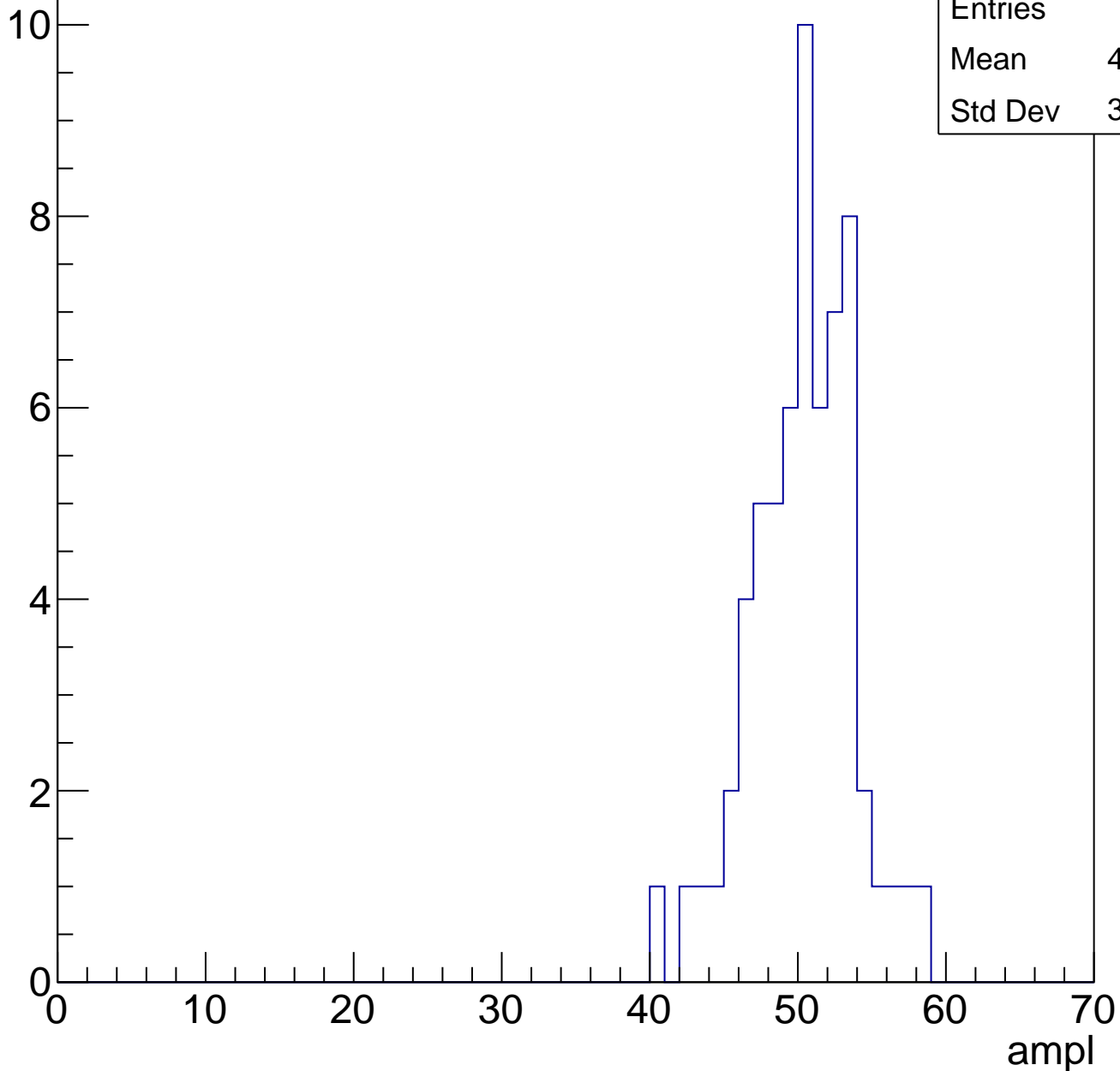


B1L102S, U11-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	49.84
Std Dev	3.437

Entry

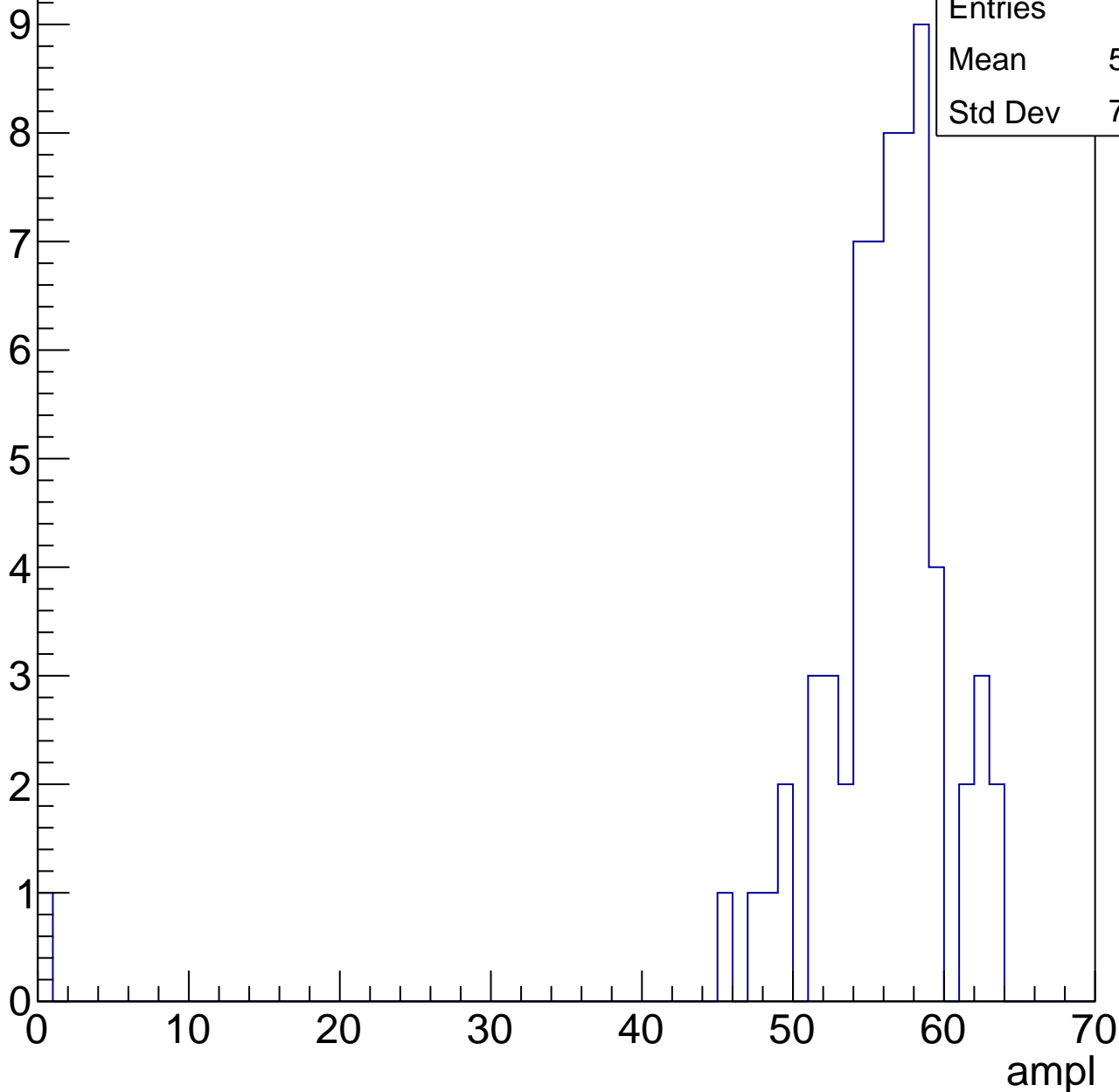


B1L102S, U11-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

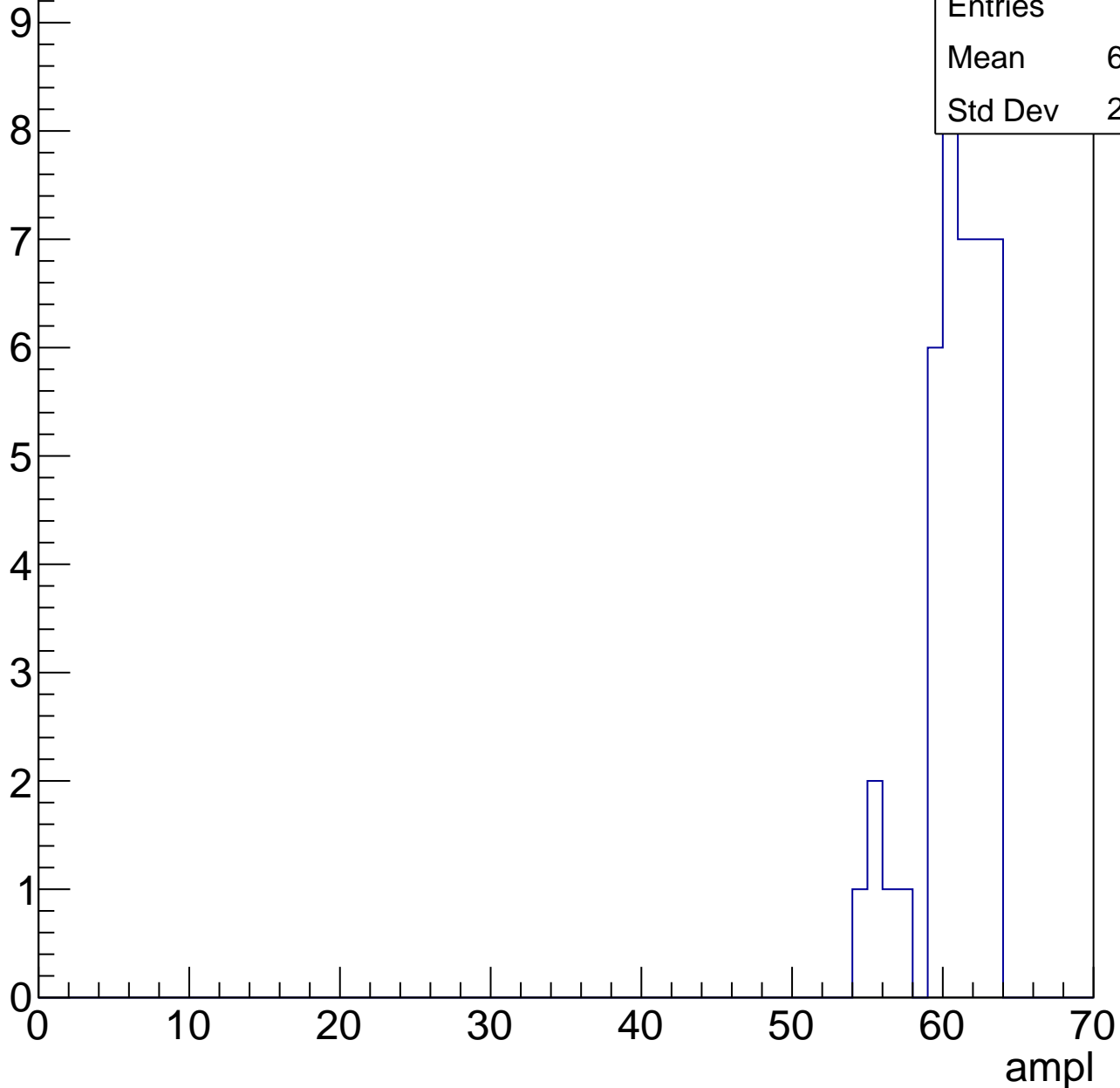
Entries	64
Mean	54.88
Std Dev	7.837



B1L102S, U11-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

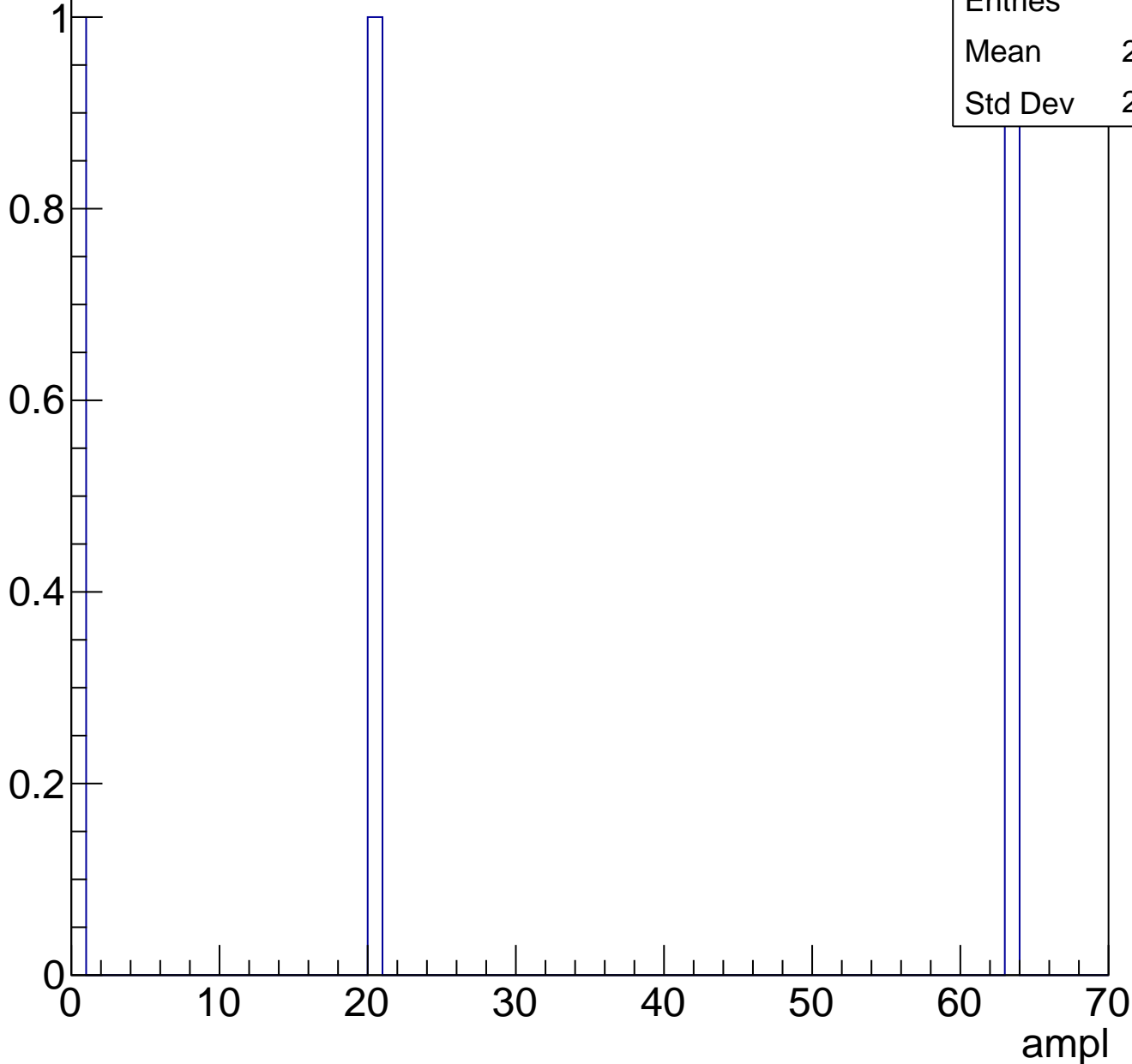
Entry



B1L102S, U11-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B1L102S, U11-ch50, adc0

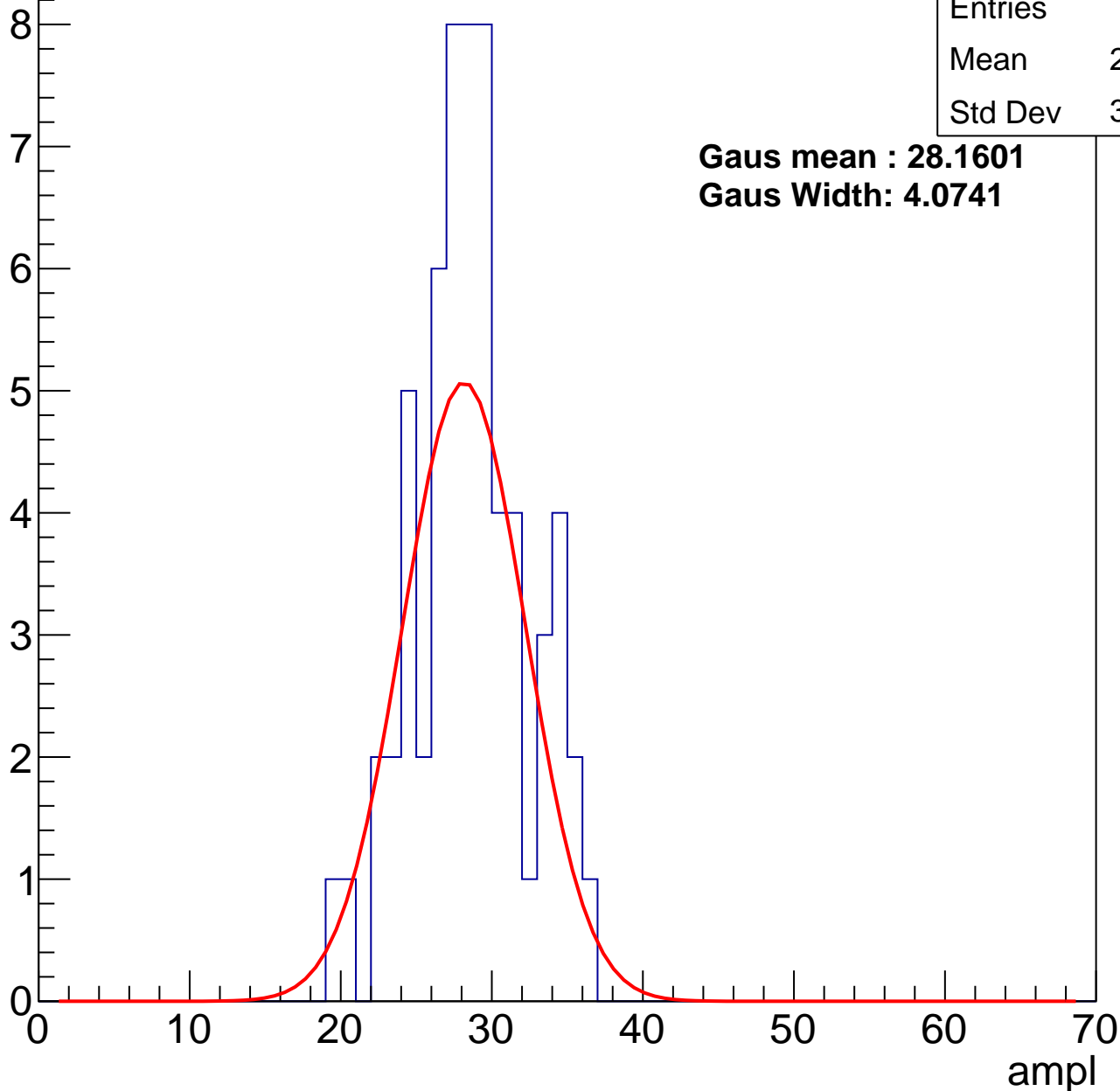
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.13
Std Dev	3.709

Gaus mean : 28.1601

Gaus Width: 4.0741



B1L102S, U11-ch50, adc1

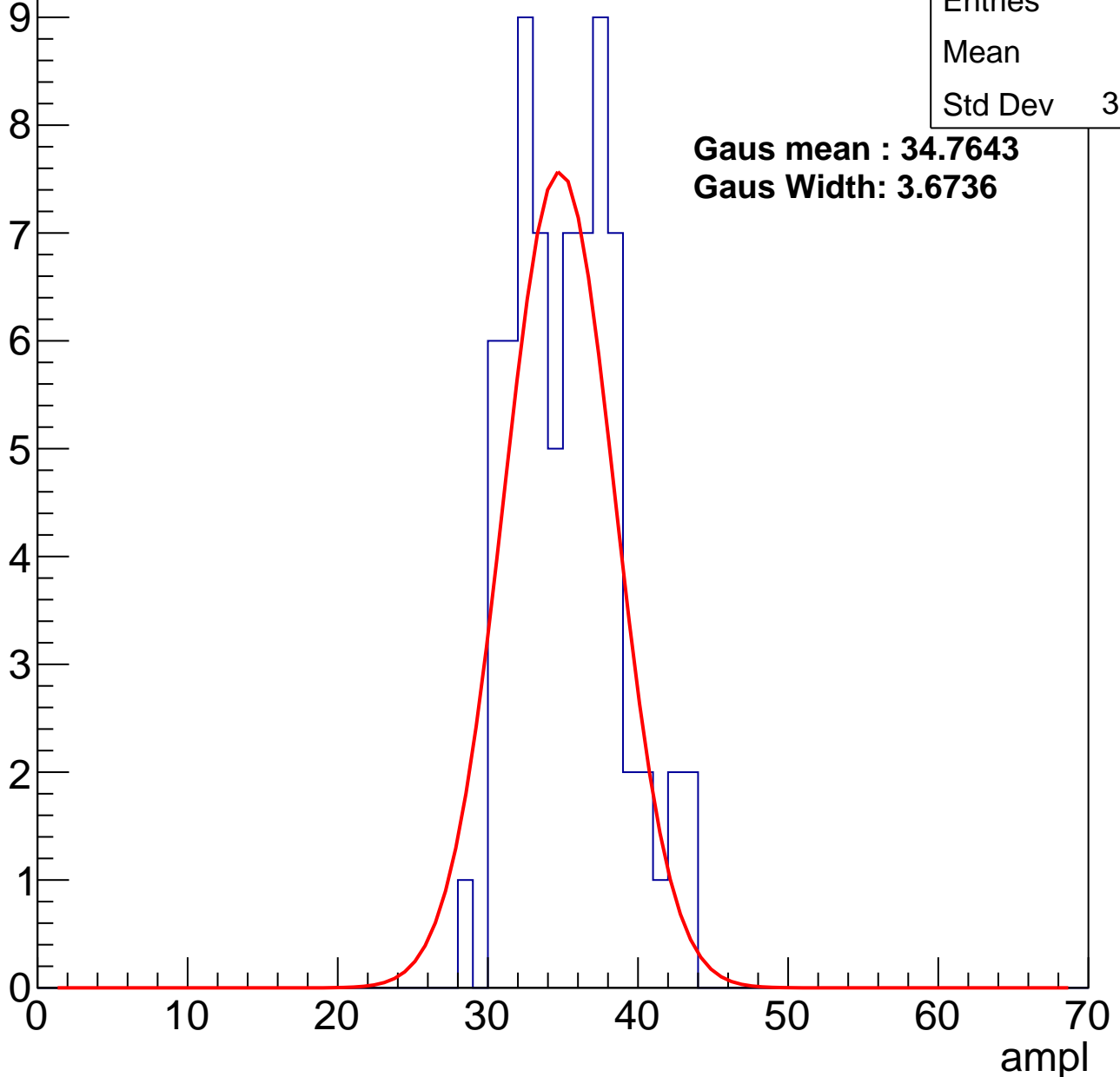
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.9
Std Dev	3.429

Gaus mean : 34.7643

Gaus Width: 3.6736



B1L102S, U11-ch50, adc2

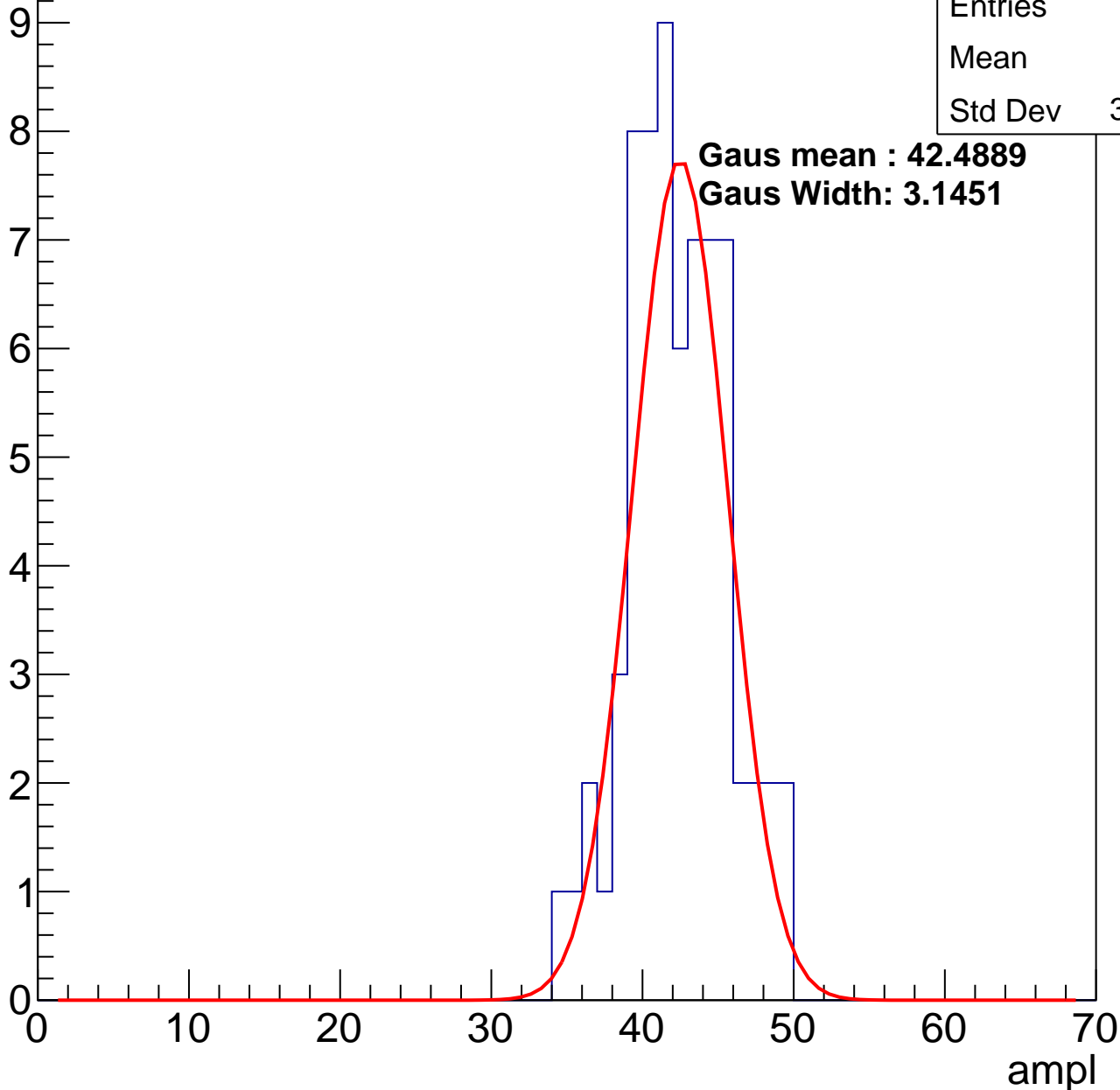
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.9
Std Dev	3.255

Gaus mean : 42.4889

Gaus Width: 3.1451

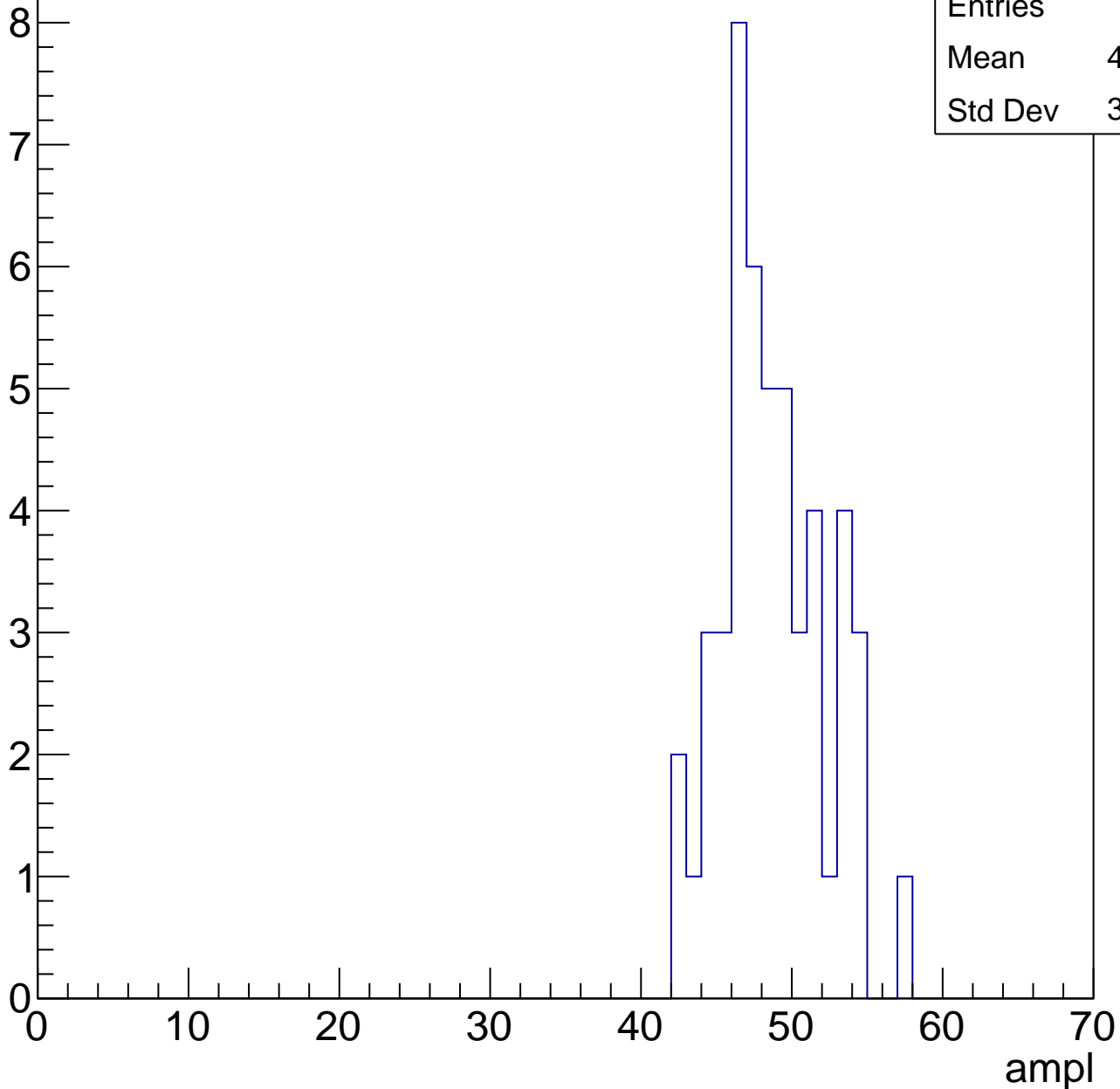


B1L102S, U11-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

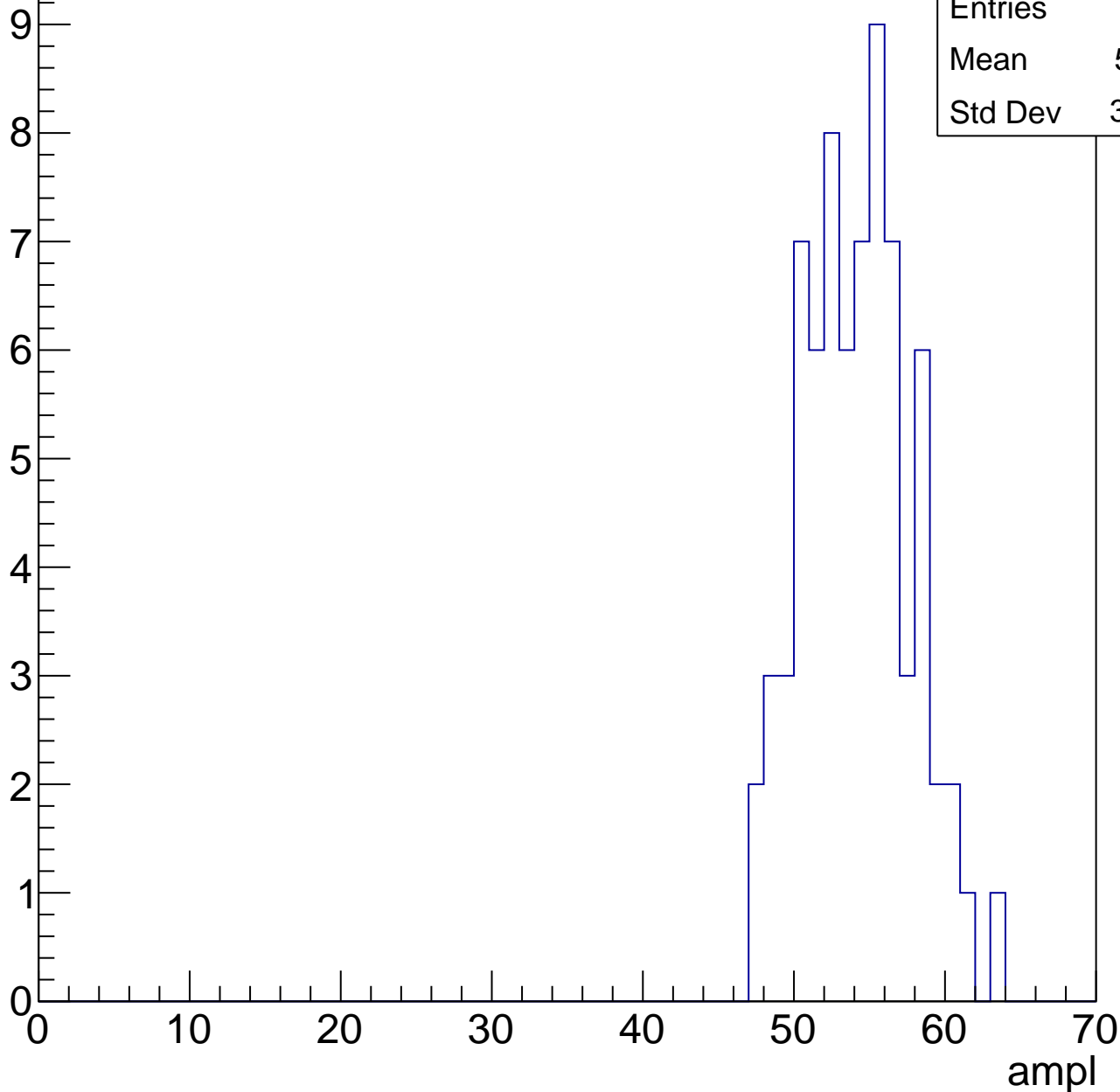
Entries	49
Mean	48.29
Std Dev	3.393



B1L102S, U11-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

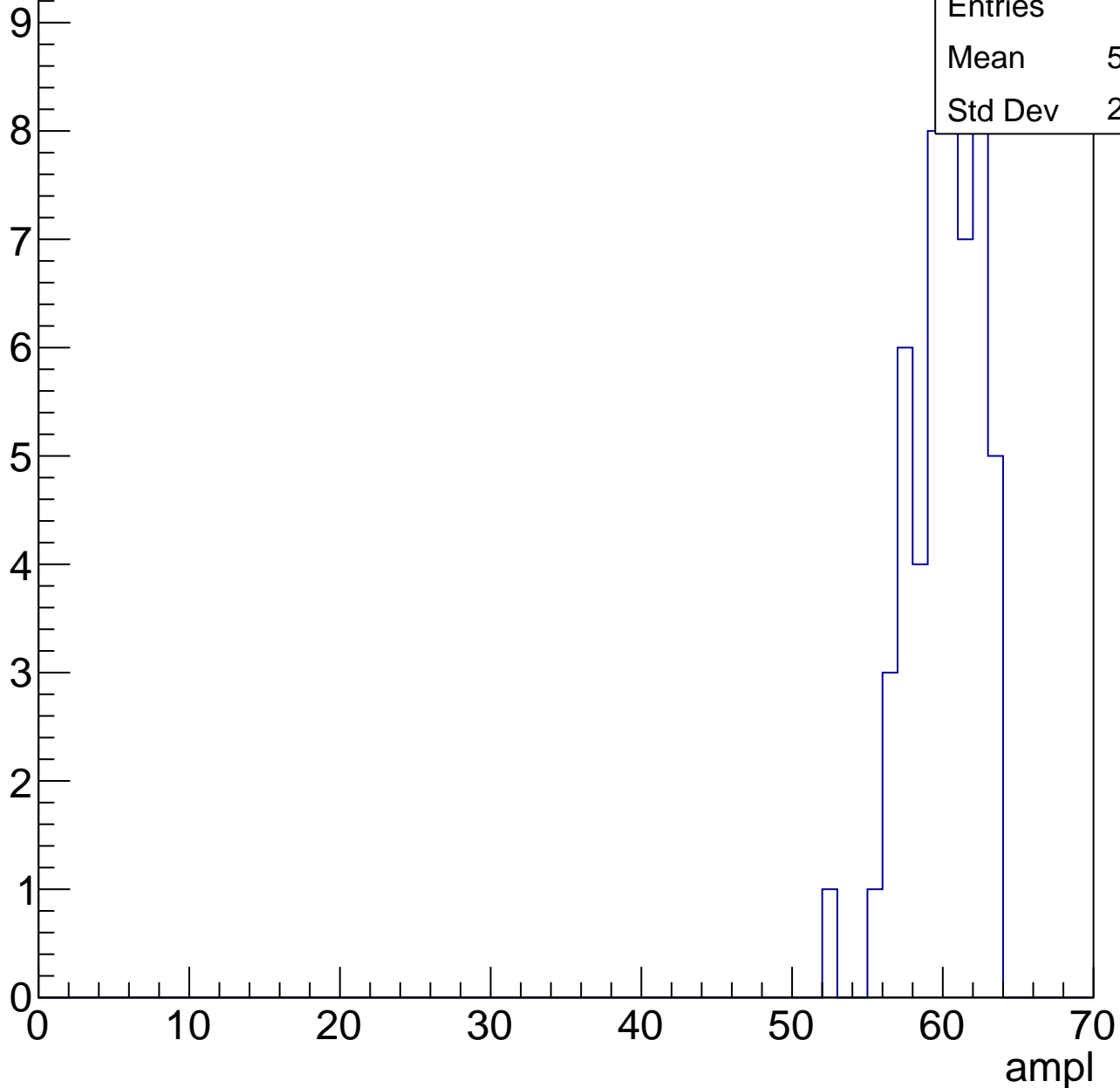
Entry



B1L102S, U11-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

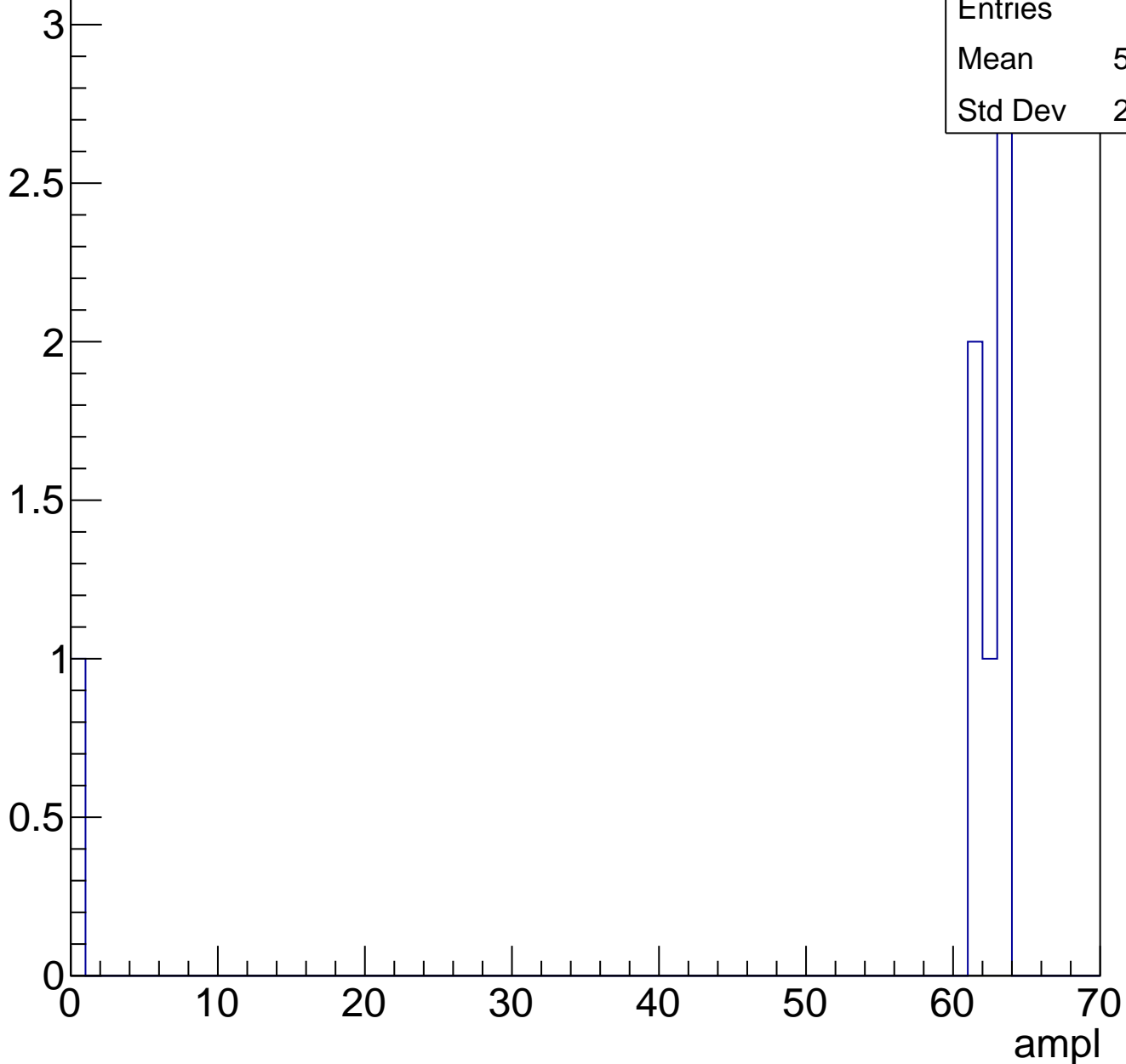
Entry



B1L102S, U11-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch51, adc0

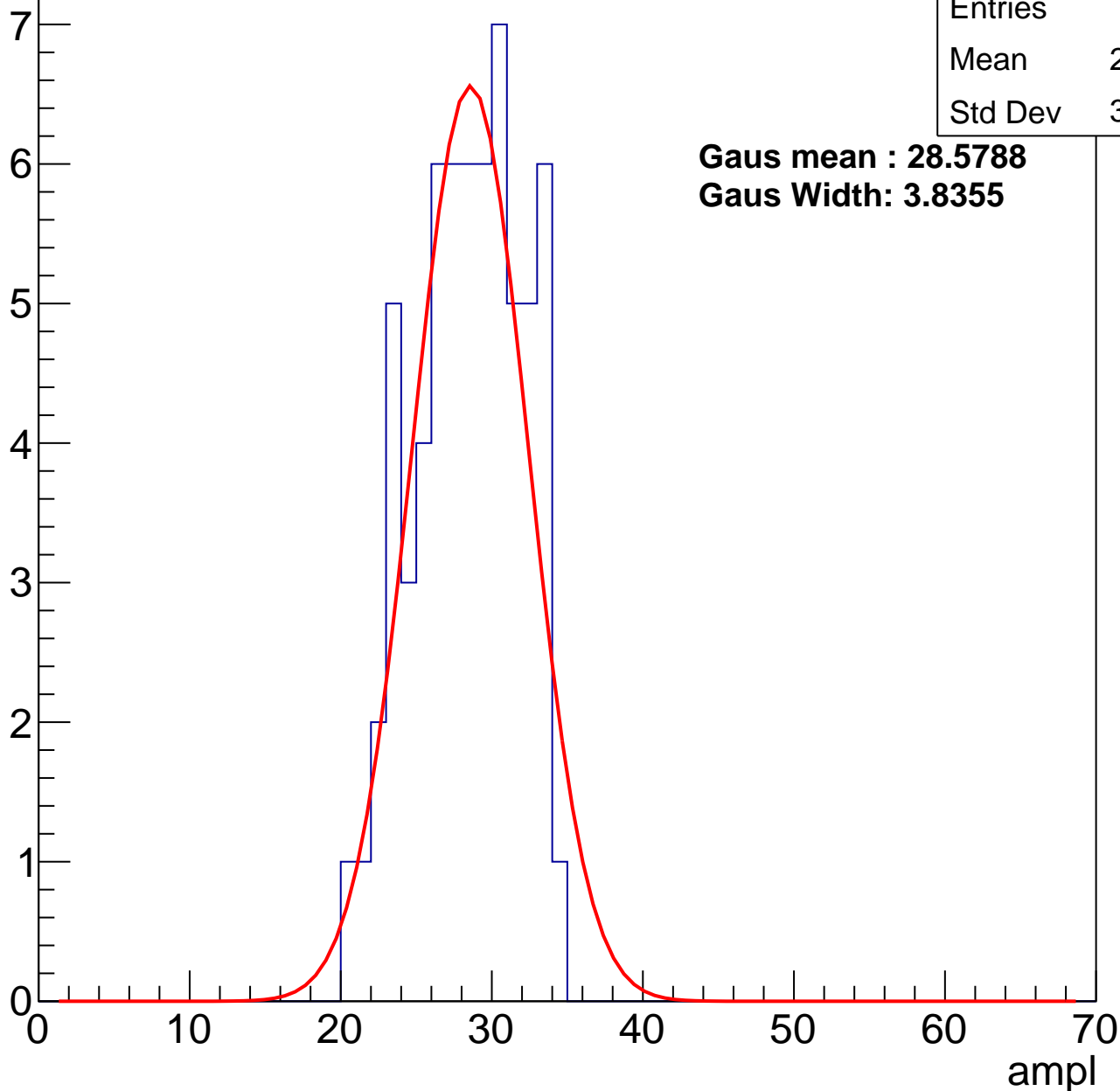
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.95
Std Dev	3.466

Gaus mean : 28.5788

Gaus Width: 3.8355



B1L102S, U11-ch51, adc1

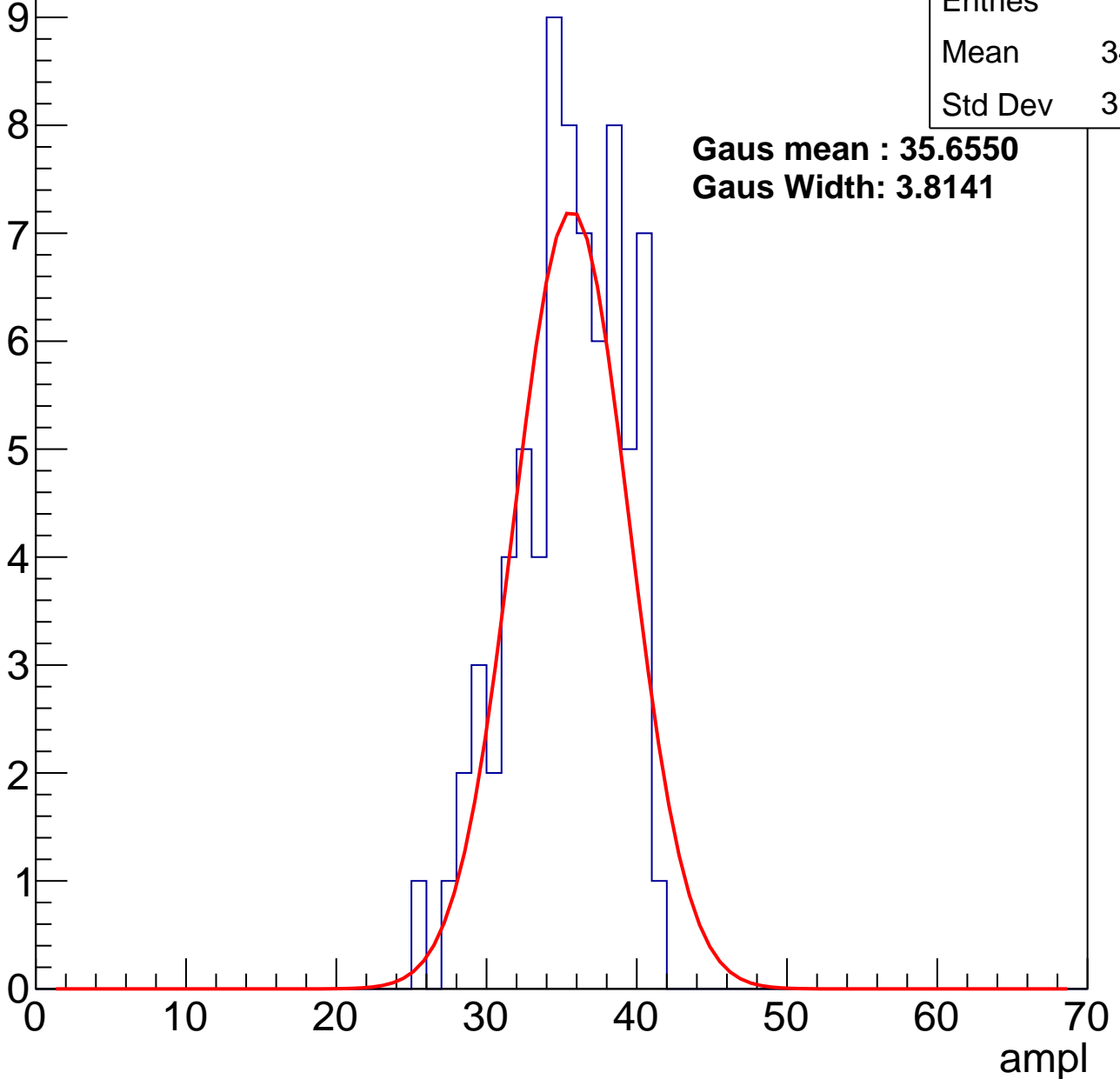
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.95
Std Dev	3.603

Gaus mean : 35.6550

Gaus Width: 3.8141



B1L102S, U11-ch51, adc2

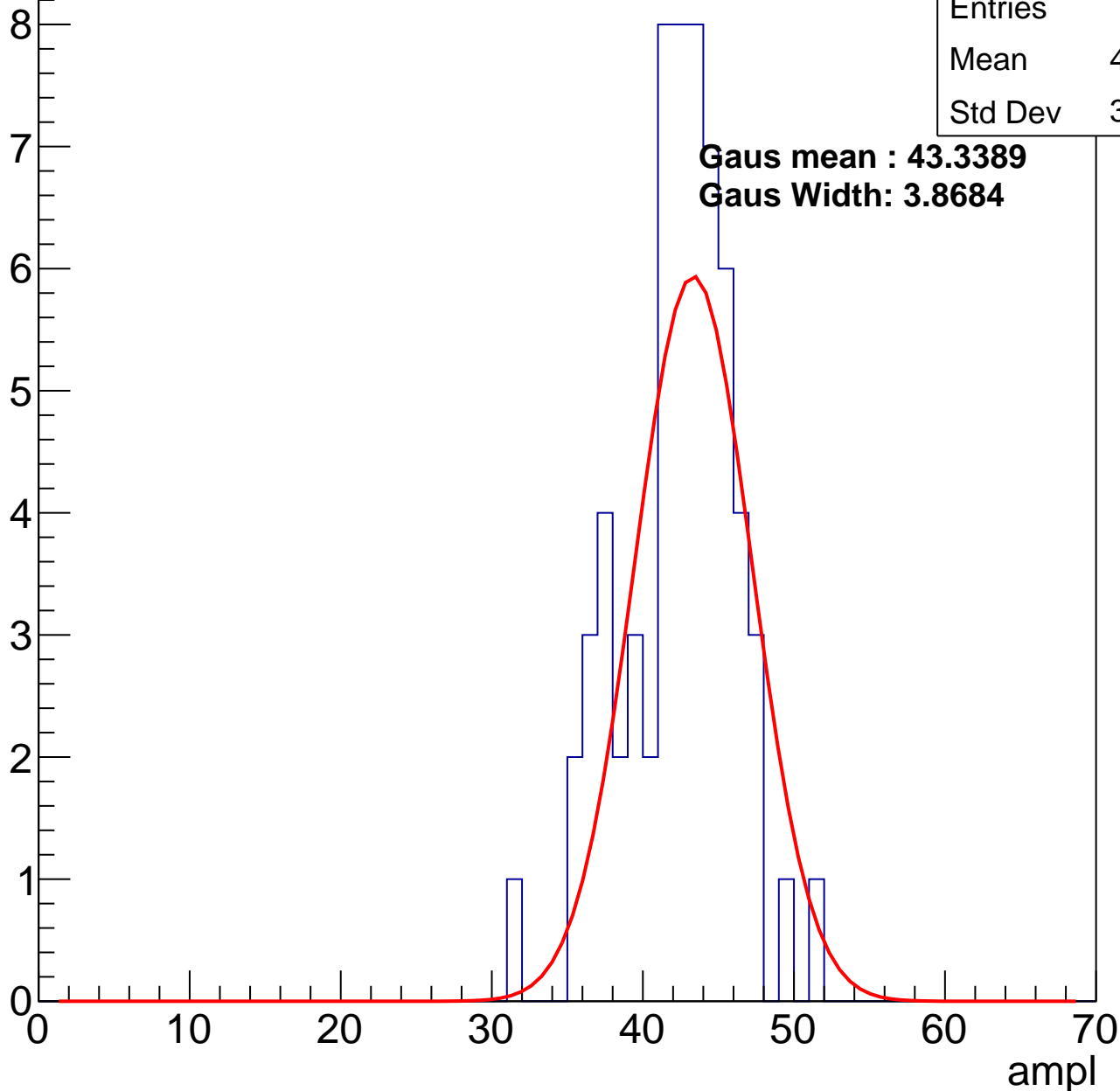
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.92
Std Dev	3.696

Gaus mean : 43.3389

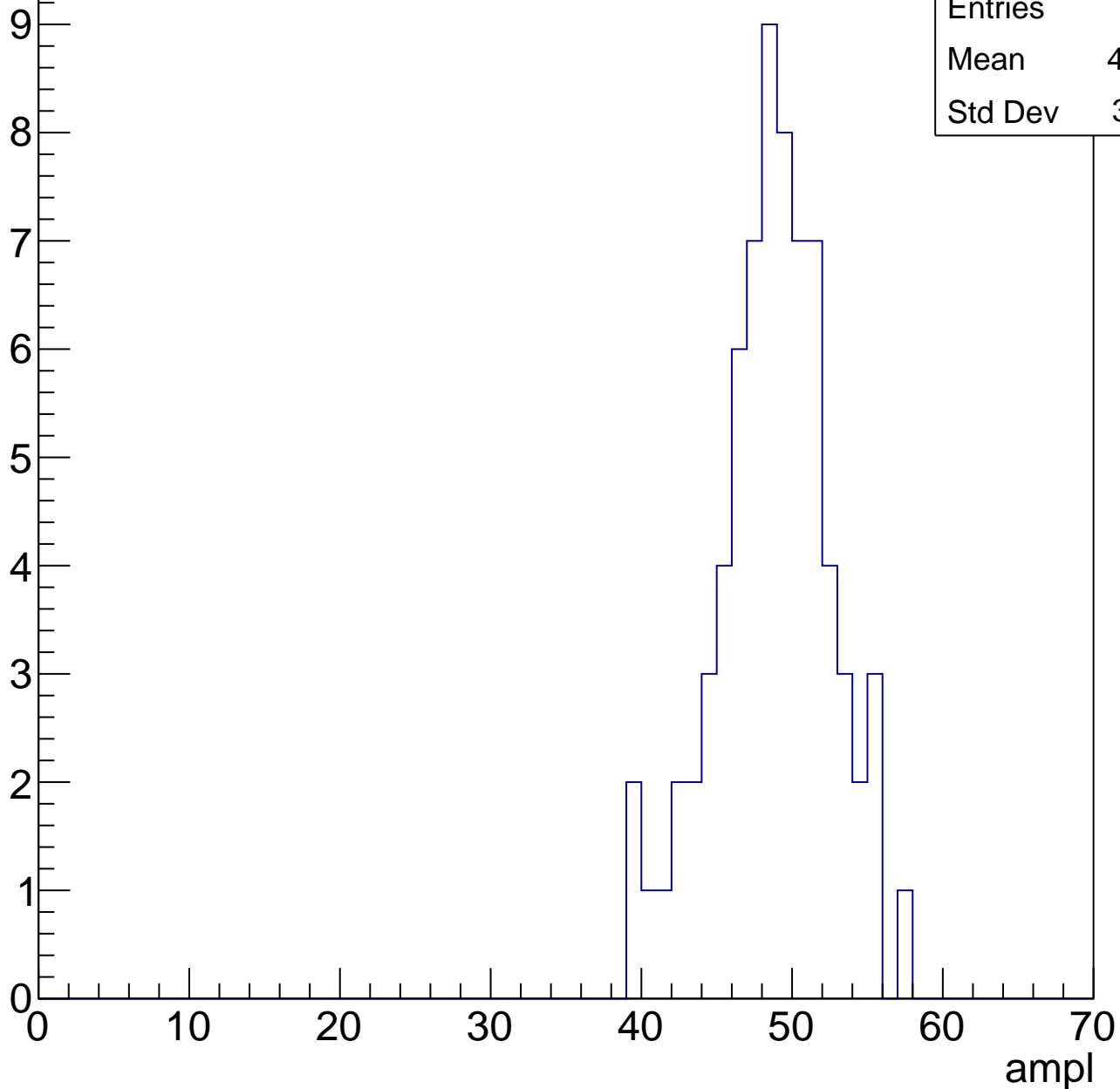
Gaus Width: 3.8684



B1L102S, U11-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



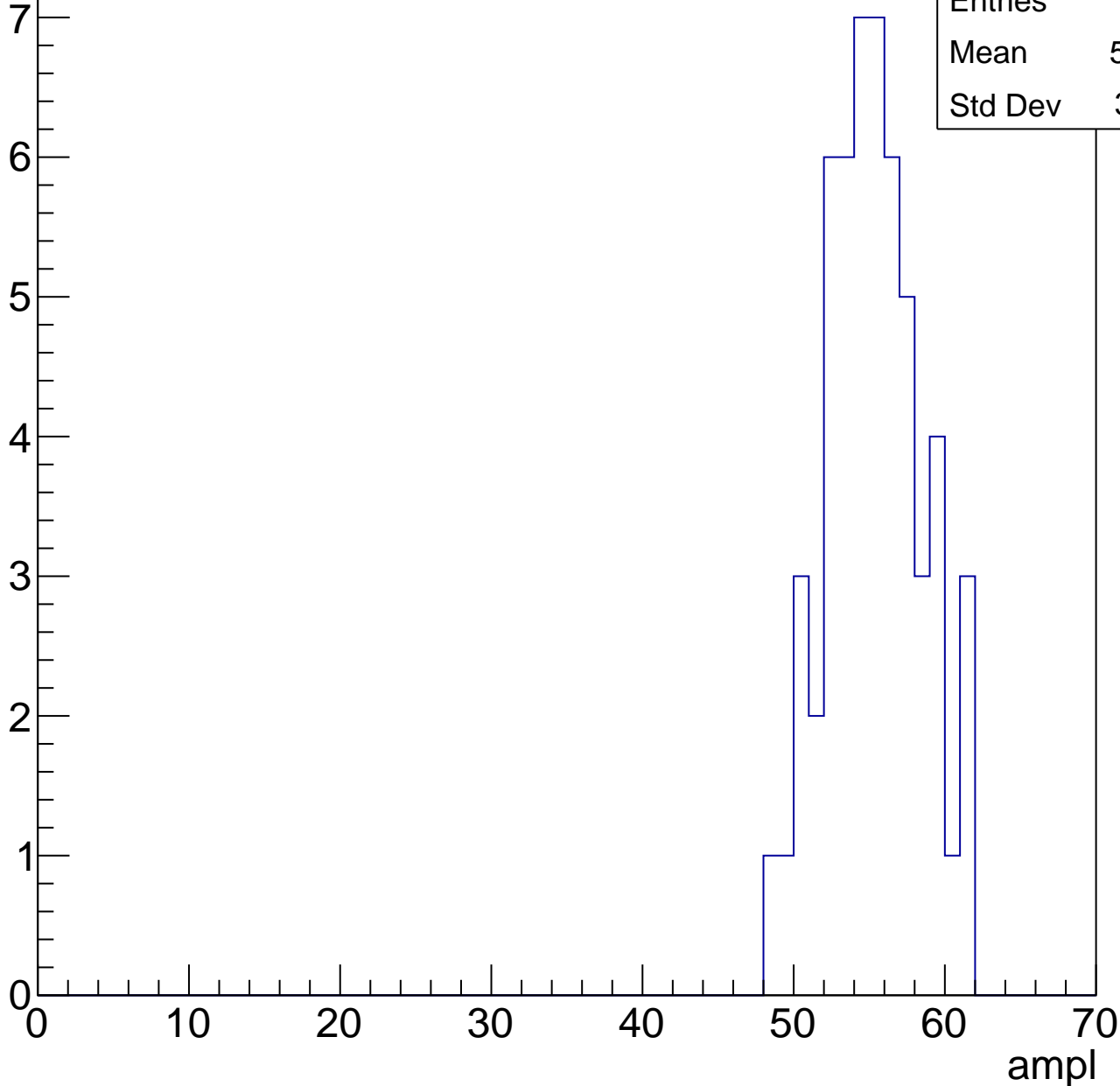
Entries	72
Mean	48.25
Std Dev	3.811

B1L102S, U11-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

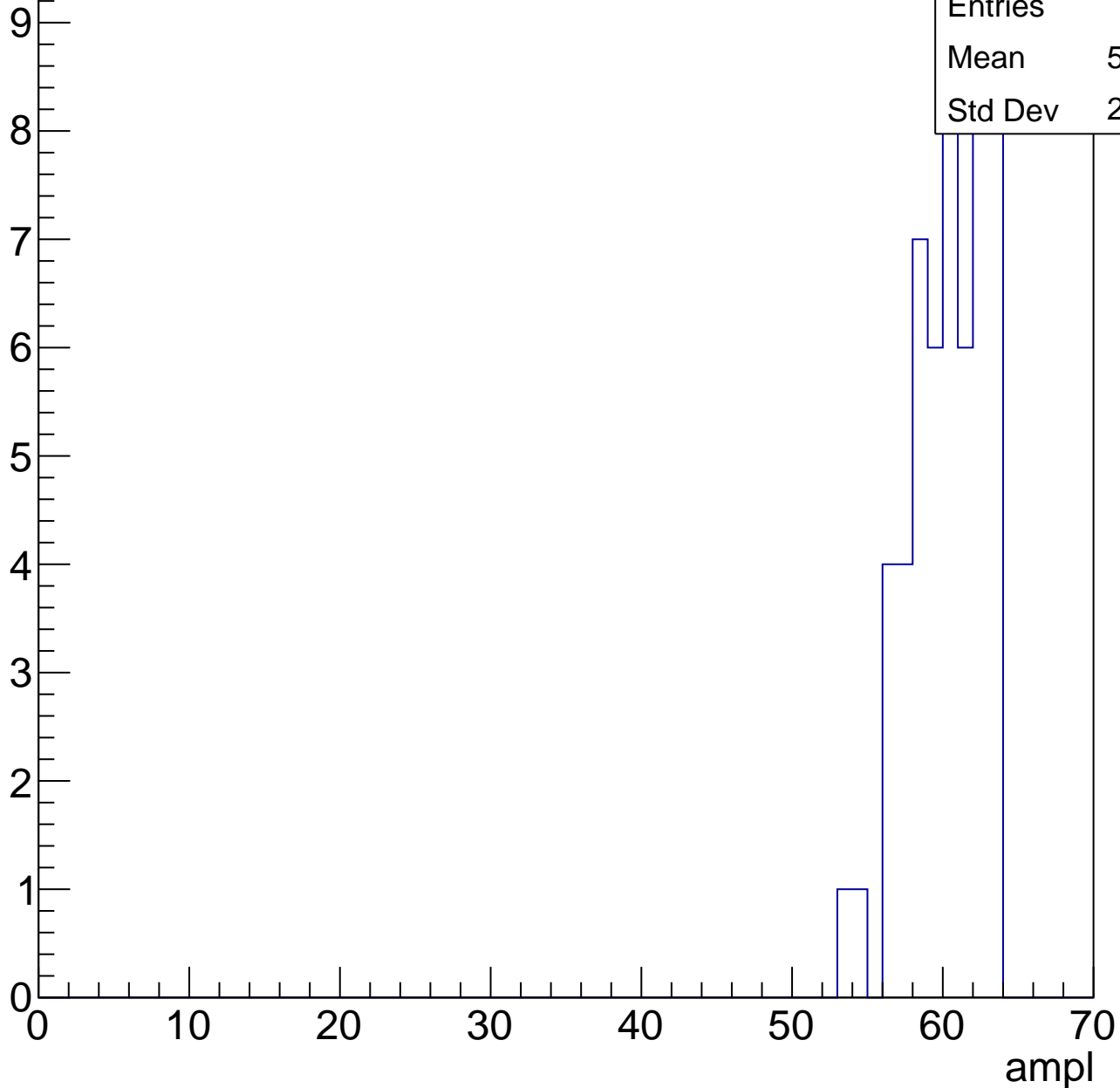
Entries	55
Mean	54.84
Std Dev	3.091



B1L102S, U11-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch52, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	27.97
Std Dev	5.035

Gaus mean : 29.1253

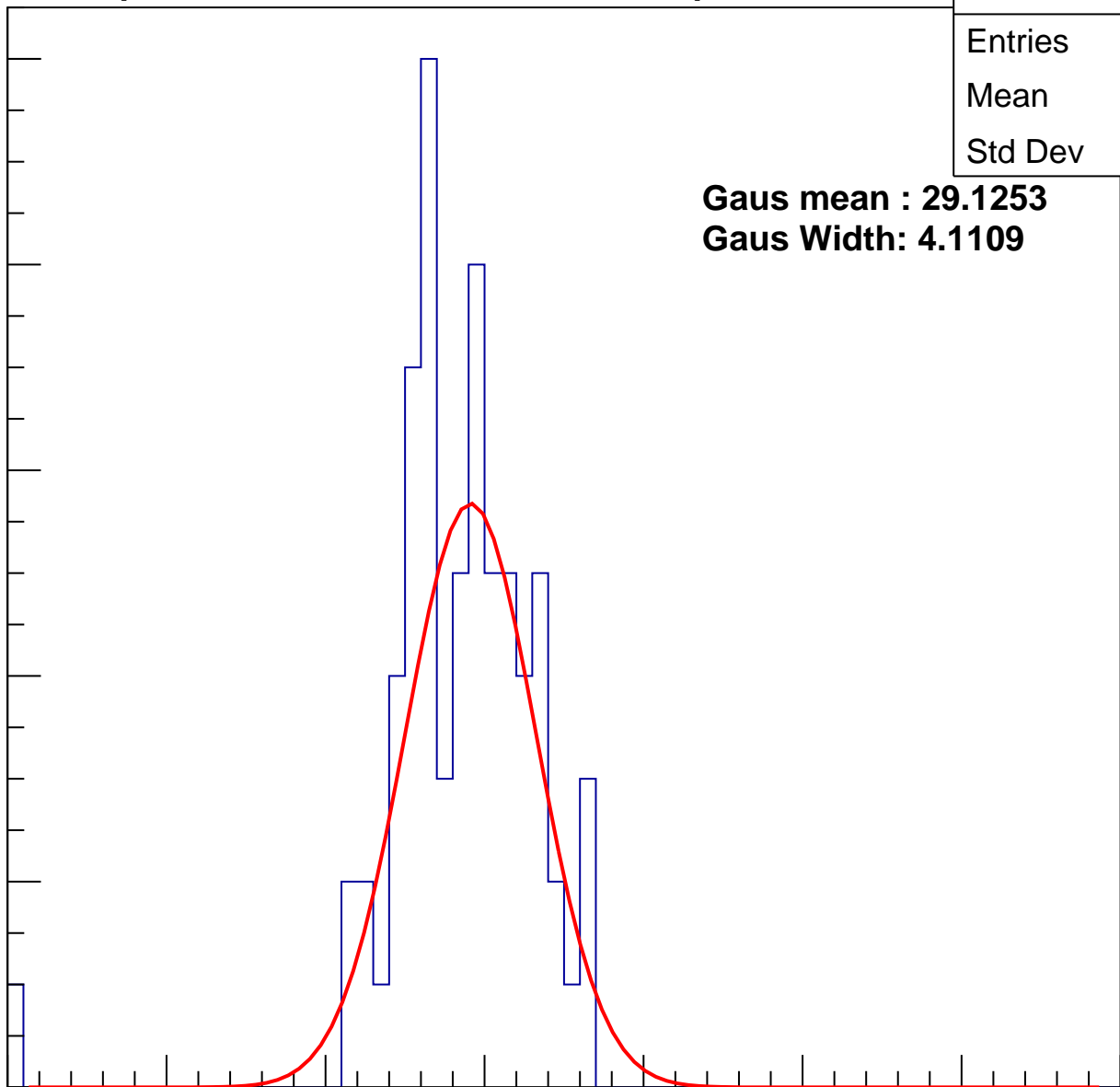
Gaus Width: 4.1109

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



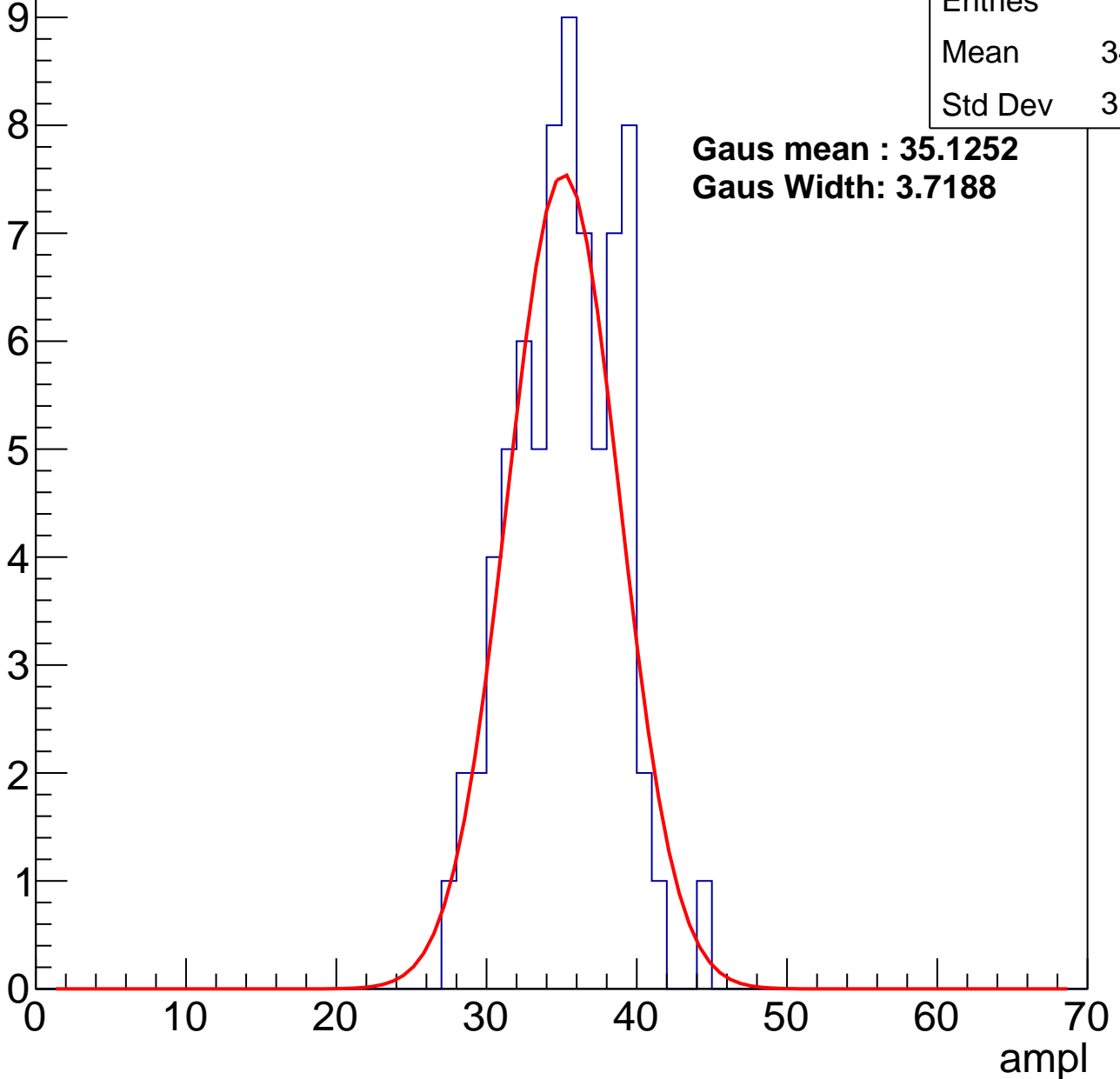
B1L102S, U11-ch52, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.79
Std Dev	3.468

Gaus mean : 35.1252
Gaus Width: 3.7188



B1L102S, U11-ch52, adc2

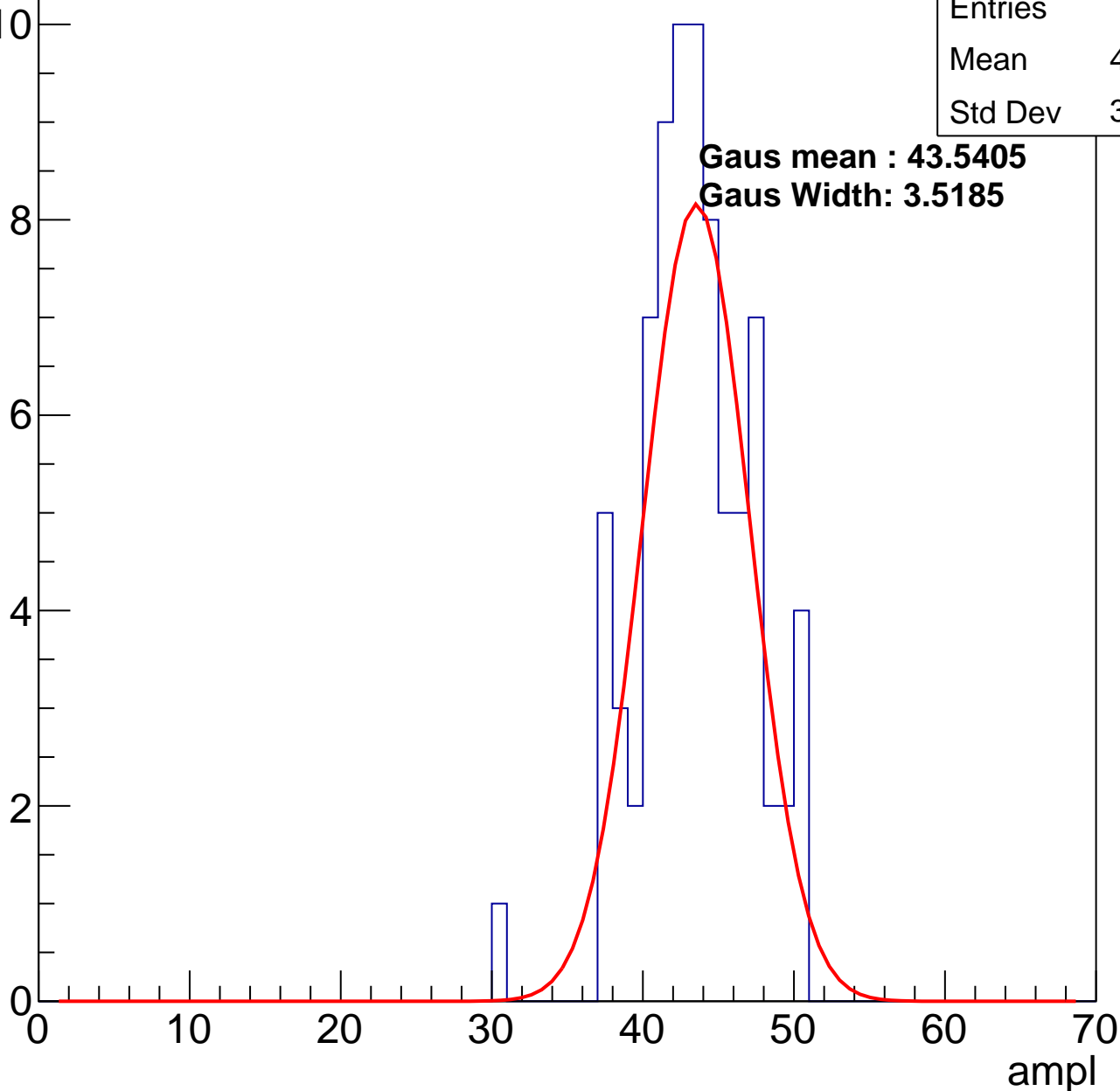
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	42.95
Std Dev	3.653

Gaus mean : 43.5405

Gaus Width: 3.5185

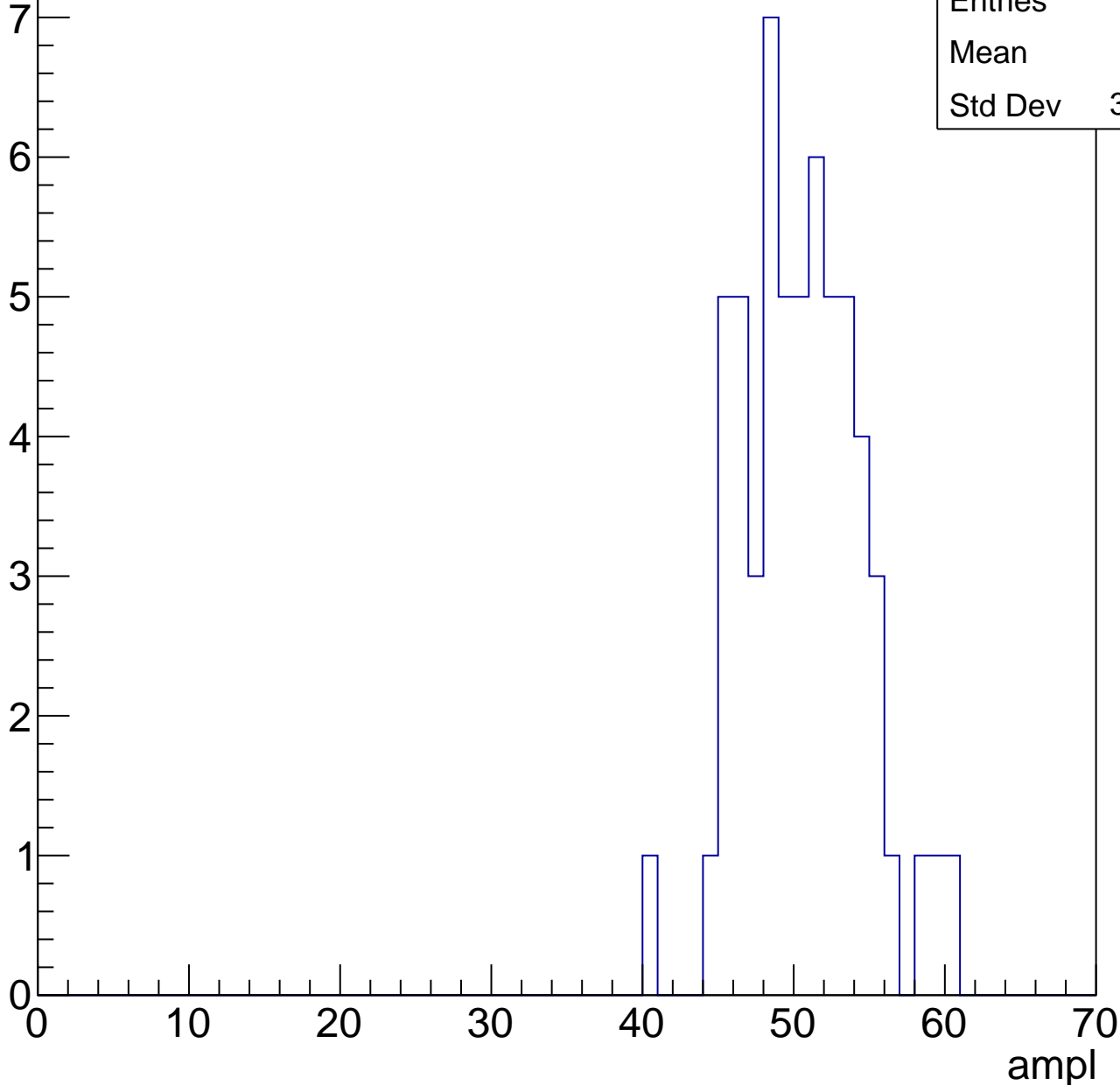


B1L102S, U11-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.1
Std Dev	3.887

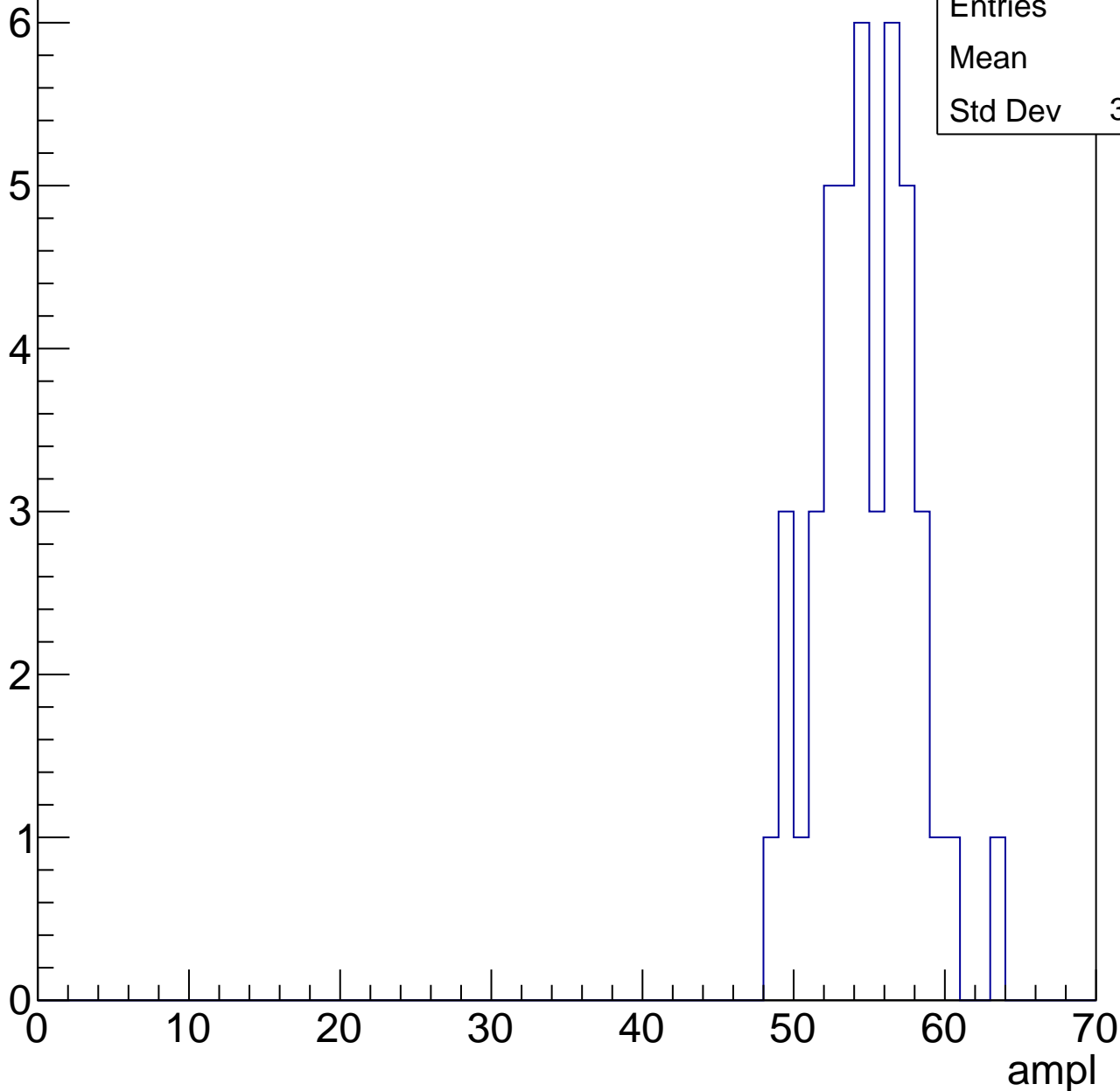


B1L102S, U11-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.3
Std Dev	3.152

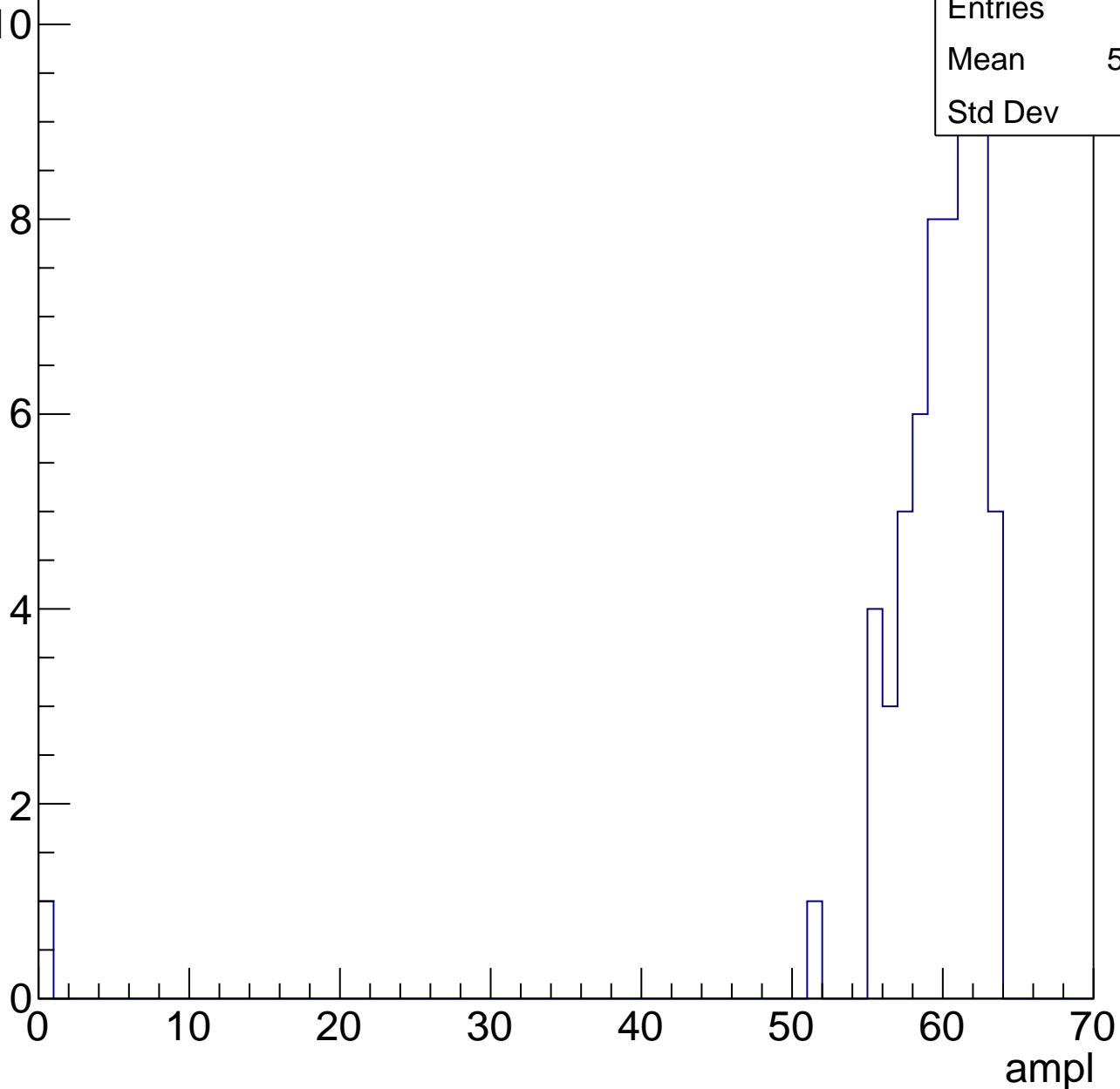


B1L102S, U11-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.47
Std Dev	8.02



B1L102S, U11-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

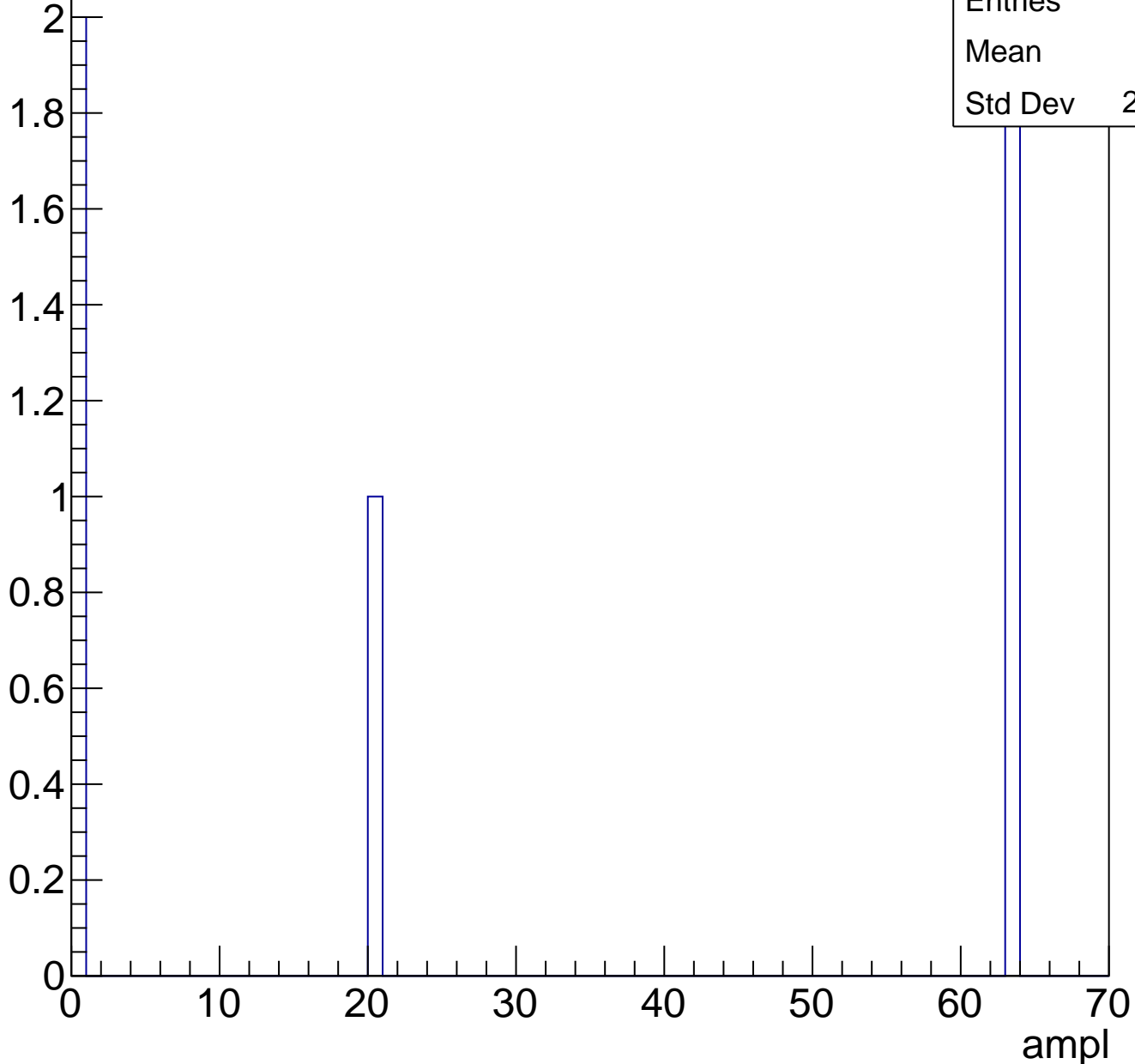
Entry



B1L102S, U11-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	29.2
Std Dev	28.55

B1L102S, U11-ch53, adc0

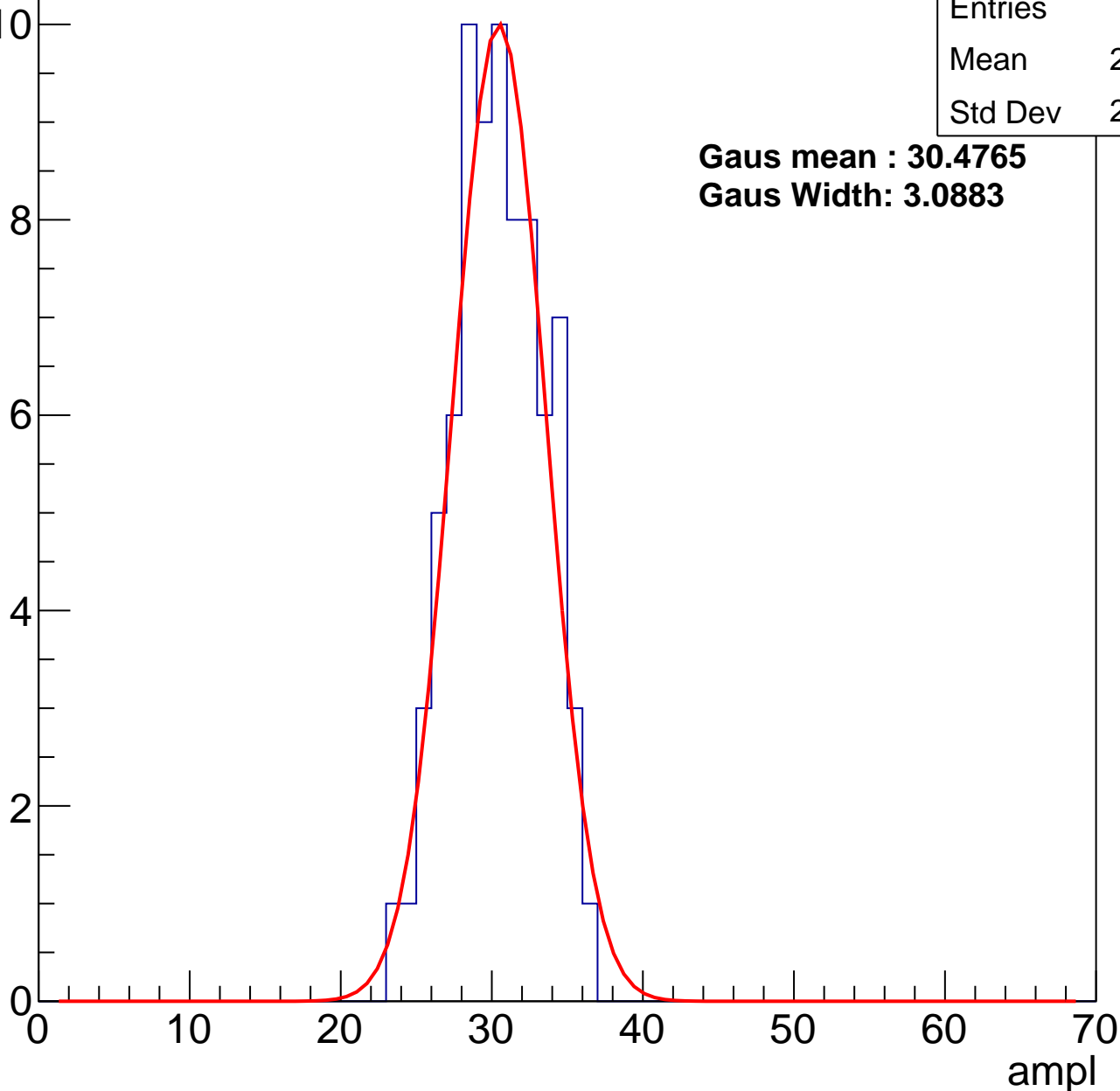
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.95
Std Dev	2.908

Gaus mean : 30.4765

Gaus Width: 3.0883



B1L102S, U11-ch53, adc1

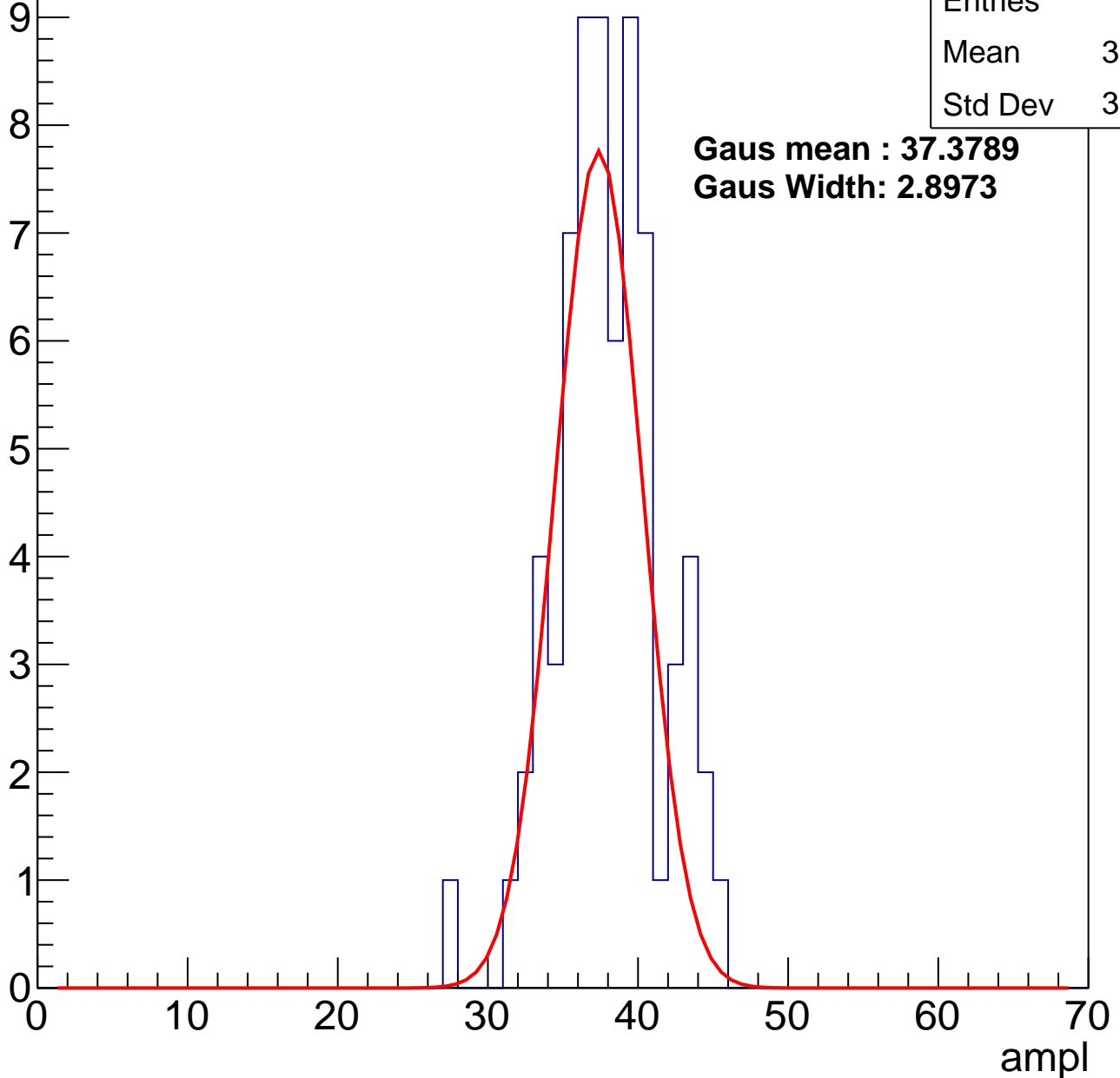
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.52
Std Dev	3.399

Gaus mean : 37.3789

Gaus Width: 2.8973

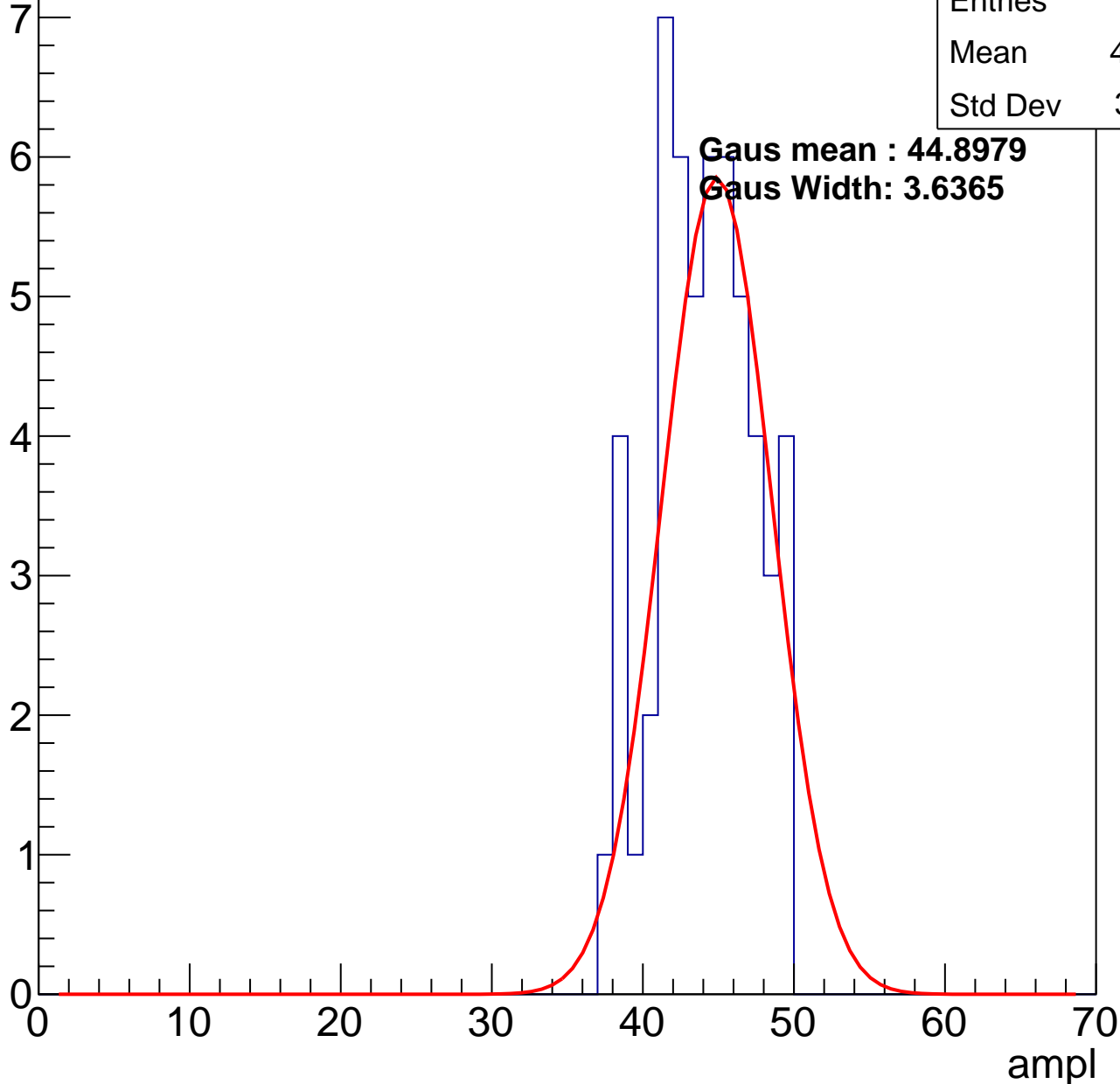


B1L102S, U11-ch53, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	43.59
Std Dev	3.171

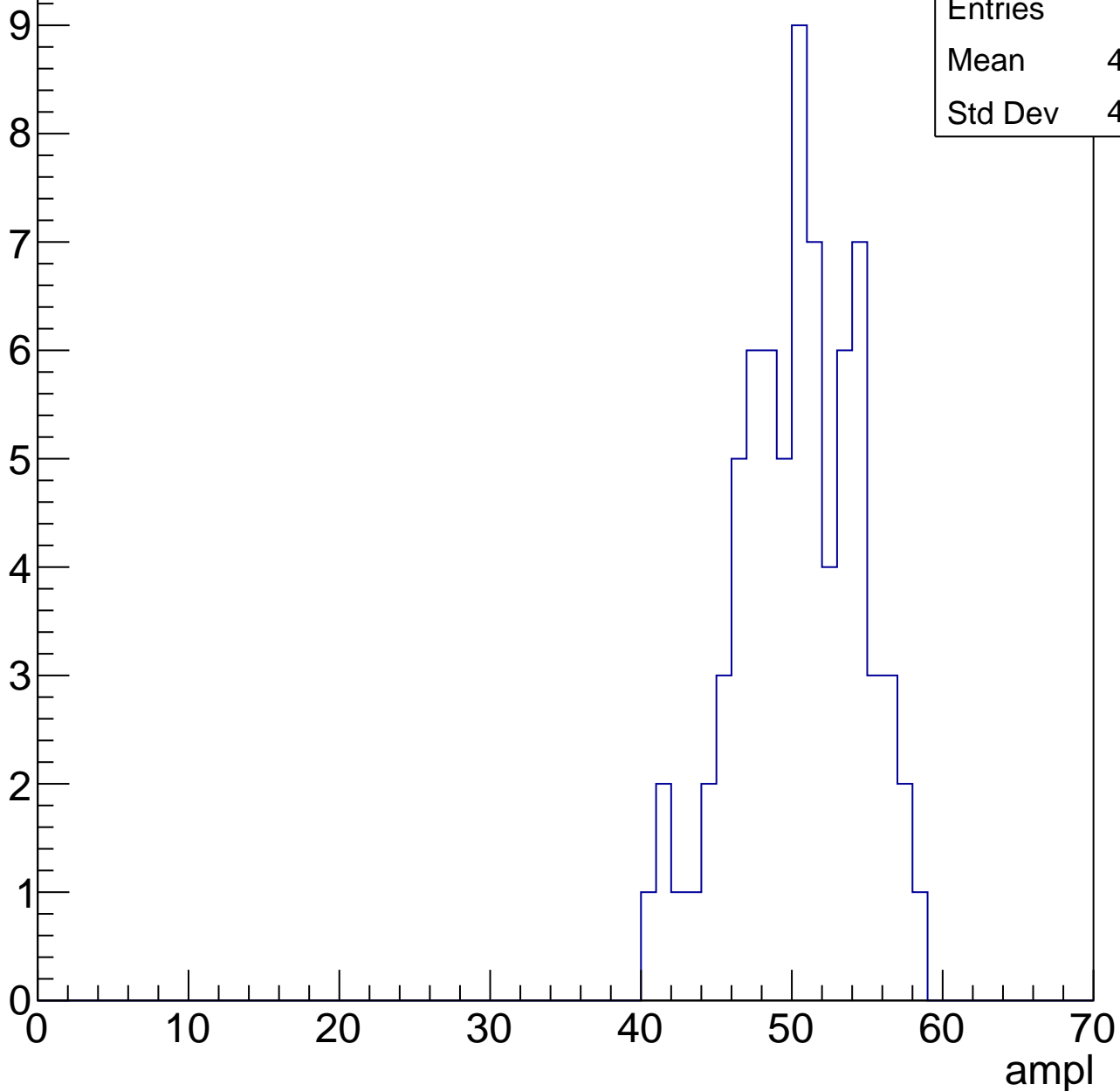


B1L102S, U11-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	49.88
Std Dev	4.057

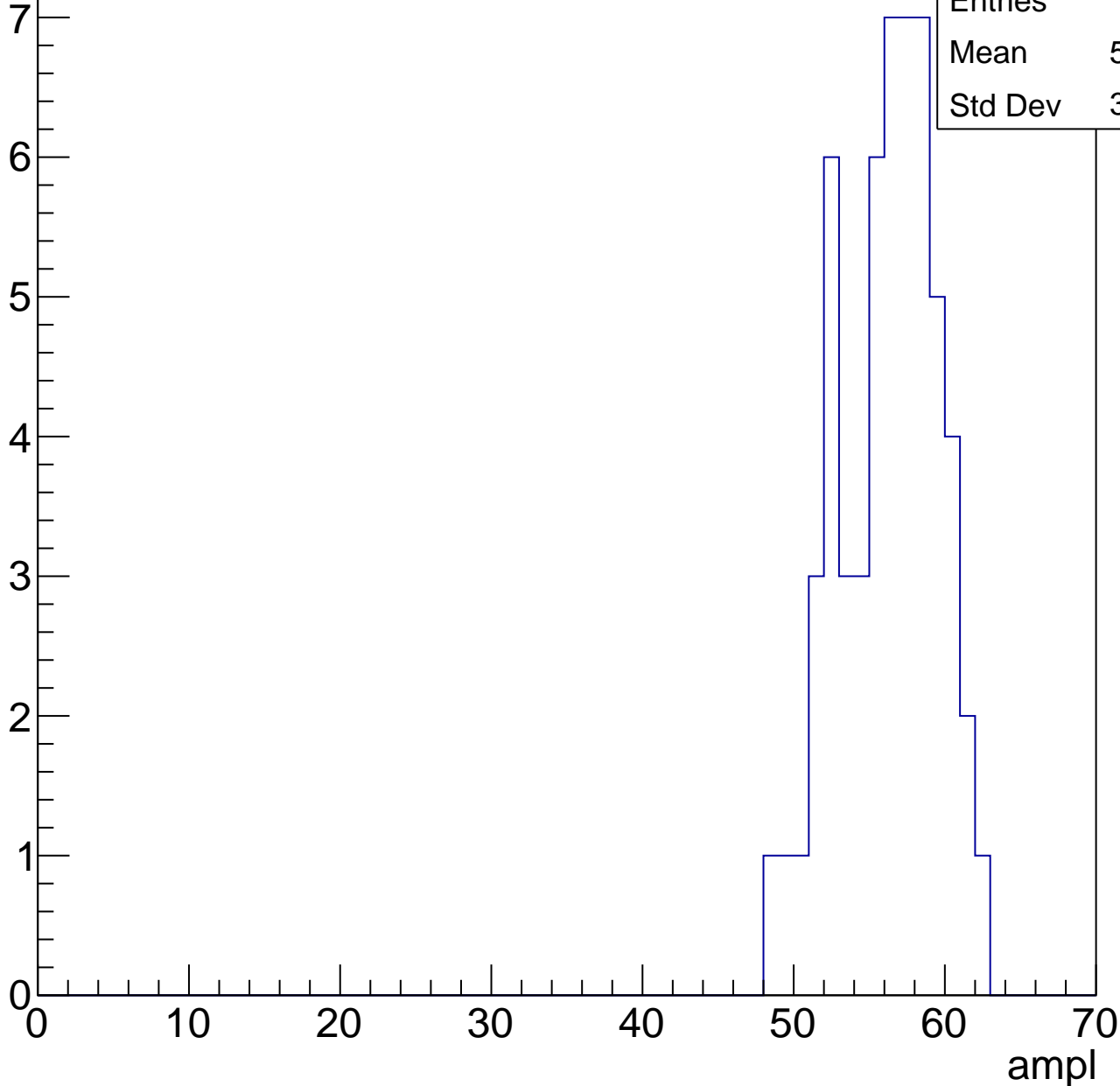


B1L102S, U11-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

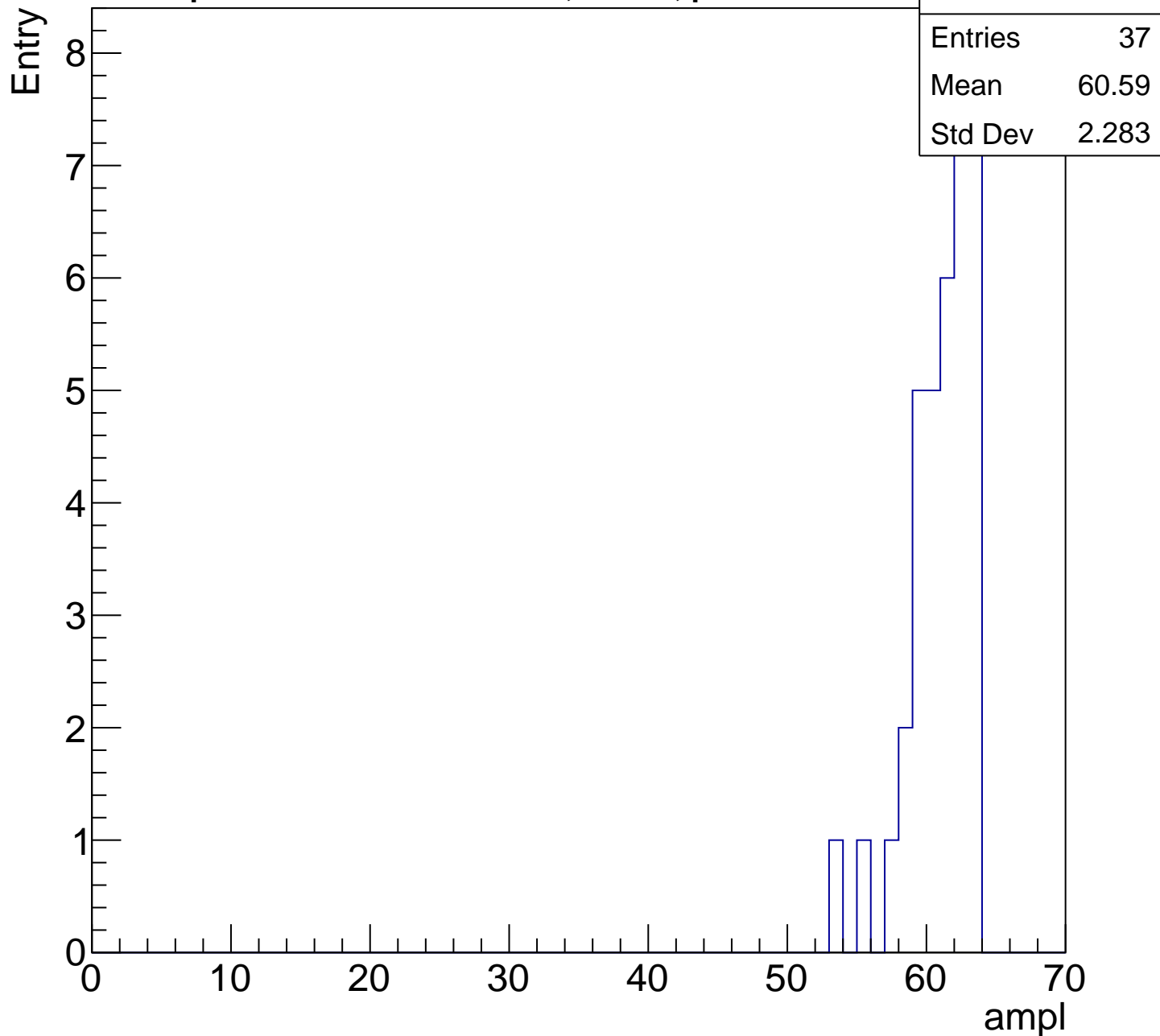
Entry

Entries	57
Mean	55.77
Std Dev	3.223



B1L102S, U11-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U11-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

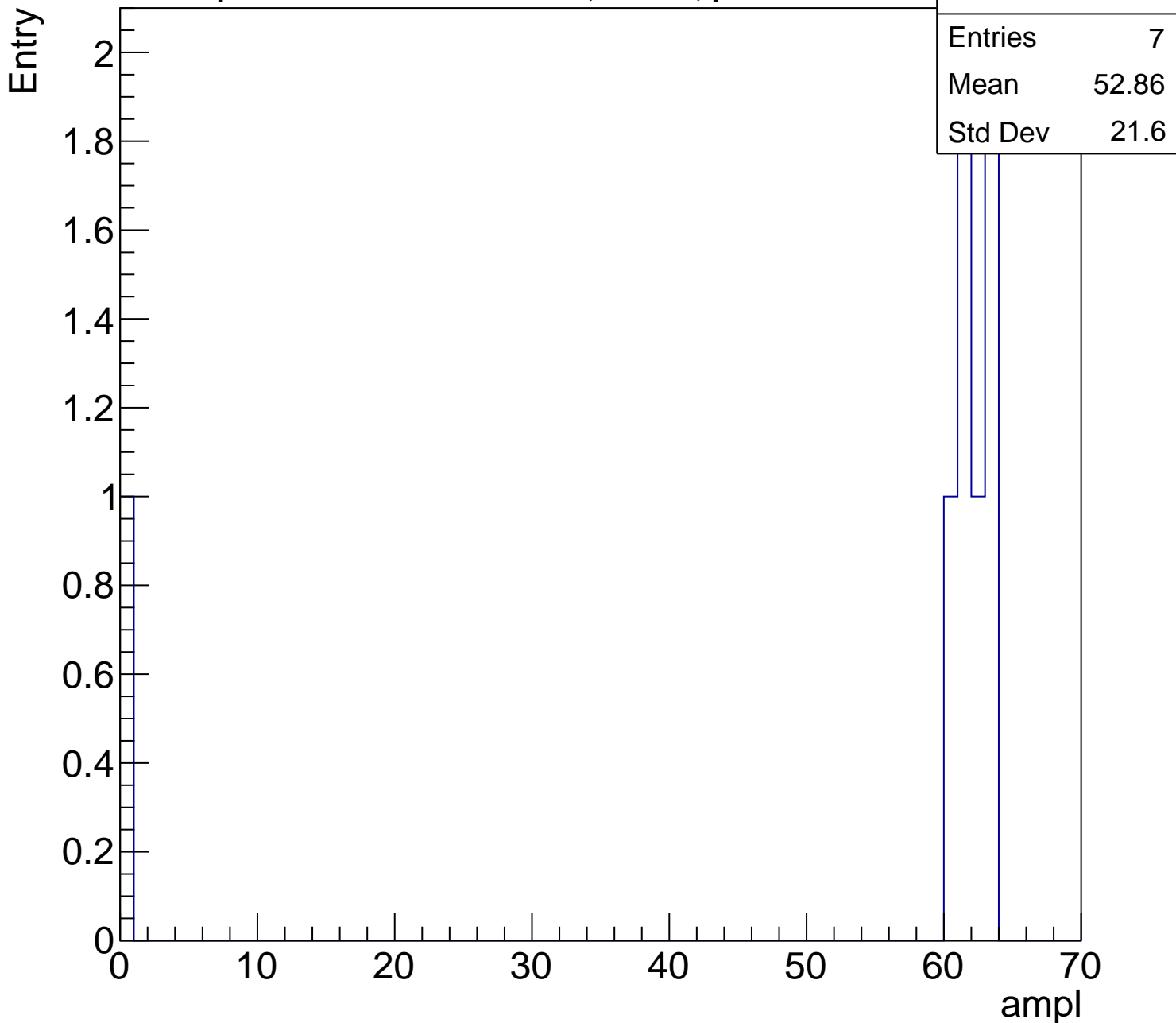
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.6

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch54, adc0

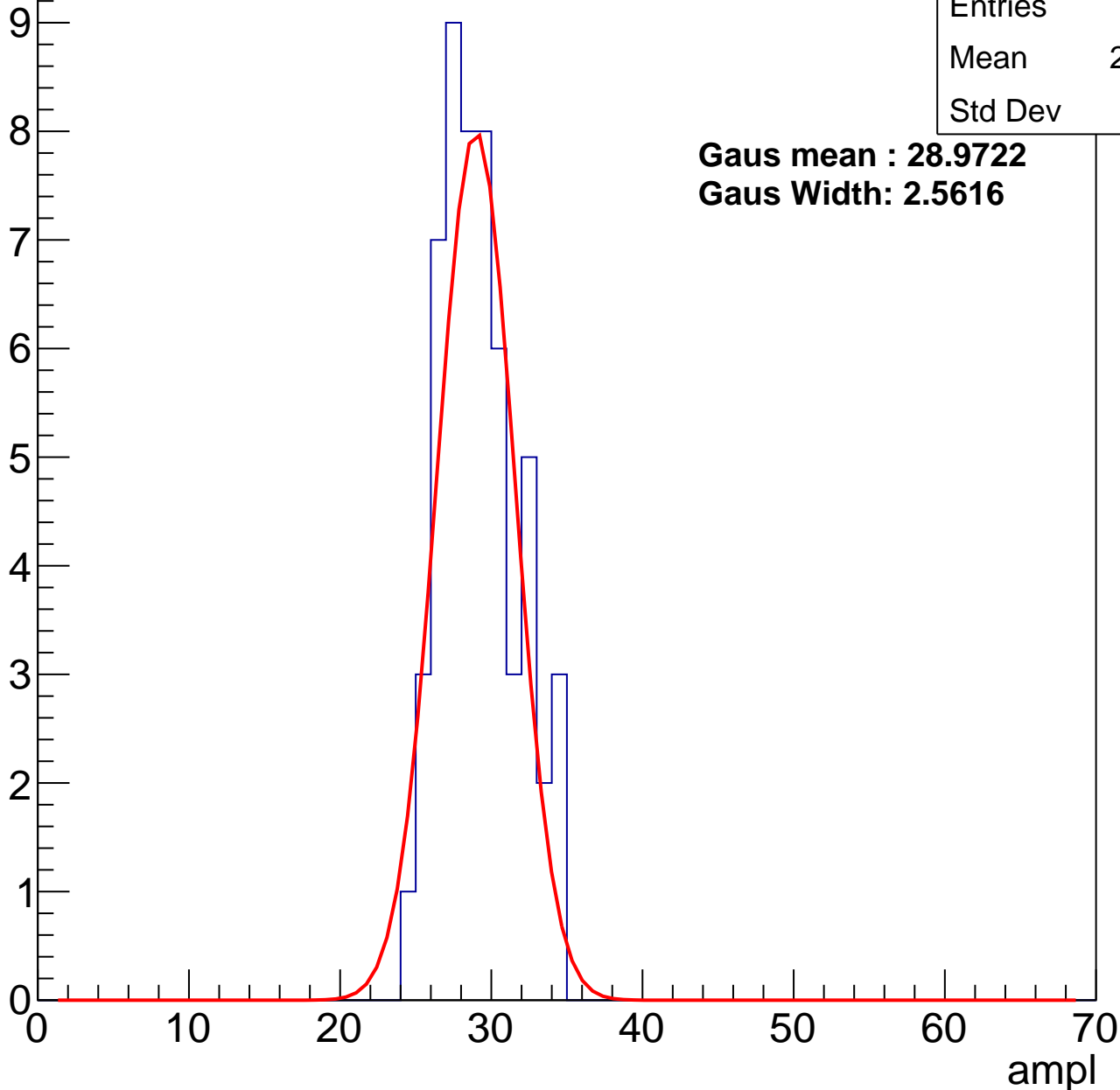
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.75
Std Dev	2.51

Gaus mean : 28.9722

Gaus Width: 2.5616



B1L102S, U11-ch54, adc1

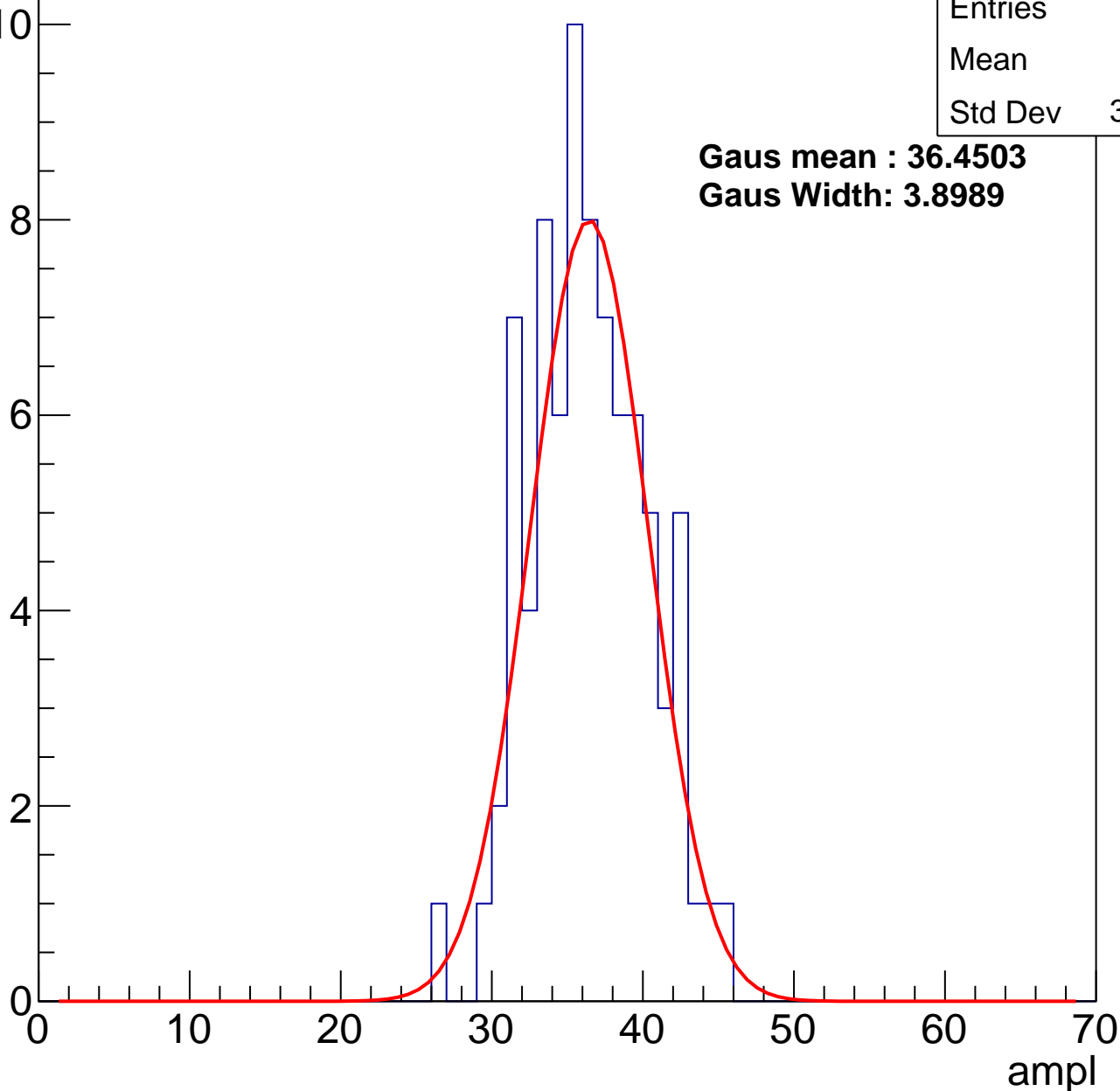
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	36
Std Dev	3.793

Gaus mean : 36.4503

Gaus Width: 3.8989



B1L102S, U11-ch54, adc2

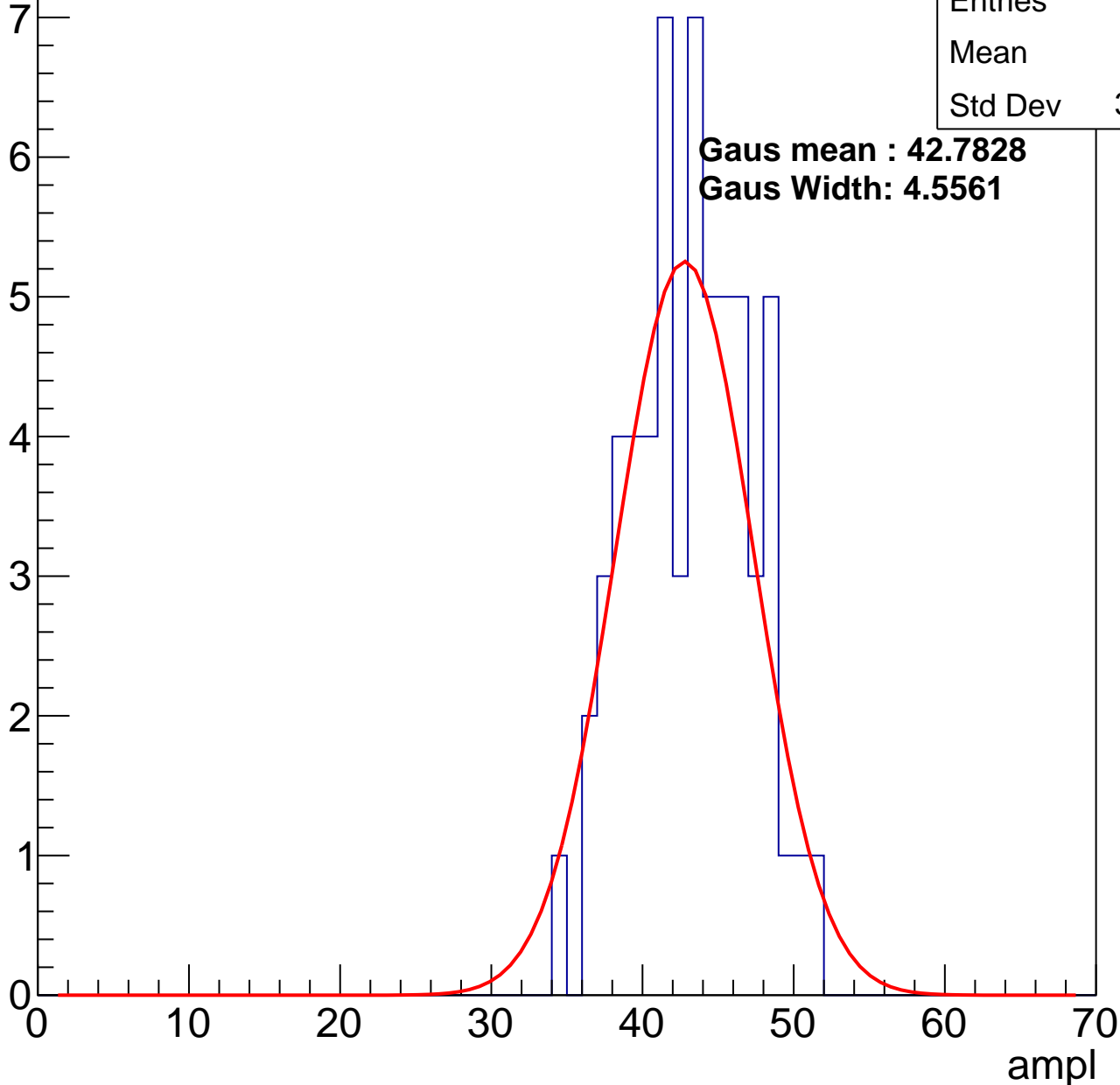
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.7
Std Dev	3.851

Gaus mean : 42.7828

Gaus Width: 4.5561

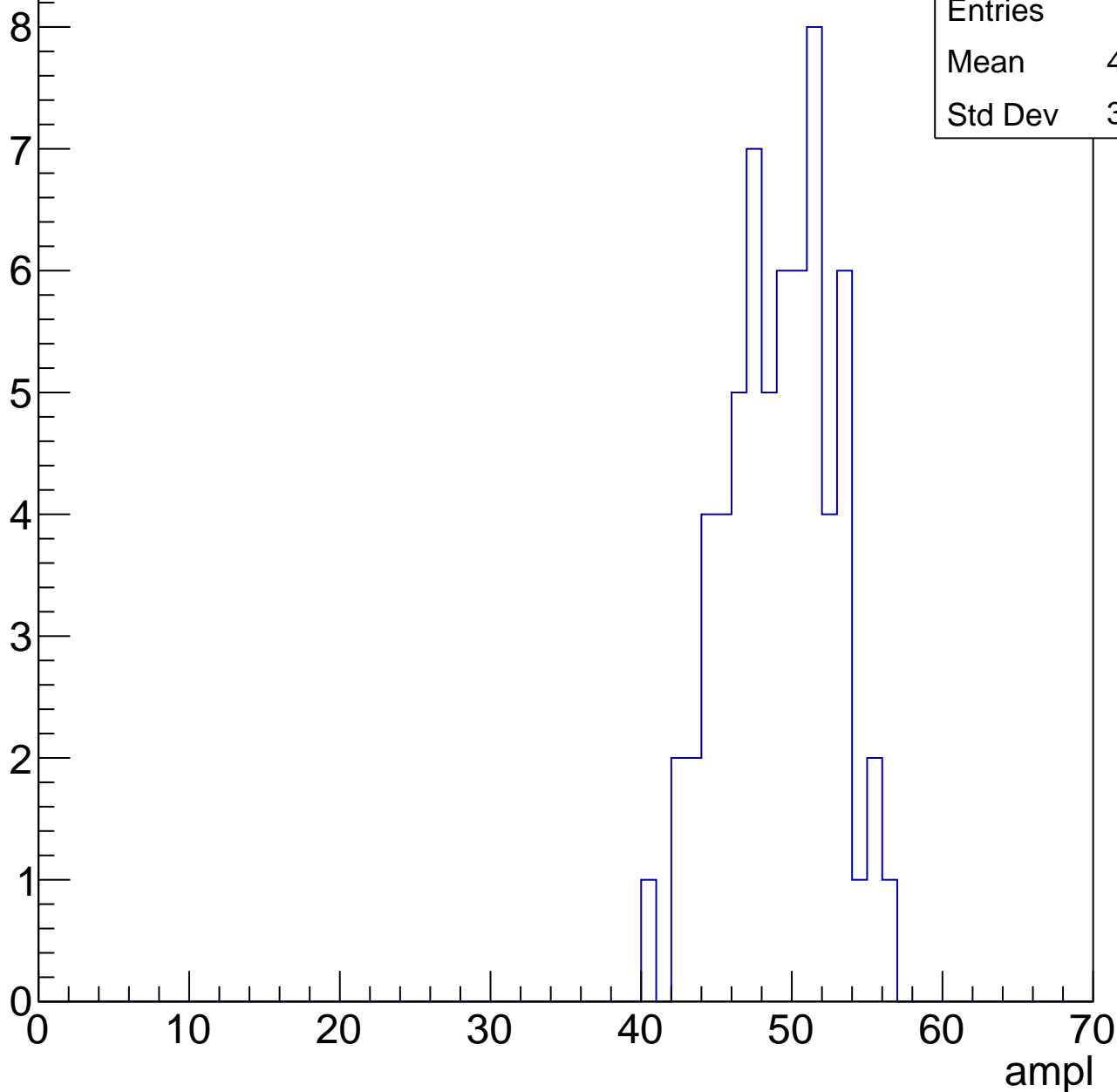


B1L102S, U11-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.64
Std Dev	3.537

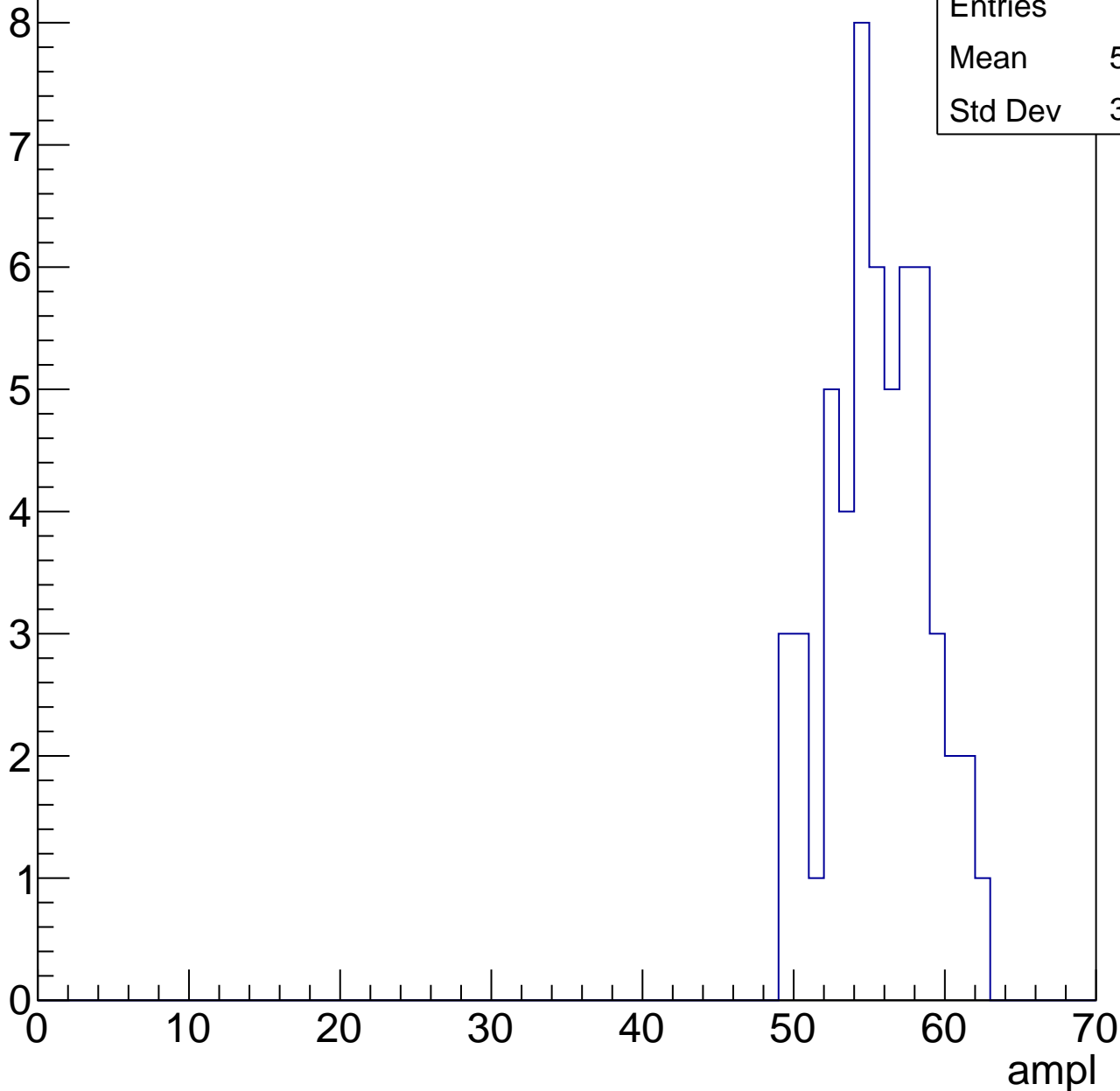


B1L102S, U11-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.15
Std Dev	3.216

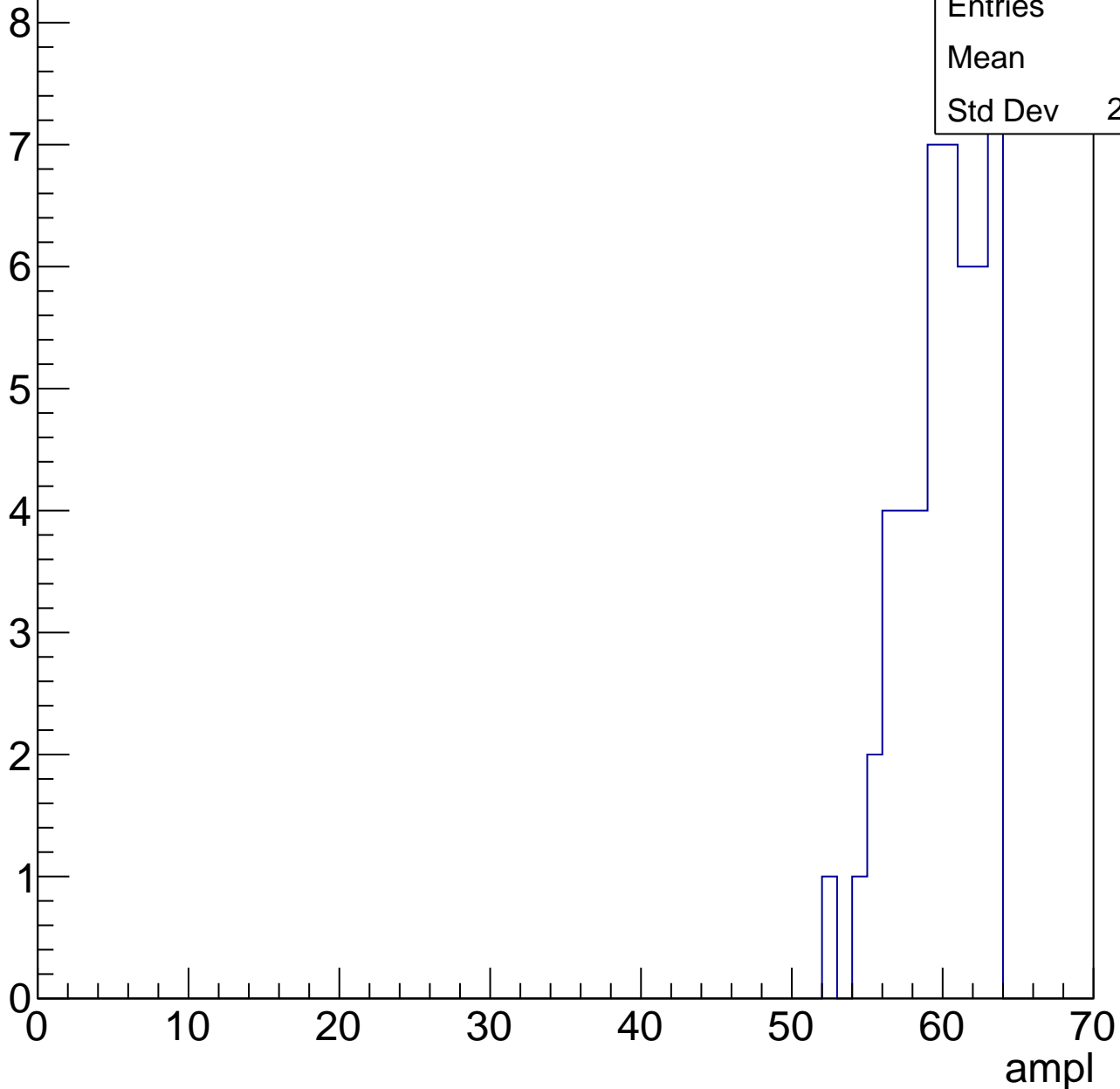


B1L102S, U11-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

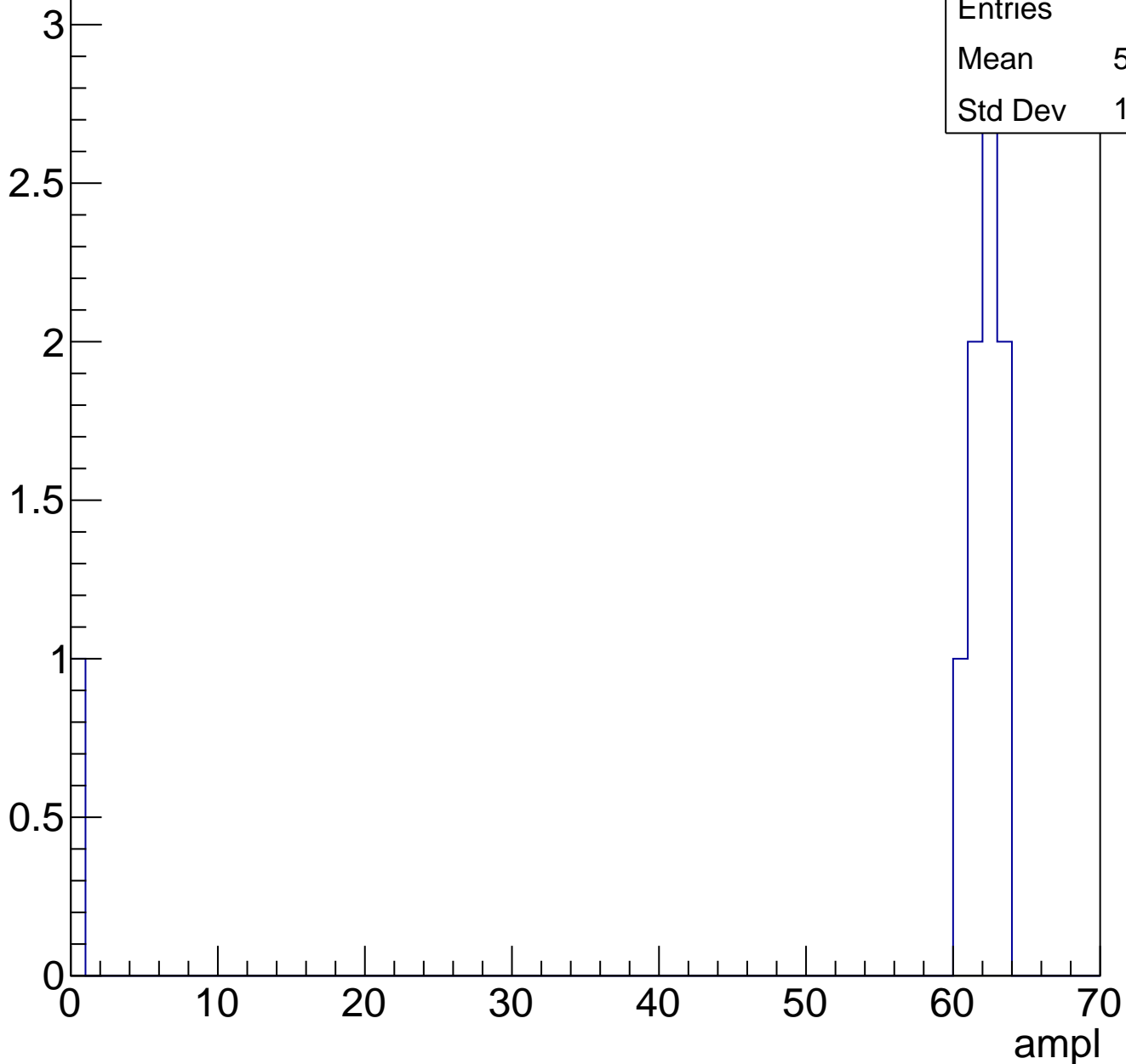
Entries	50
Mean	59.5
Std Dev	2.693



B1L102S, U11-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

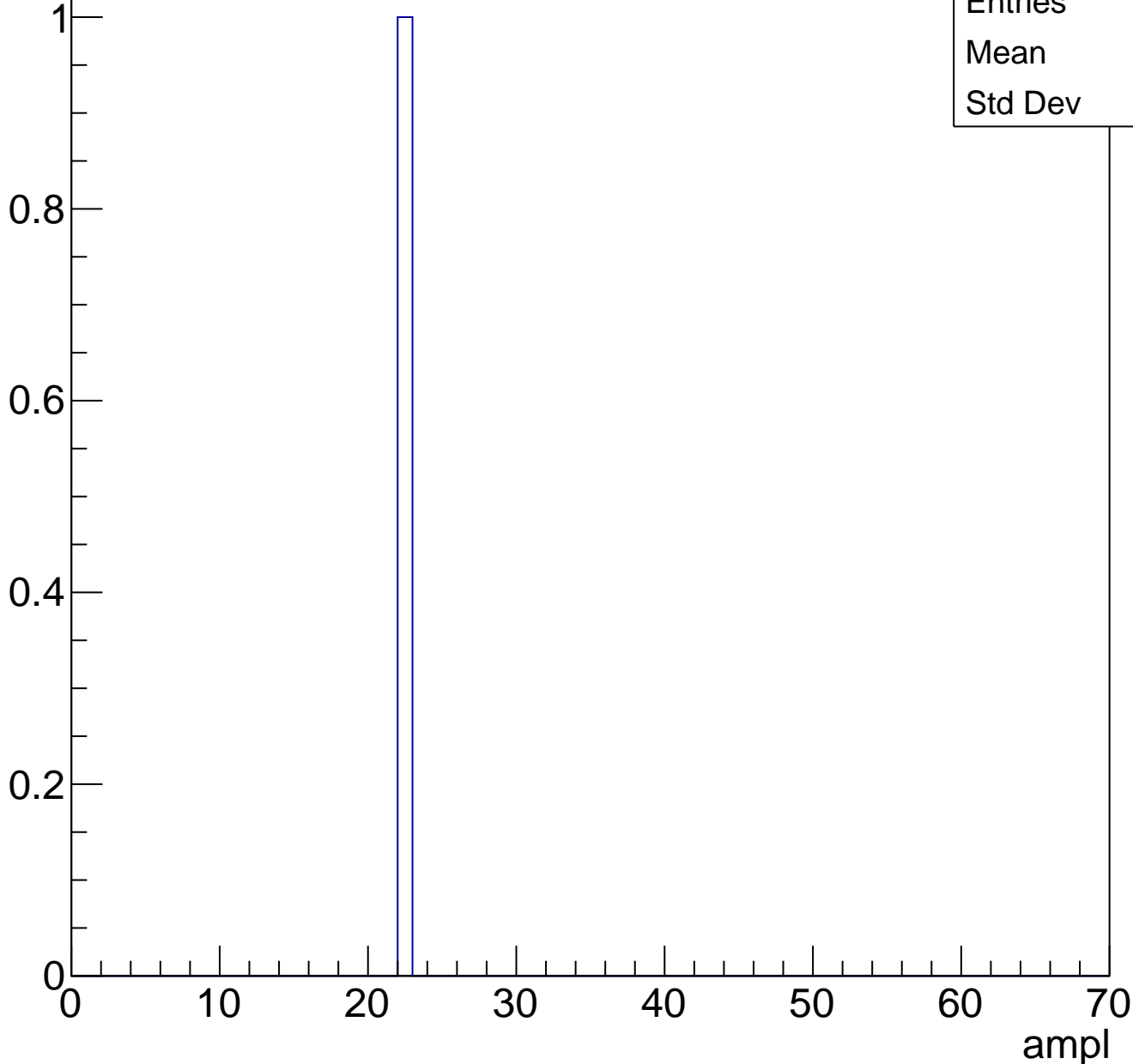
Entry



B1L102S, U11-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L102S, U11-ch55, adc0

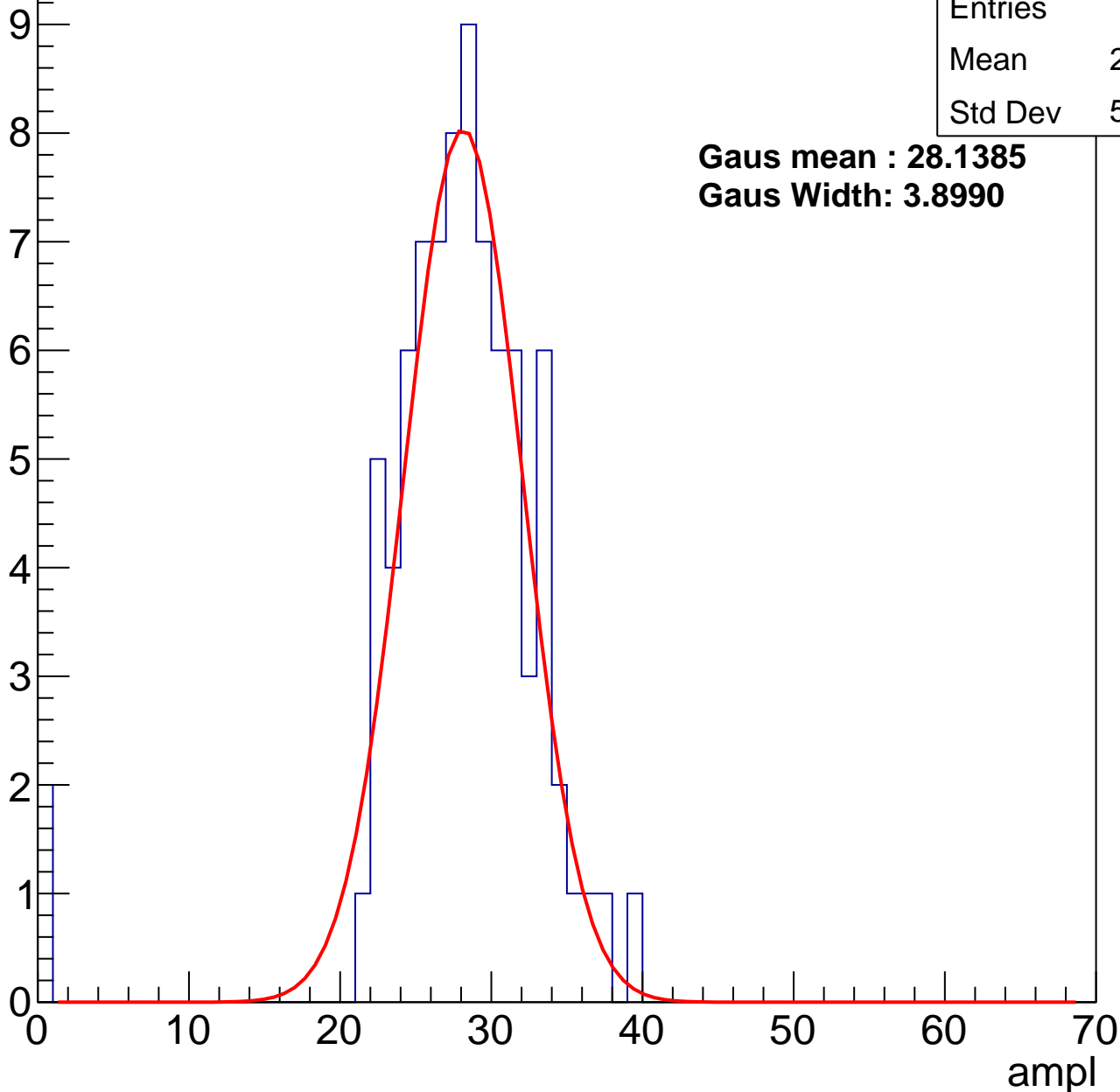
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.35
Std Dev	5.732

Gaus mean : 28.1385

Gaus Width: 3.8990



B1L102S, U11-ch55, adc1

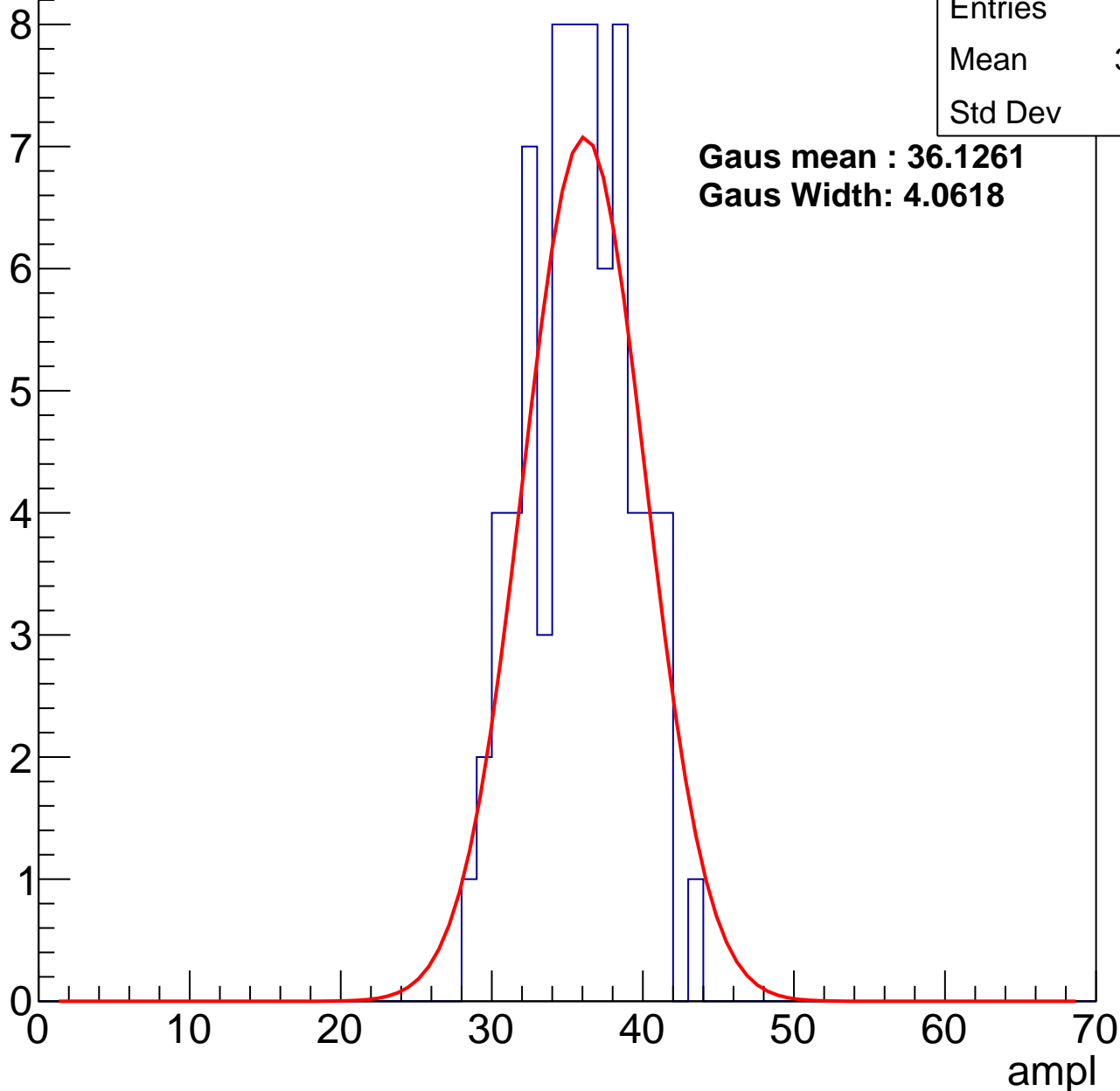
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.31
Std Dev	3.41

Gaus mean : 36.1261

Gaus Width: 4.0618



B1L102S, U11-ch55, adc2

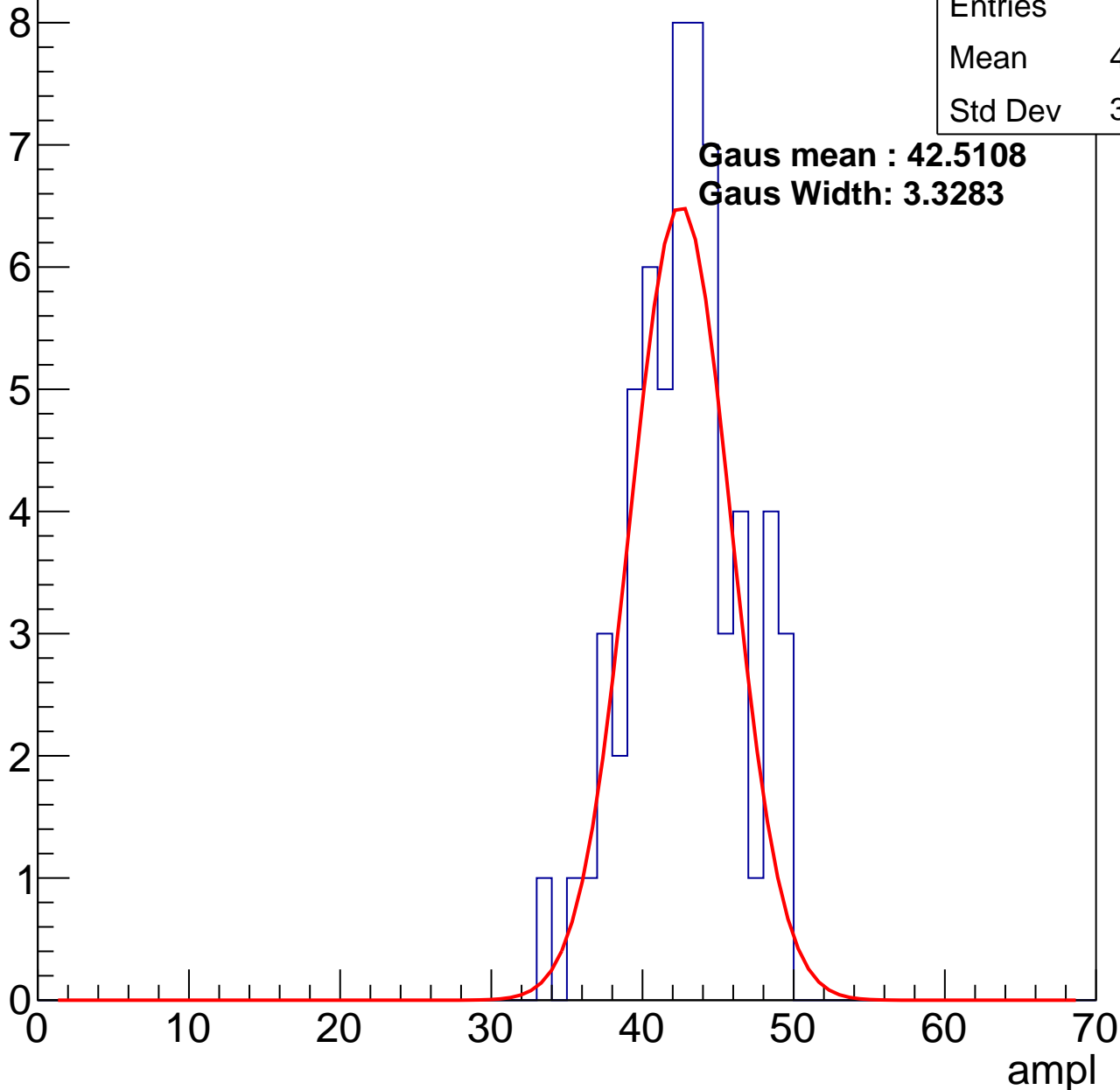
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.32
Std Dev	3.555

Gaus mean : 42.5108

Gaus Width: 3.3283

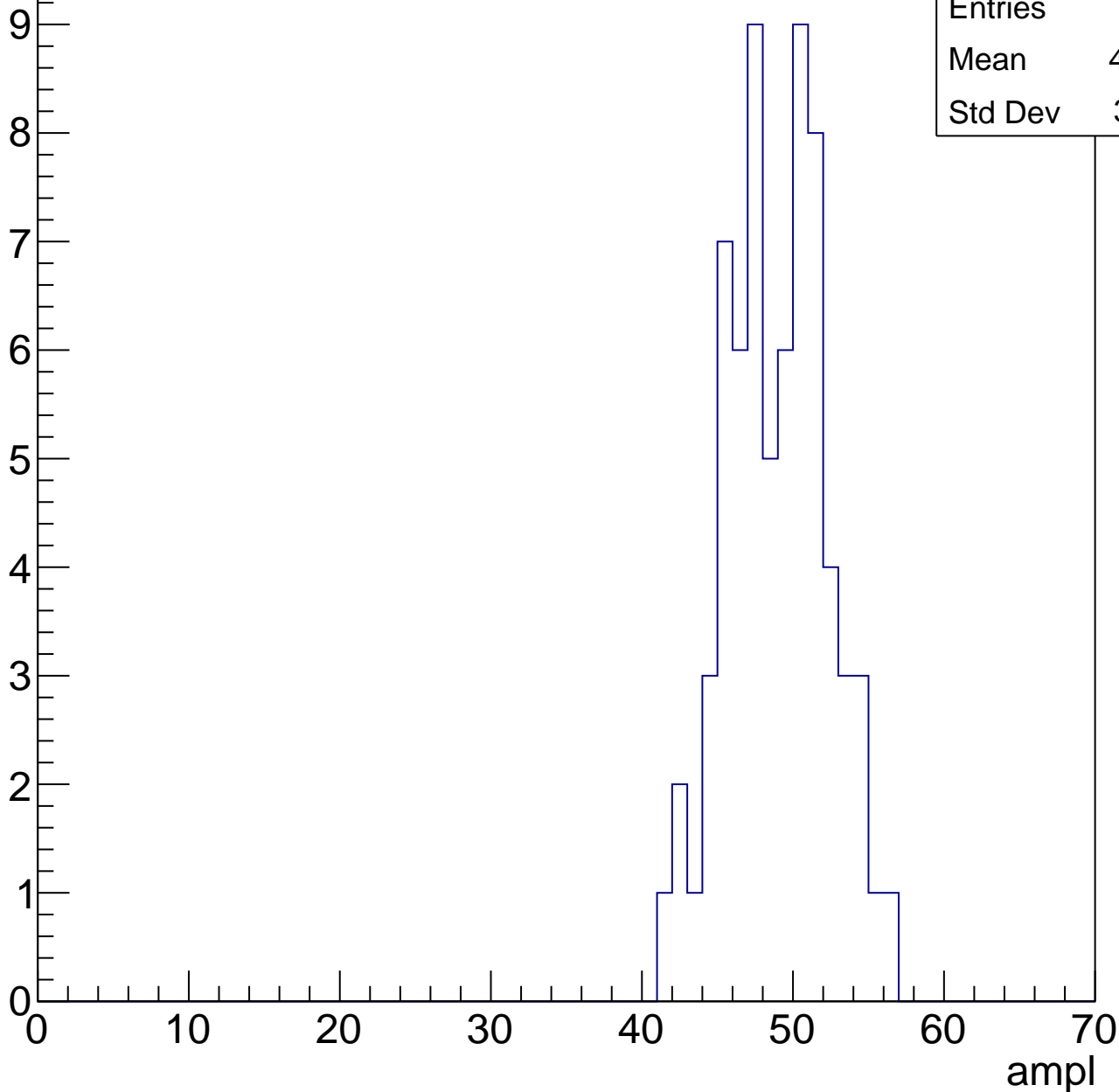


B1L102S, U11-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.49
Std Dev	3.291



B1L102S, U11-ch55, adc4

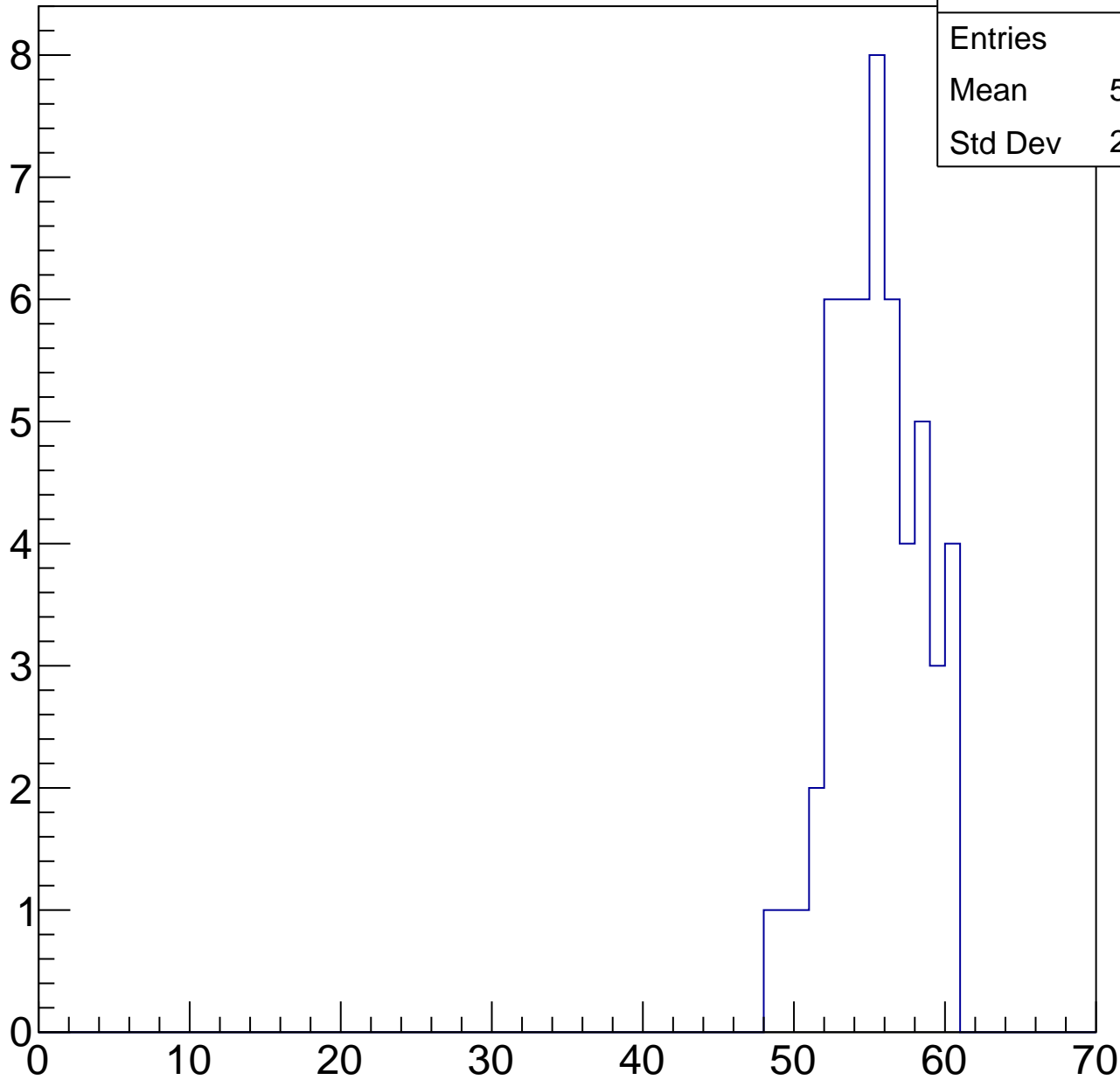
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	54.98
Std Dev	2.885

ampl

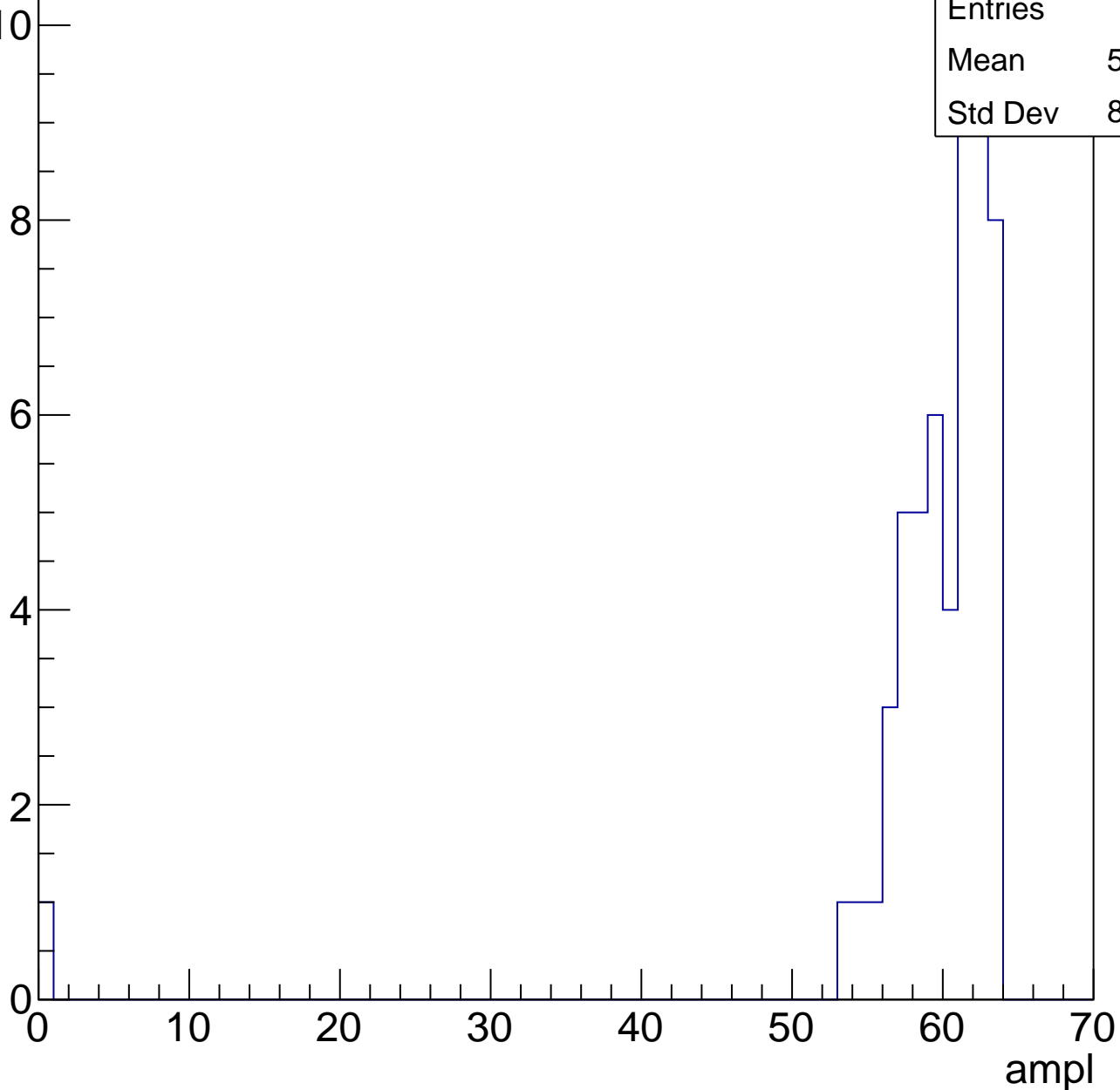


B1L102S, U11-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.74
Std Dev	8.459



B1L102S, U11-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.43
Std Dev	1.99

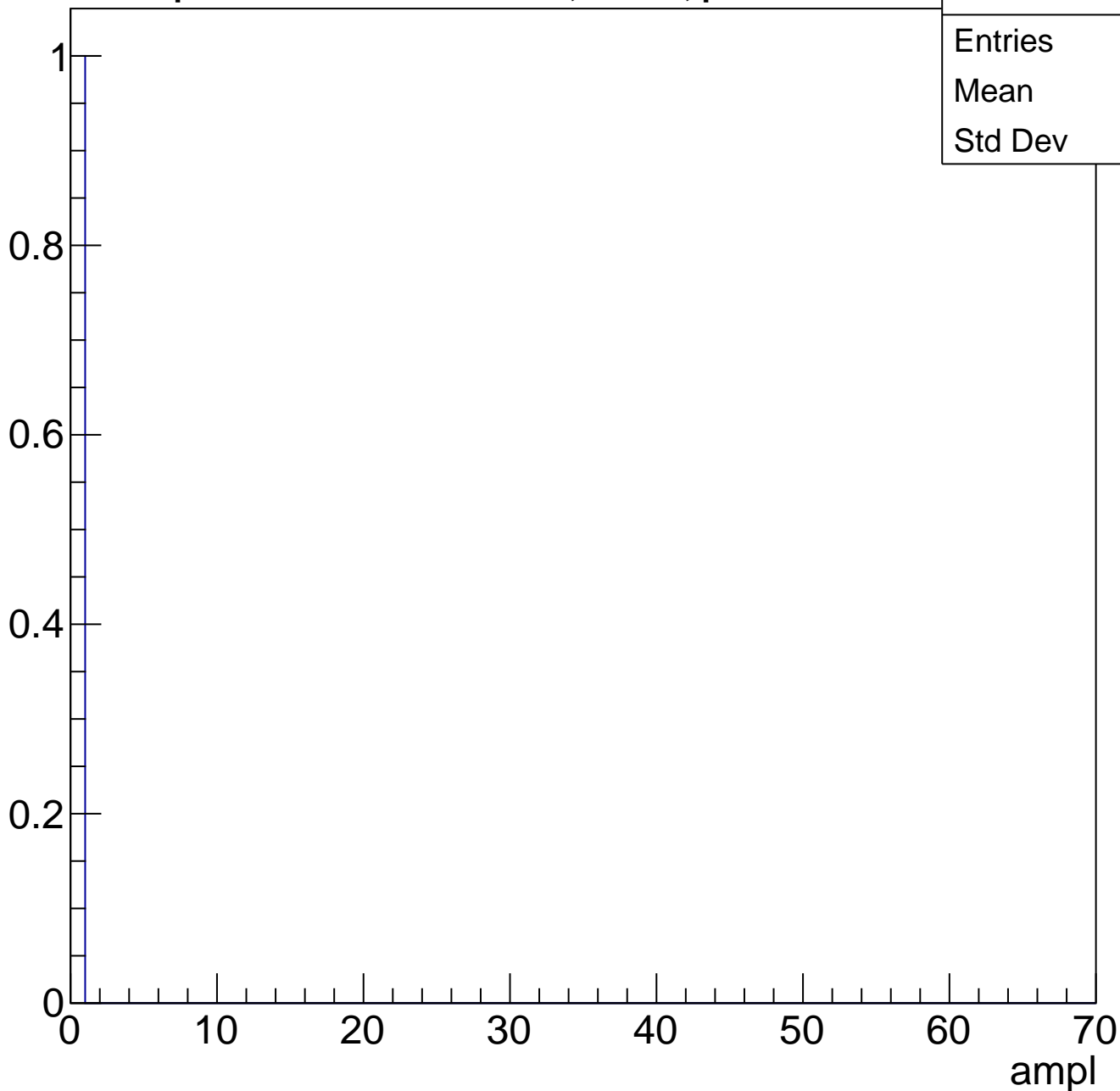
ampl

0 10 20 30 40 50 60 70

B1L102S, U11-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch56, adc0

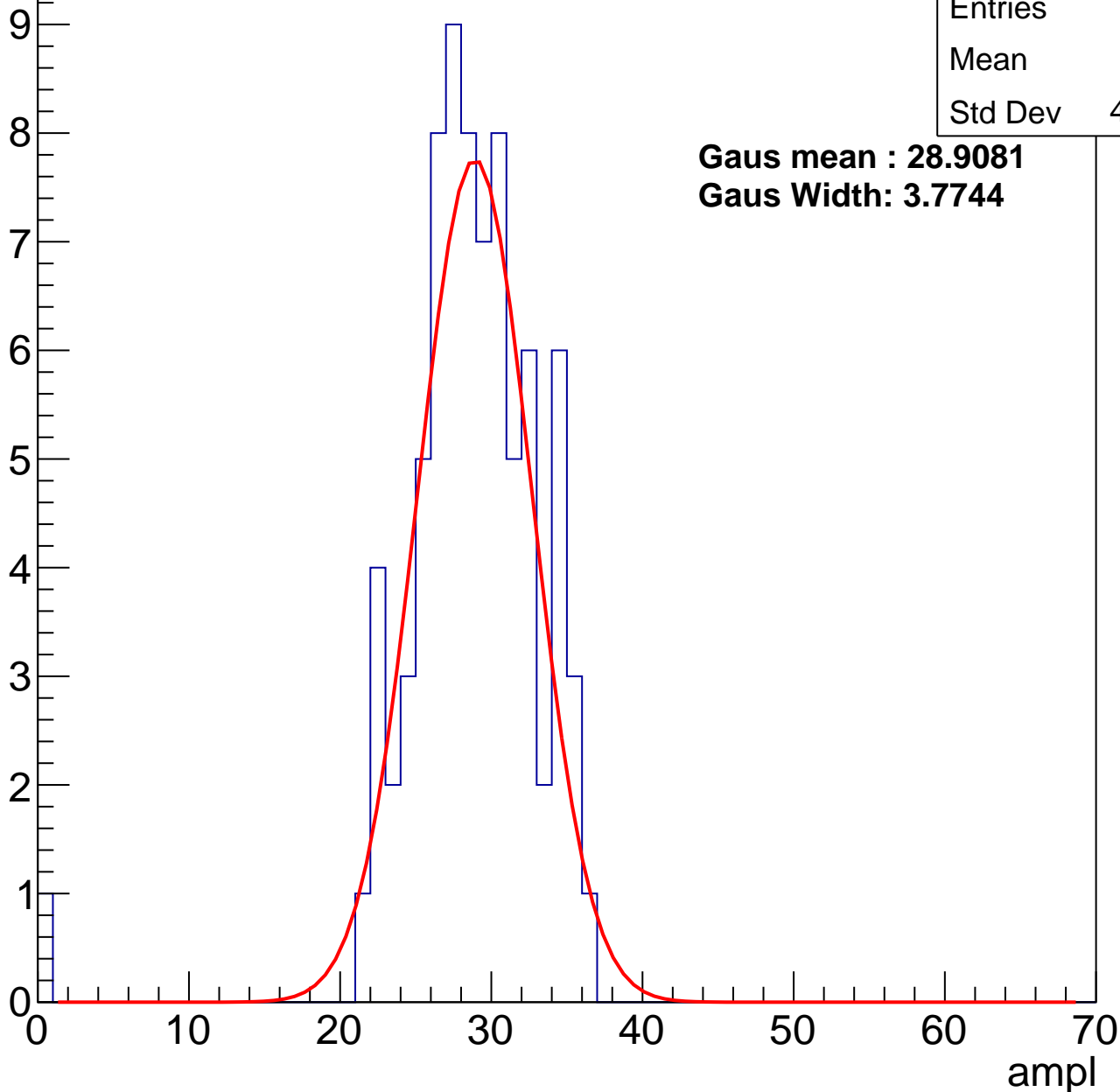
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.2
Std Dev	4.793

Gaus mean : 28.9081

Gaus Width: 3.7744



B1L102S, U11-ch56, adc1

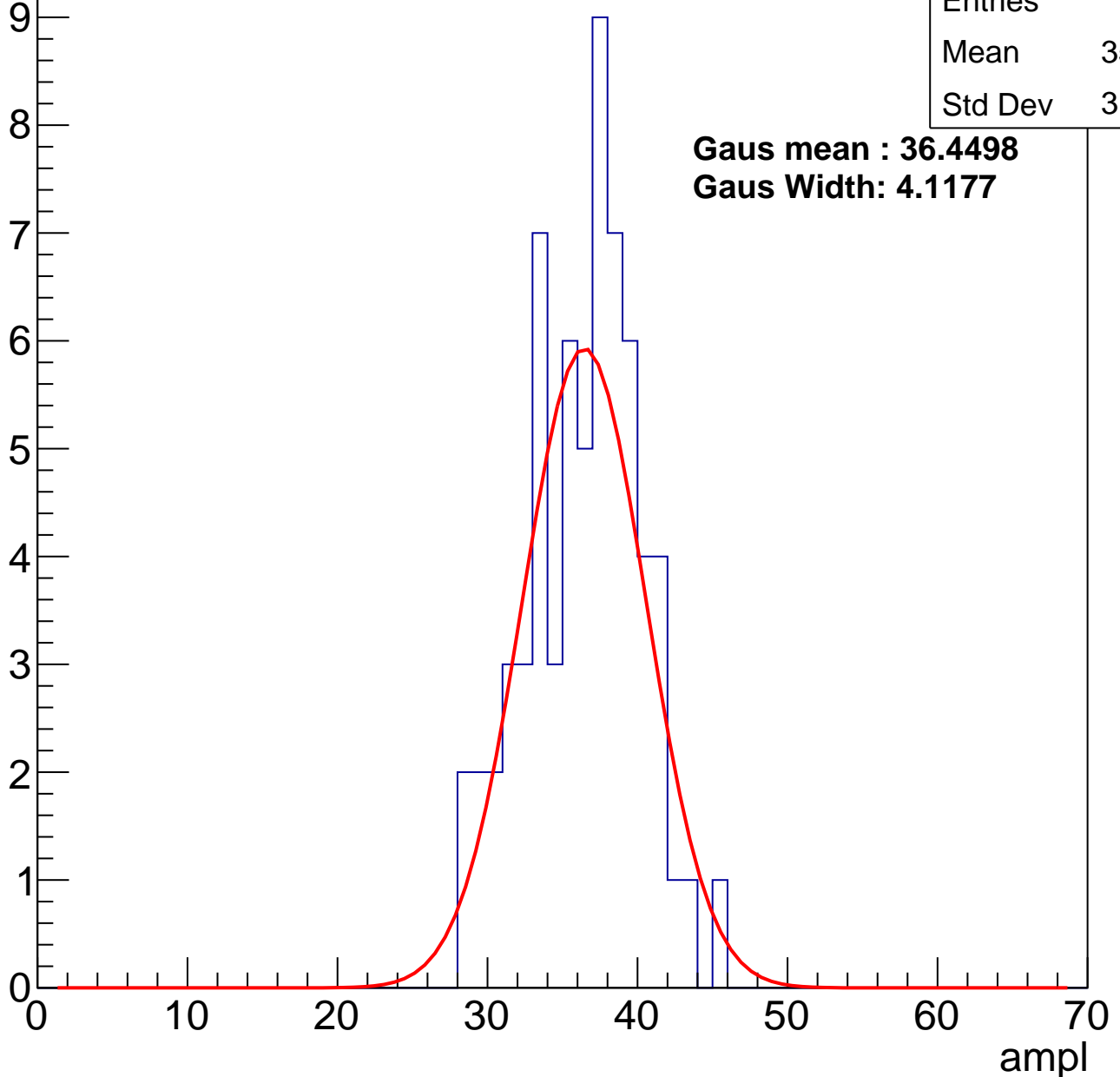
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.95
Std Dev	3.739

Gaus mean : 36.4498

Gaus Width: 4.1177



B1L102S, U11-ch56, adc2

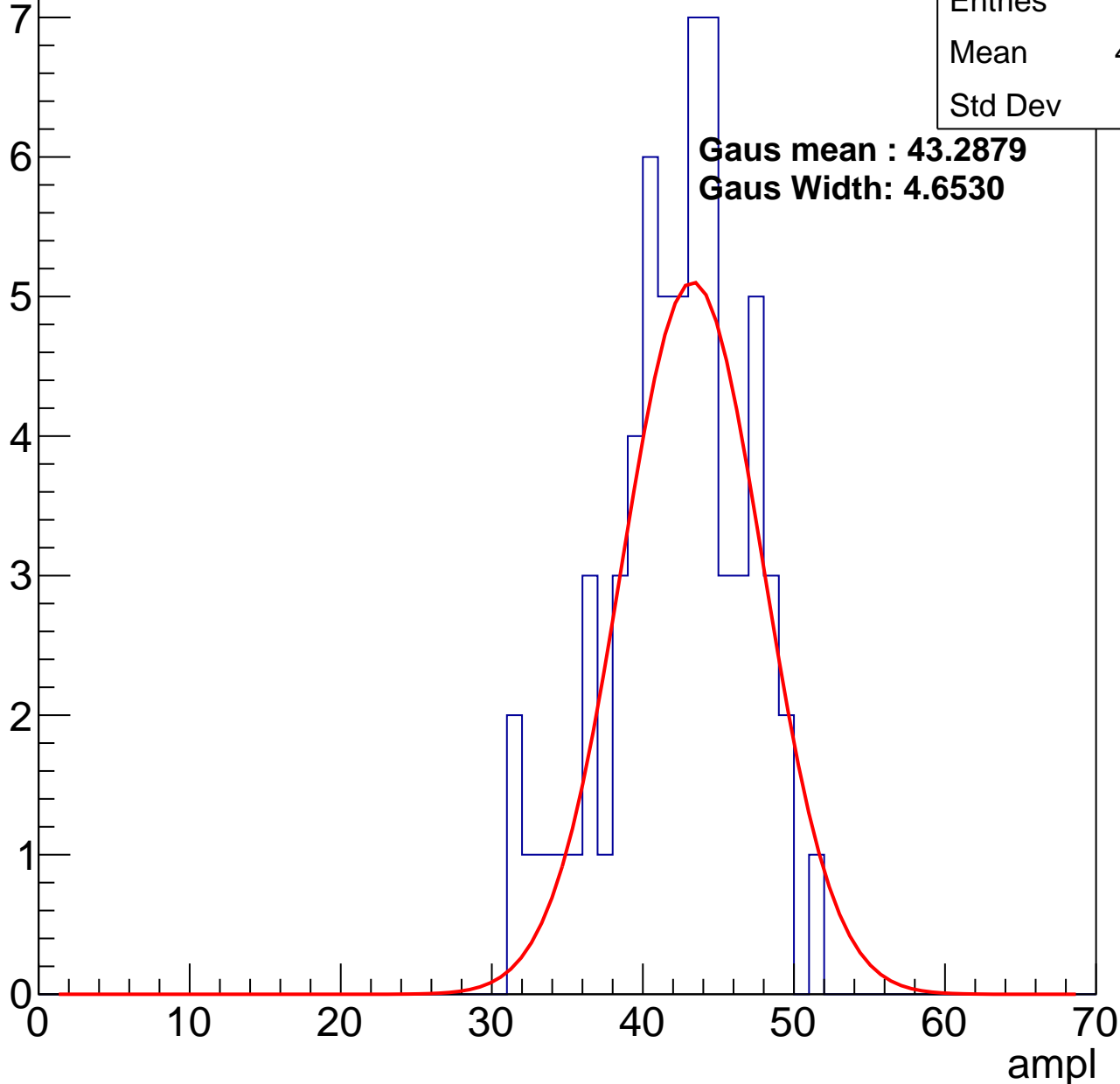
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.81
Std Dev	4.51

Gaus mean : 43.2879

Gaus Width: 4.6530

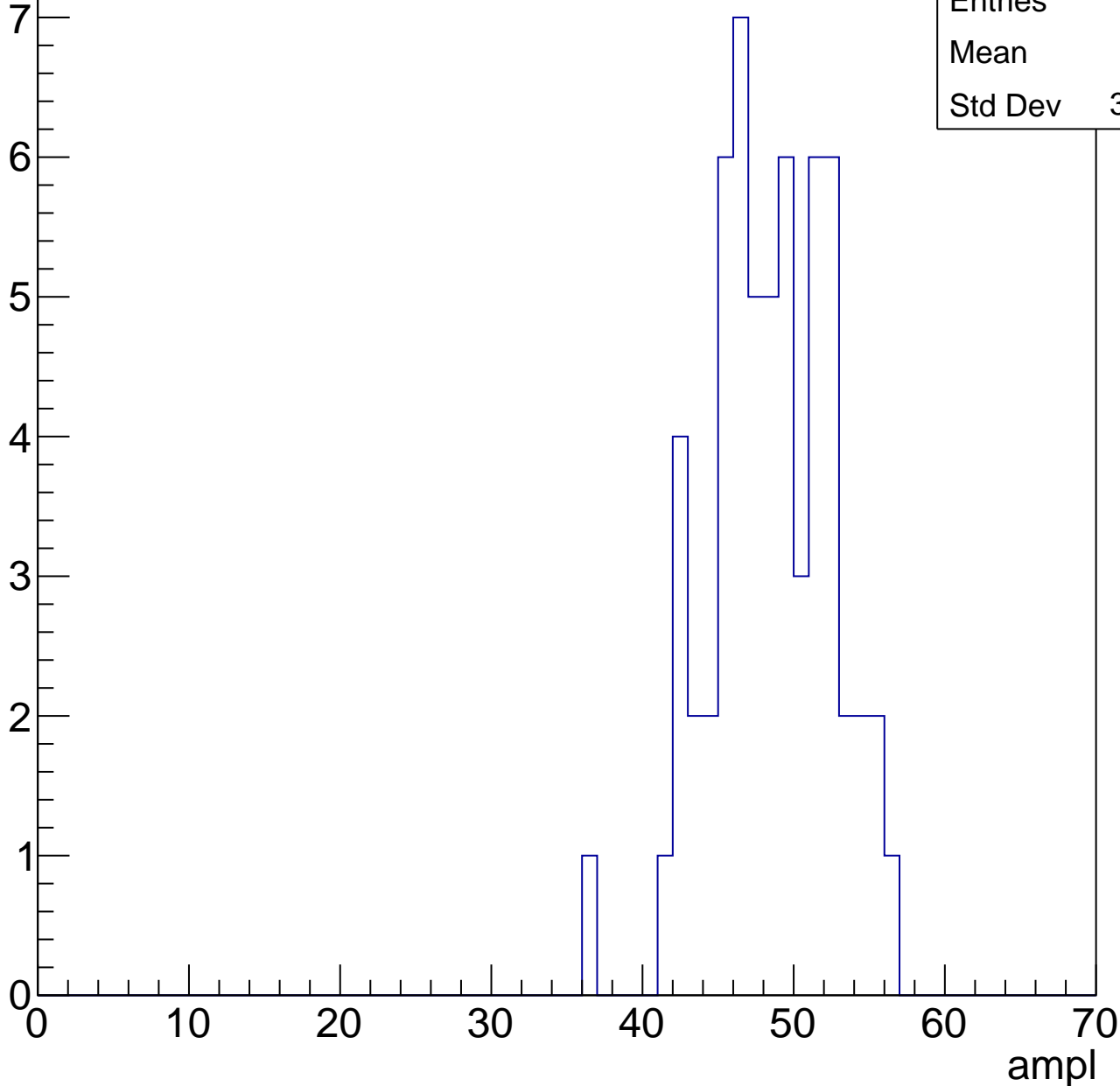


B1L102S, U11-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48
Std Dev	3.963

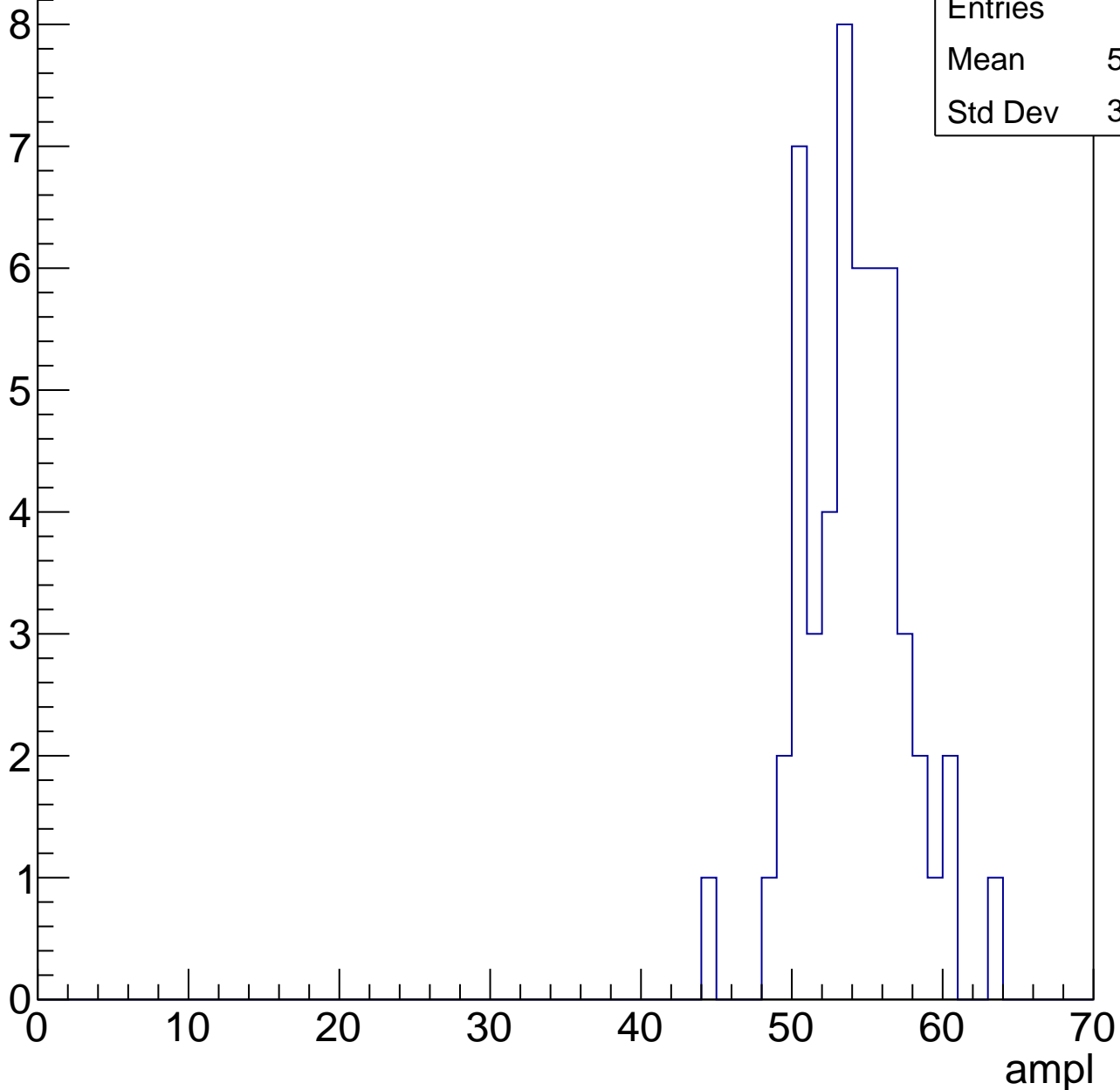


B1L102S, U11-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	53.66
Std Dev	3.392

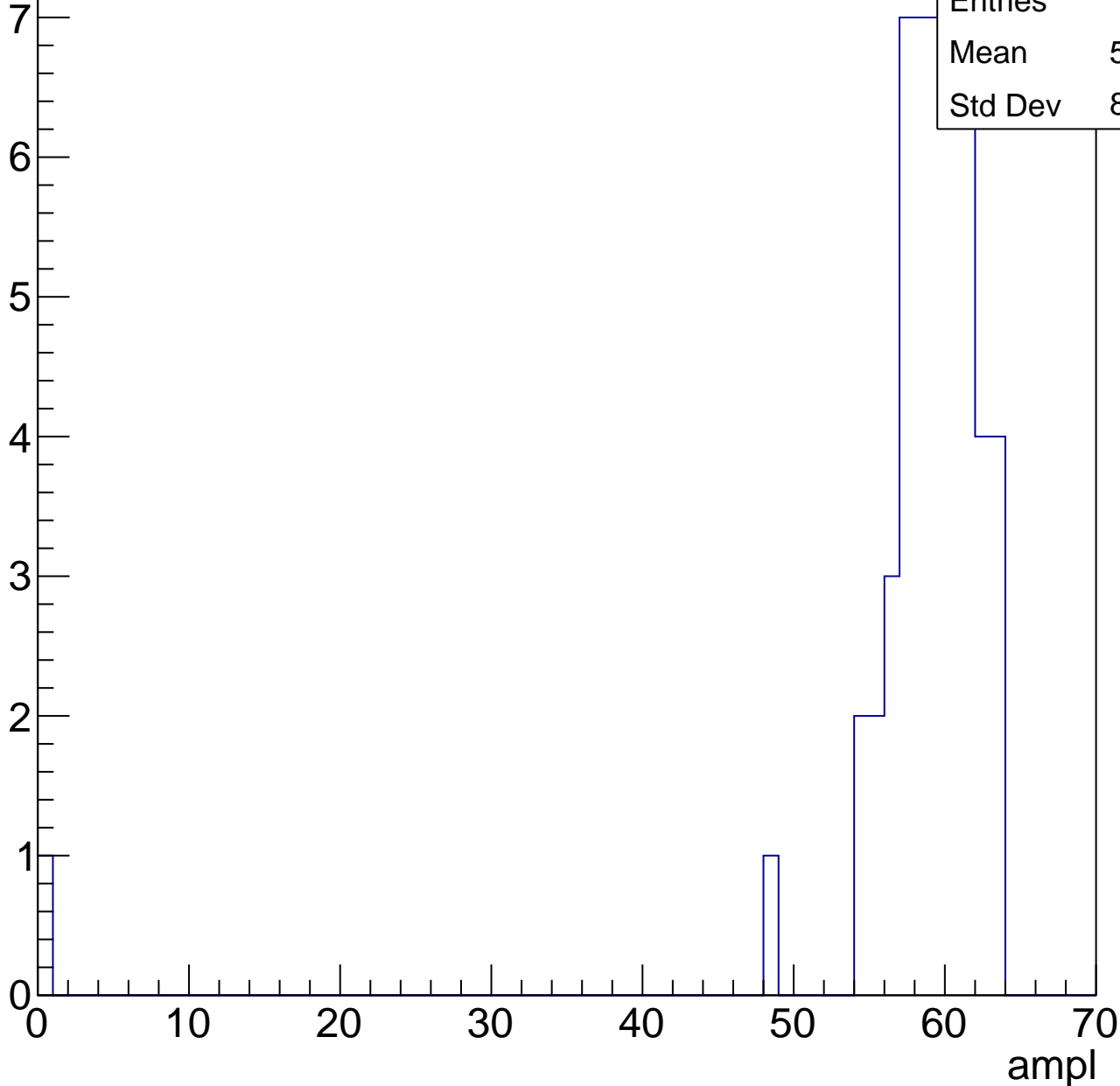


B1L102S, U11-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.67
Std Dev	8.537

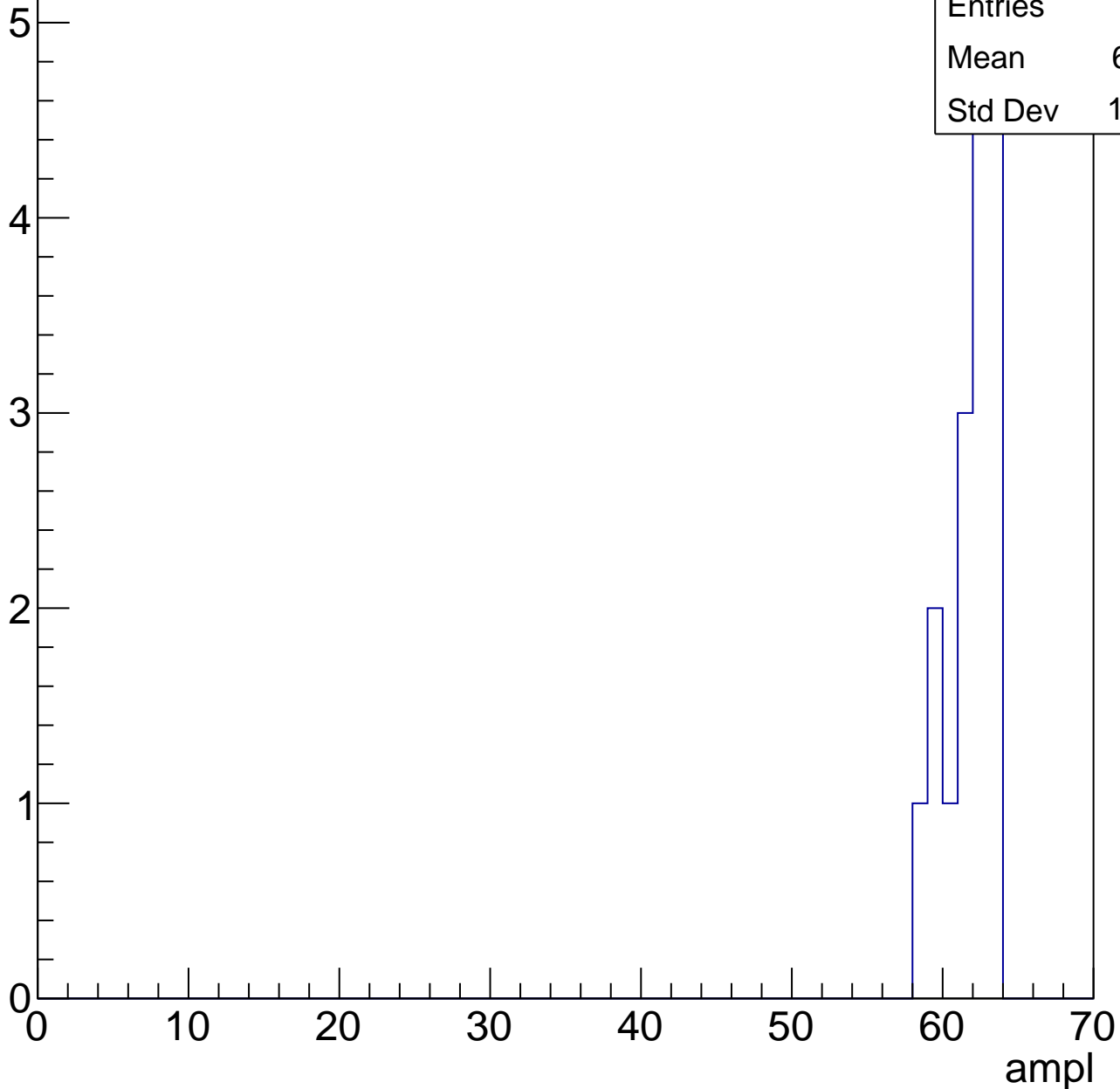


B1L102S, U11-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.41
Std Dev	1.536



B1L102S, U11-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch57, adc0

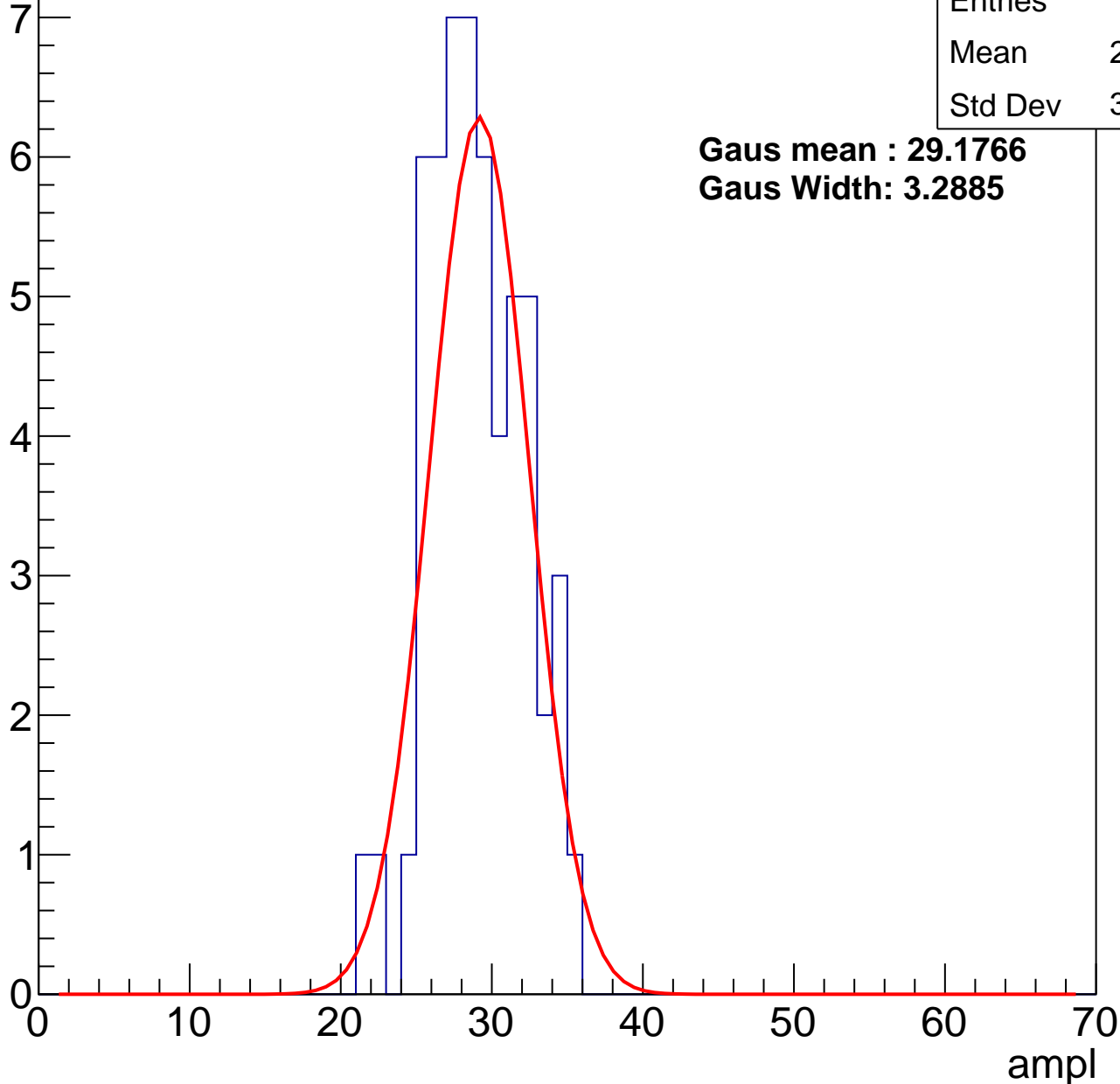
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.55
Std Dev	3.086

Gaus mean : 29.1766

Gaus Width: 3.2885



B1L102S, U11-ch57, adc1

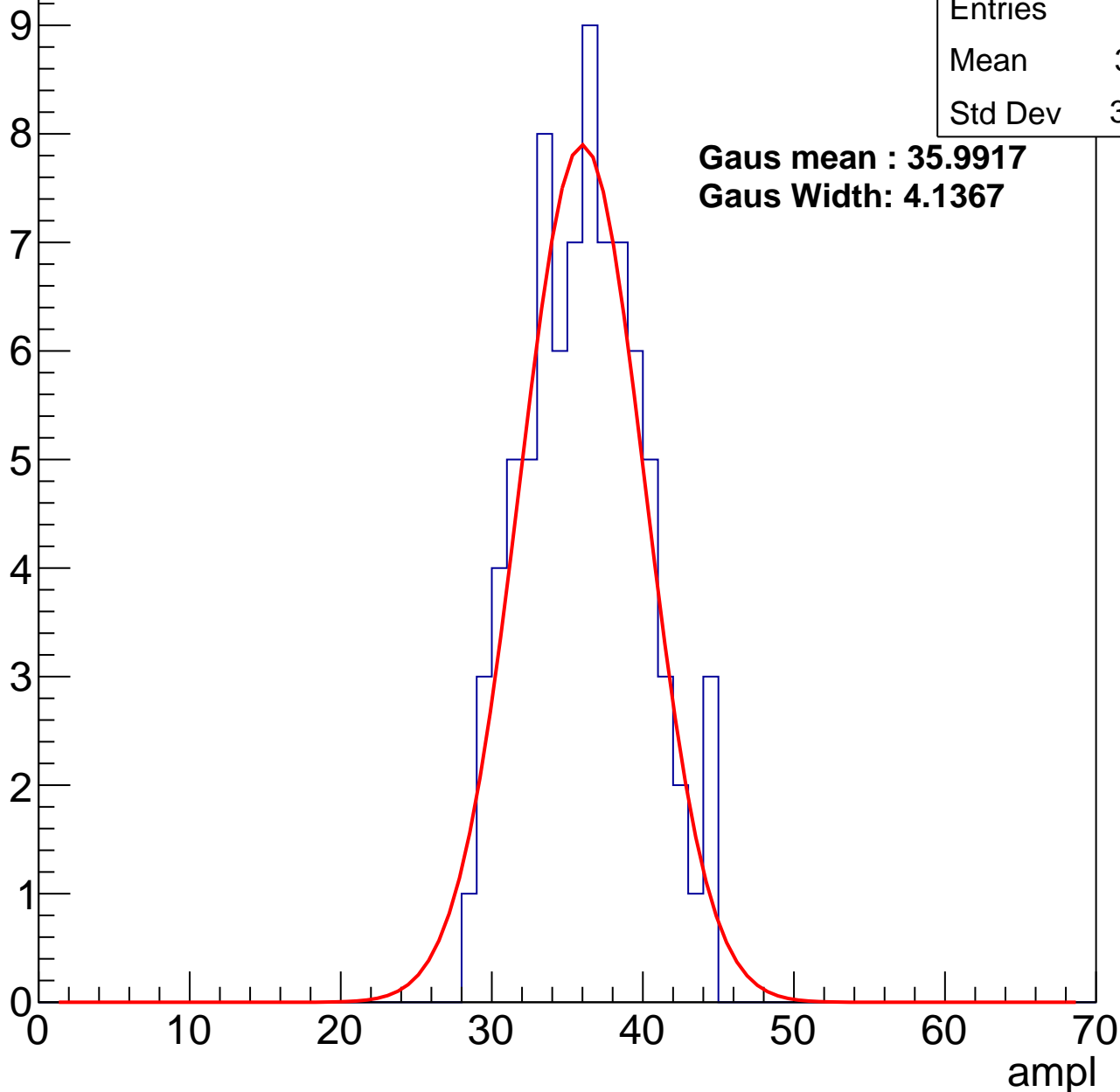
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.71
Std Dev	3.846

Gaus mean : 35.9917

Gaus Width: 4.1367



B1L102S, U11-ch57, adc2

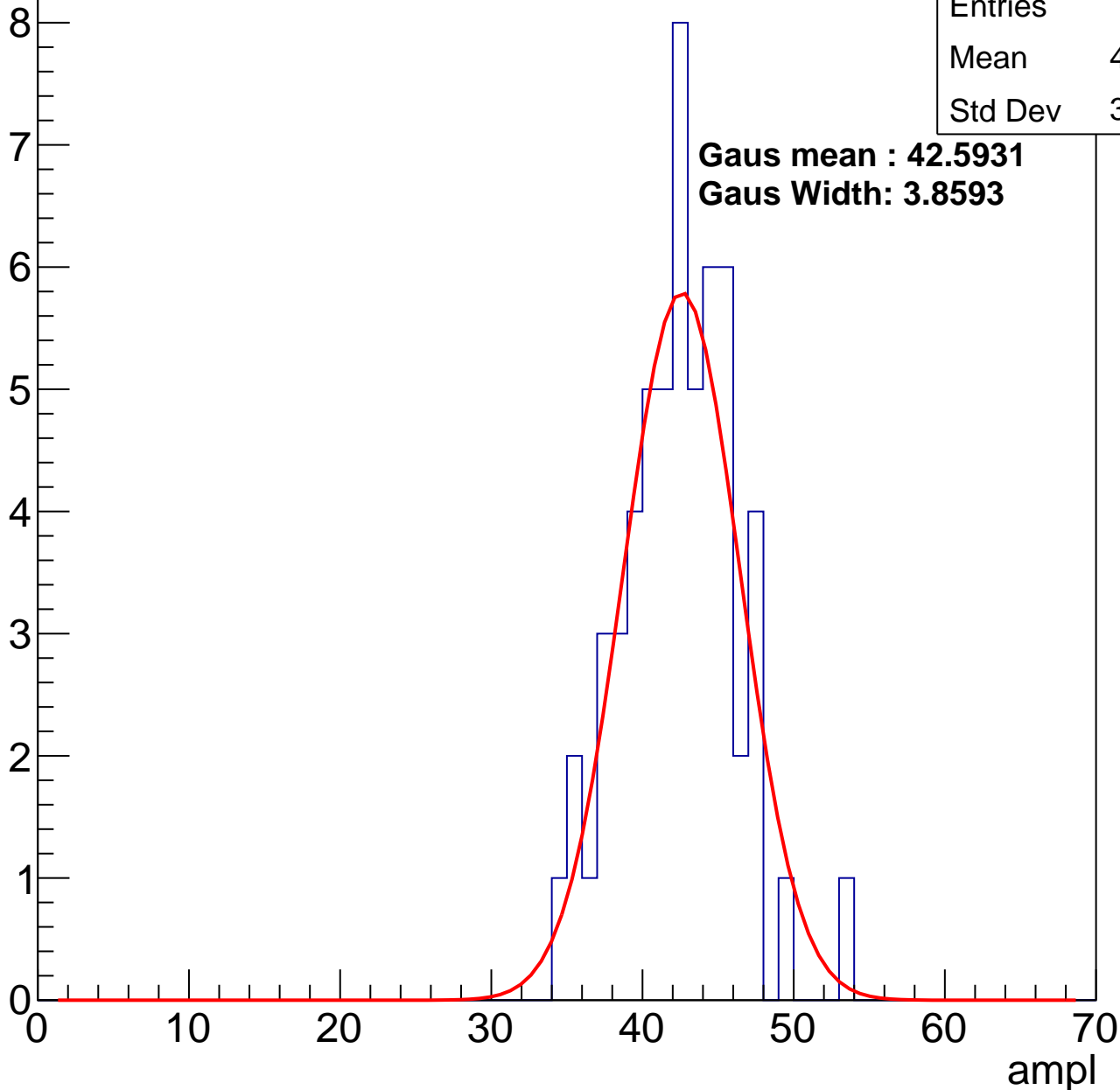
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.98
Std Dev	3.668

Gaus mean : 42.5931

Gaus Width: 3.8593



B1L102S, U11-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	49.14
Std Dev	3.706

Entry

10

8

6

4

2

0

0

10

20

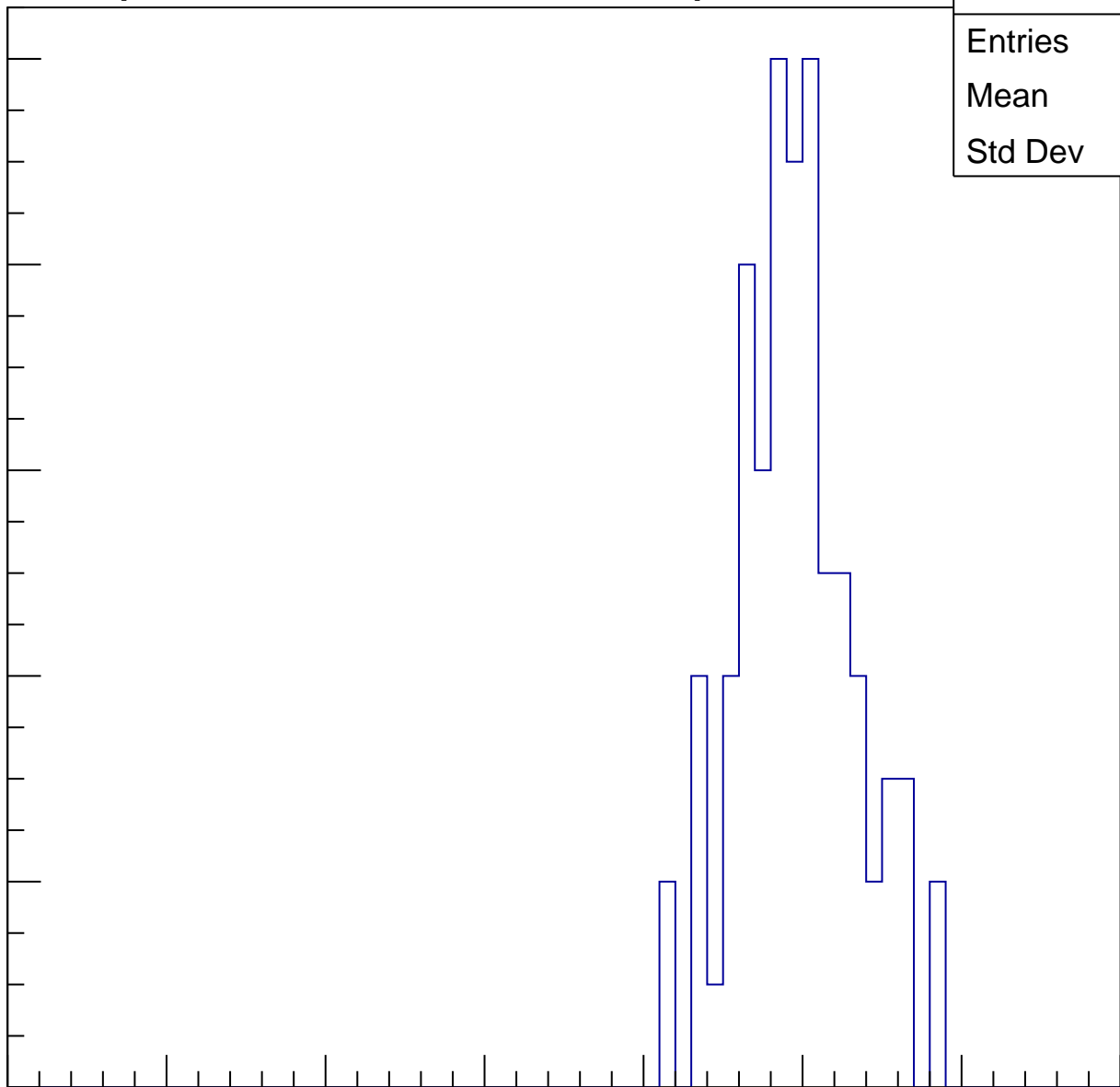
30

40

50

60

ampl

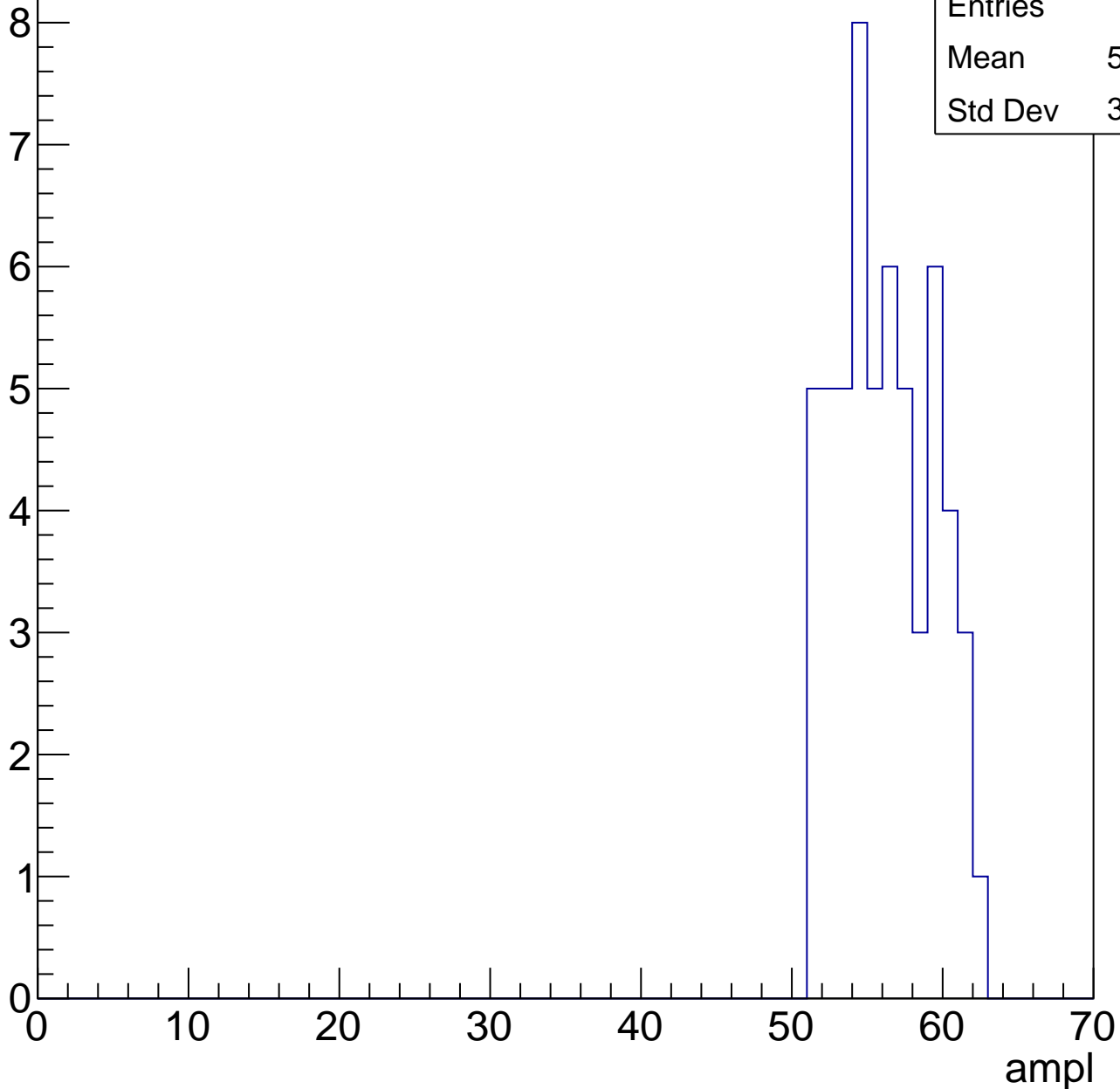


B1L102S, U11-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.73
Std Dev	3.073

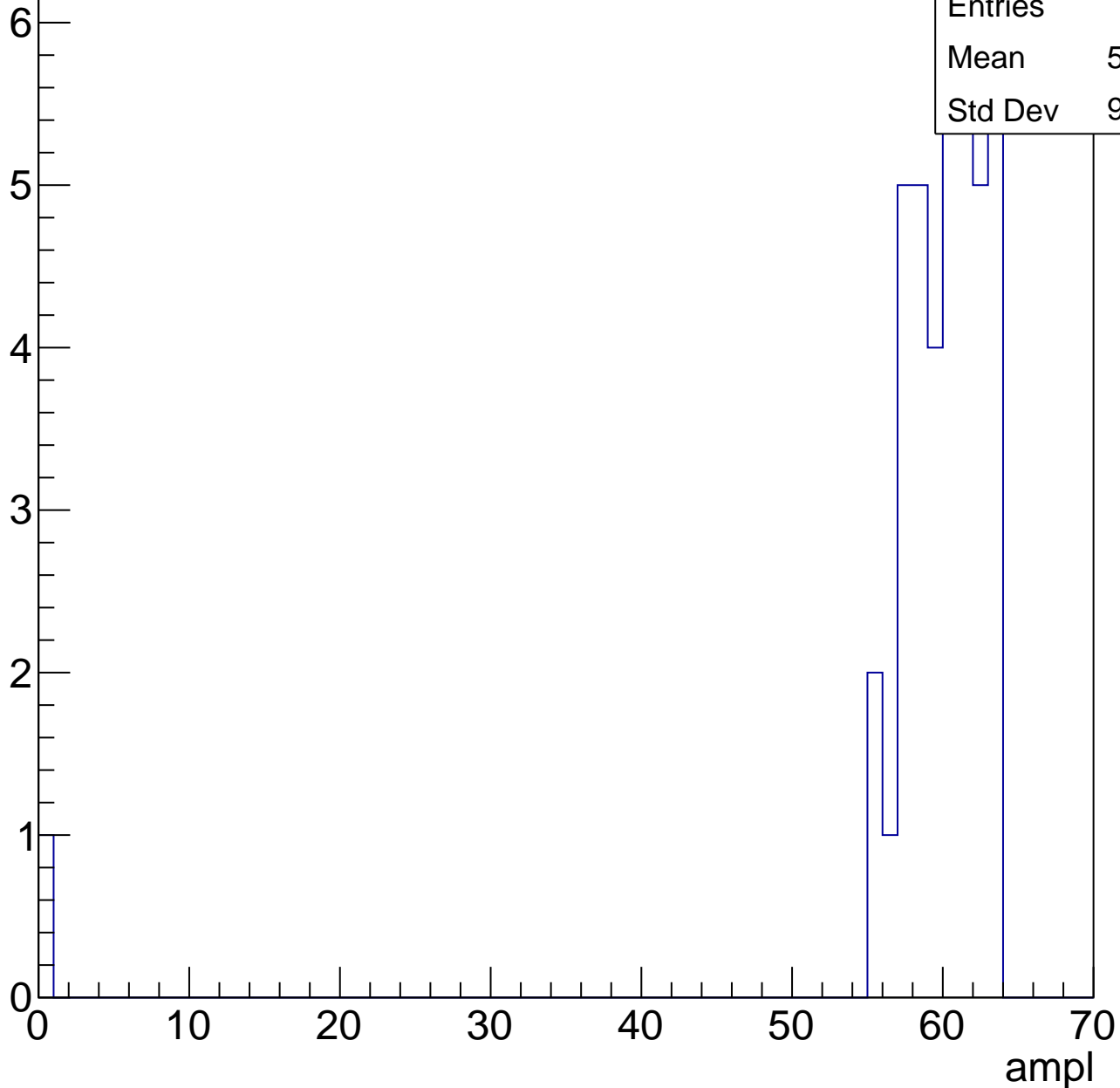


B1L102S, U11-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

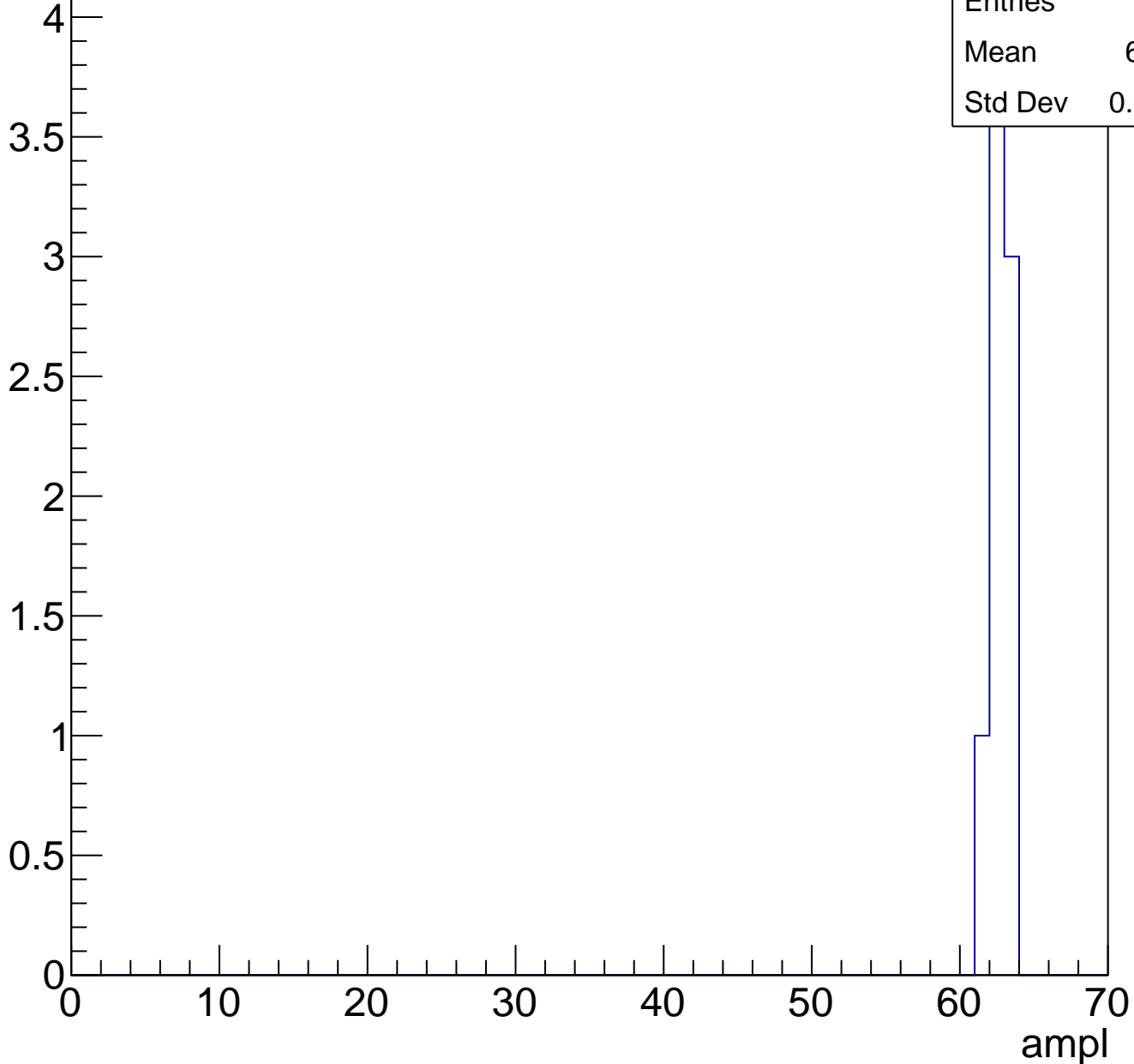
Entries	41
Mean	58.32
Std Dev	9.498



B1L102S, U11-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

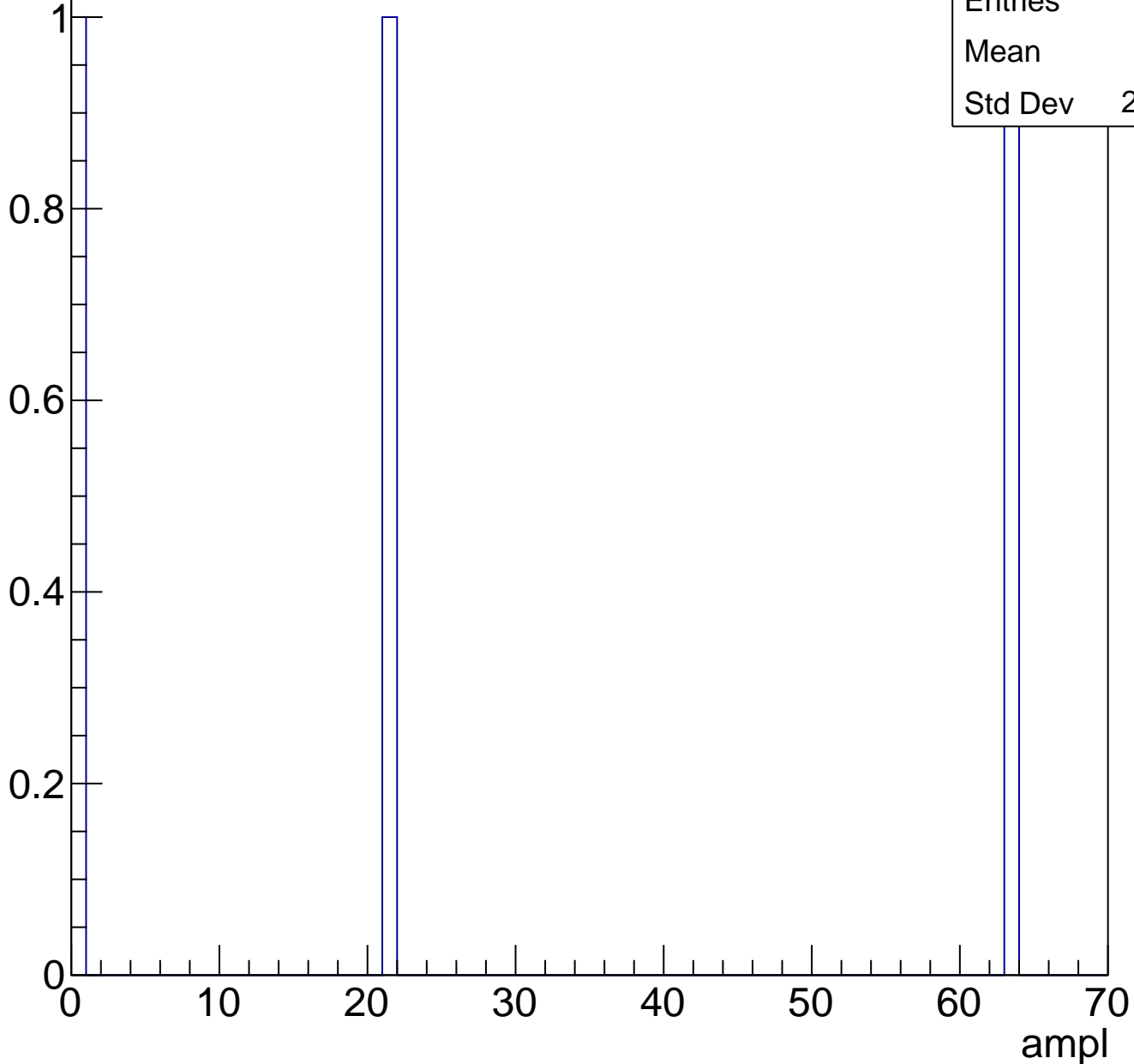
Entry



B1L102S, U11-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L102S, U11-ch58, adc0

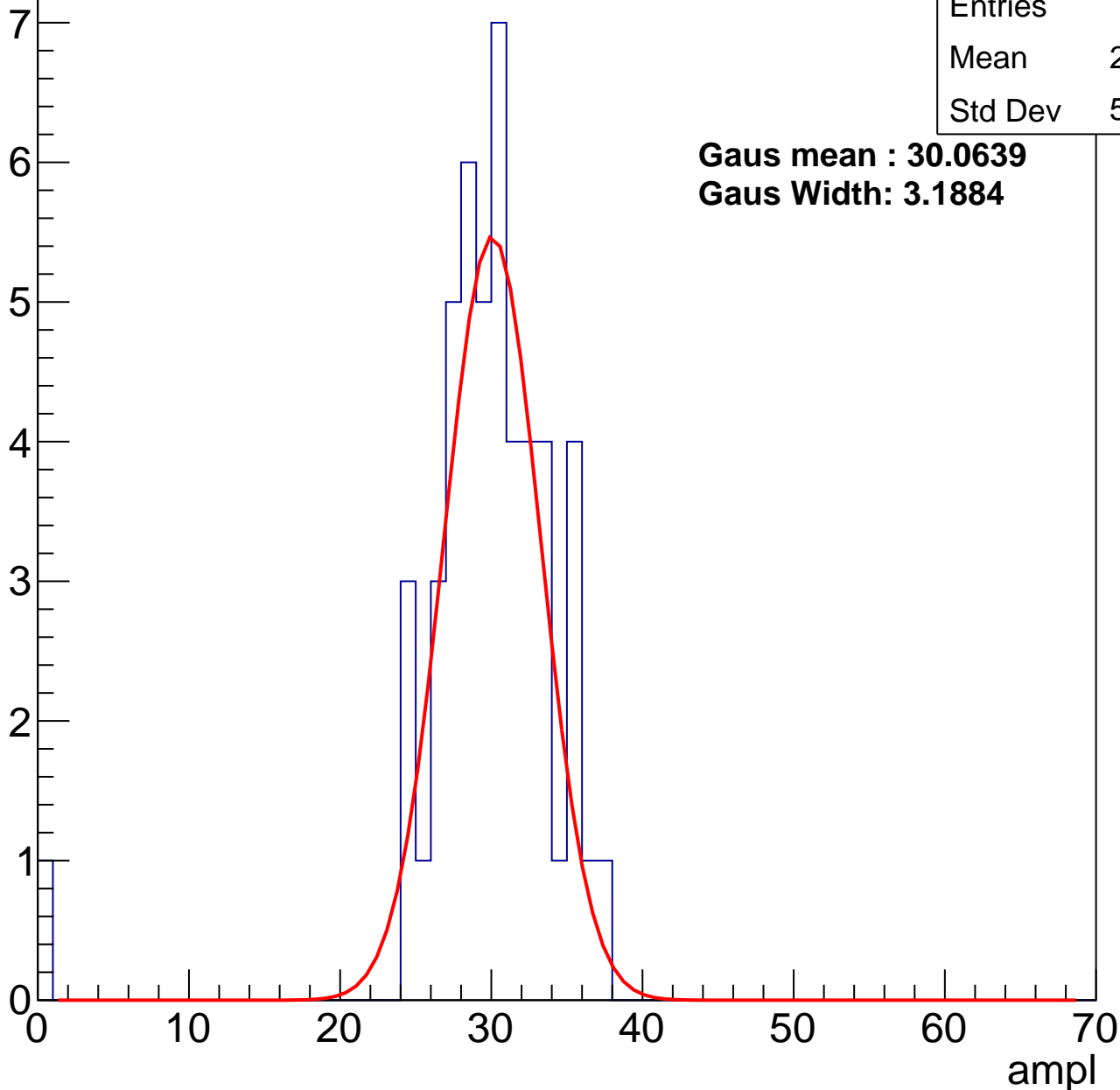
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	29.28
Std Dev	5.265

Gaus mean : 30.0639

Gaus Width: 3.1884



B1L102S, U11-ch58, adc1

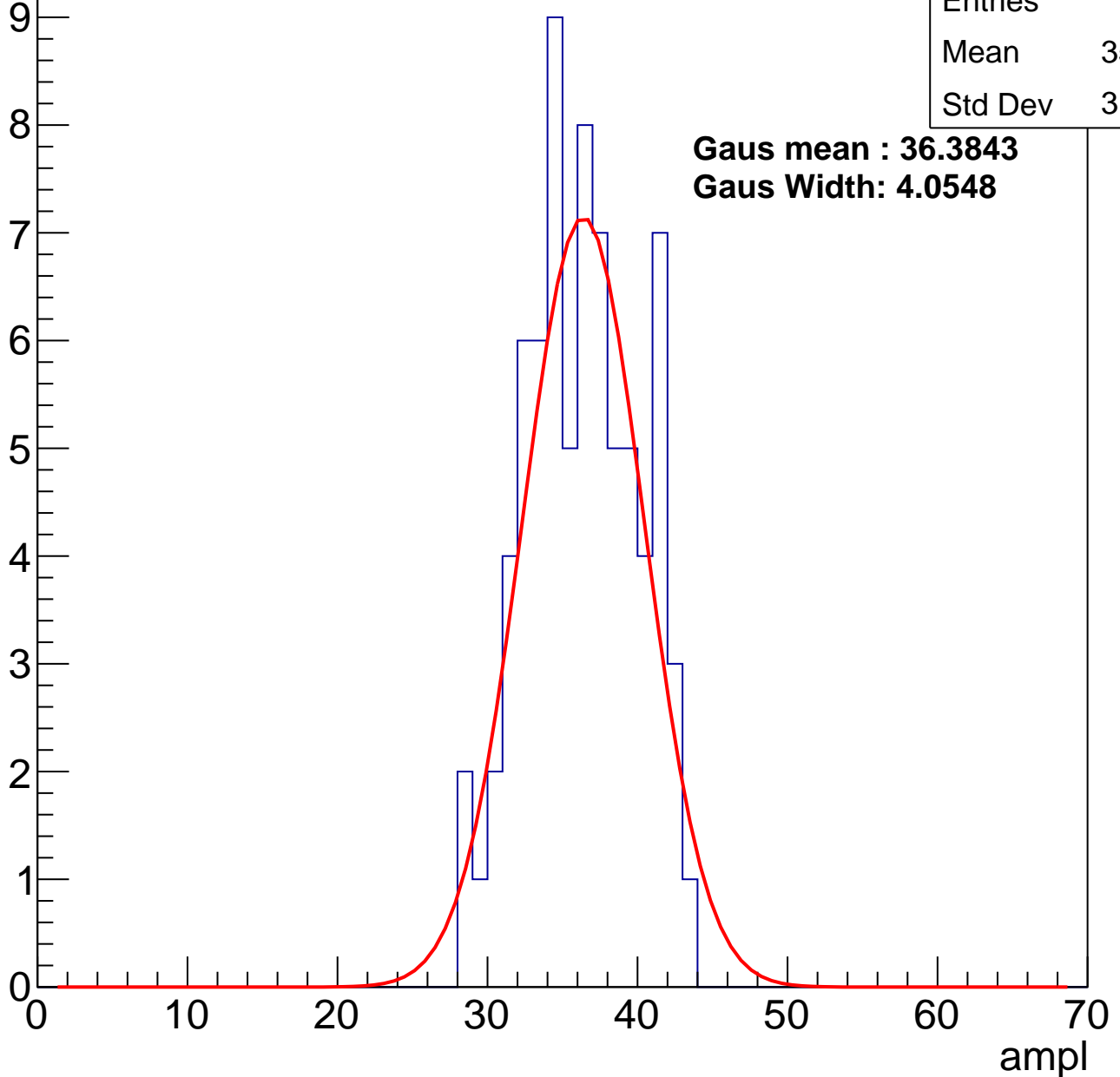
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.84
Std Dev	3.663

Gaus mean : 36.3843

Gaus Width: 4.0548



B1L102S, U11-ch58, adc2

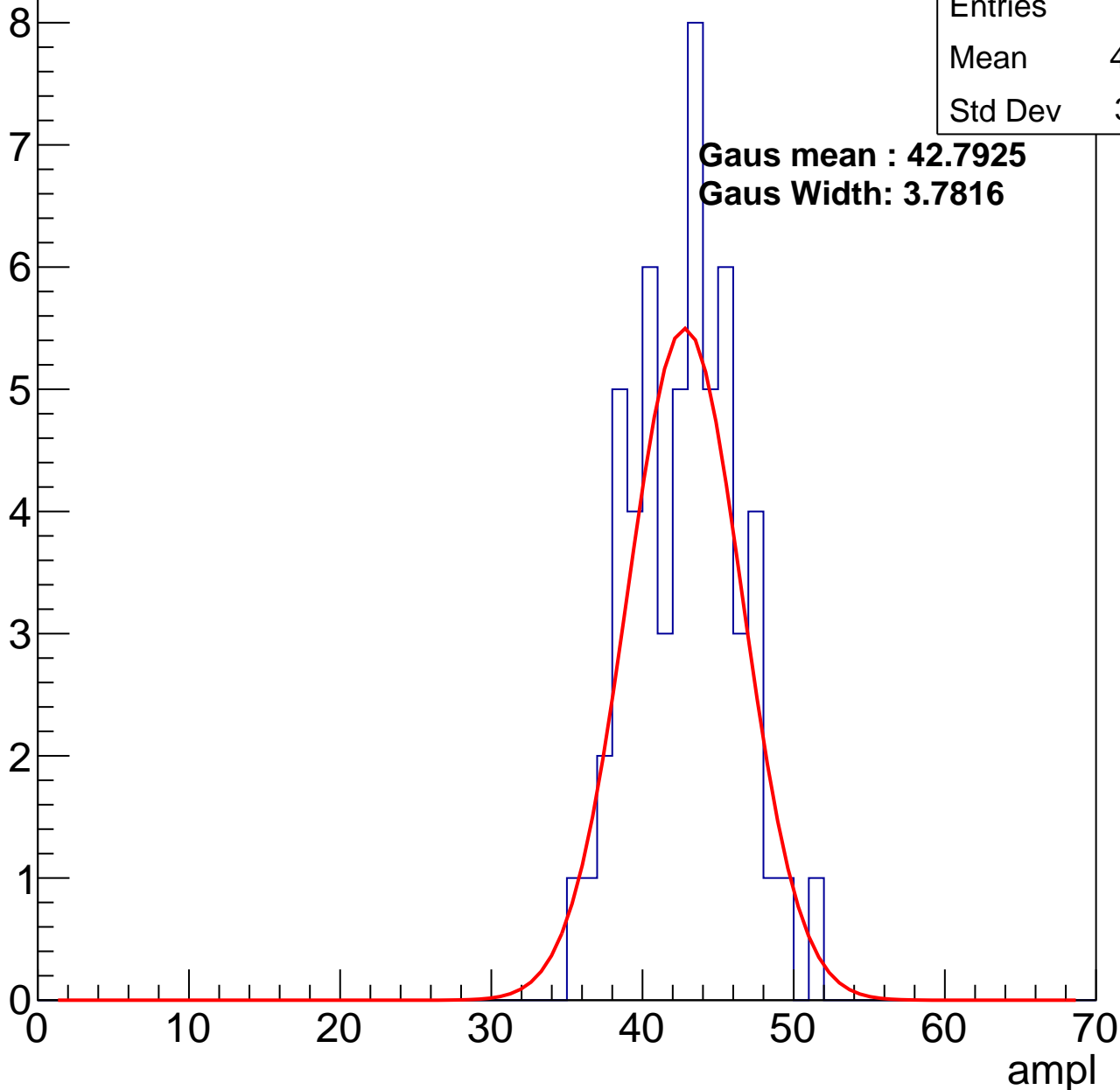
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.36
Std Dev	3.451

Gaus mean : 42.7925

Gaus Width: 3.7816

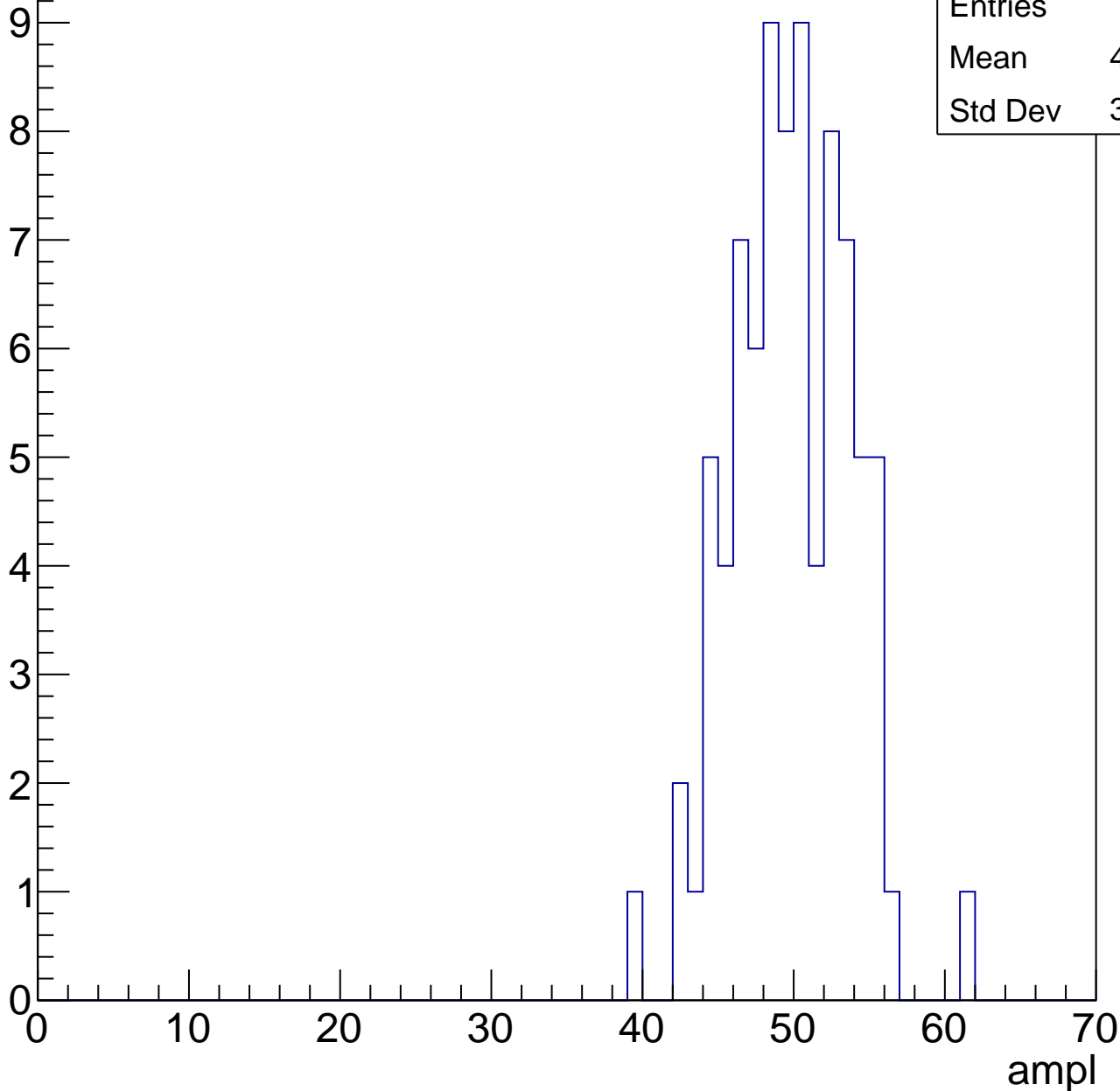


B1L102S, U11-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	49.36
Std Dev	3.826

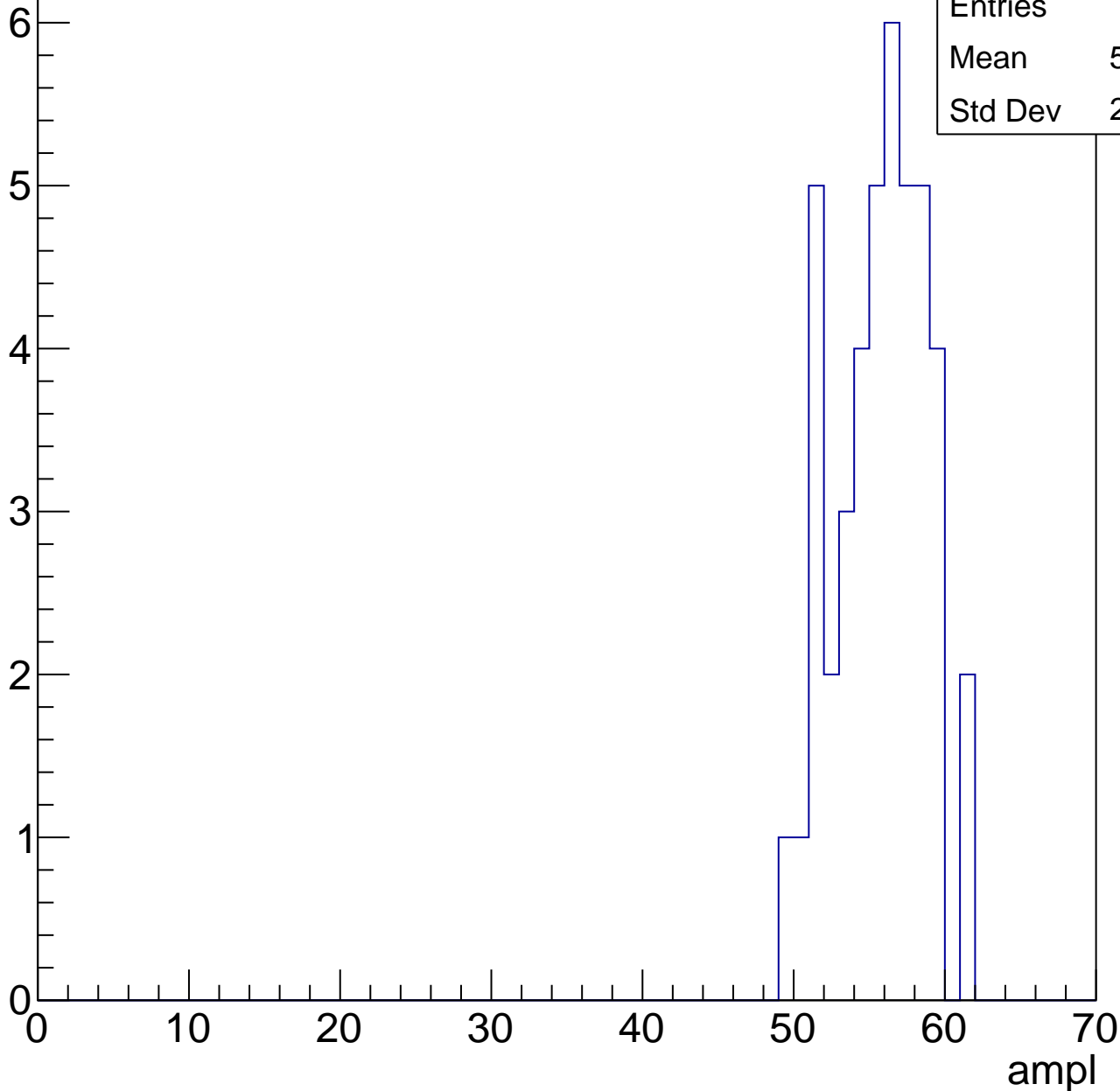


B1L102S, U11-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.28
Std Dev	2.967

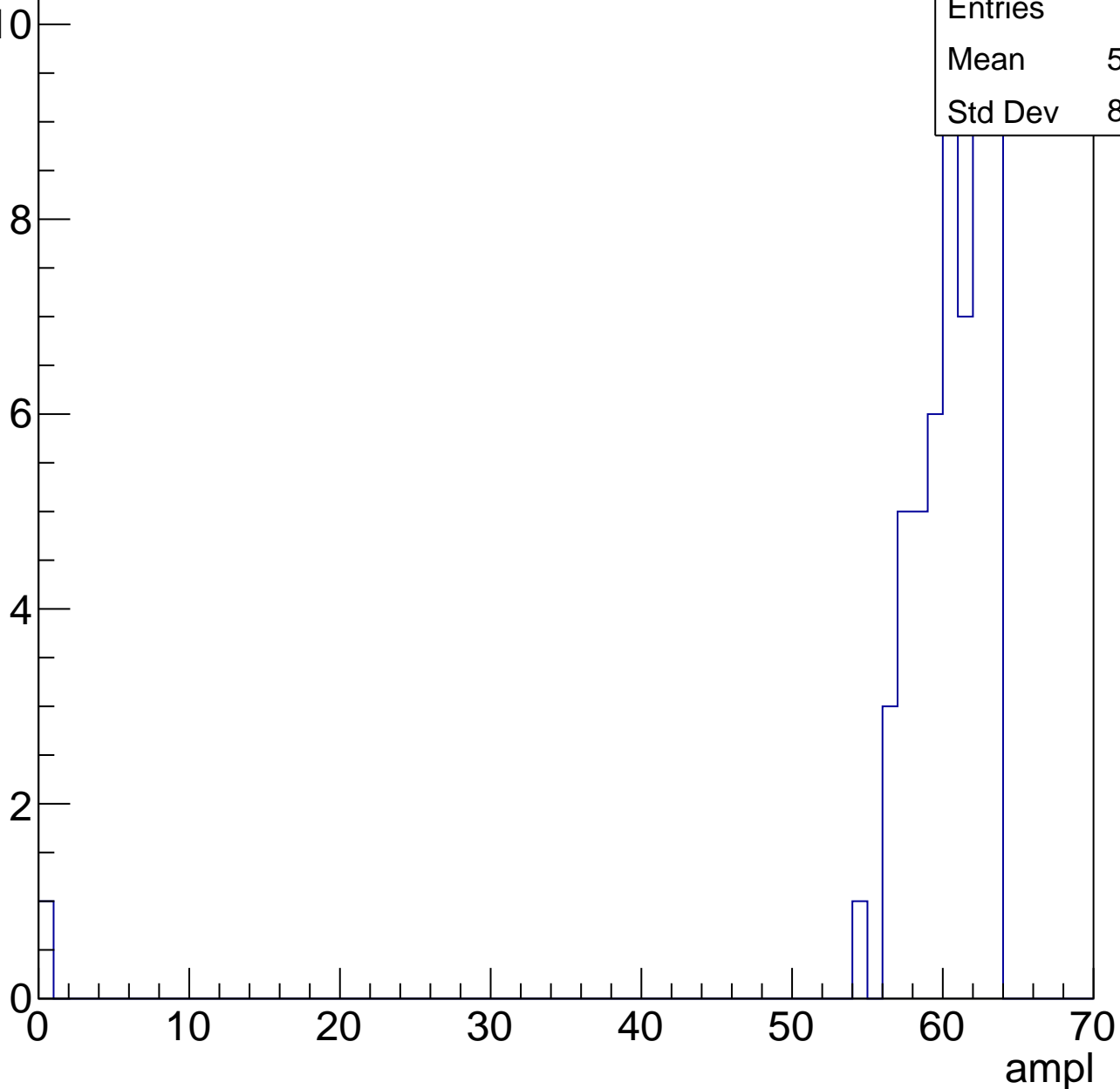


B1L102S, U11-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	59.04
Std Dev	8.197



B1L102S, U11-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U11-ch59, adc0

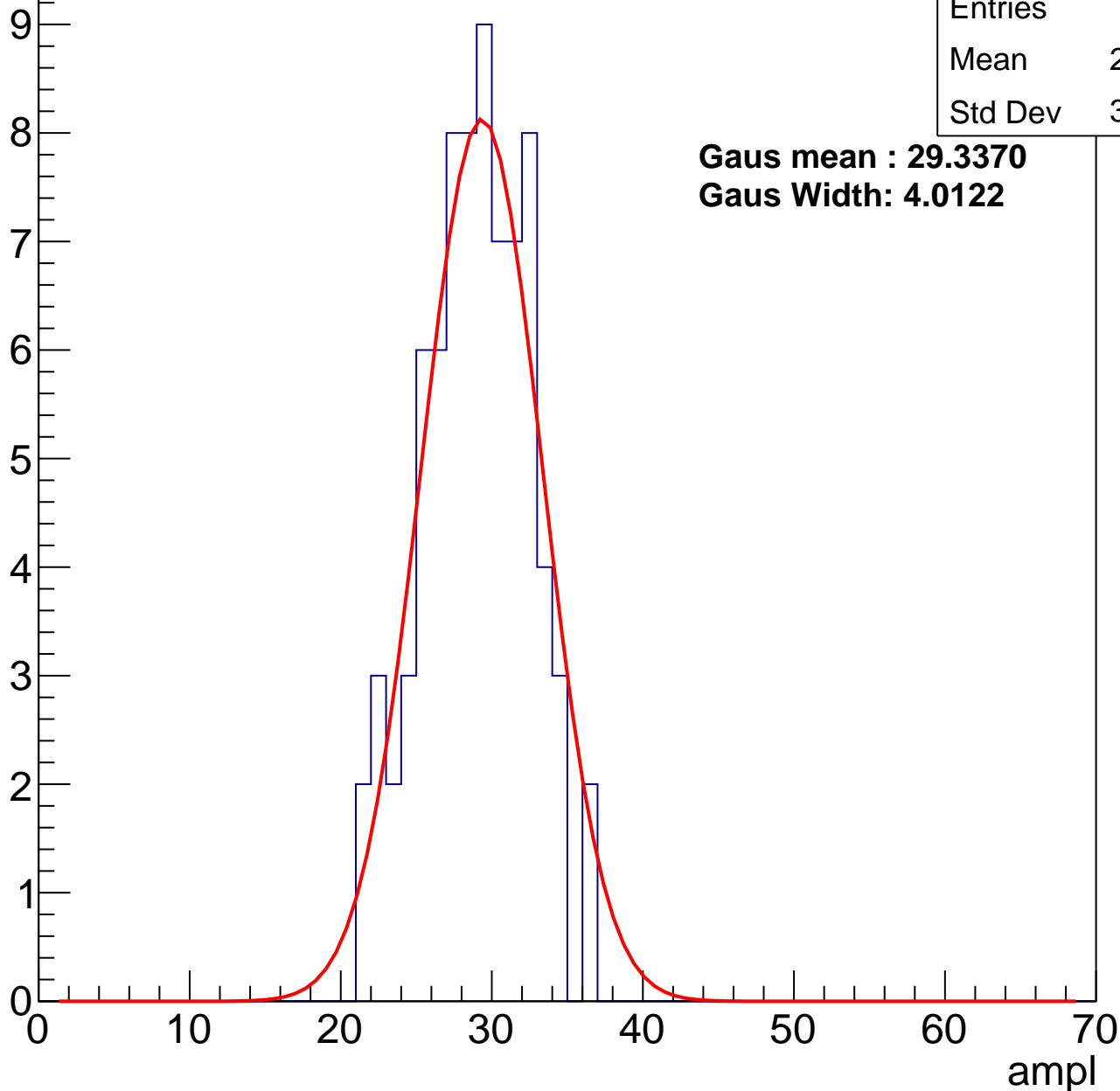
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.49
Std Dev	3.463

Gaus mean : 29.3370

Gaus Width: 4.0122



B1L102S, U11-ch59, adc1

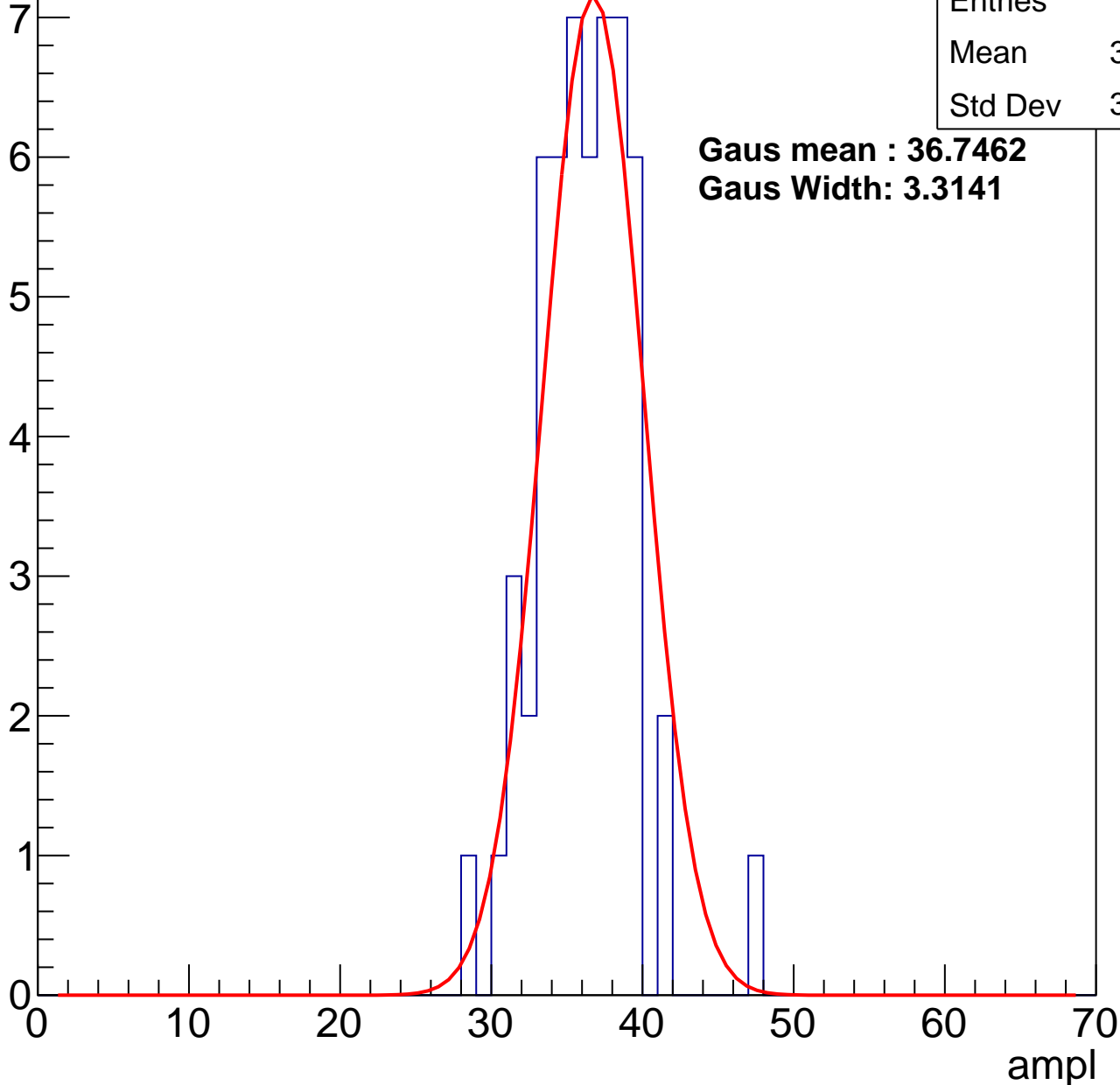
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	35.75
Std Dev	3.158

Gaus mean : 36.7462

Gaus Width: 3.3141



B1L102S, U11-ch59, adc2

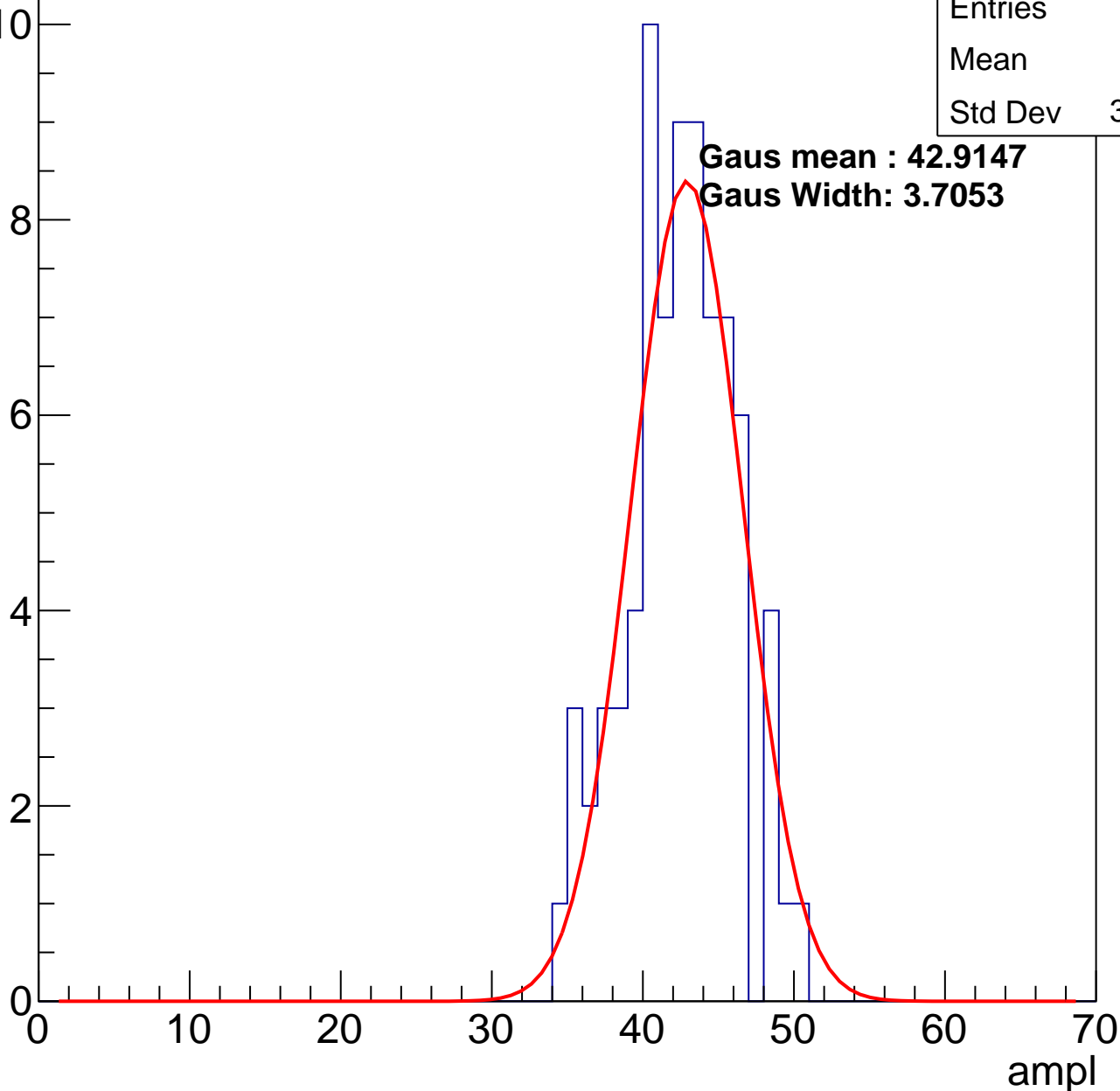
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42
Std Dev	3.498

Gaus mean : 42.9147

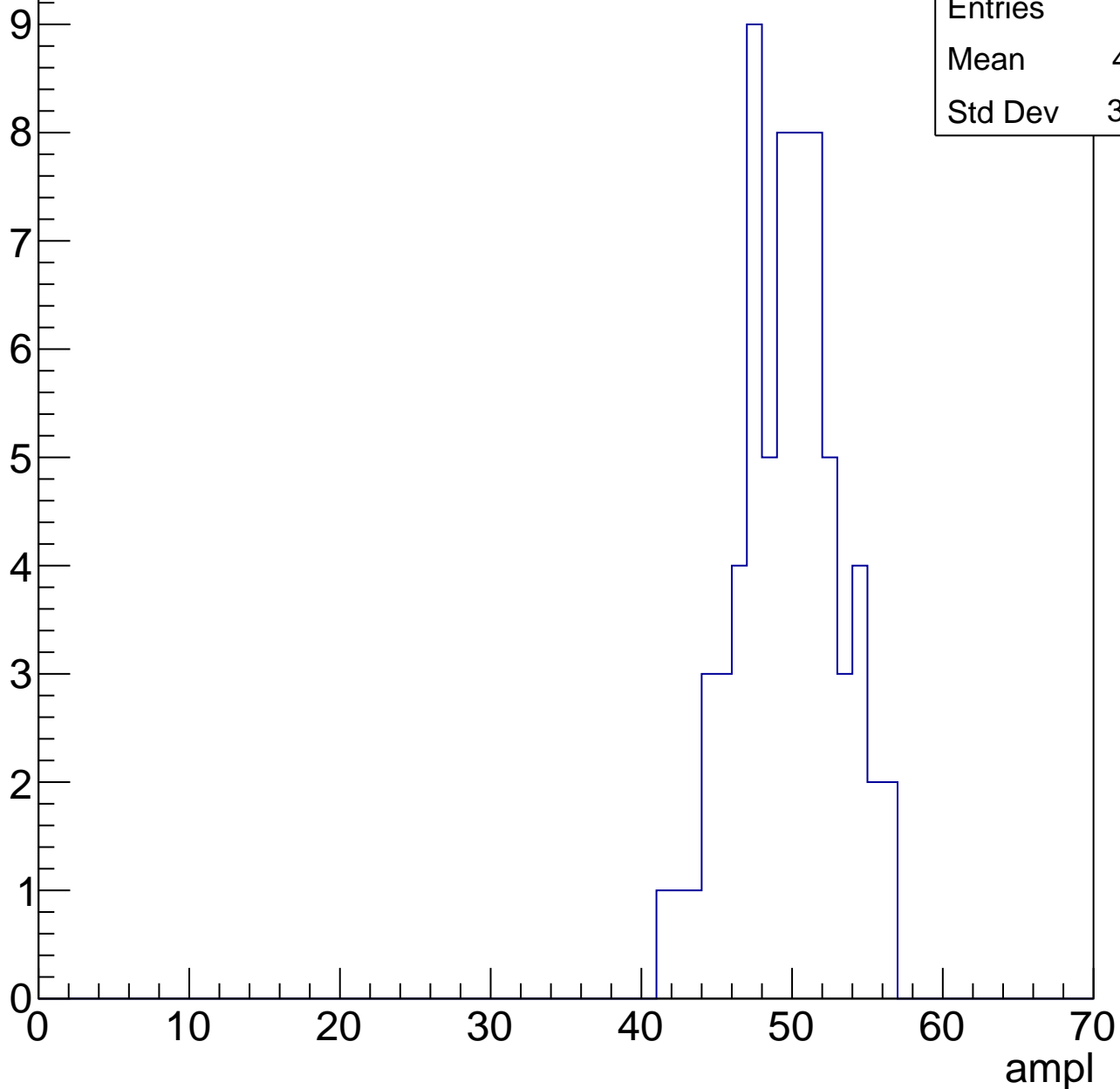
Gaus Width: 3.7053



B1L102S, U11-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

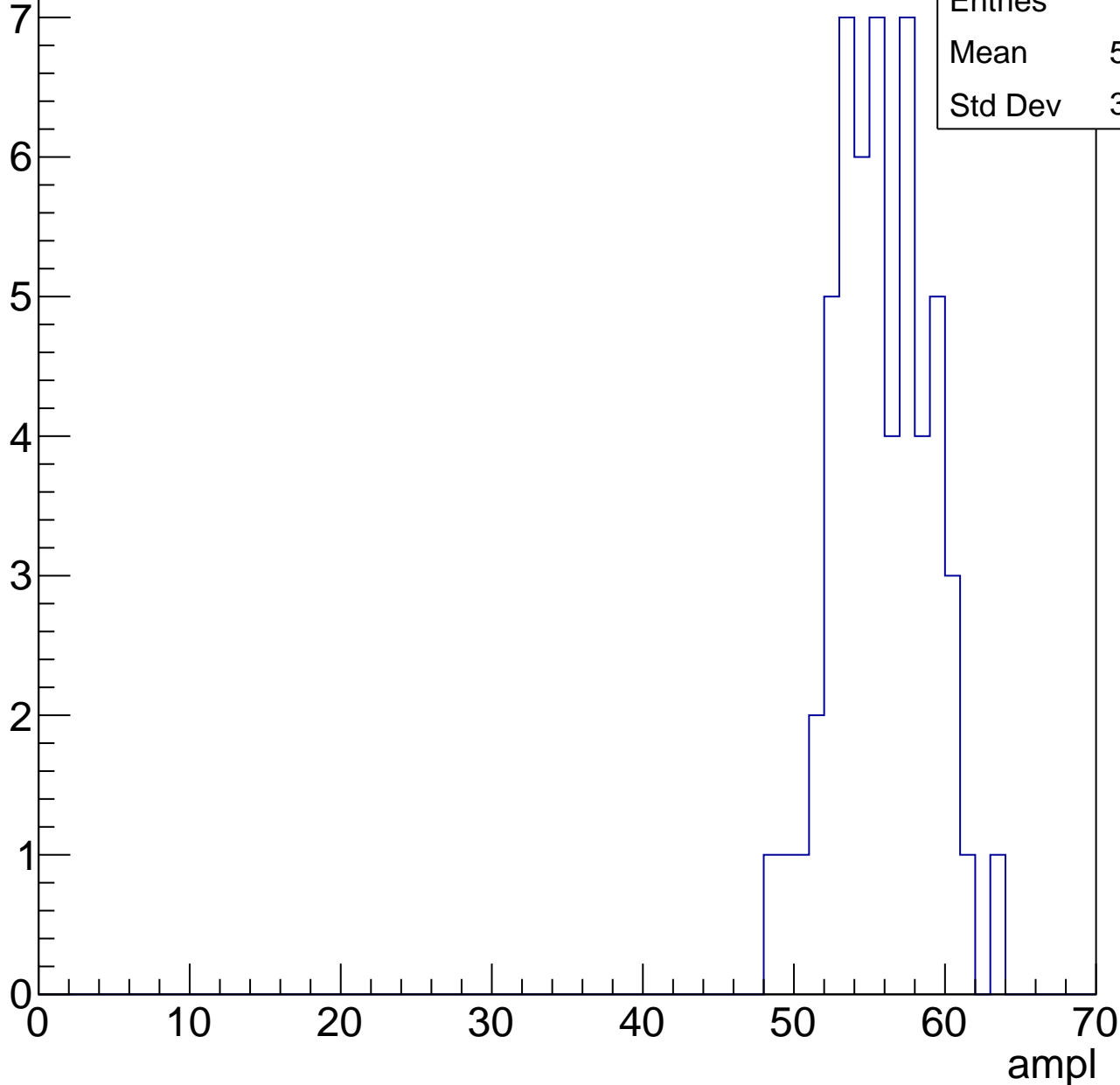


Entries	67
Mean	49.21
Std Dev	3.344

B1L102S, U11-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



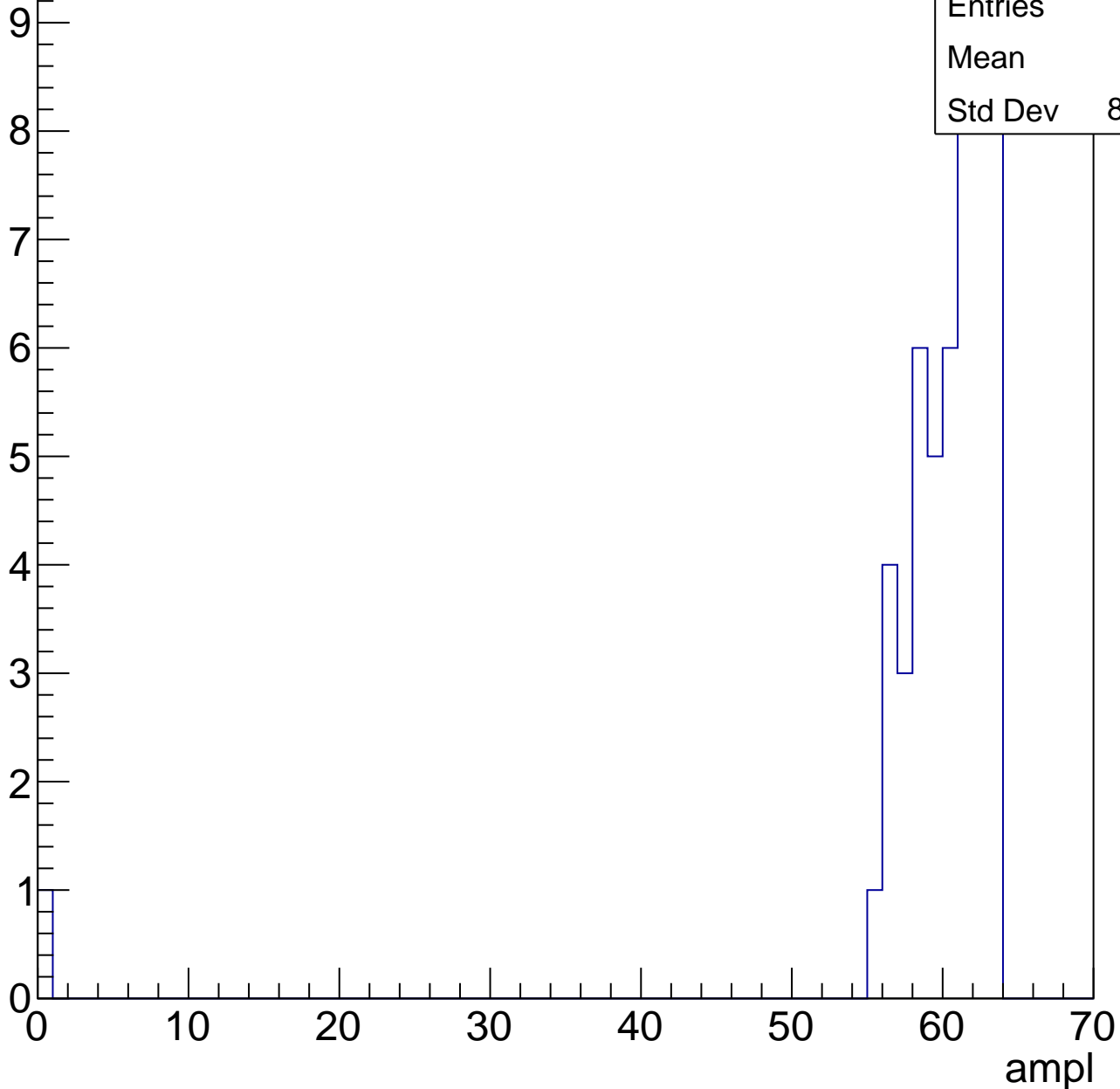
Entries	55
Mean	55.33
Std Dev	3.128

B1L102S, U11-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	59
Std Dev	8.488



B1L102S, U11-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U11-ch60, adc0

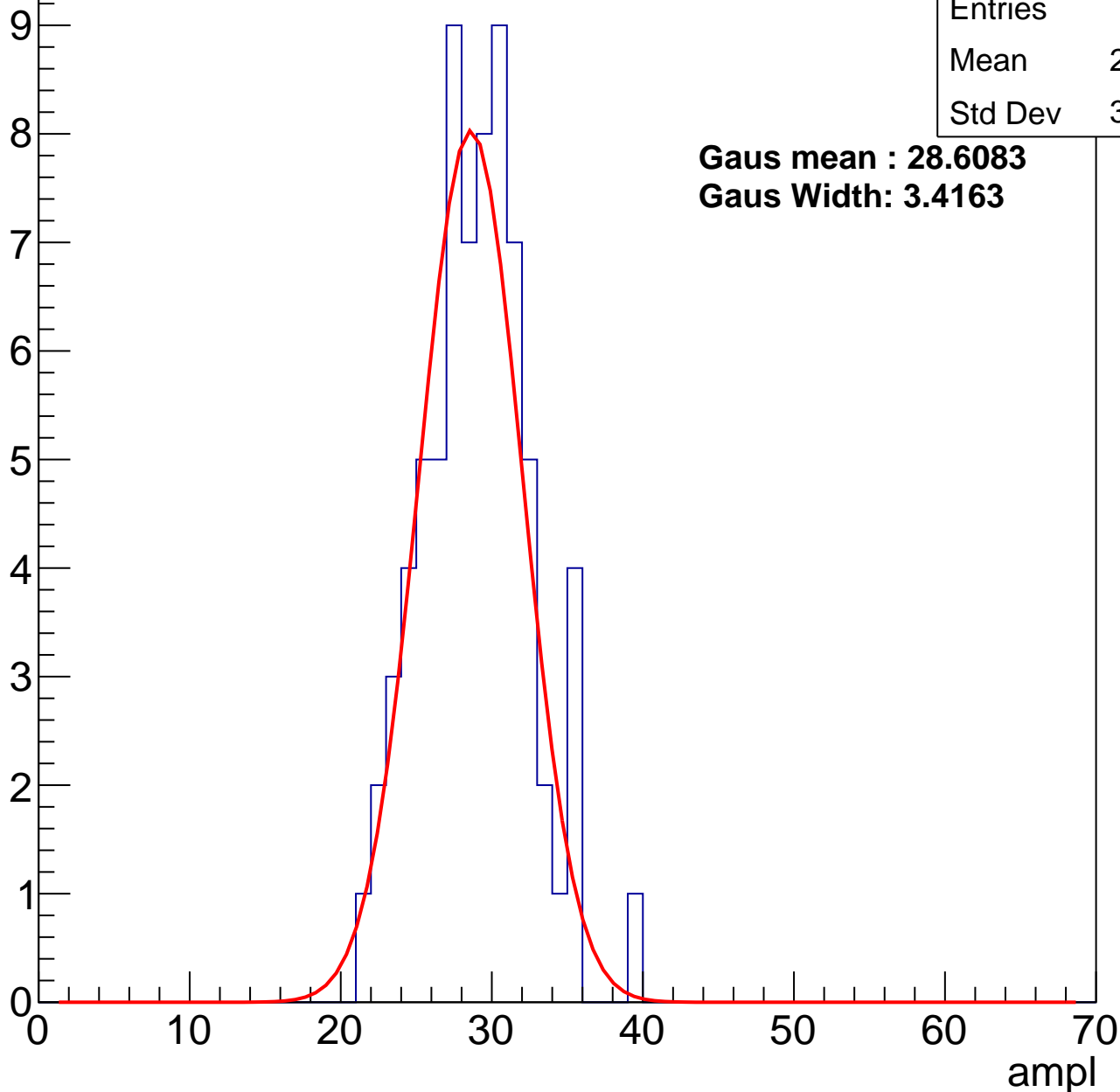
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.52
Std Dev	3.519

Gaus mean : 28.6083

Gaus Width: 3.4163



B1L102S, U11-ch60, adc1

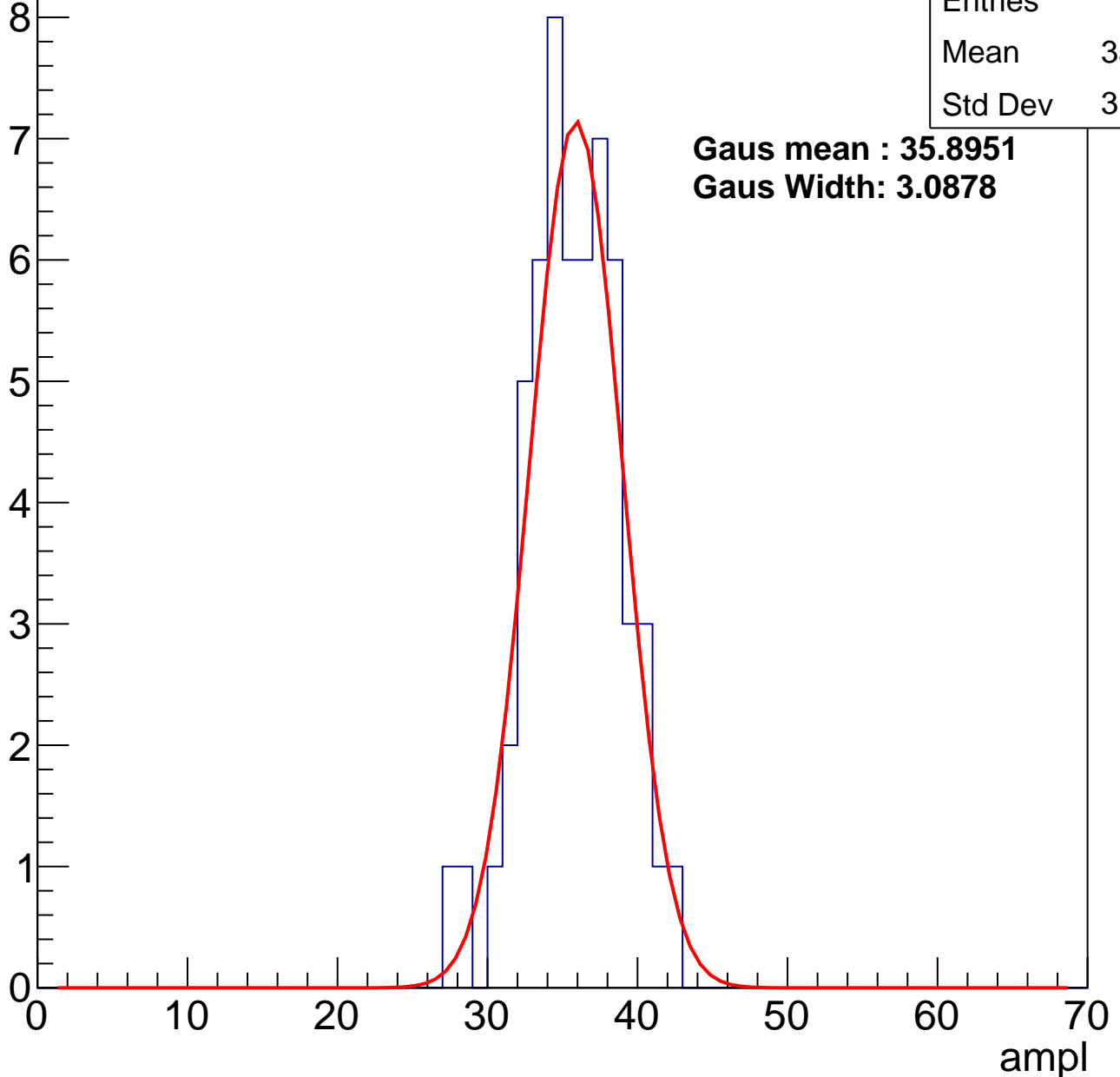
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.26
Std Dev	3.075

Gaus mean : 35.8951

Gaus Width: 3.0878



B1L102S, U11-ch60, adc2

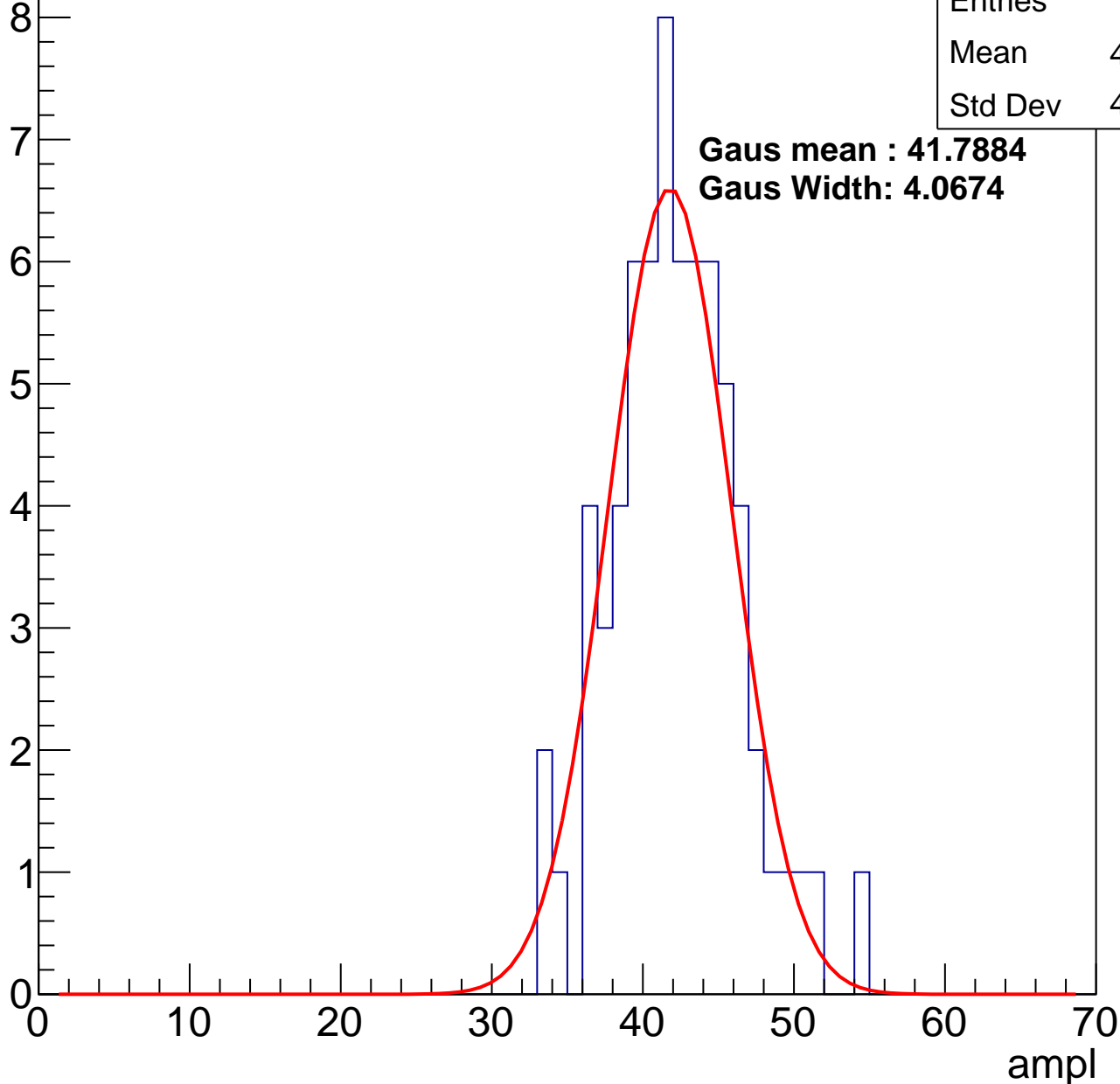
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.74
Std Dev	4.118

Gaus mean : 41.7884

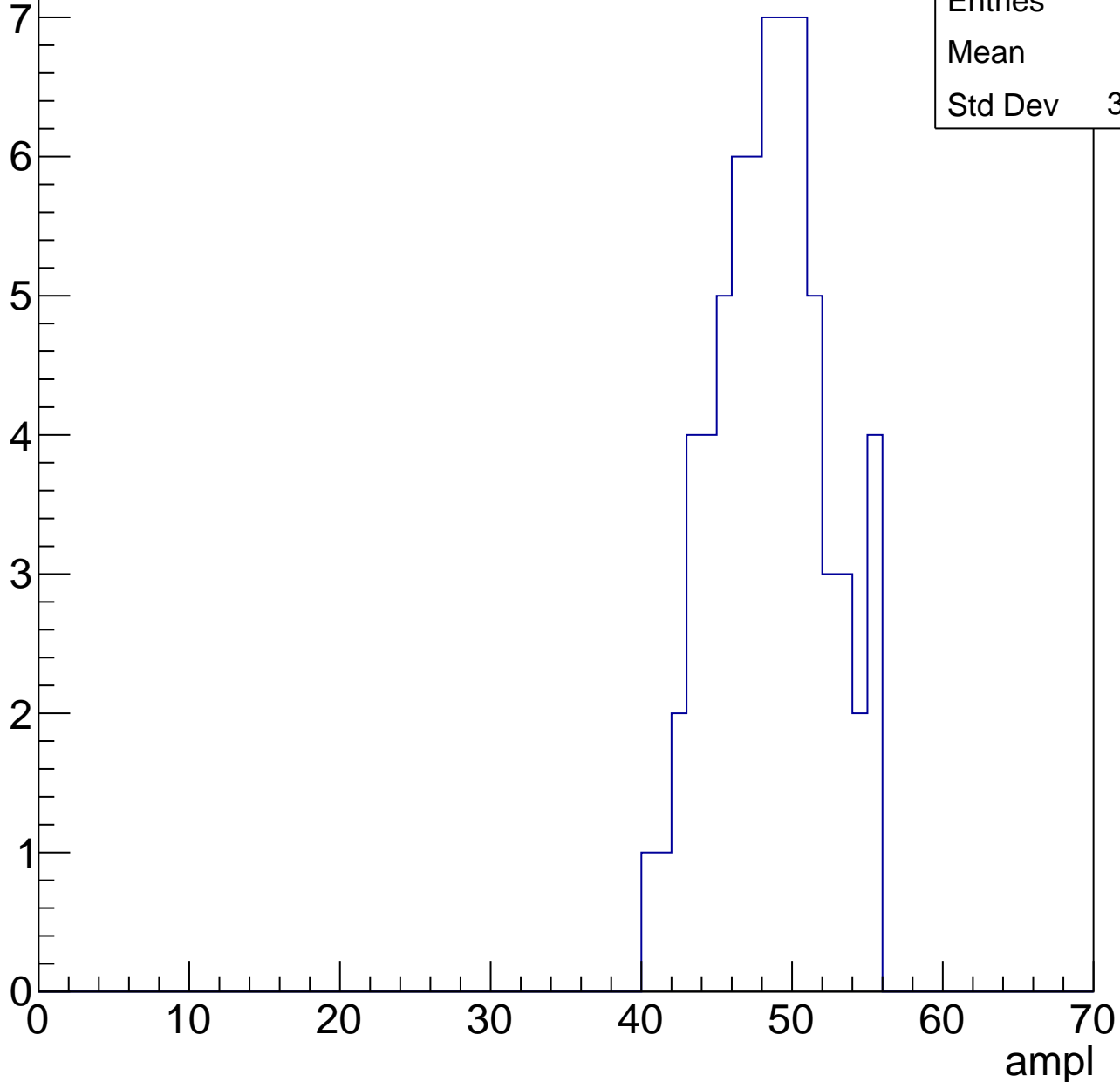
Gaus Width: 4.0674



B1L102S, U11-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

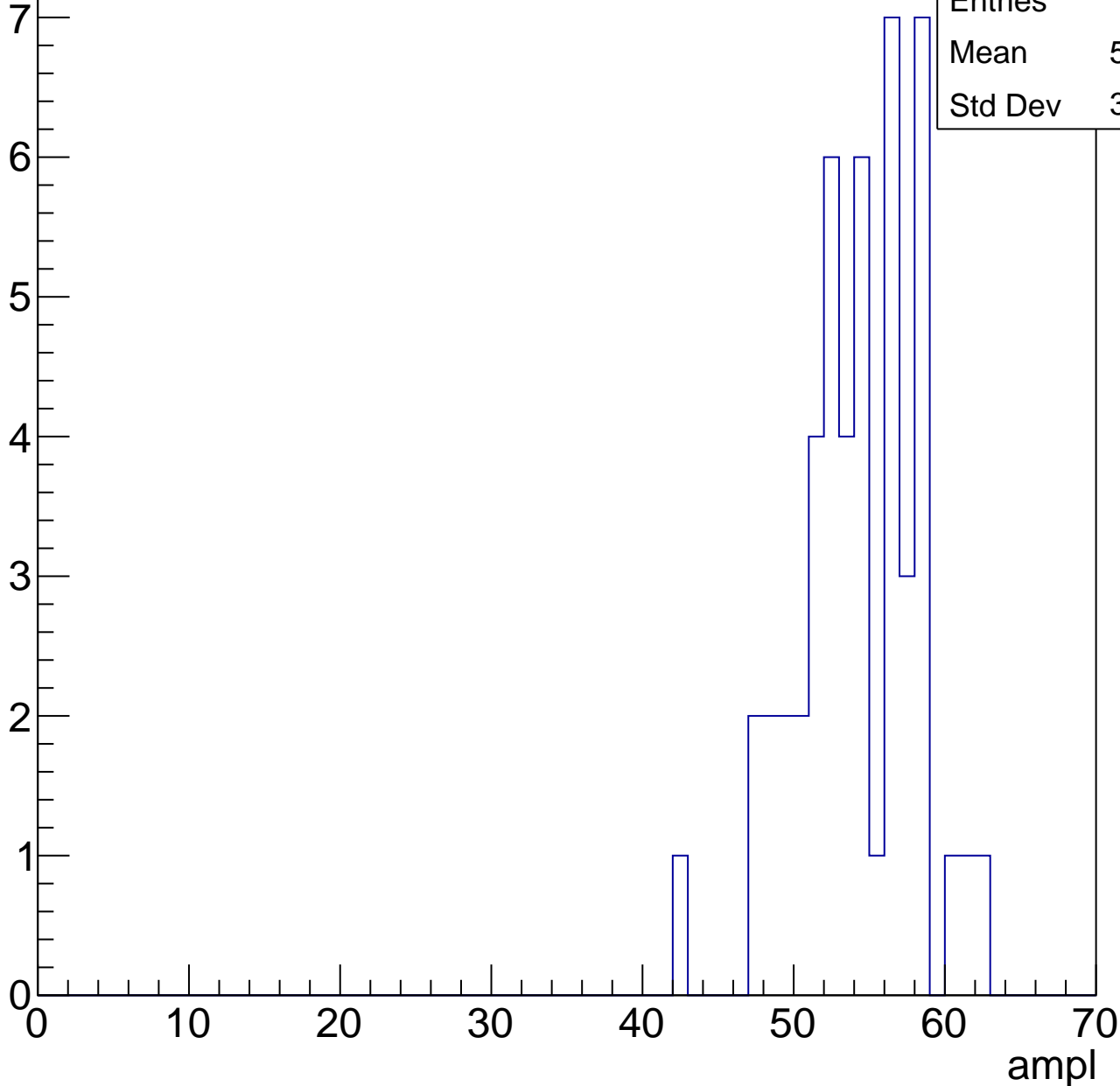


B1L102S, U11-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	53.78
Std Dev	3.936

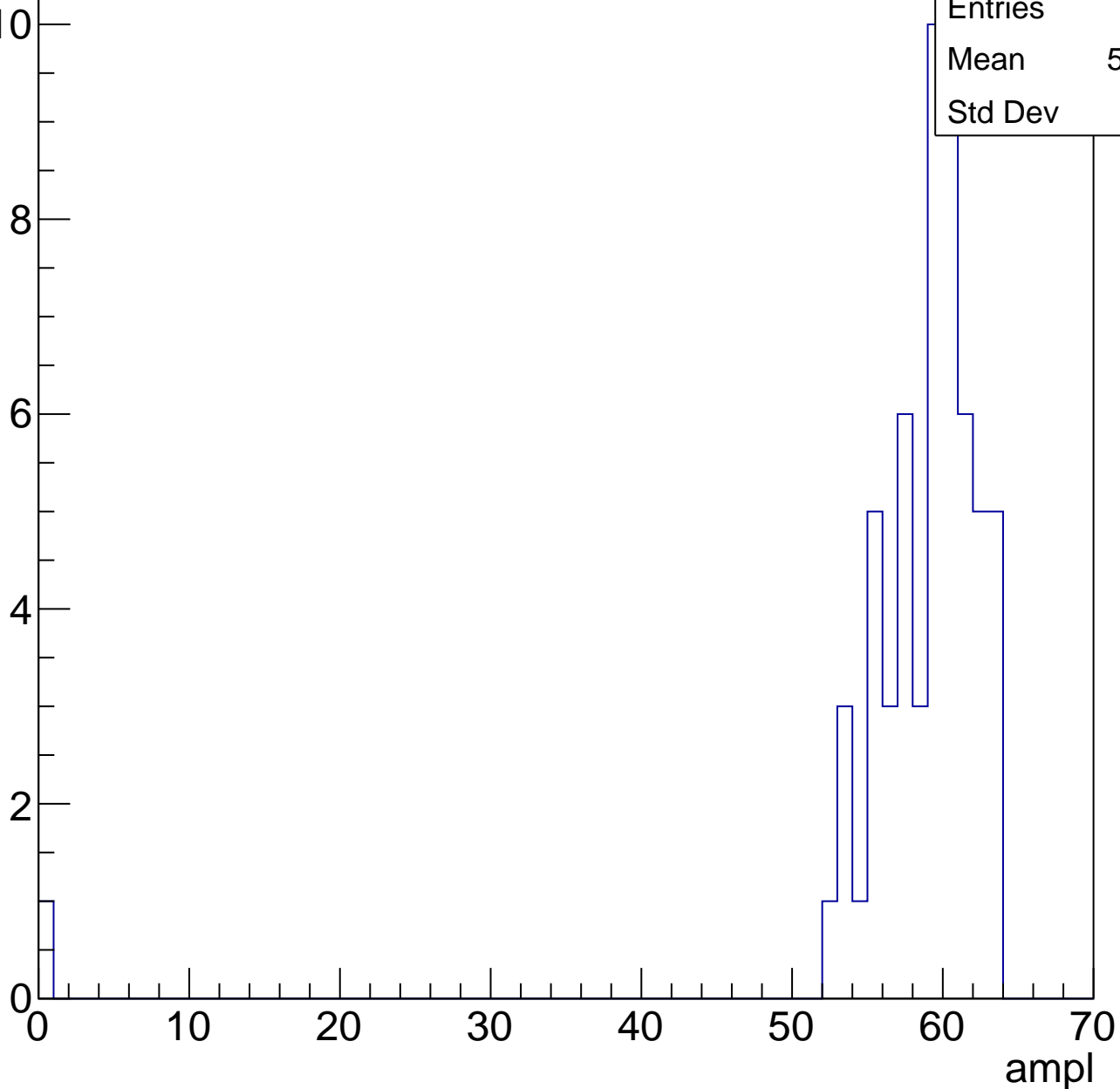


B1L102S, U11-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.67
Std Dev	8.15

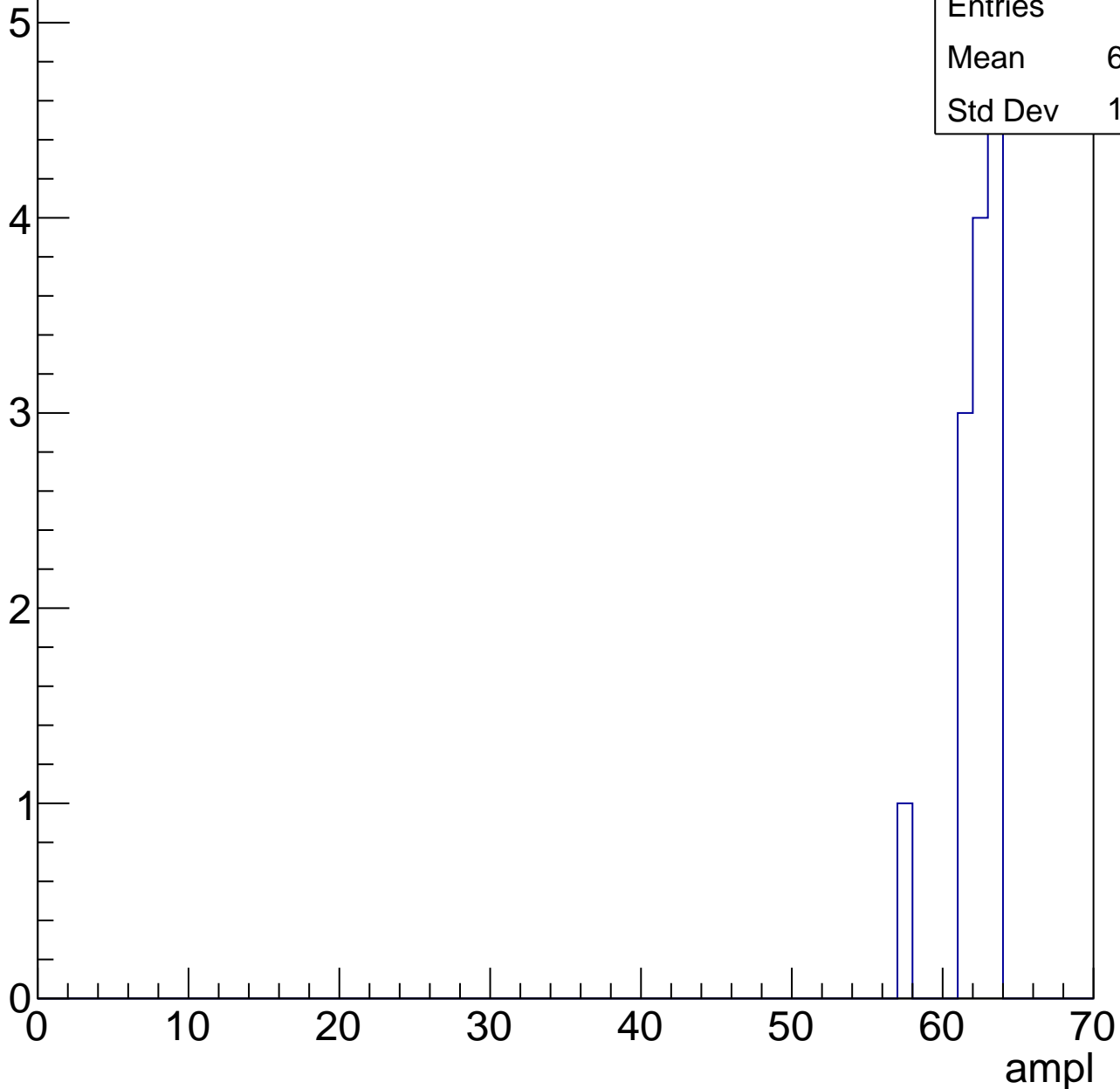


B1L102S, U11-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.77
Std Dev	1.576



B1L102S, U11-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch61, adc0

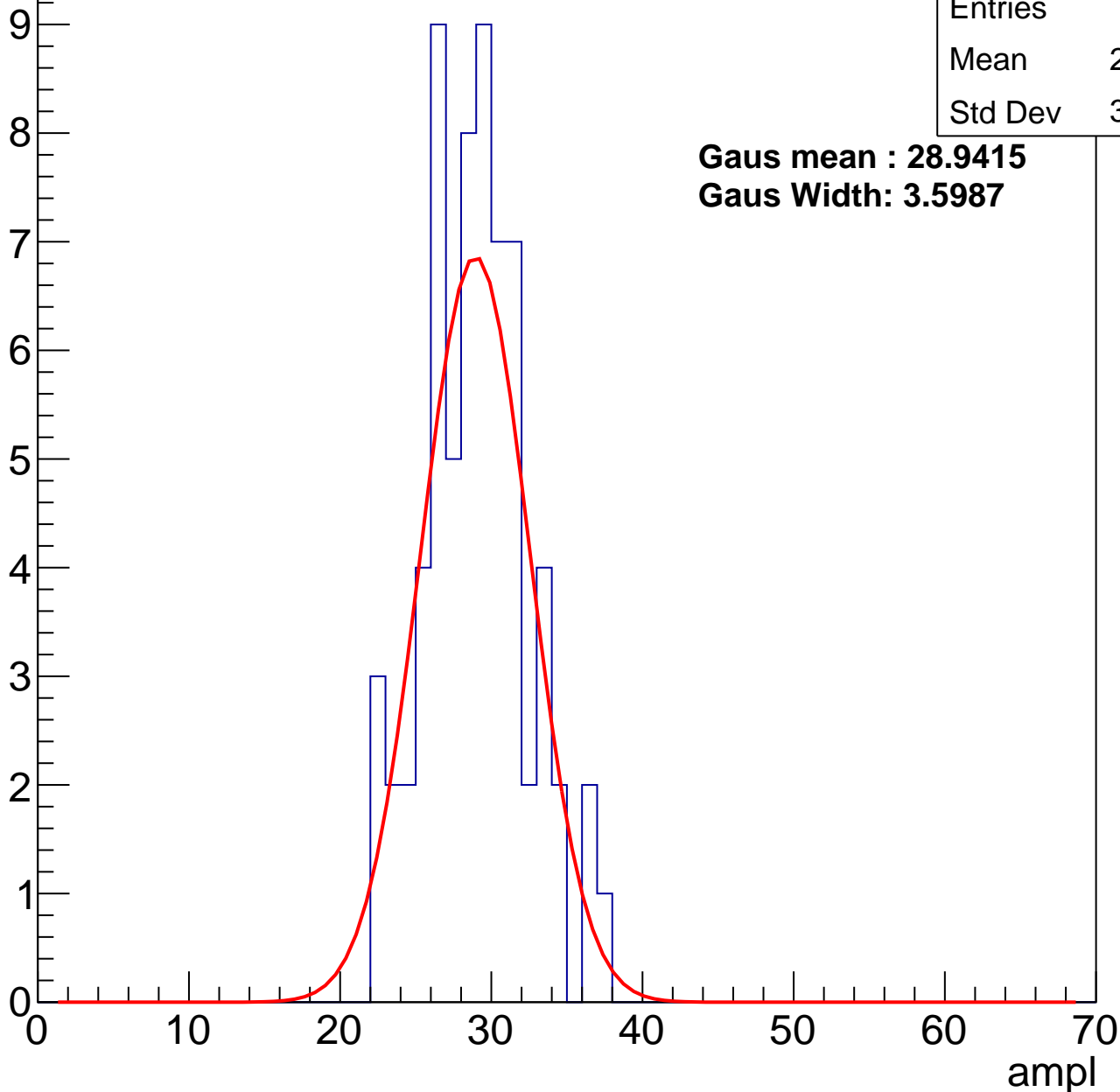
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.57
Std Dev	3.365

Gaus mean : 28.9415

Gaus Width: 3.5987



B1L102S, U11-ch61, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

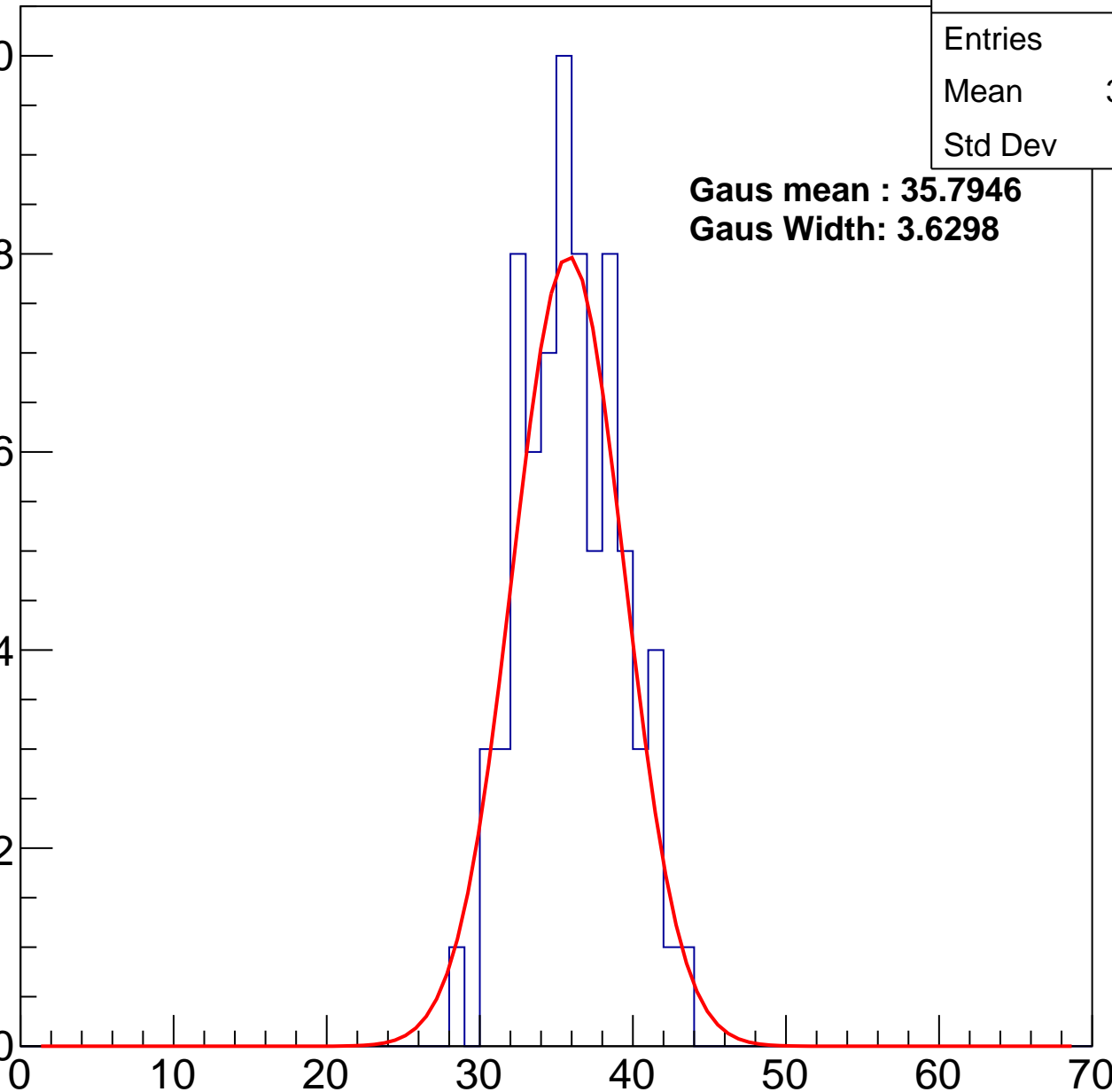
Entries	73
Mean	35.53
Std Dev	3.24

Gaus mean : 35.7946

Gaus Width: 3.6298

10
8
6
4
2
0

ampl



B1L102S, U11-ch61, adc2

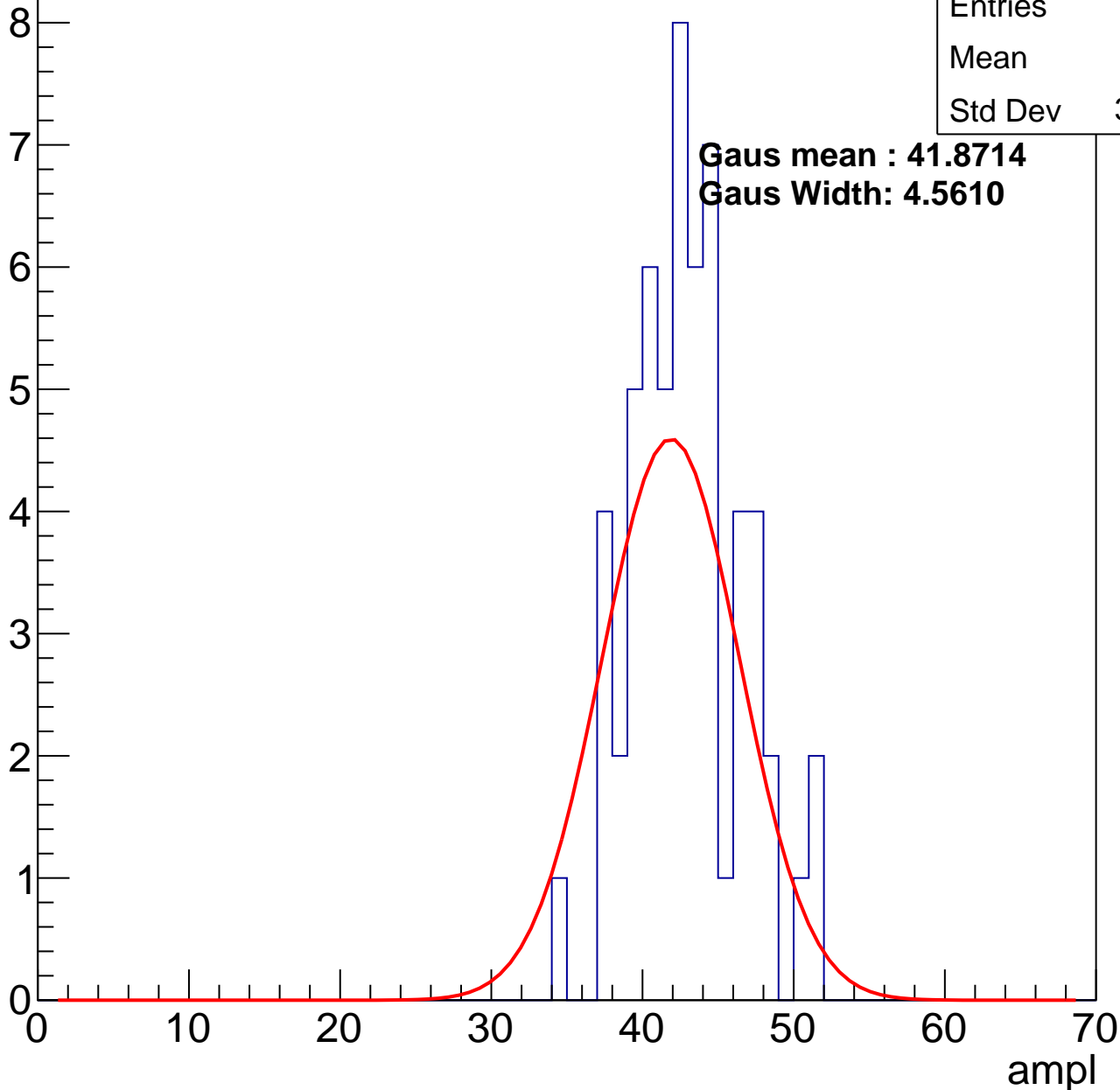
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.5
Std Dev	3.621

Gaus mean : 41.8714

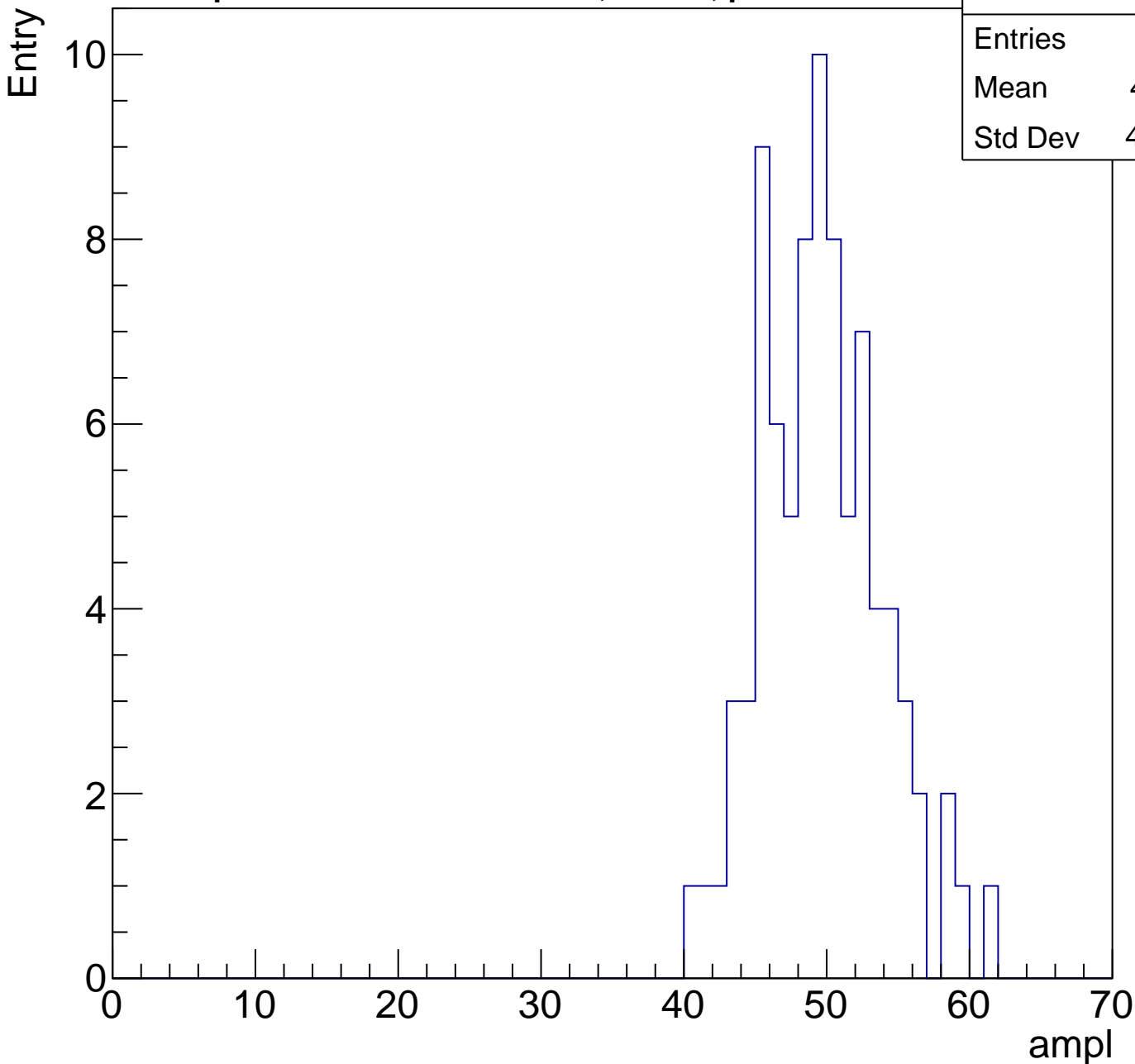
Gaus Width: 4.5610



B1L102S, U11-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	49.21
Std Dev	4.178

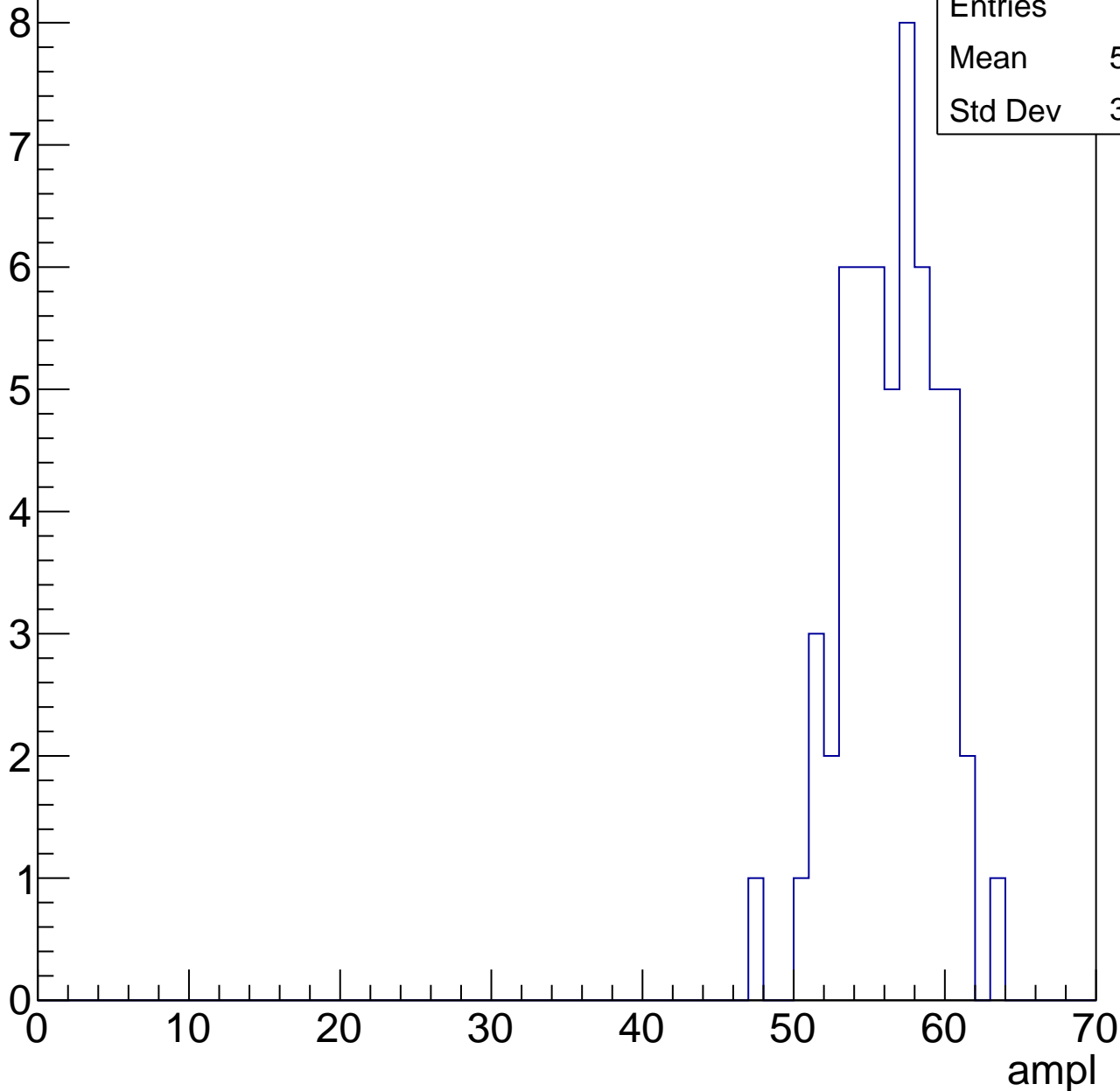


B1L102S, U11-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.96
Std Dev	3.145

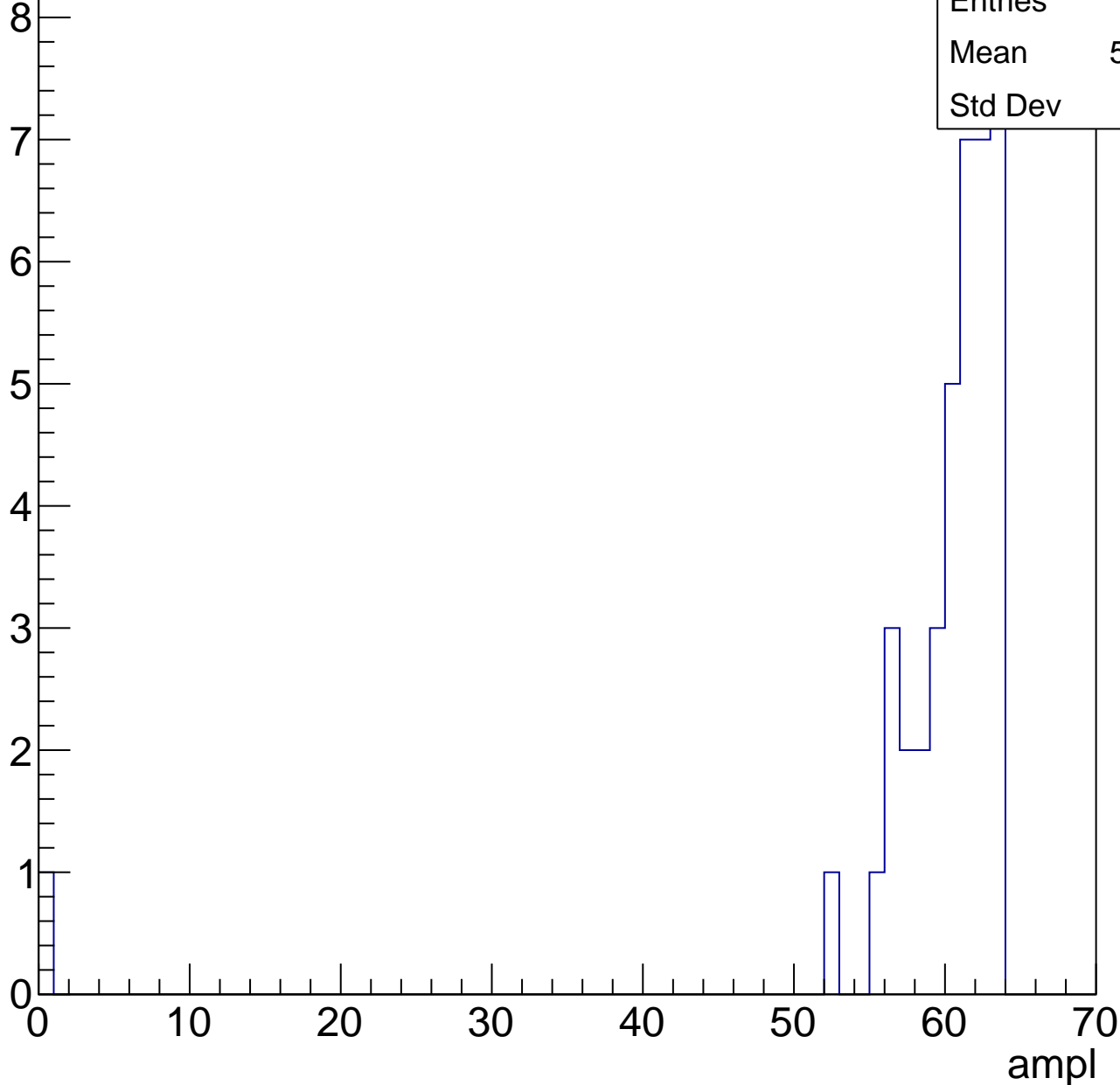


B1L102S, U11-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

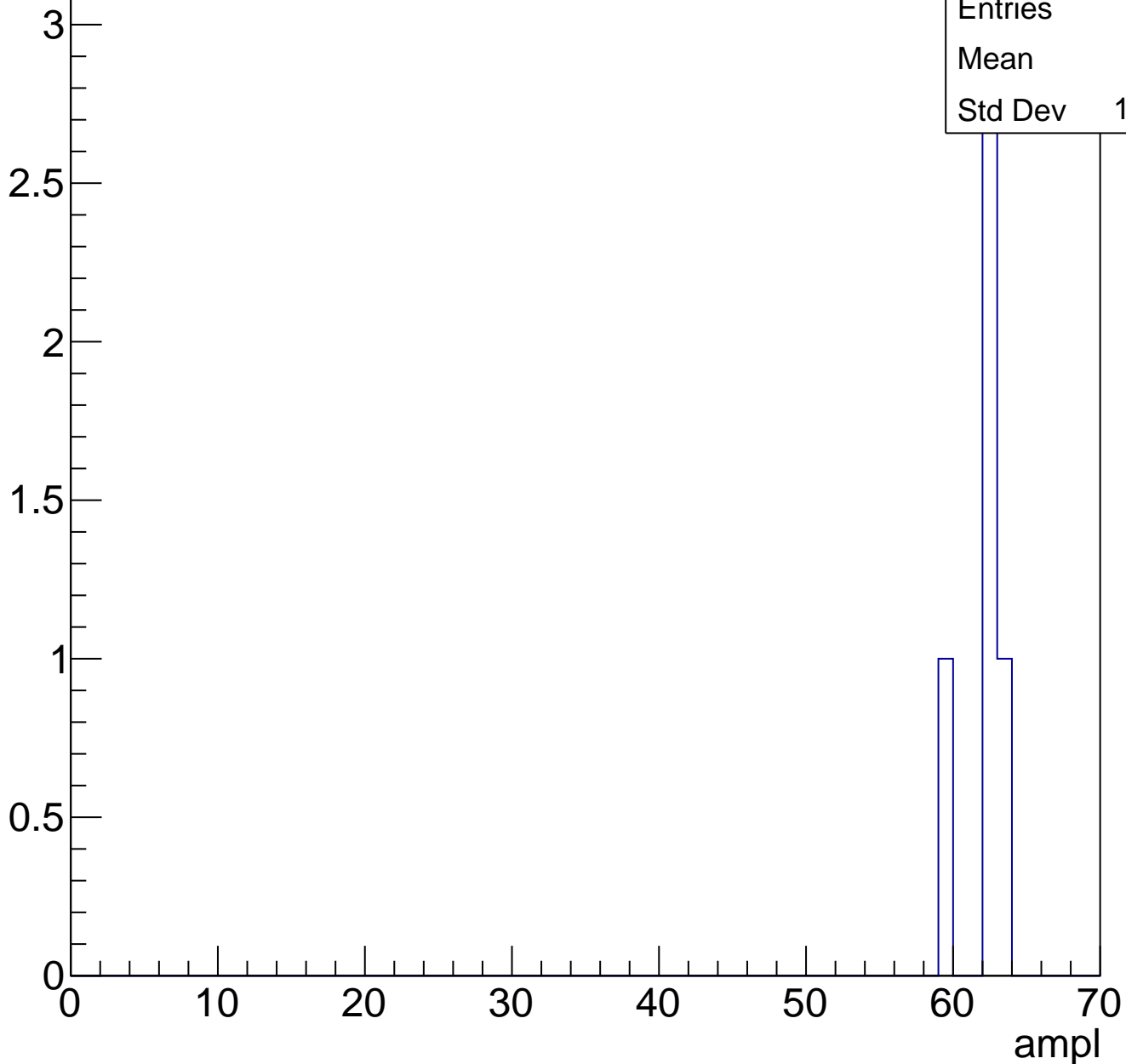
Entries	40
Mean	58.67
Std Dev	9.75



B1L102S, U11-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	61.6
Std Dev	1.356

B1L102S, U11-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch62, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	29.86
Std Dev	5.098

Gaus mean : 31.3165

Gaus Width: 3.4987

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L102S, U11-ch62, adc1

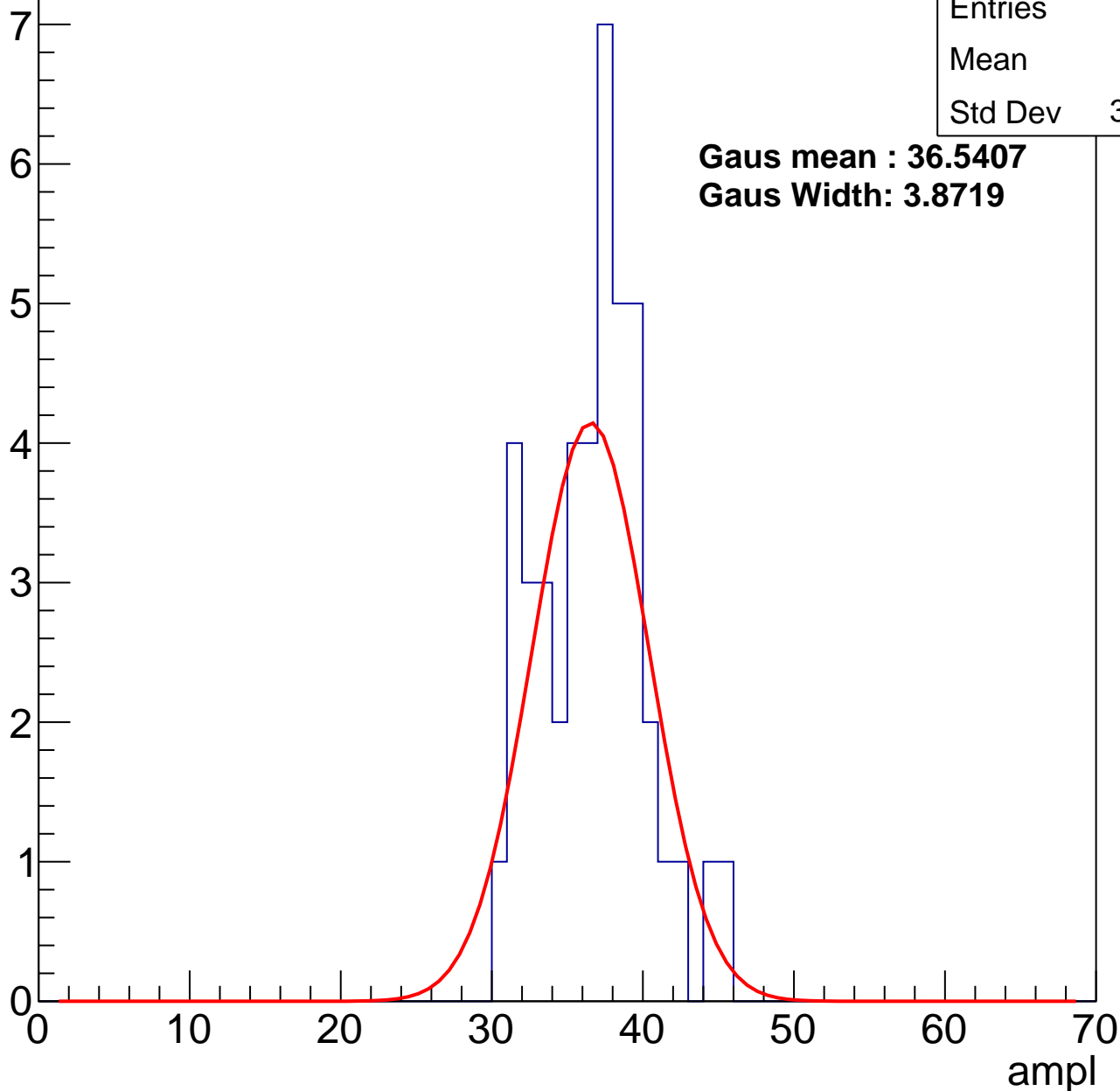
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	36.3
Std Dev	3.468

Gaus mean : 36.5407

Gaus Width: 3.8719



B1L102S, U11-ch62, adc2

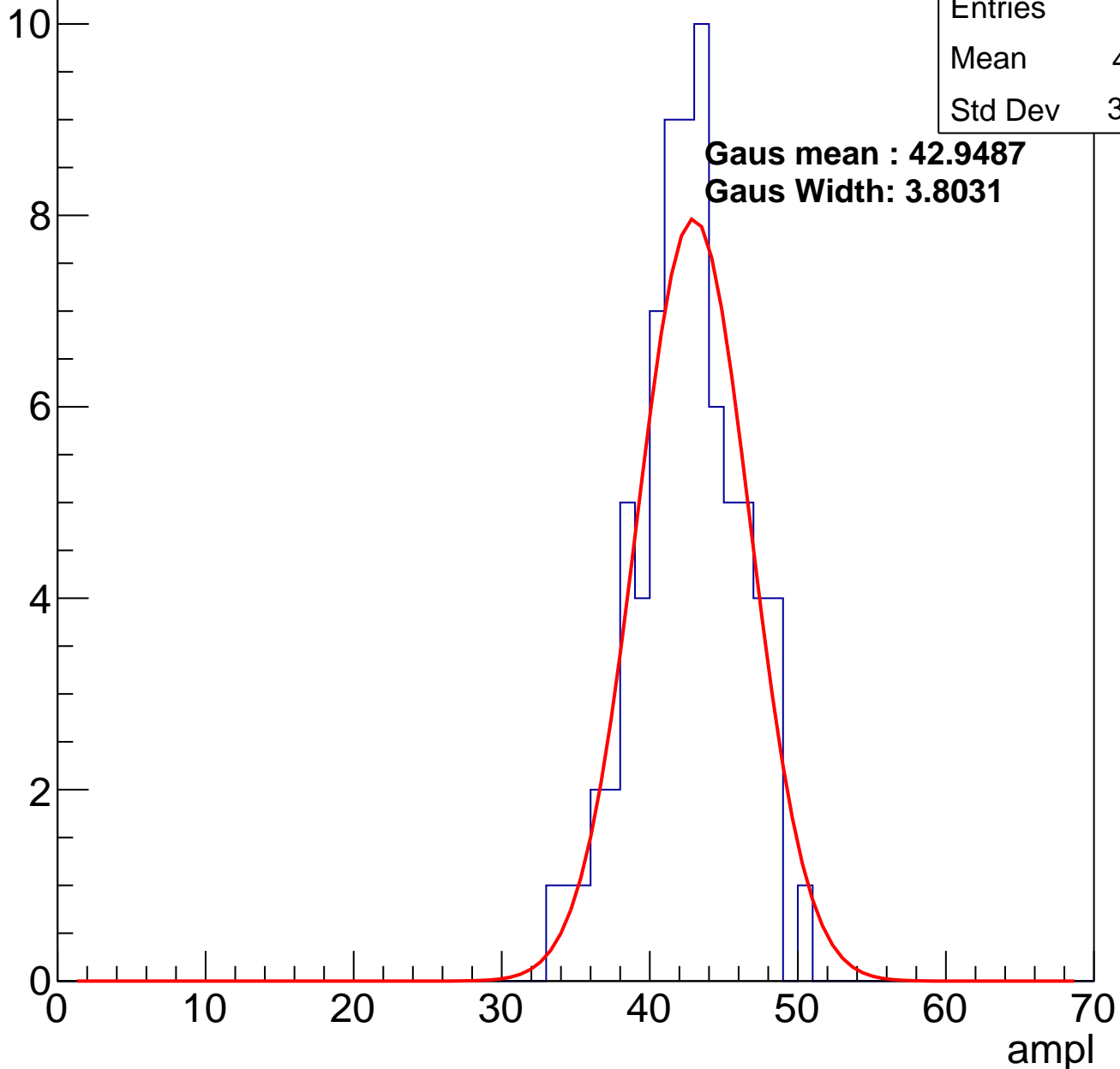
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	42.11
Std Dev	3.508

Gaus mean : 42.9487

Gaus Width: 3.8031

Entry

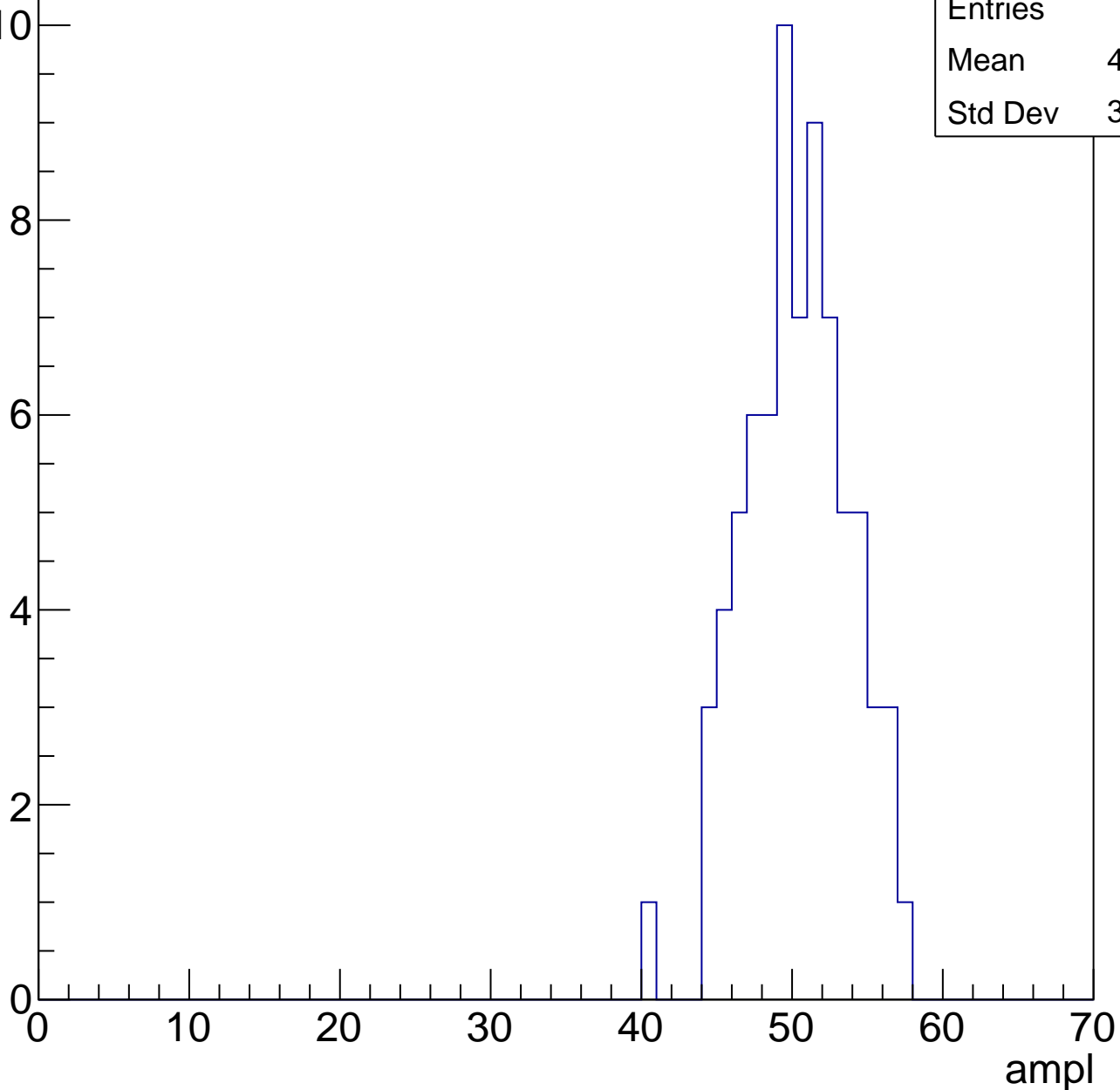


B1L102S, U11-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

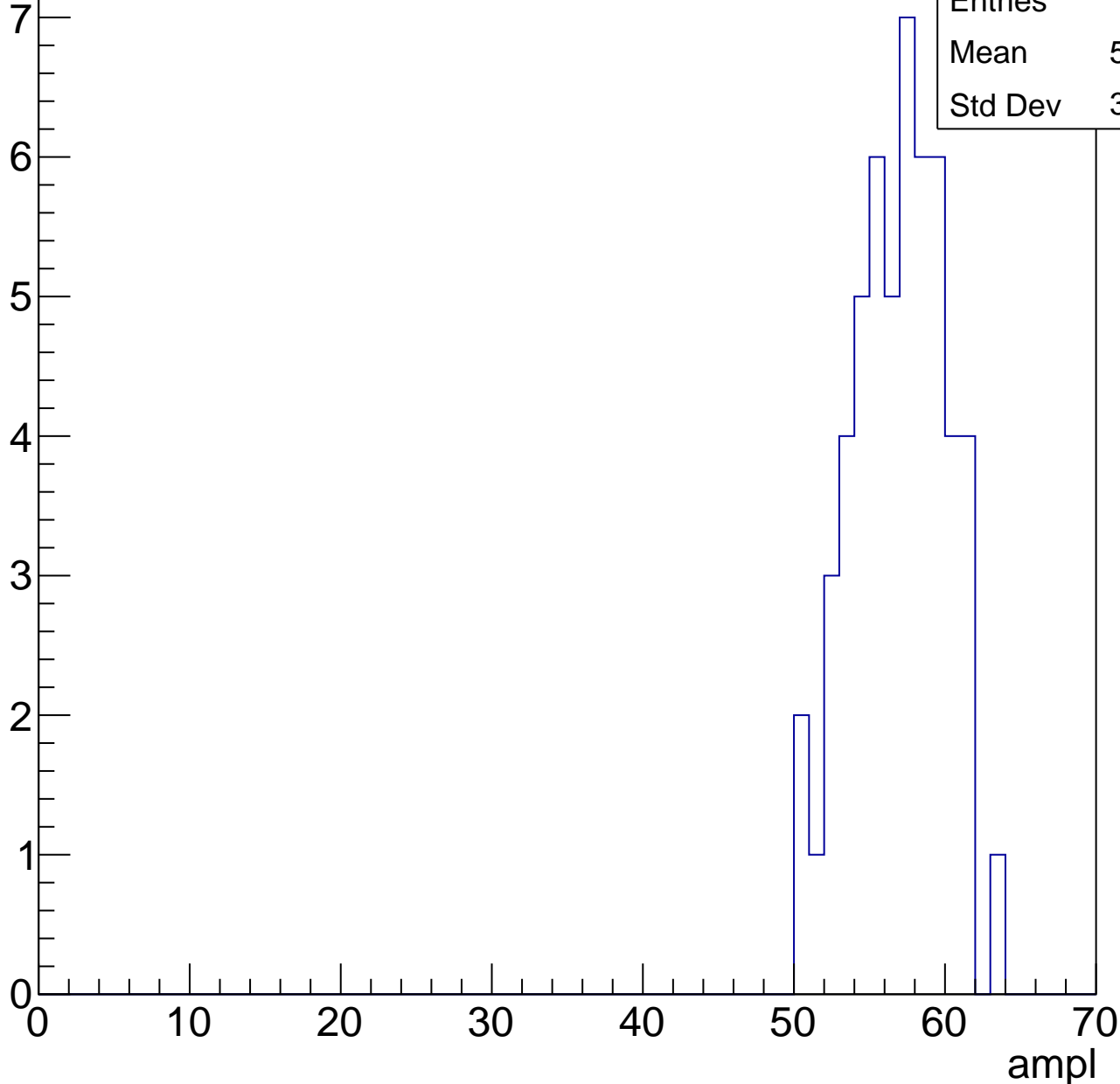
Entries	75
Mean	49.87
Std Dev	3.403



B1L102S, U11-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

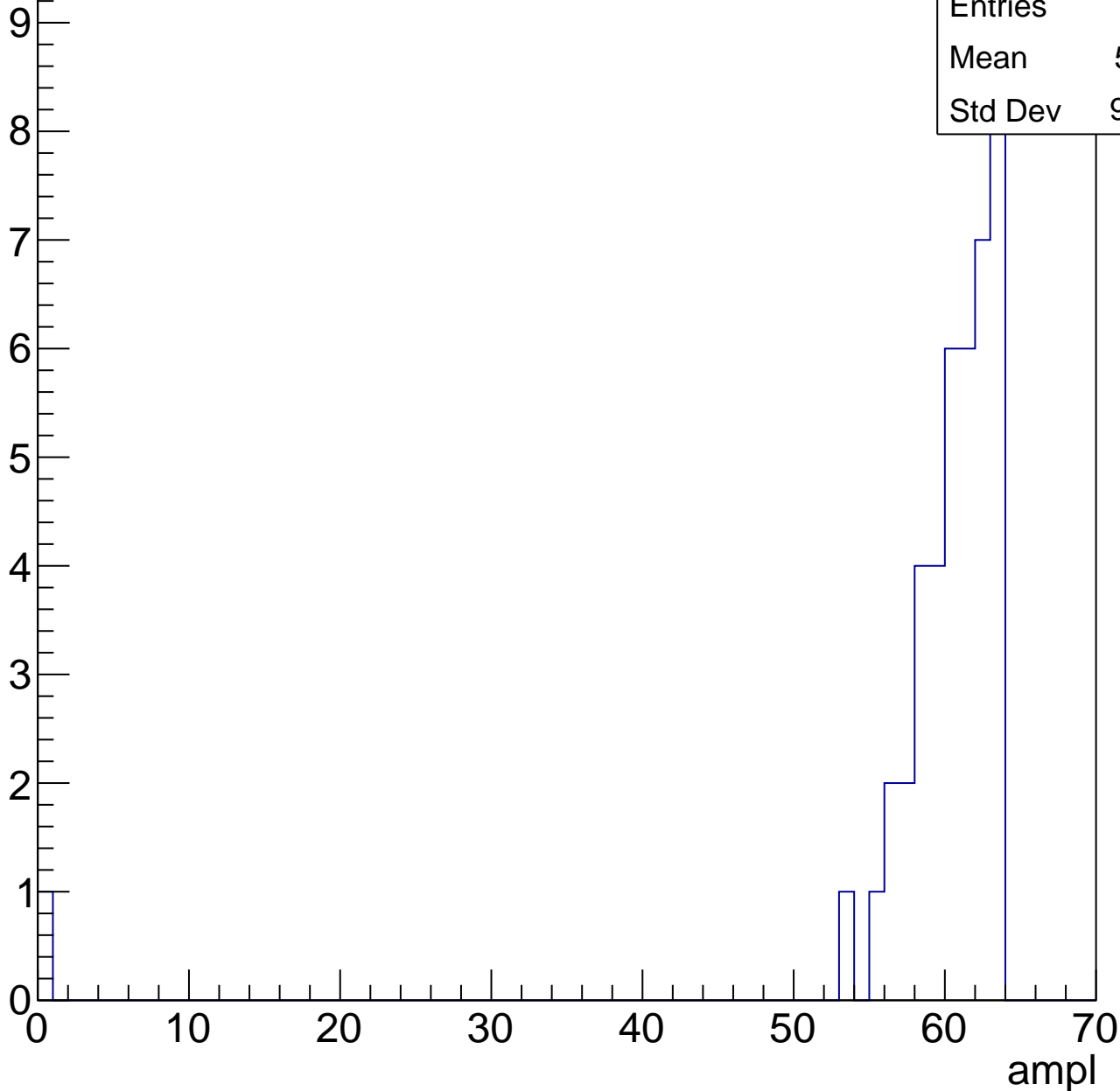


B1L102S, U11-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.81
Std Dev	9.399



B1L102S, U11-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch63, adc0

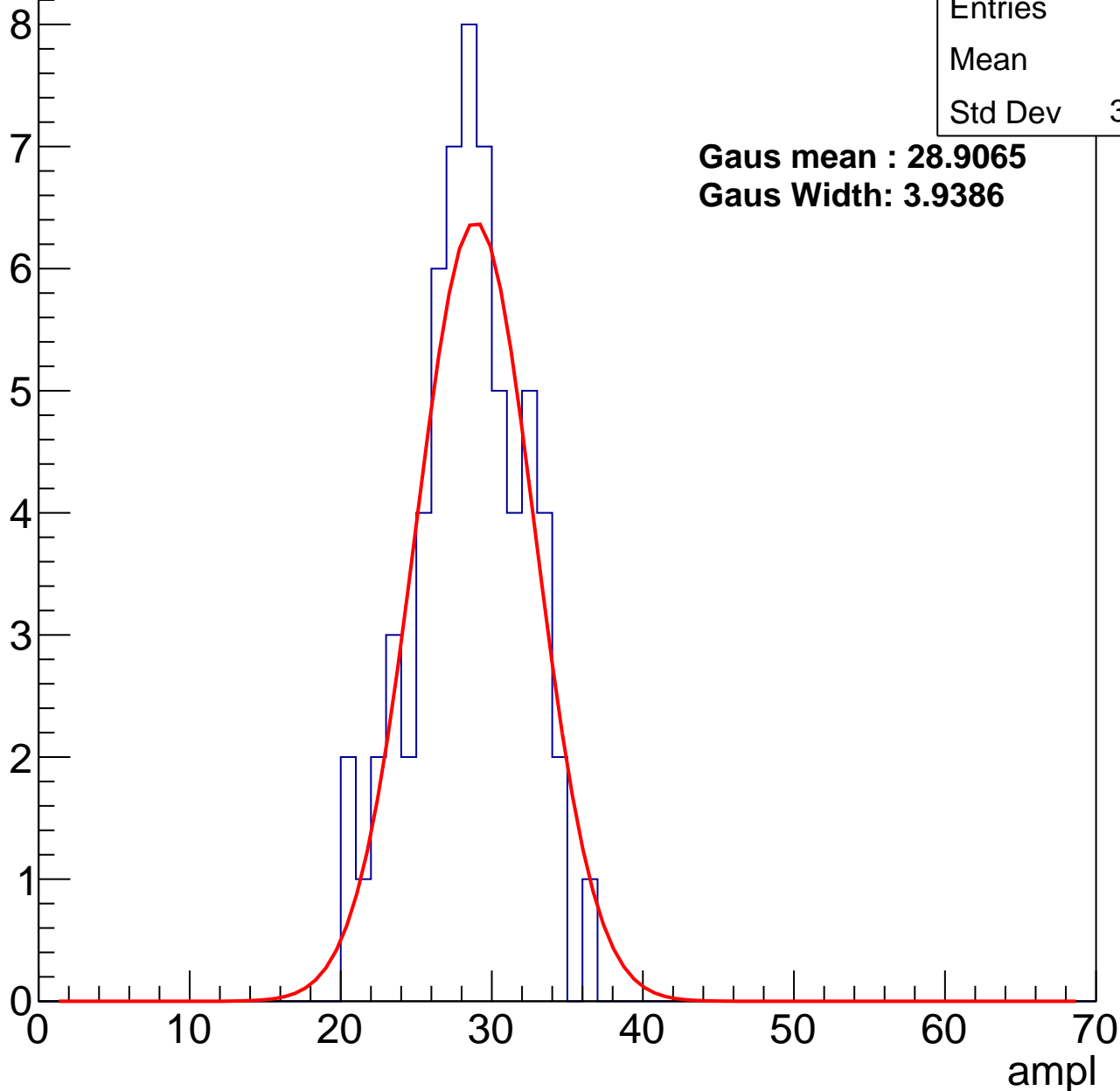
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28
Std Dev	3.568

Gaus mean : 28.9065

Gaus Width: 3.9386



B1L102S, U11-ch63, adc1

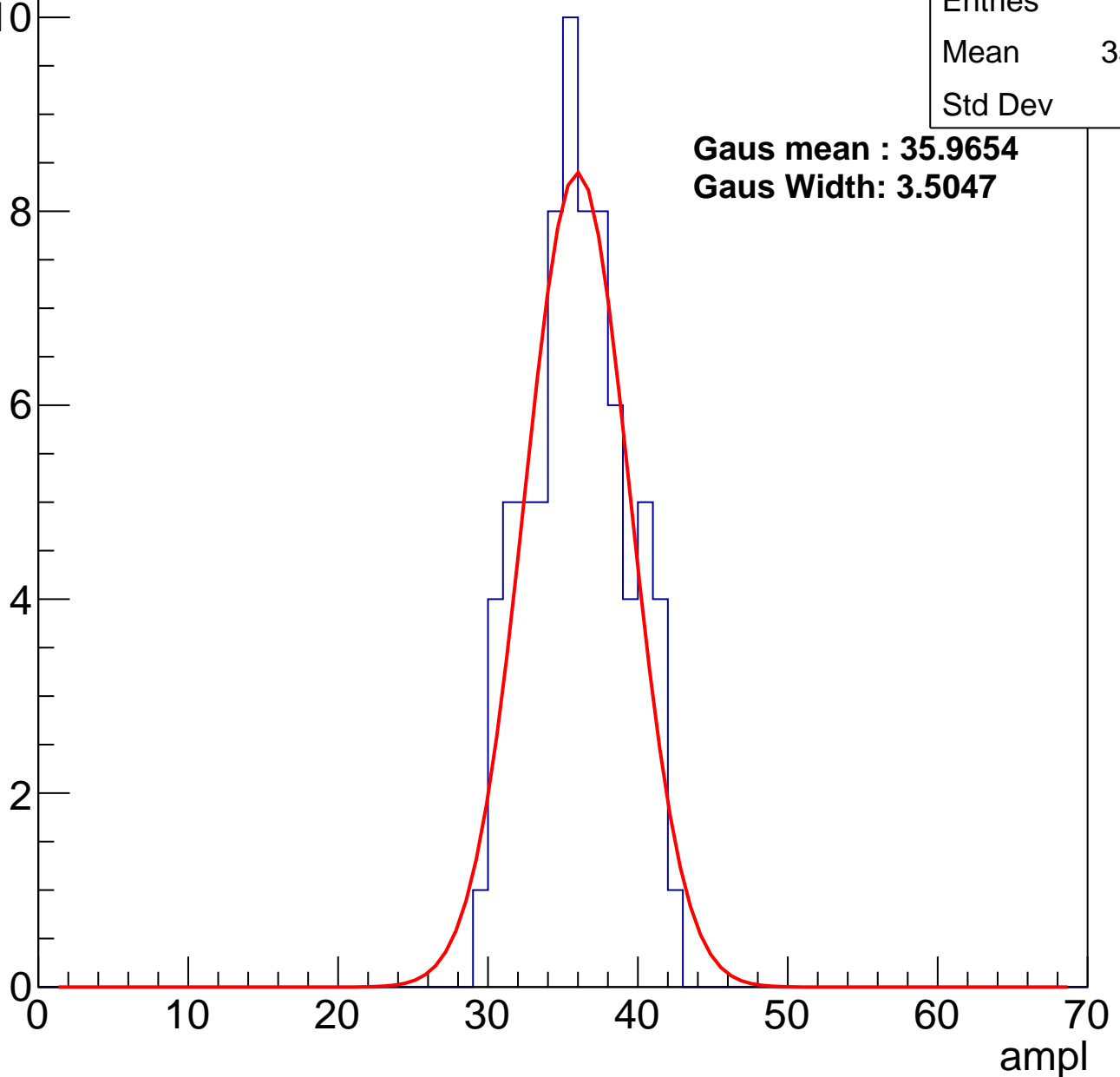
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.47
Std Dev	3.18

Gaus mean : 35.9654

Gaus Width: 3.5047



B1L102S, U11-ch63, adc2

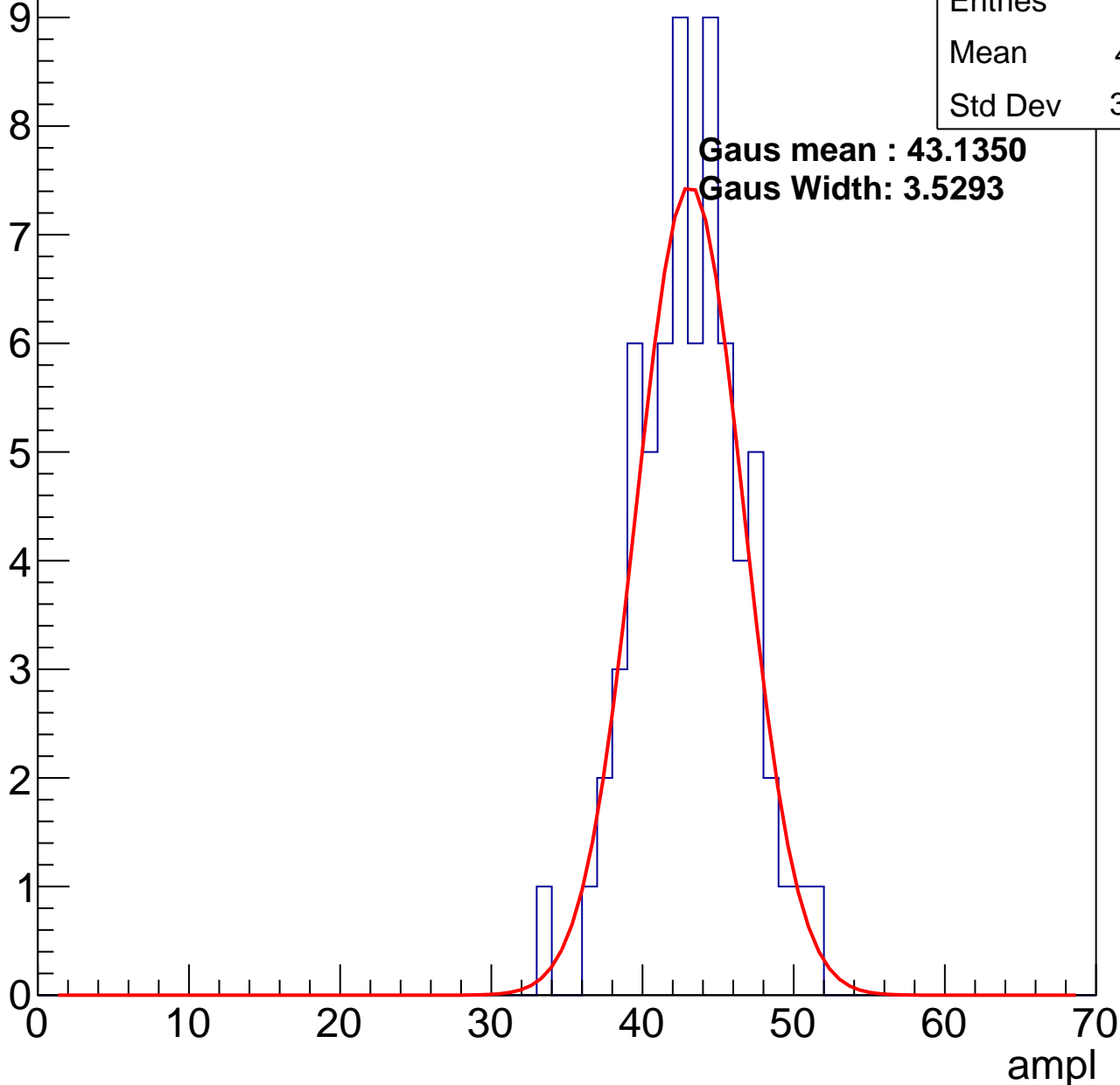
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.71
Std Dev	3.456

Gaus mean : 43.1350

Gaus Width: 3.5293

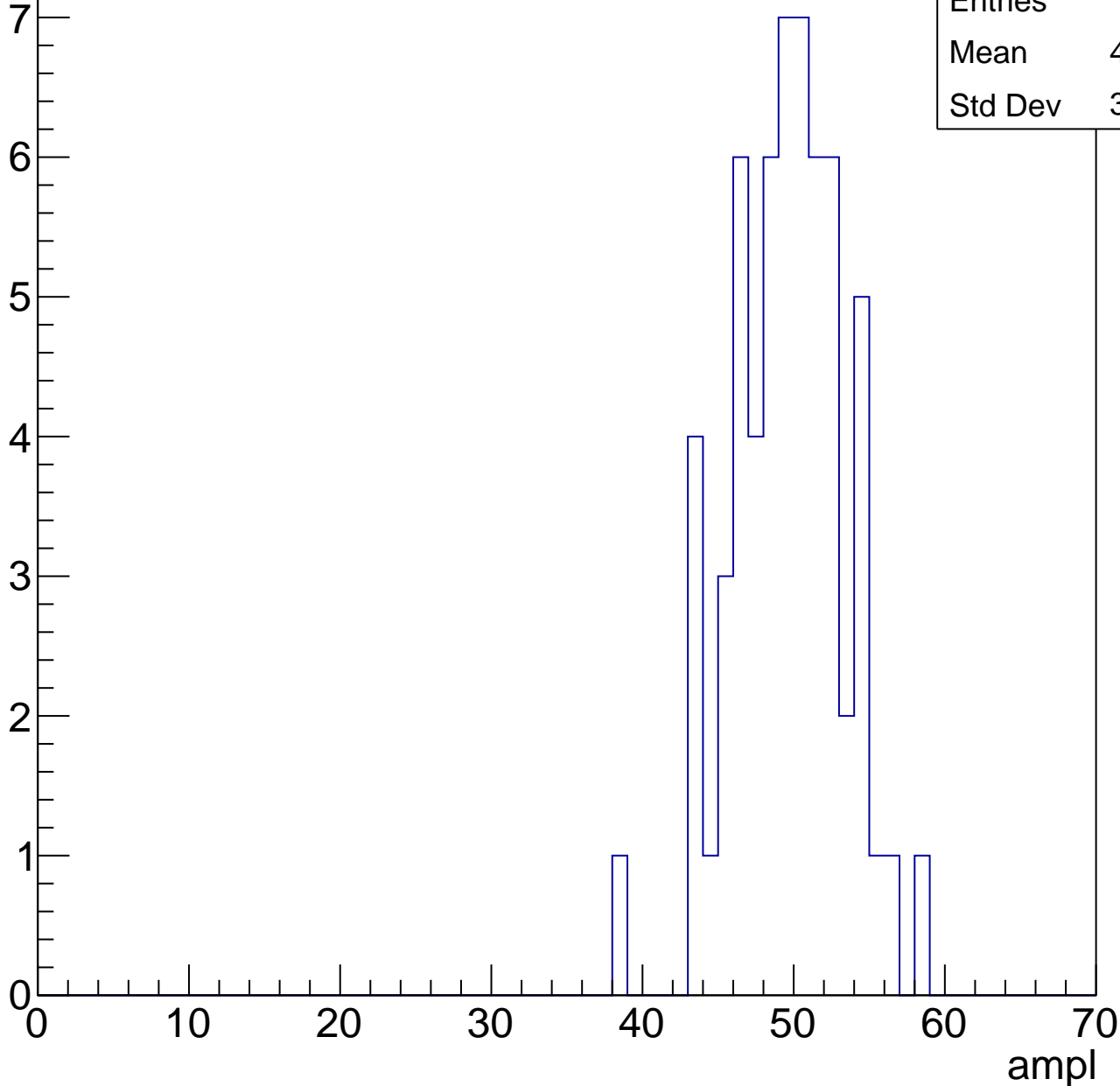


B1L102S, U11-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.13
Std Dev	3.669

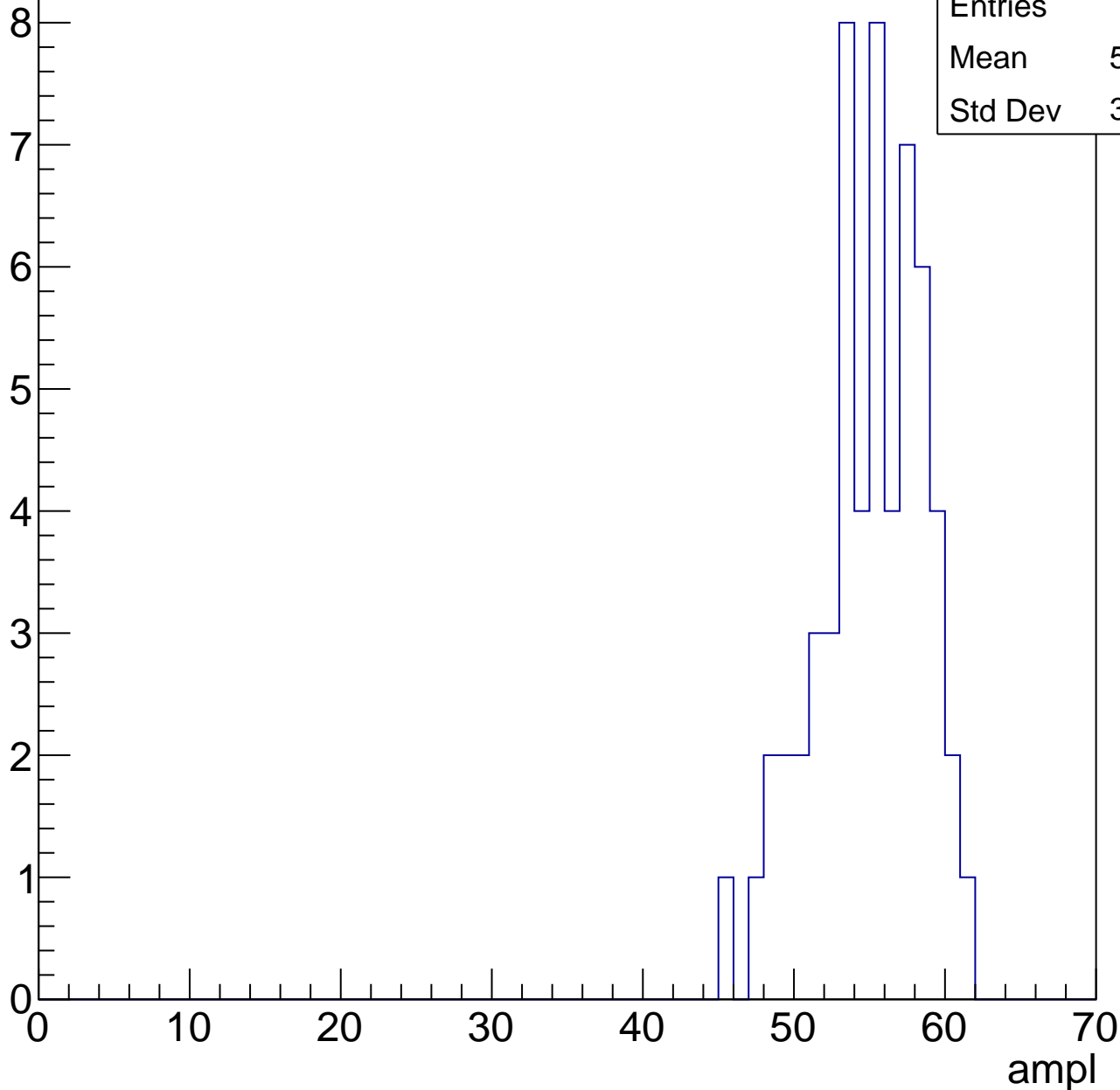


B1L102S, U11-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.53
Std Dev	3.519



B1L102S, U11-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

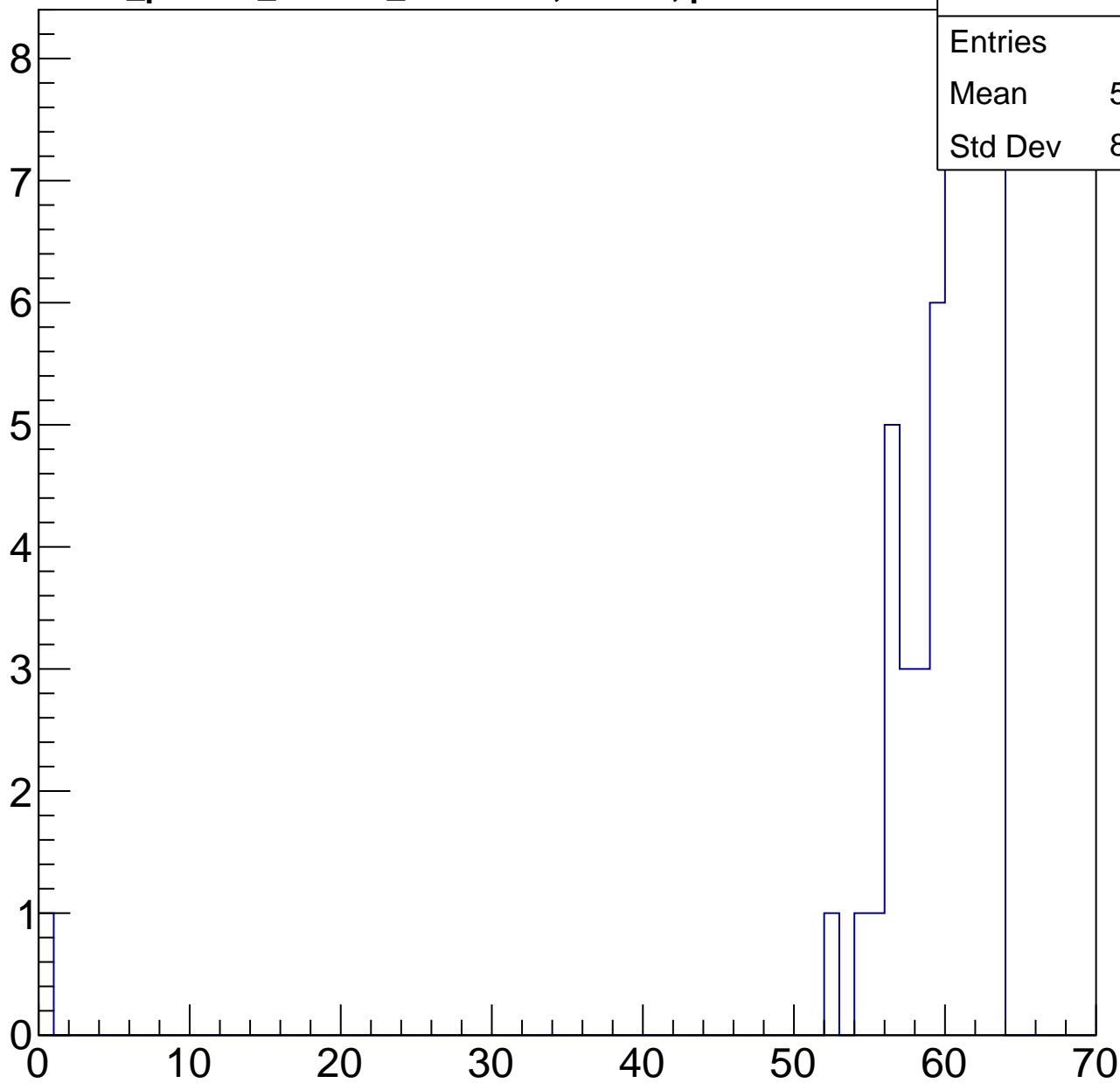
Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.64
Std Dev	8.539

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch64, adc0

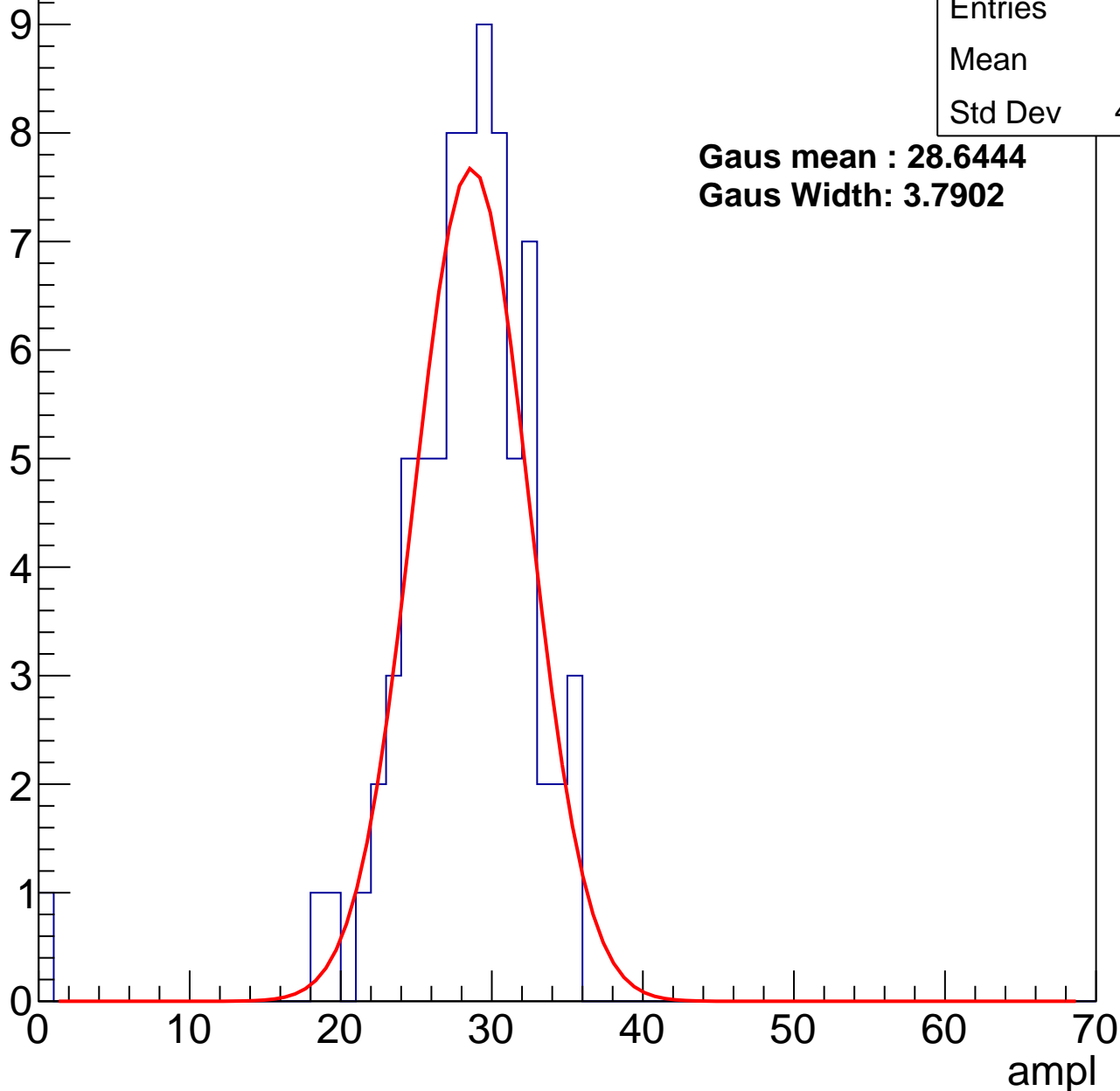
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.7
Std Dev	4.831

Gaus mean : 28.6444

Gaus Width: 3.7902



B1L102S, U11-ch64, adc1

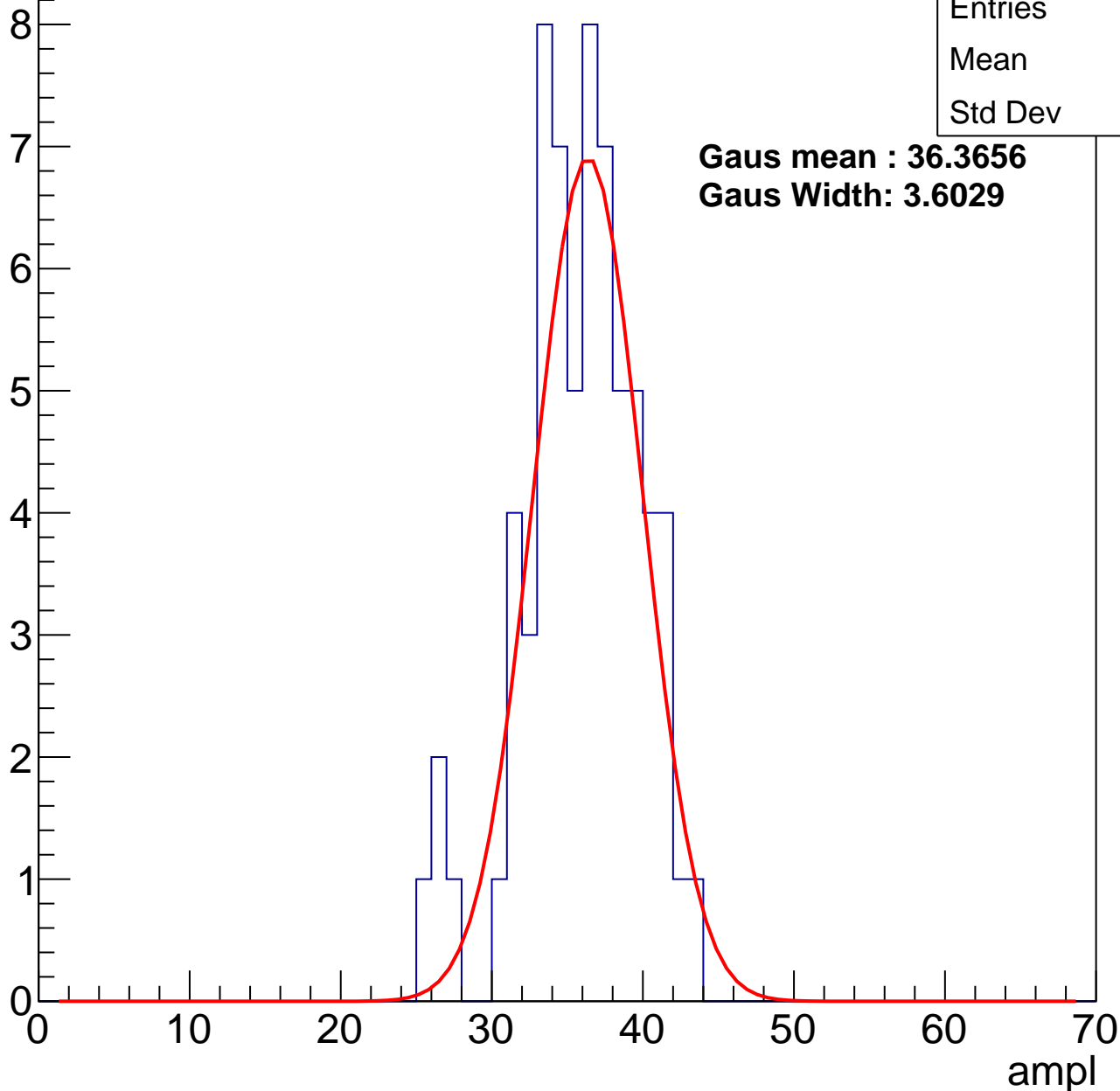
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.4
Std Dev	3.84

Gaus mean : 36.3656

Gaus Width: 3.6029



B1L102S, U11-ch64, adc2

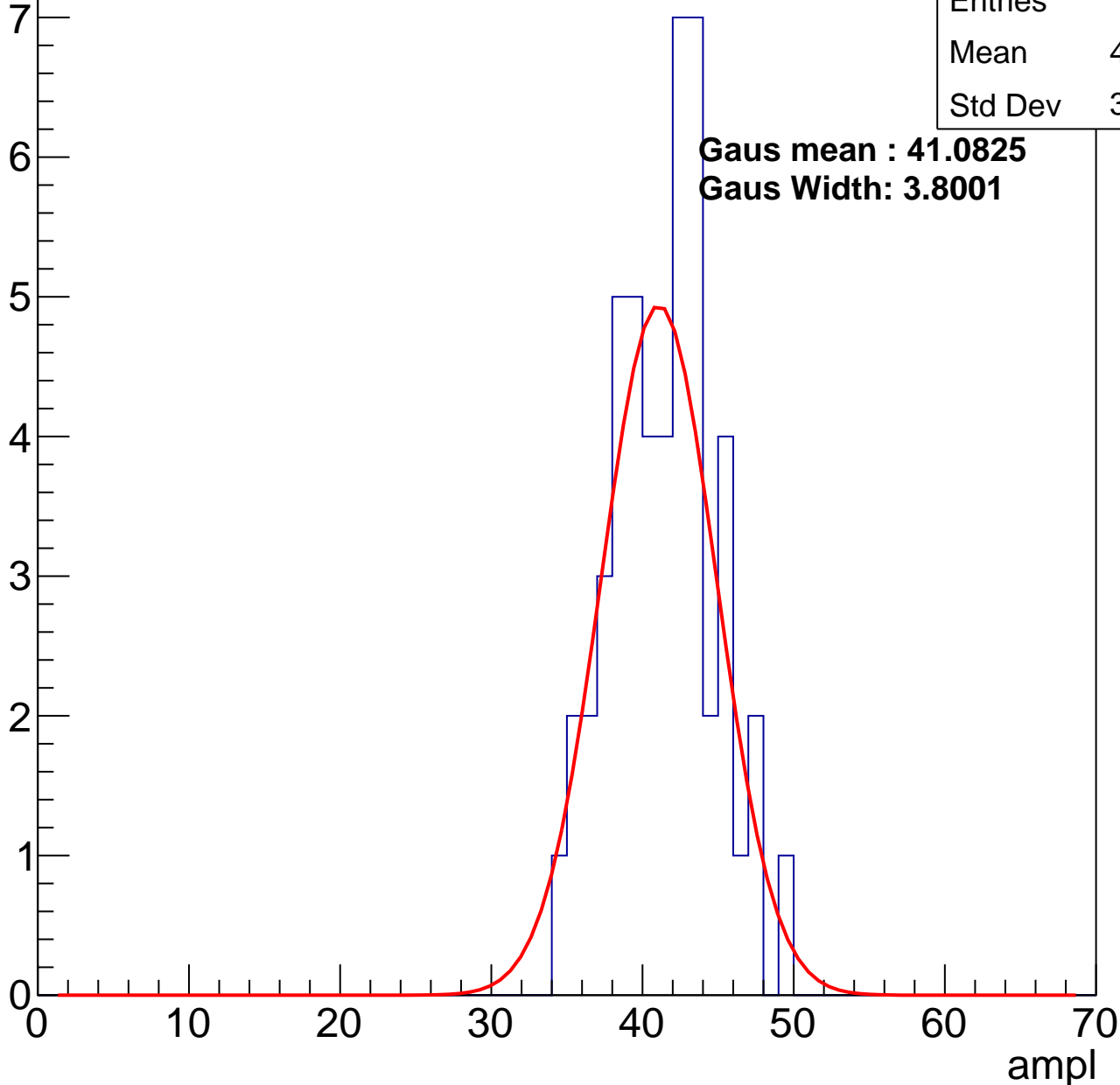
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	40.96
Std Dev	3.364

Gaus mean : 41.0825

Gaus Width: 3.8001

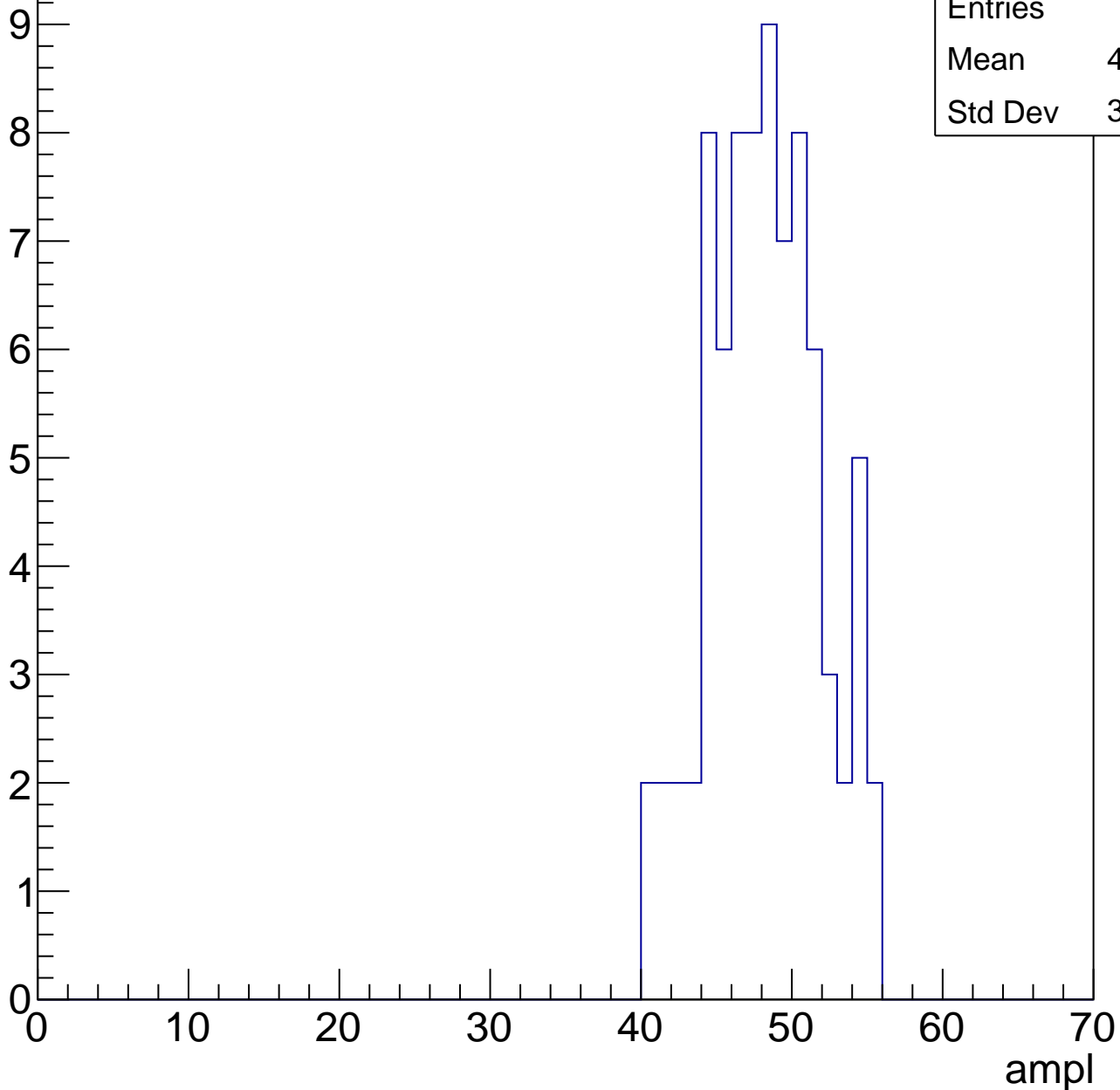


B1L102S, U11-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	47.76
Std Dev	3.596

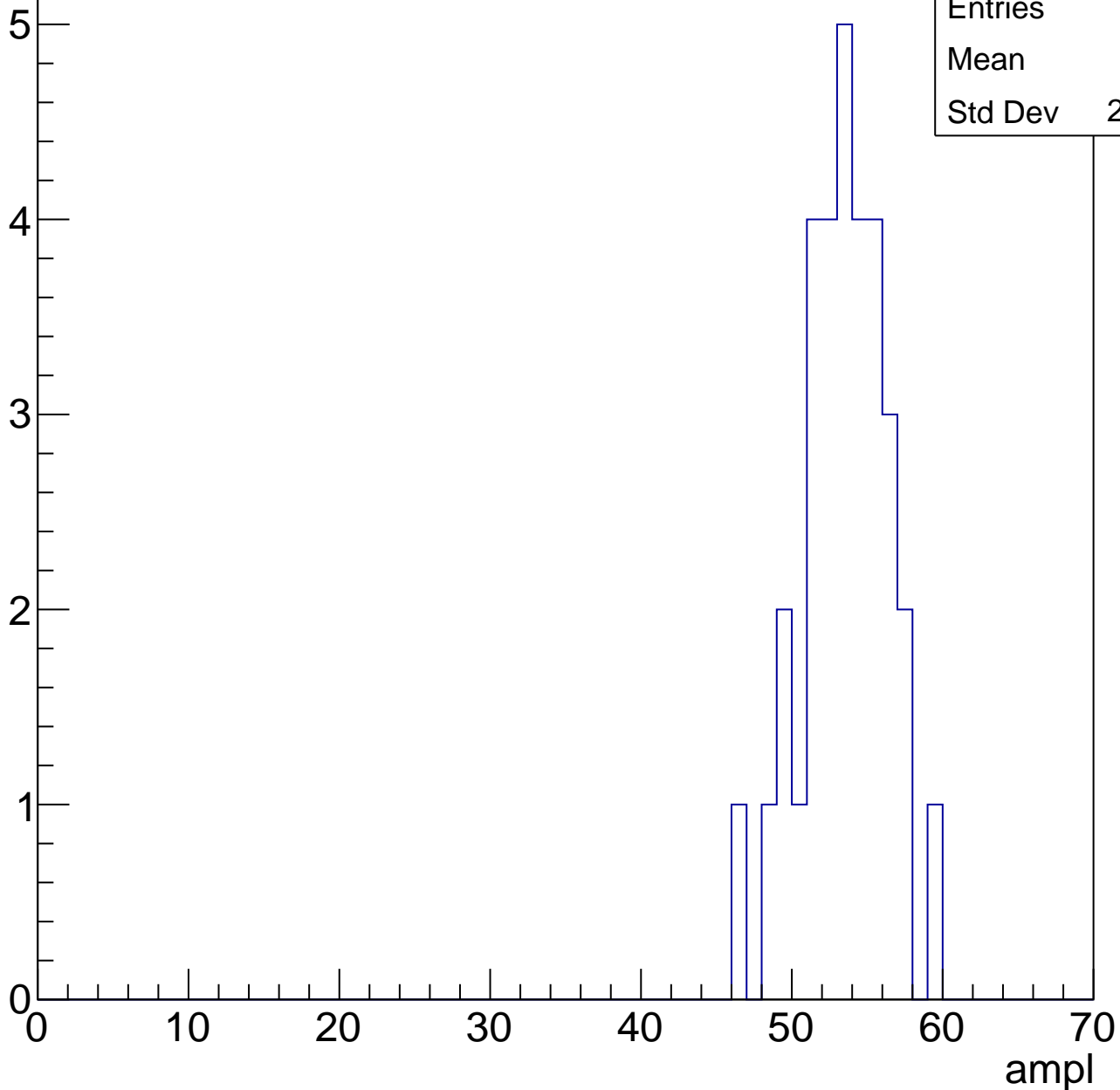


B1L102S, U11-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	53
Std Dev	2.795

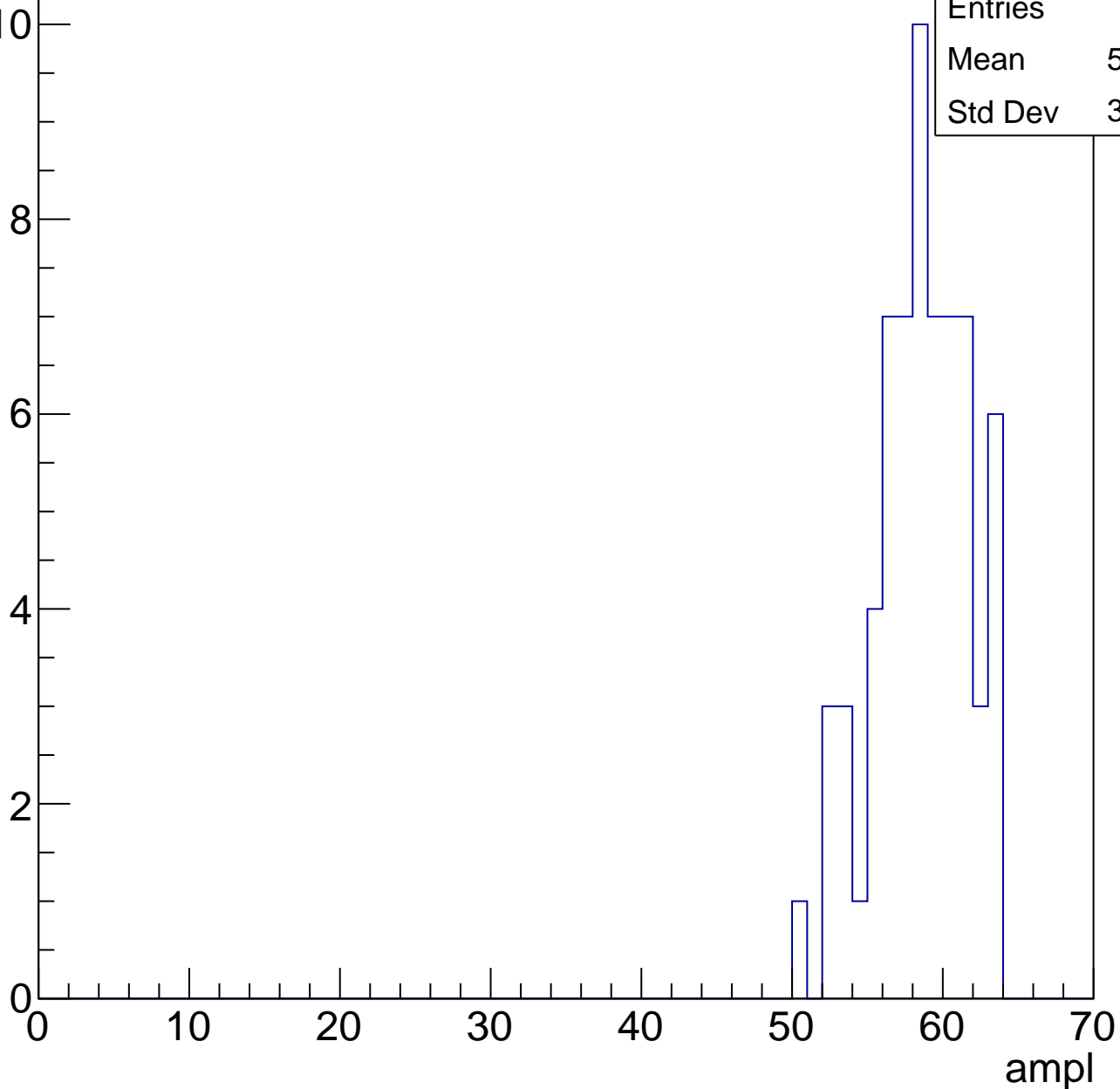


B1L102S, U11-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	58.09
Std Dev	3.088

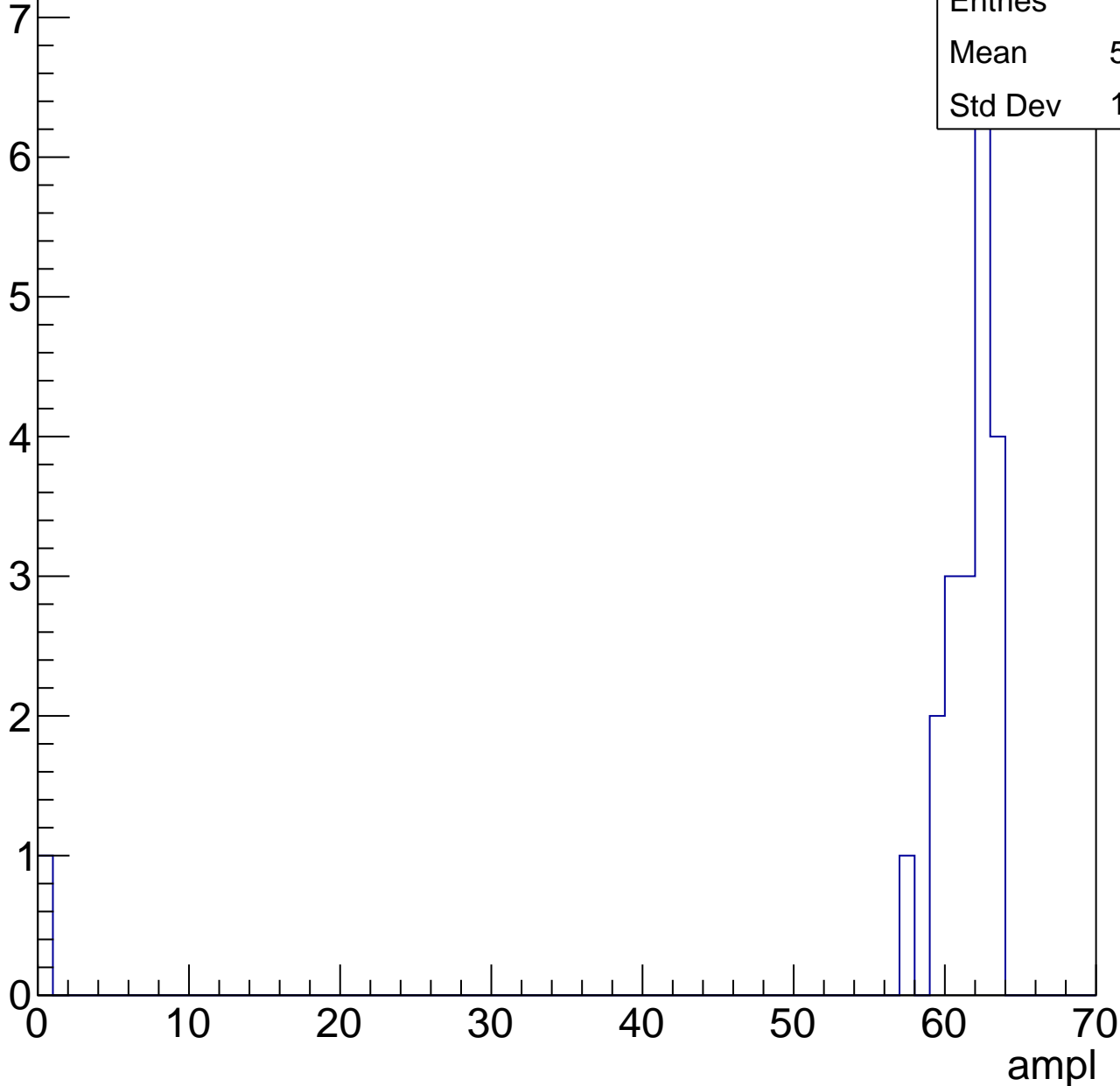


B1L102S, U11-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	58.29
Std Dev	13.12



B1L102S, U11-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch65, adc0

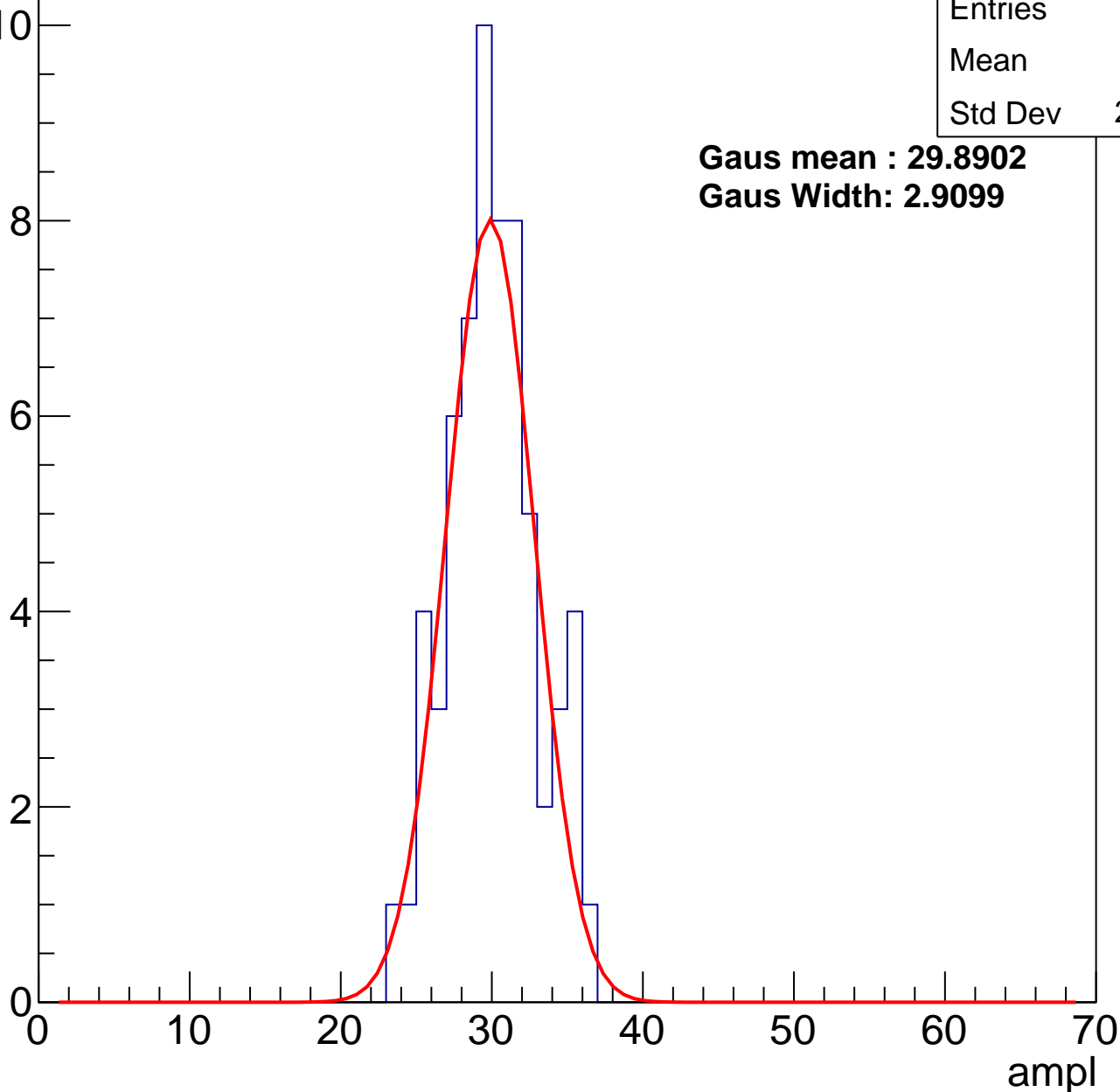
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.6
Std Dev	2.941

Gaus mean : 29.8902

Gaus Width: 2.9099



B1L102S, U11-ch65, adc1

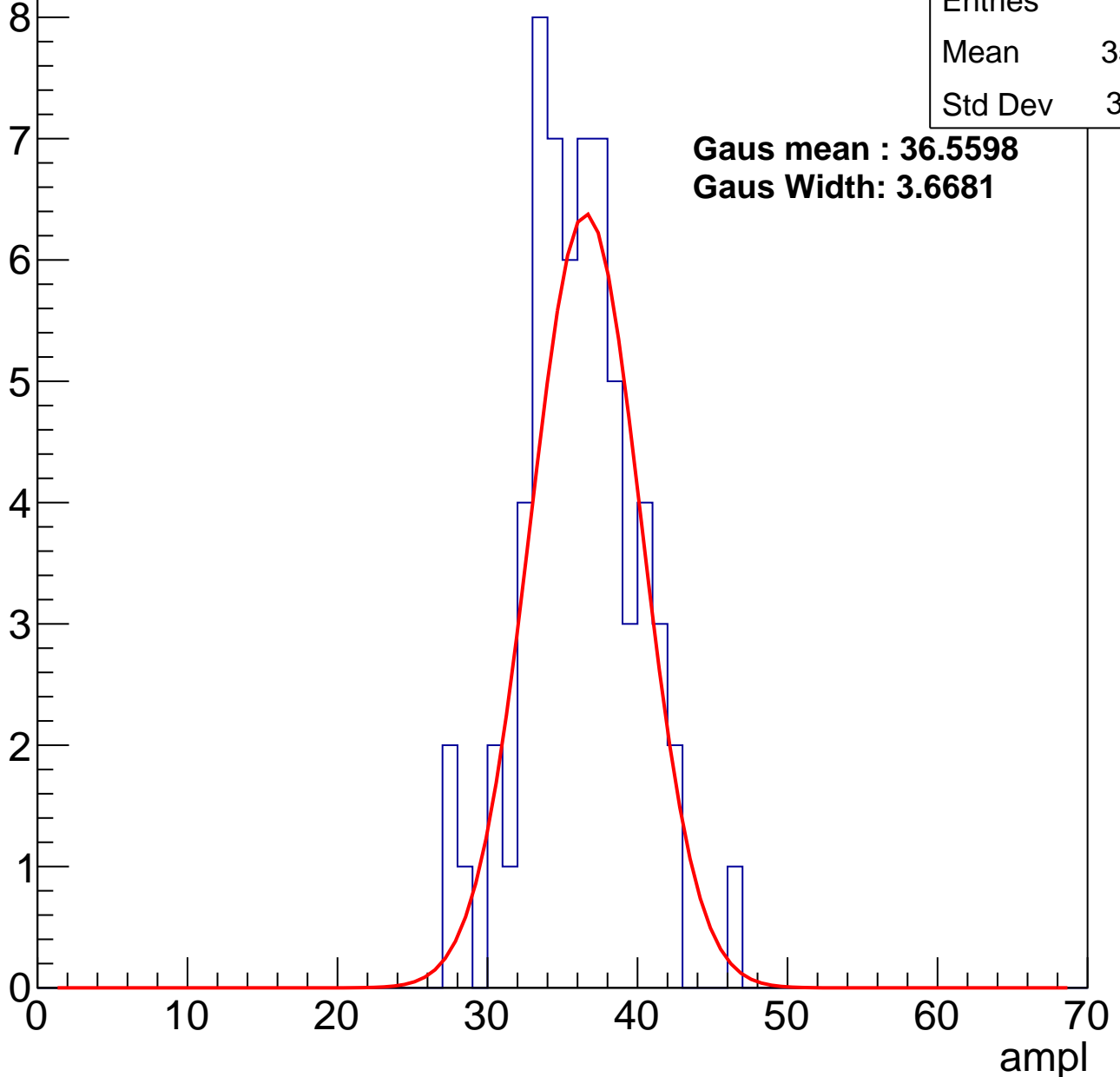
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.62
Std Dev	3.671

Gaus mean : 36.5598

Gaus Width: 3.6681



B1L102S, U11-ch65, adc2

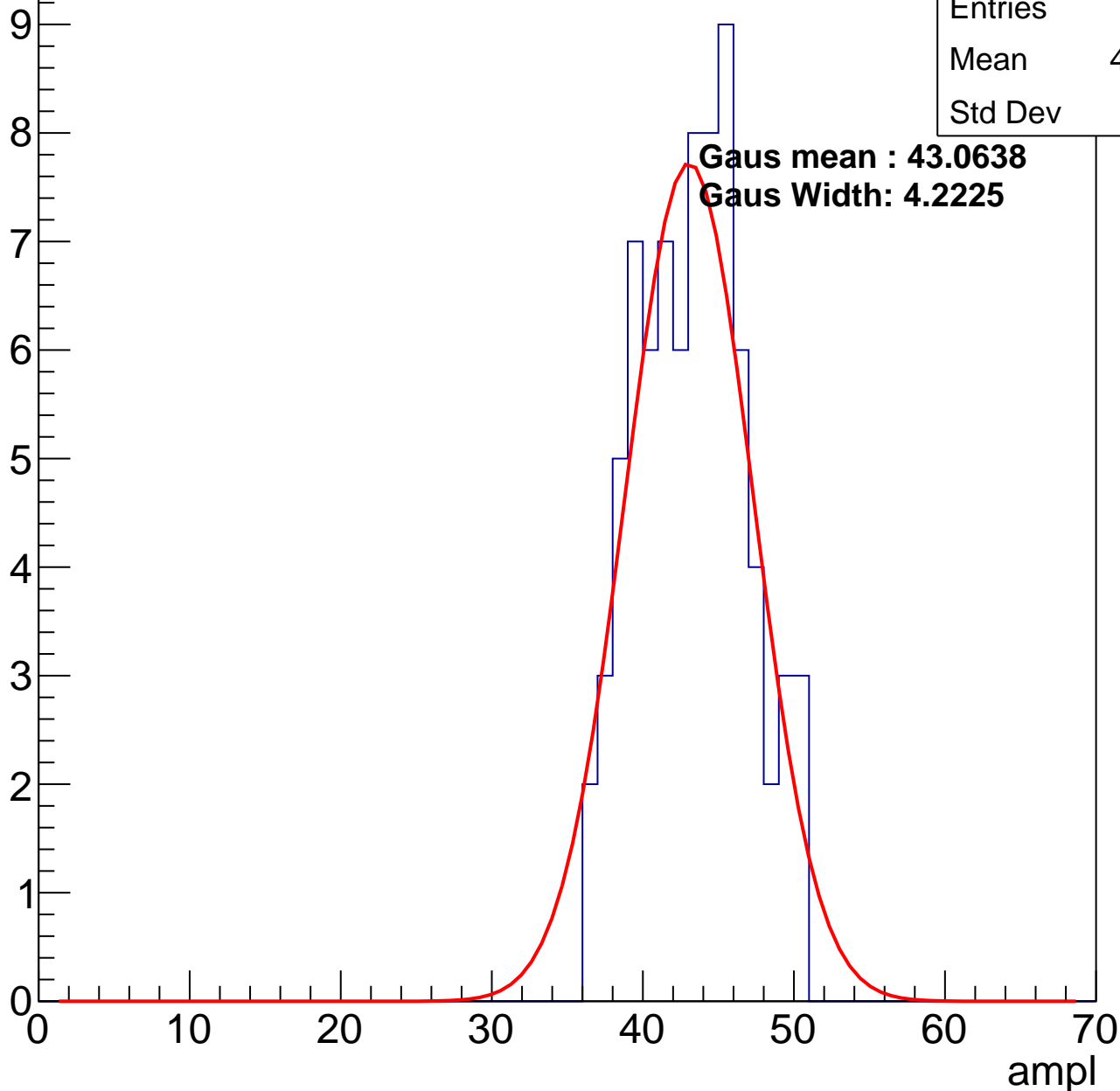
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	42.82
Std Dev	3.55

Gaus mean : 43.0638

Gaus Width: 4.2225

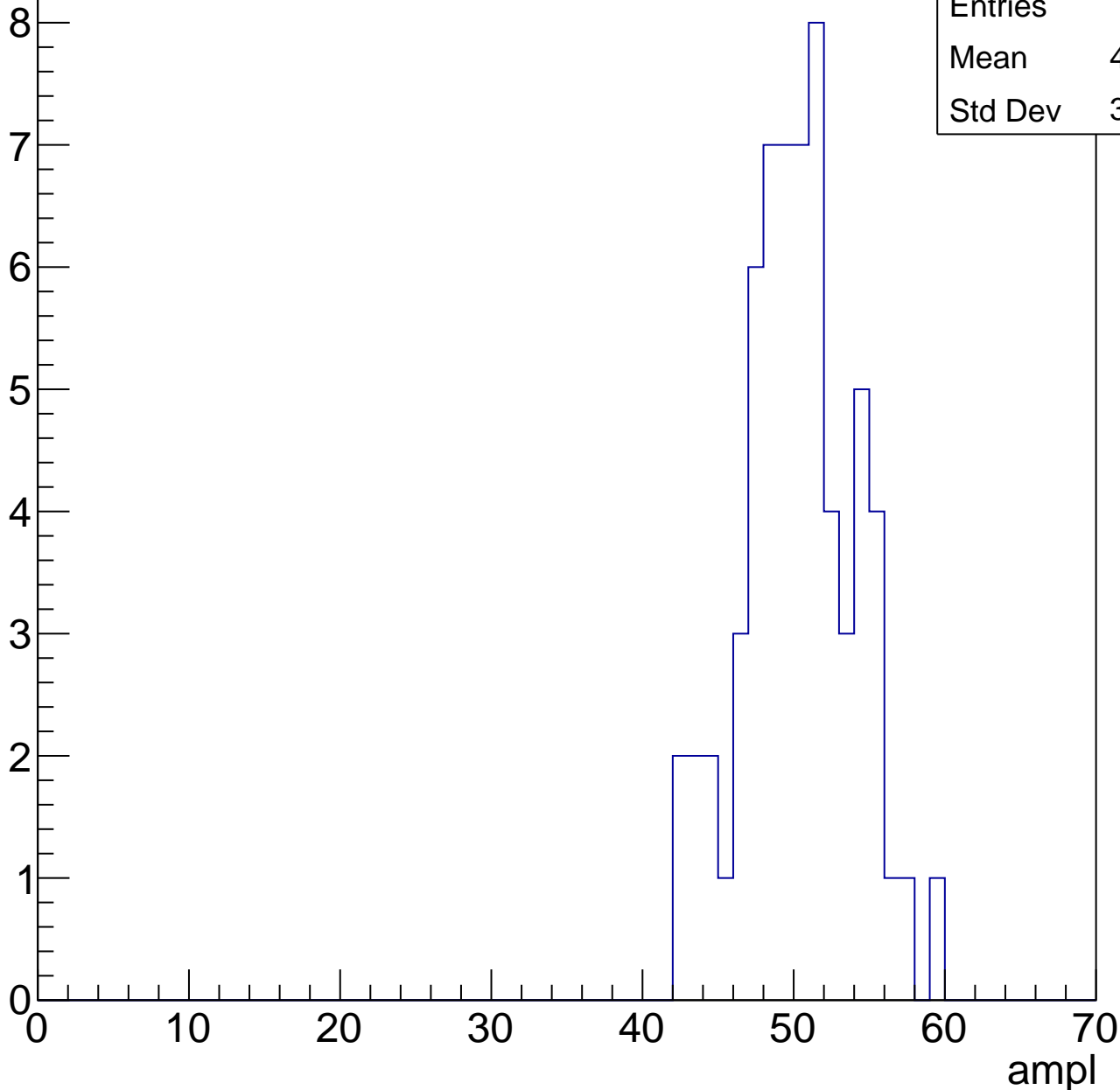


B1L102S, U11-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.83
Std Dev	3.655

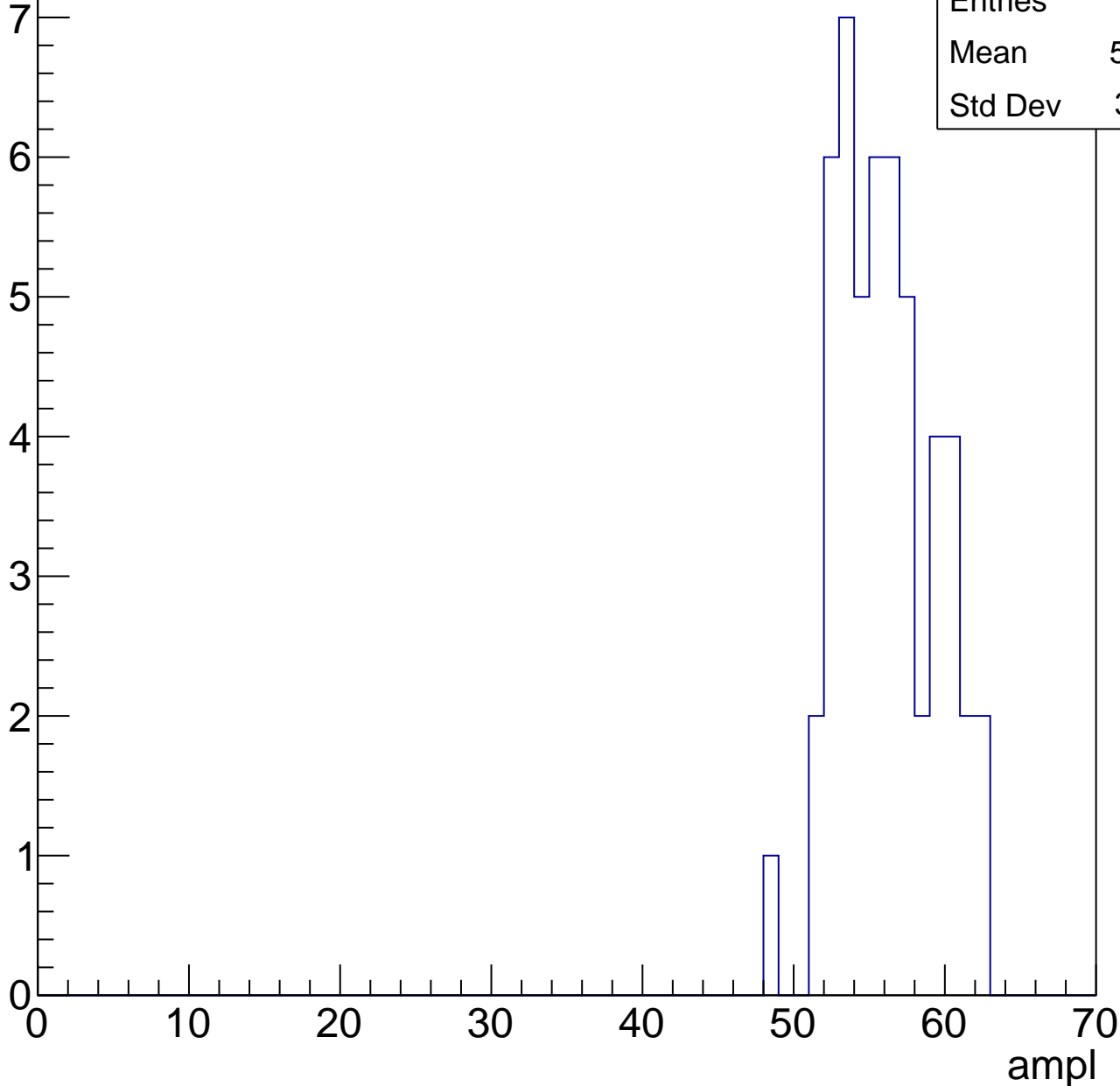


B1L102S, U11-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.62
Std Dev	3.181



B1L102S, U11-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59
Std Dev	8.611

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L102S, U11-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

60

Std Dev

3

B1L102S, U11-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U11-ch66, adc0

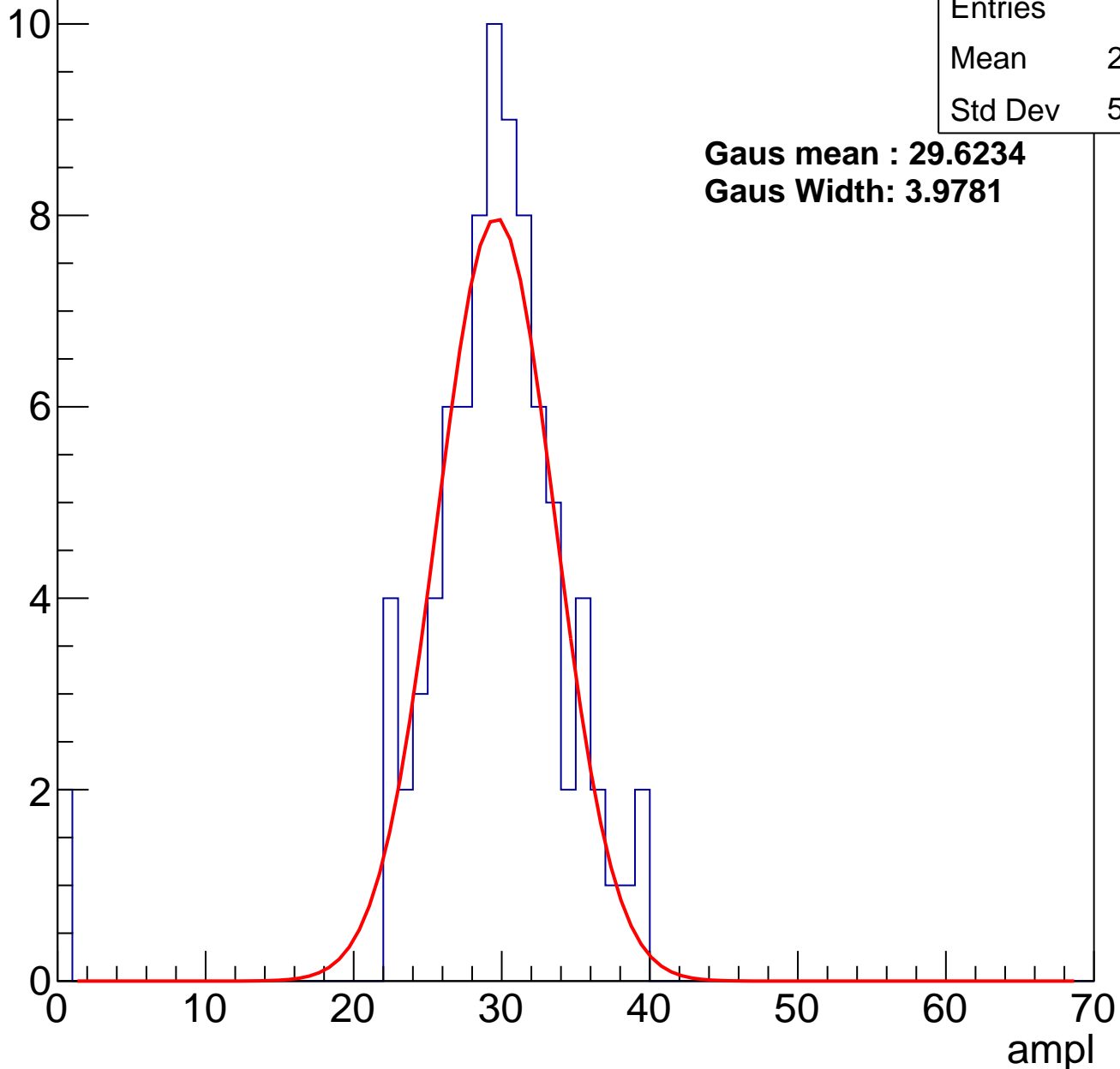
calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	28.78
Std Dev	5.924

Gaus mean : 29.6234

Gaus Width: 3.9781

Entry



B1L102S, U11-ch66, adc1

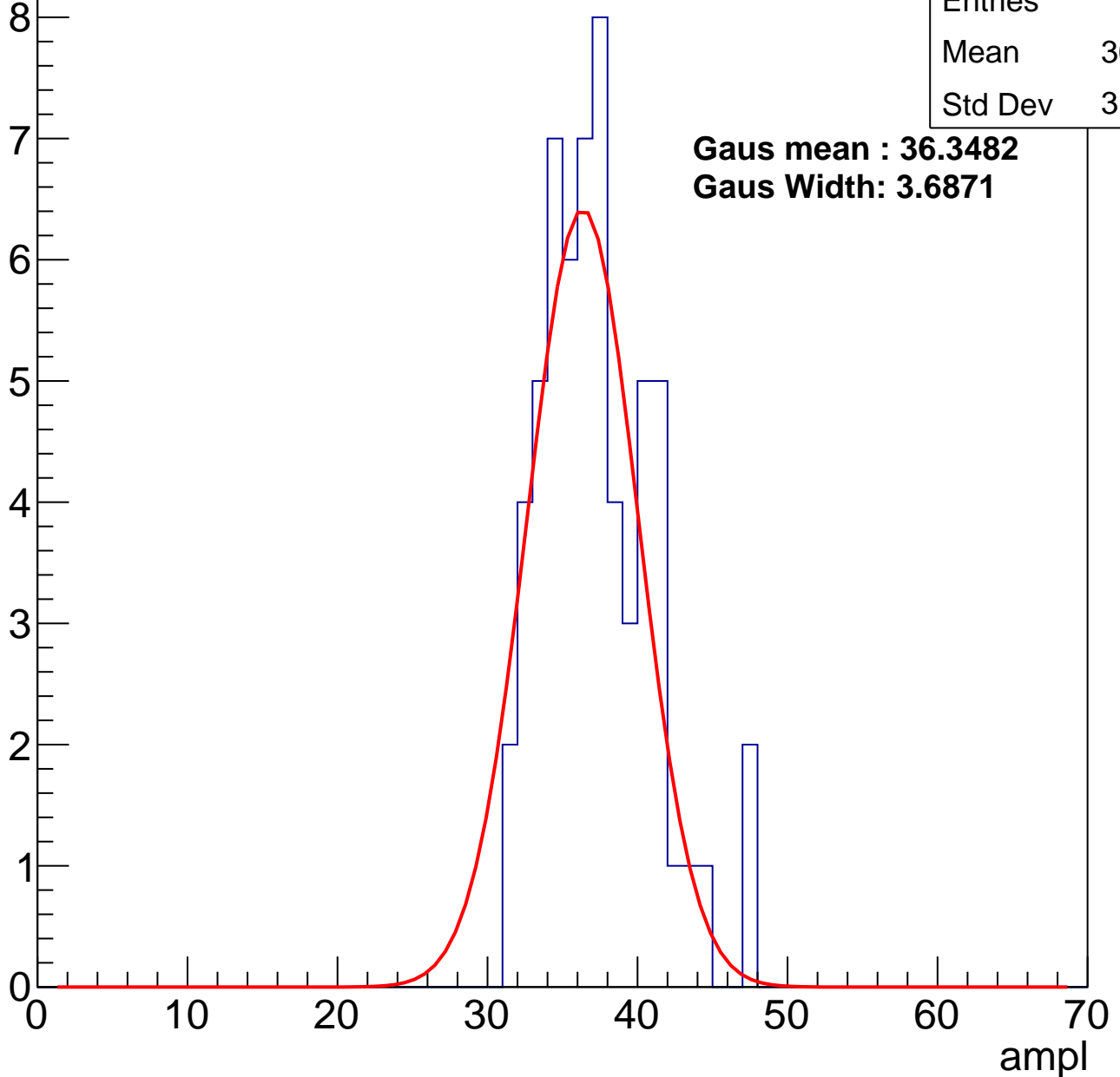
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.85
Std Dev	3.612

Gaus mean : 36.3482

Gaus Width: 3.6871



B1L102S, U11-ch66, adc2

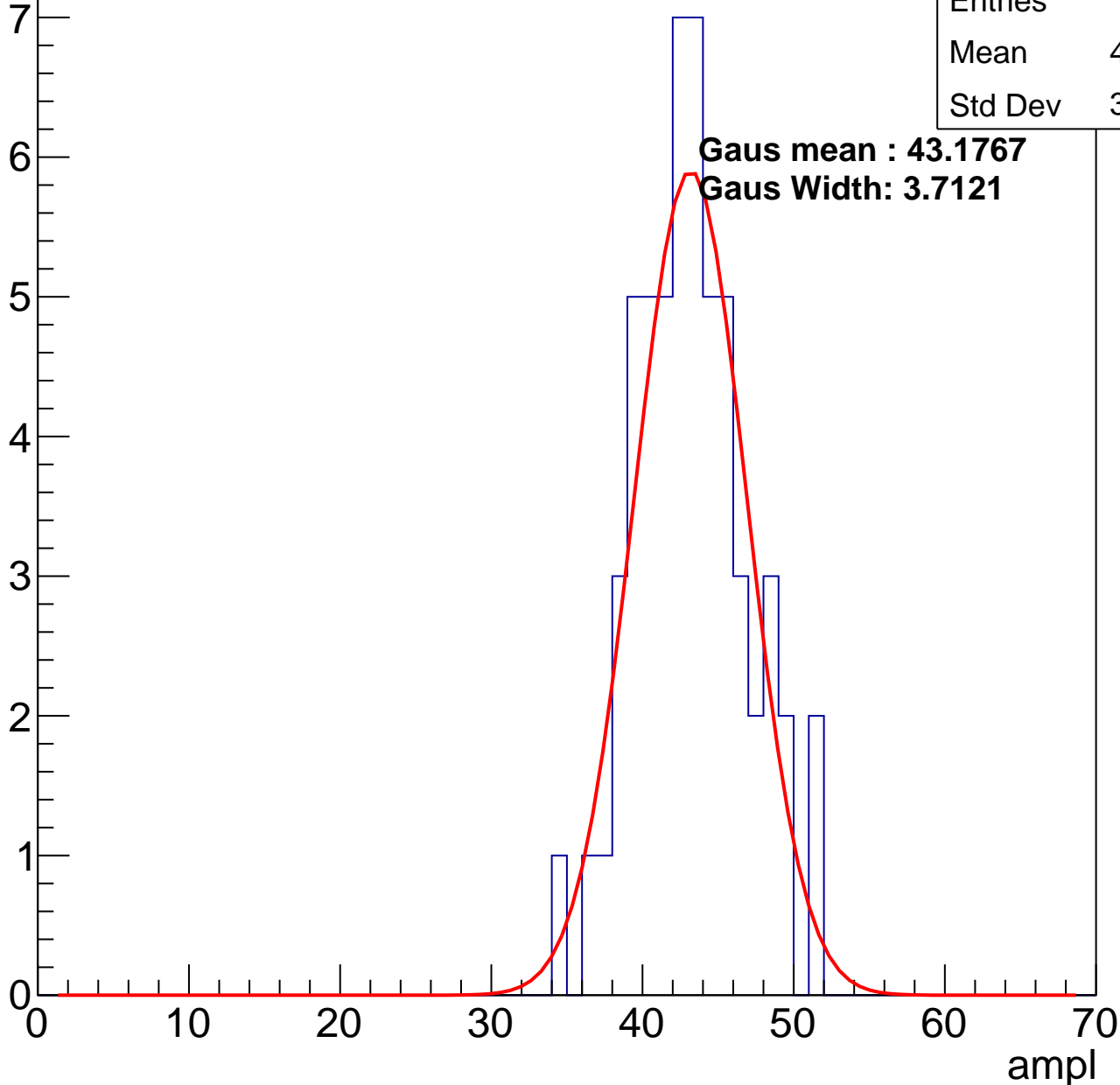
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.75
Std Dev	3.624

Gaus mean : 43.1767

Gaus Width: 3.7121

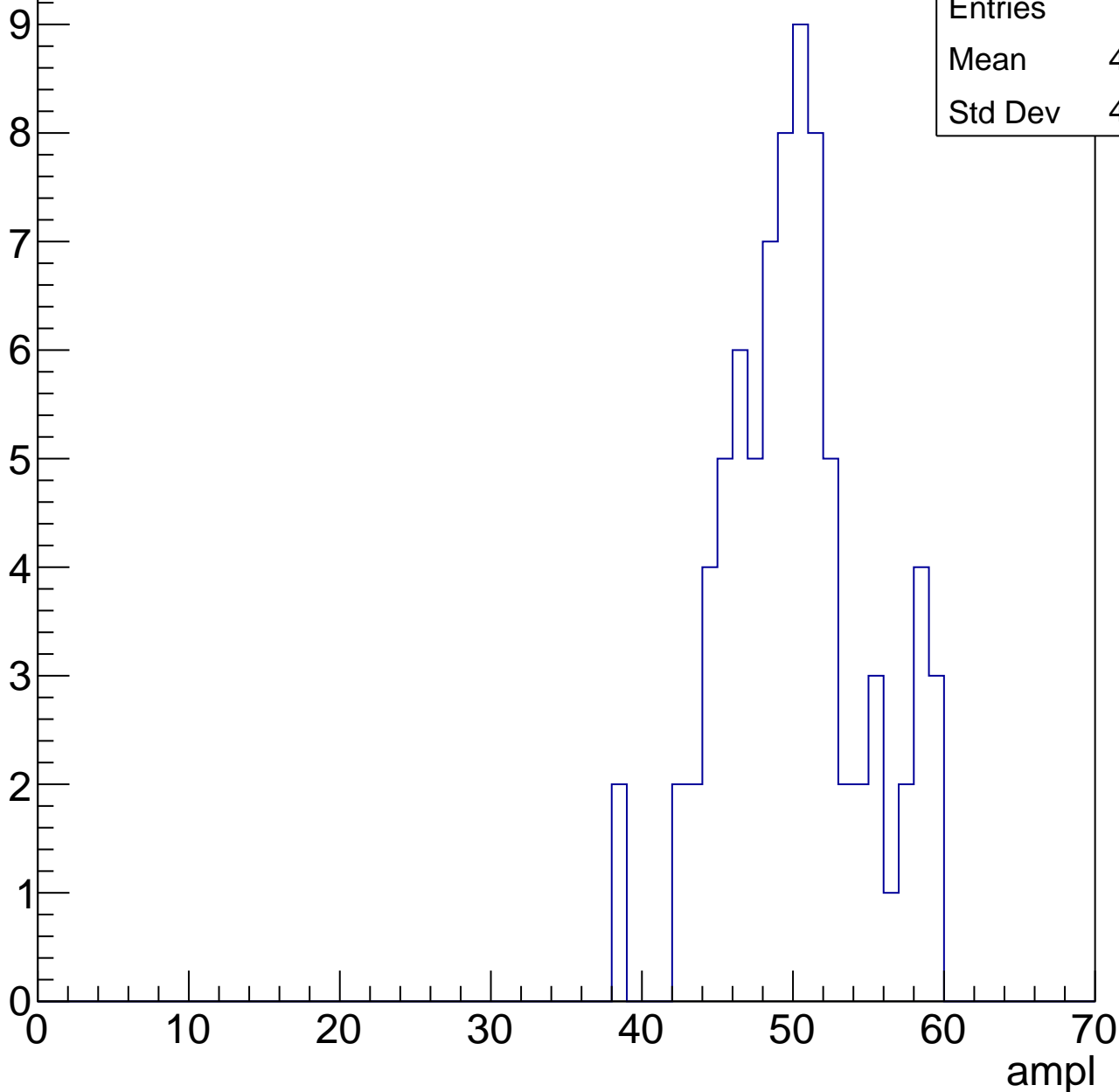


B1L102S, U11-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	49.52
Std Dev	4.648

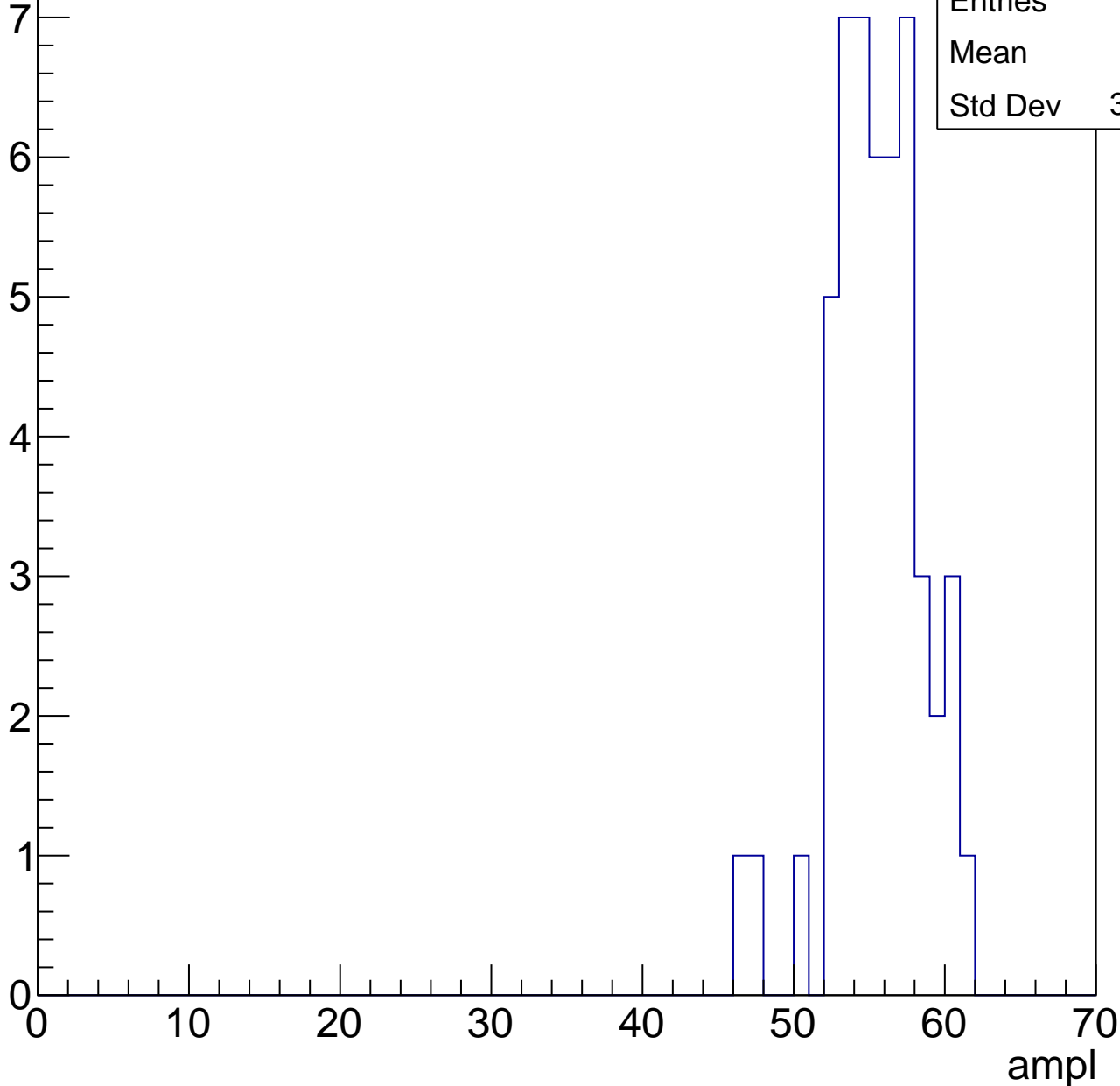


B1L102S, U11-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55
Std Dev	3.013

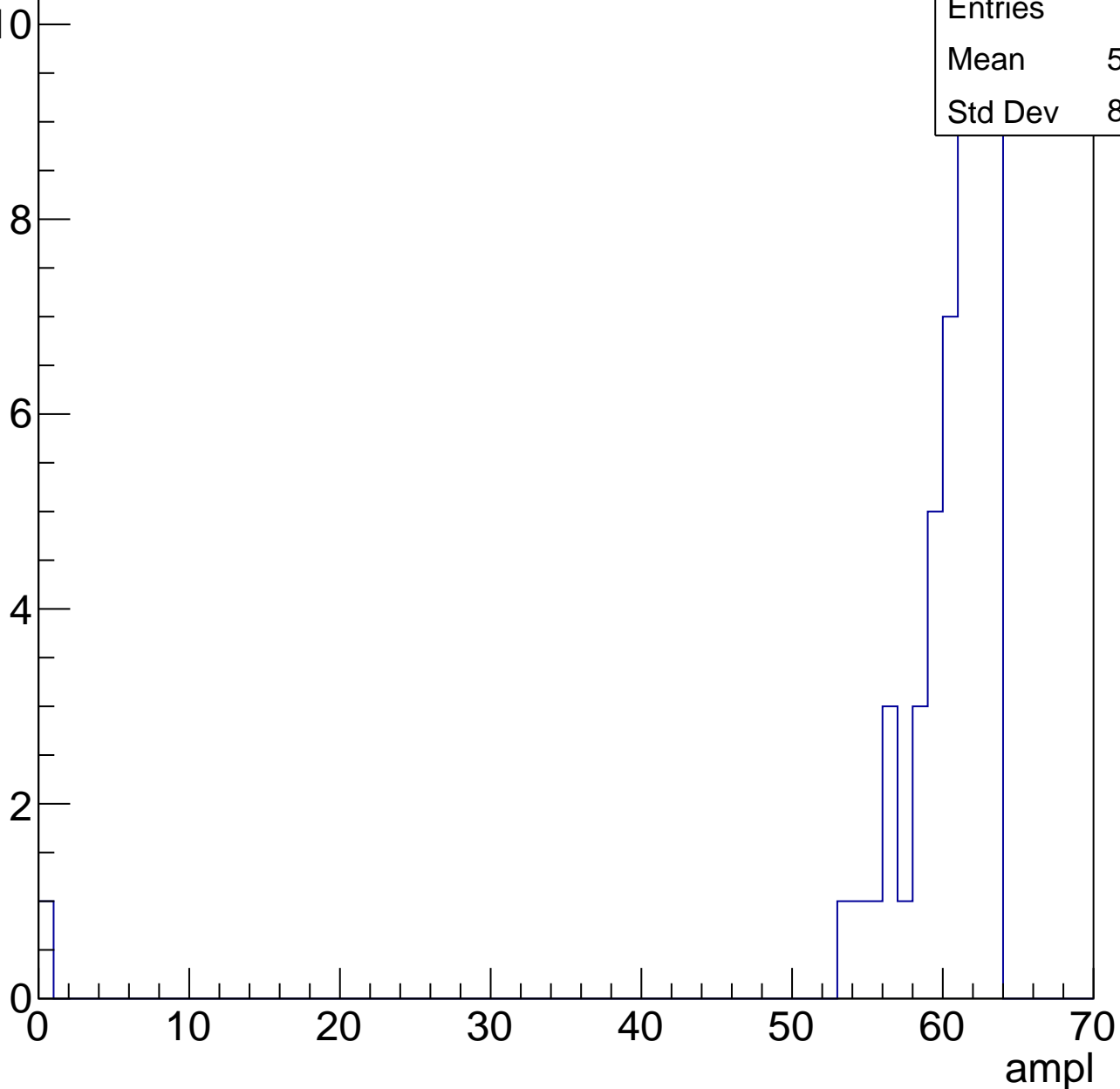


B1L102S, U11-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	59.13
Std Dev	8.643



B1L102S, U11-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch67, adc0

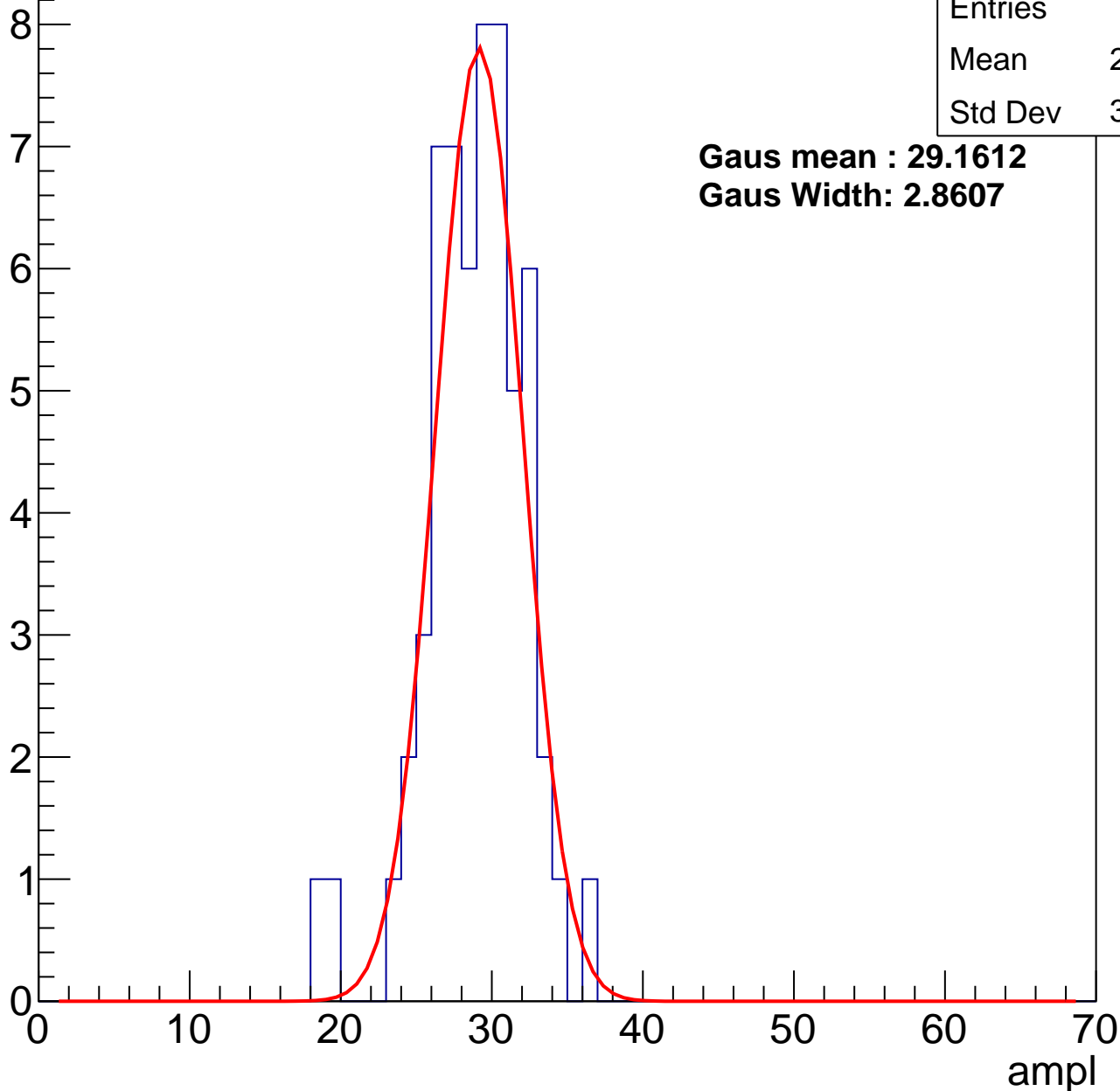
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.42
Std Dev	3.243

Gaus mean : 29.1612

Gaus Width: 2.8607



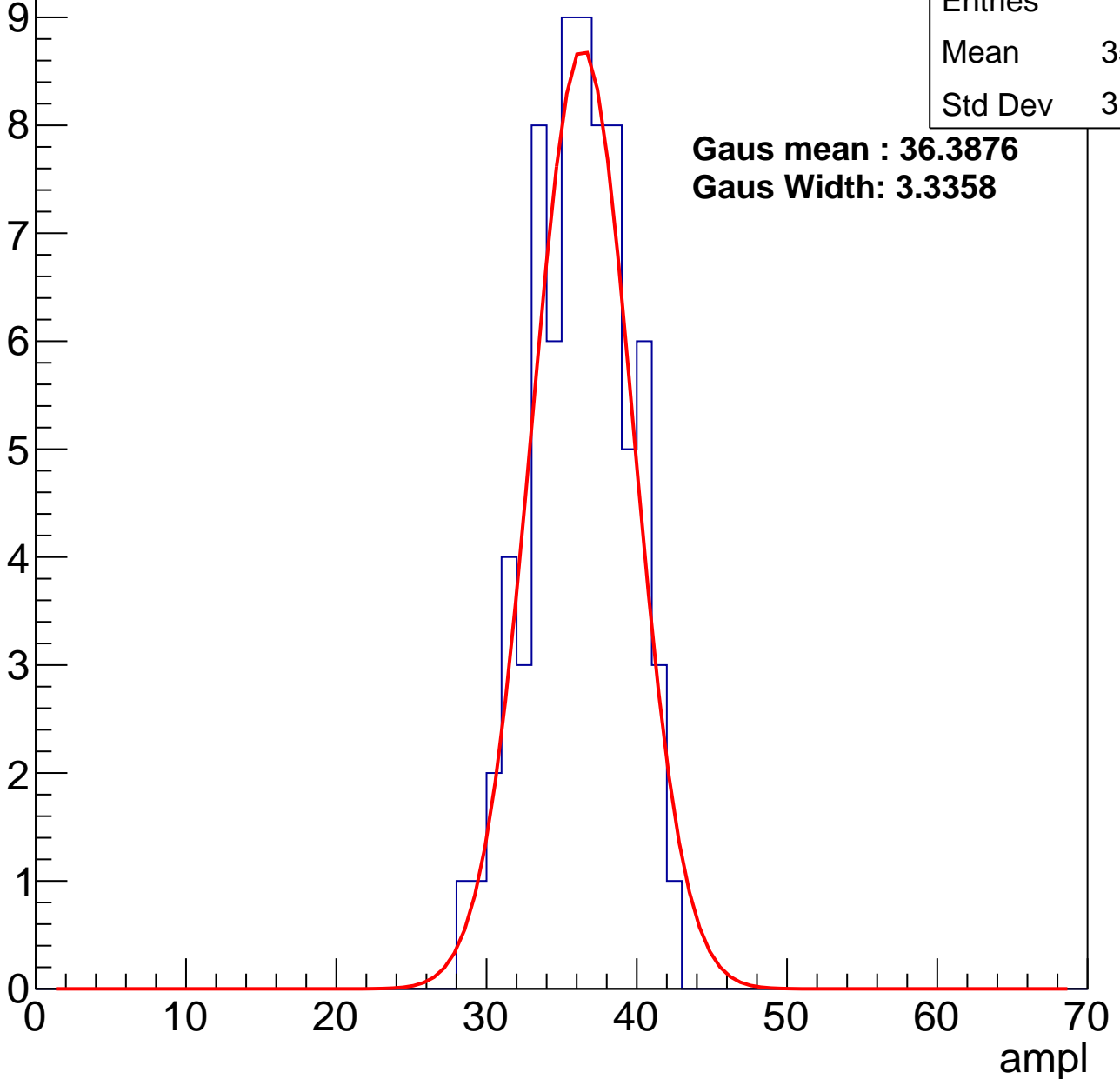
B1L102S, U11-ch67, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.73
Std Dev	3.129

Gaus mean : 36.3876
Gaus Width: 3.3358



B1L102S, U11-ch67, adc2

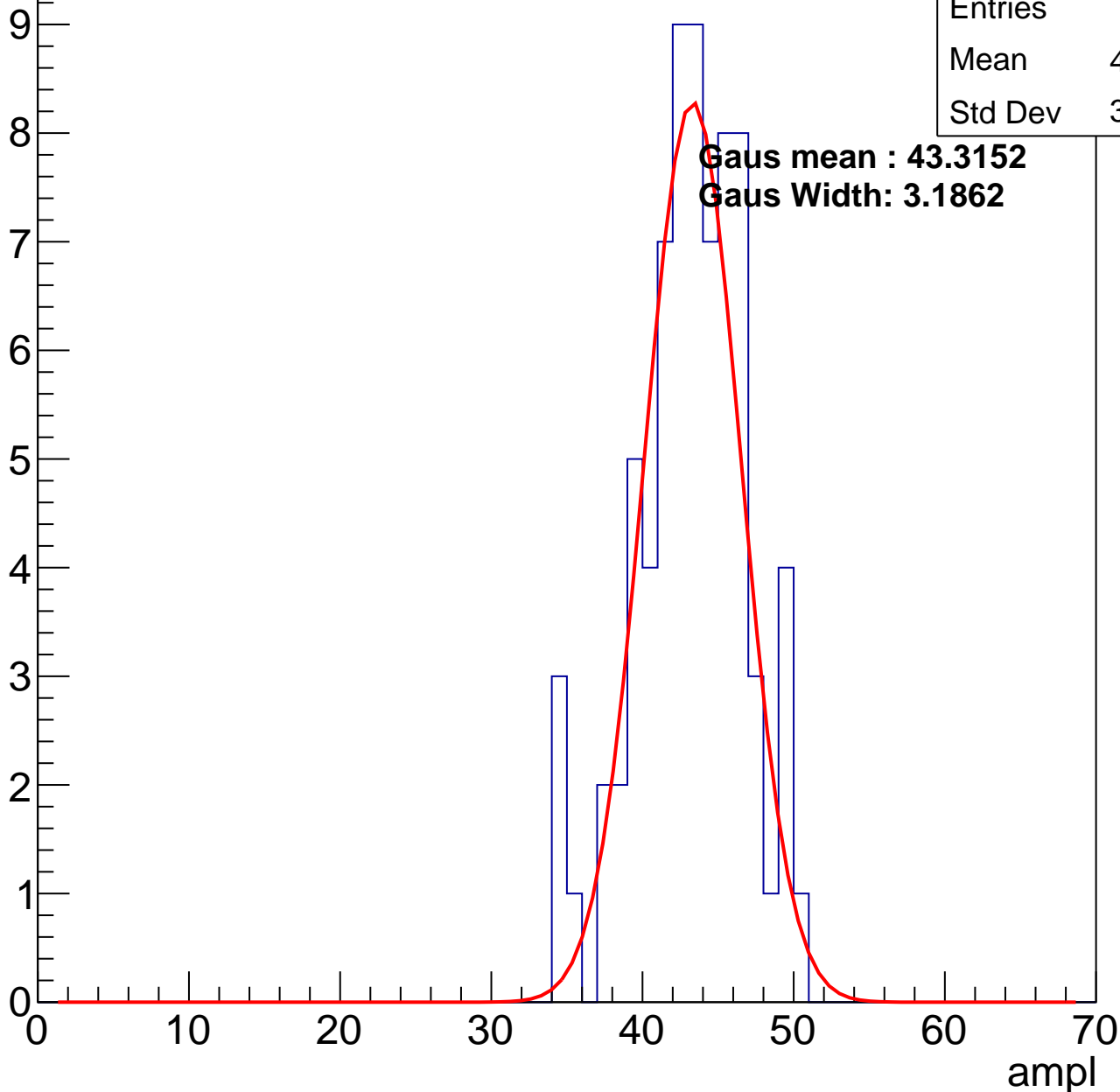
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.77
Std Dev	3.593

Gaus mean : 43.3152

Gaus Width: 3.1862

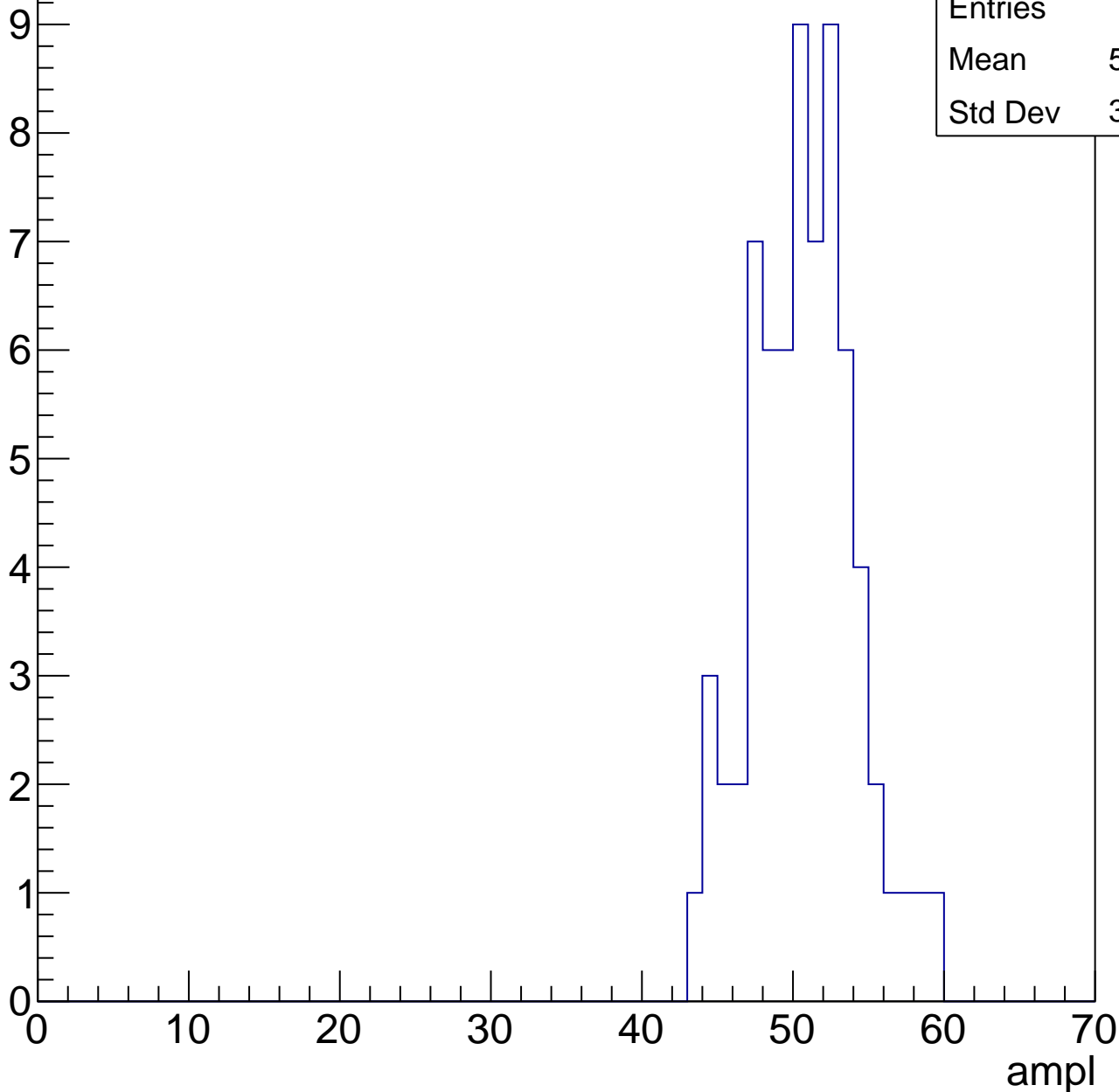


B1L102S, U11-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

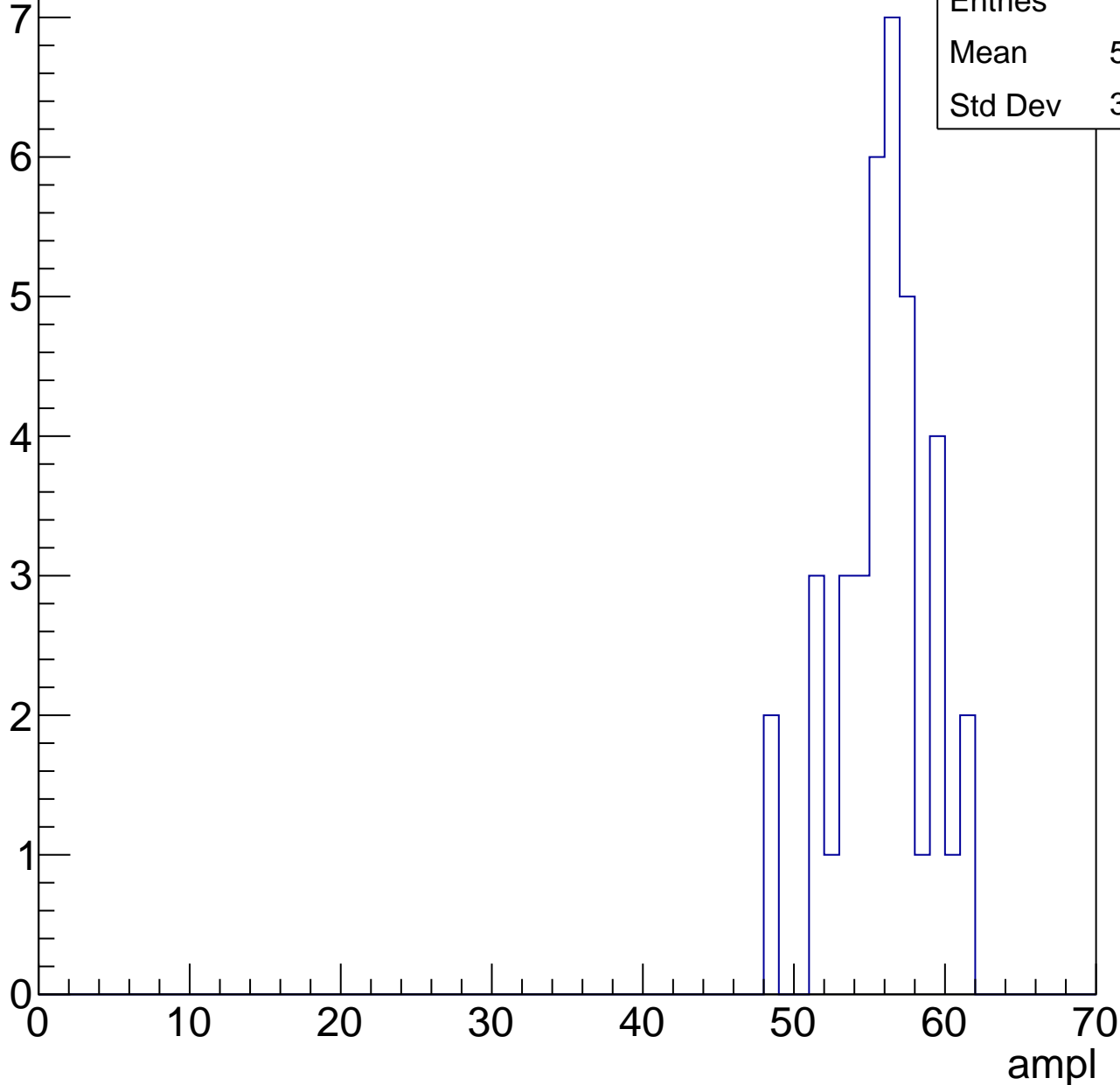
Entries	68
Mean	50.25
Std Dev	3.362



B1L102S, U11-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch67, adc5

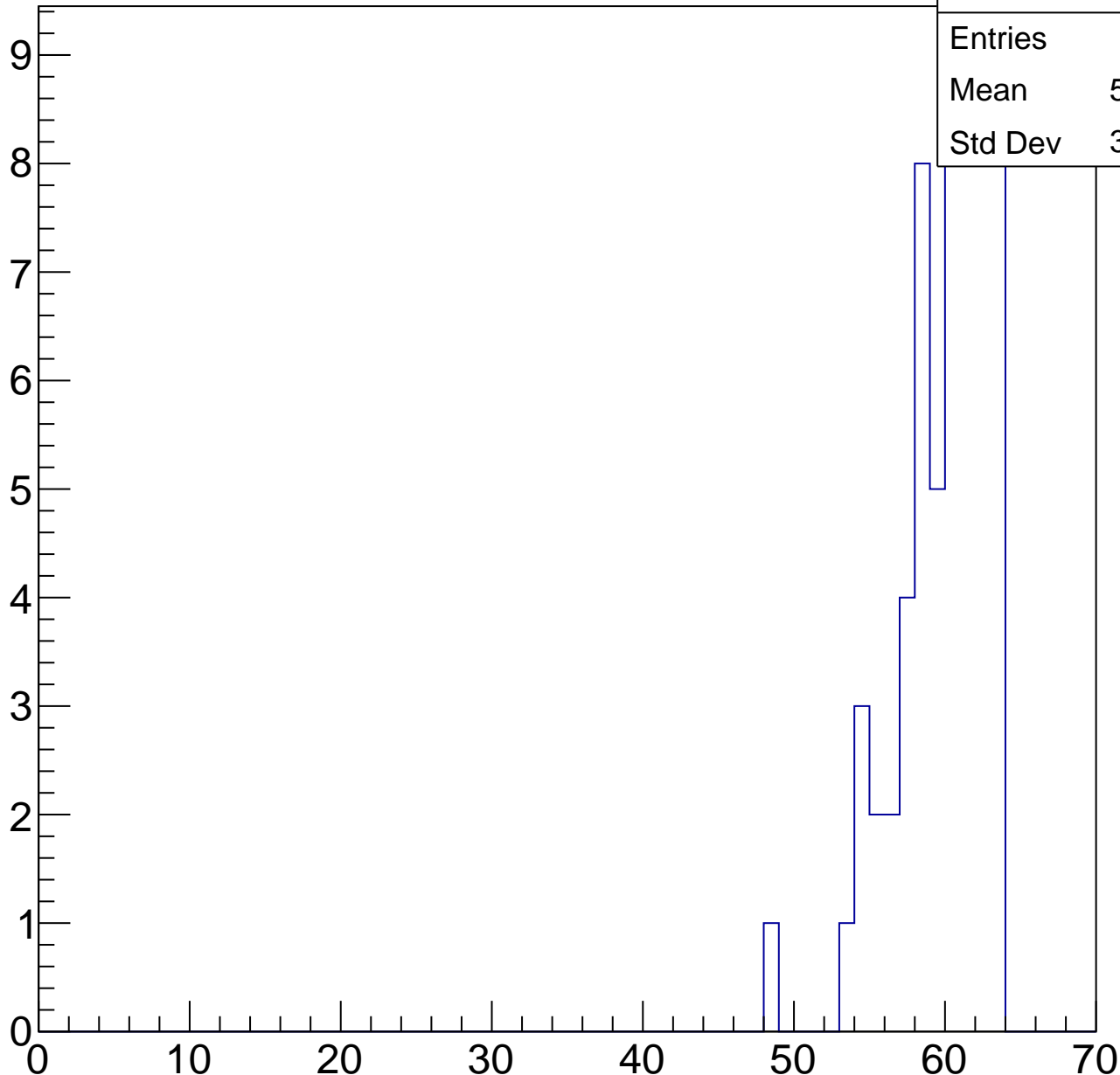
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	59.43
Std Dev	3.032

ampl



B1L102S, U11-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch68, adc0

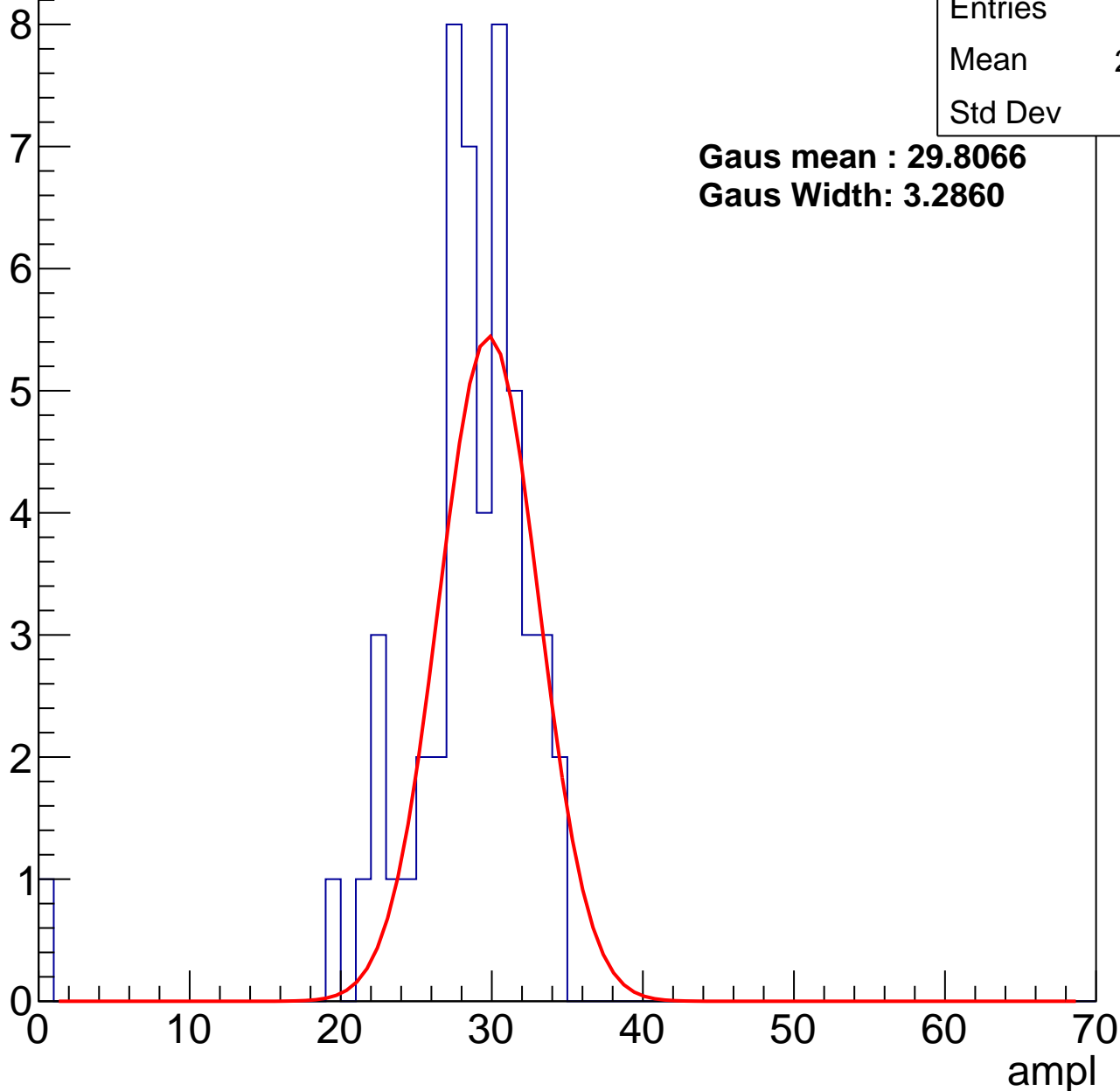
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	27.71
Std Dev	5.13

Gaus mean : 29.8066

Gaus Width: 3.2860



B1L102S, U11-ch68, adc1

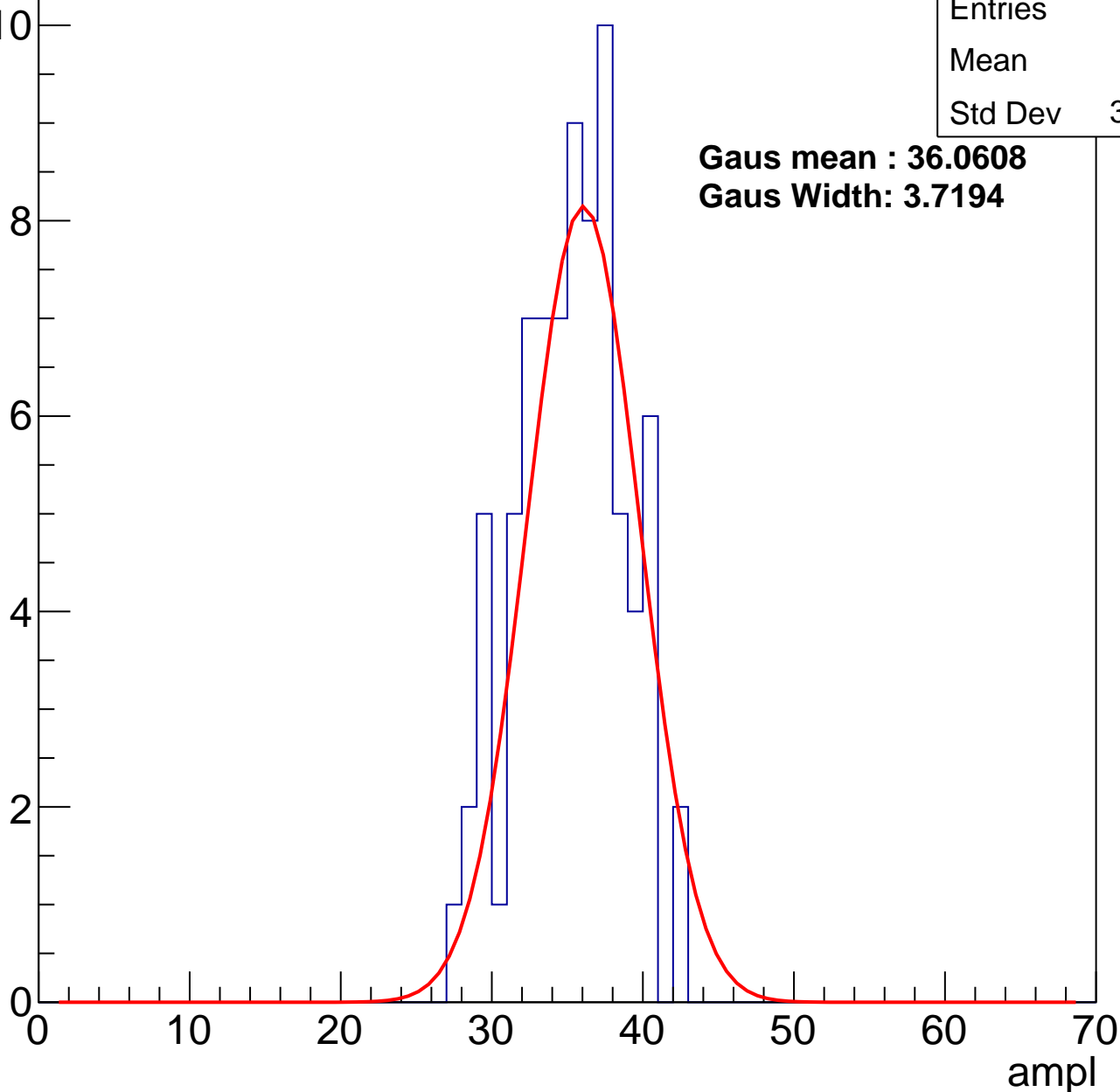
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	34.8
Std Dev	3.462

Gaus mean : 36.0608

Gaus Width: 3.7194



B1L102S, U11-ch68, adc2

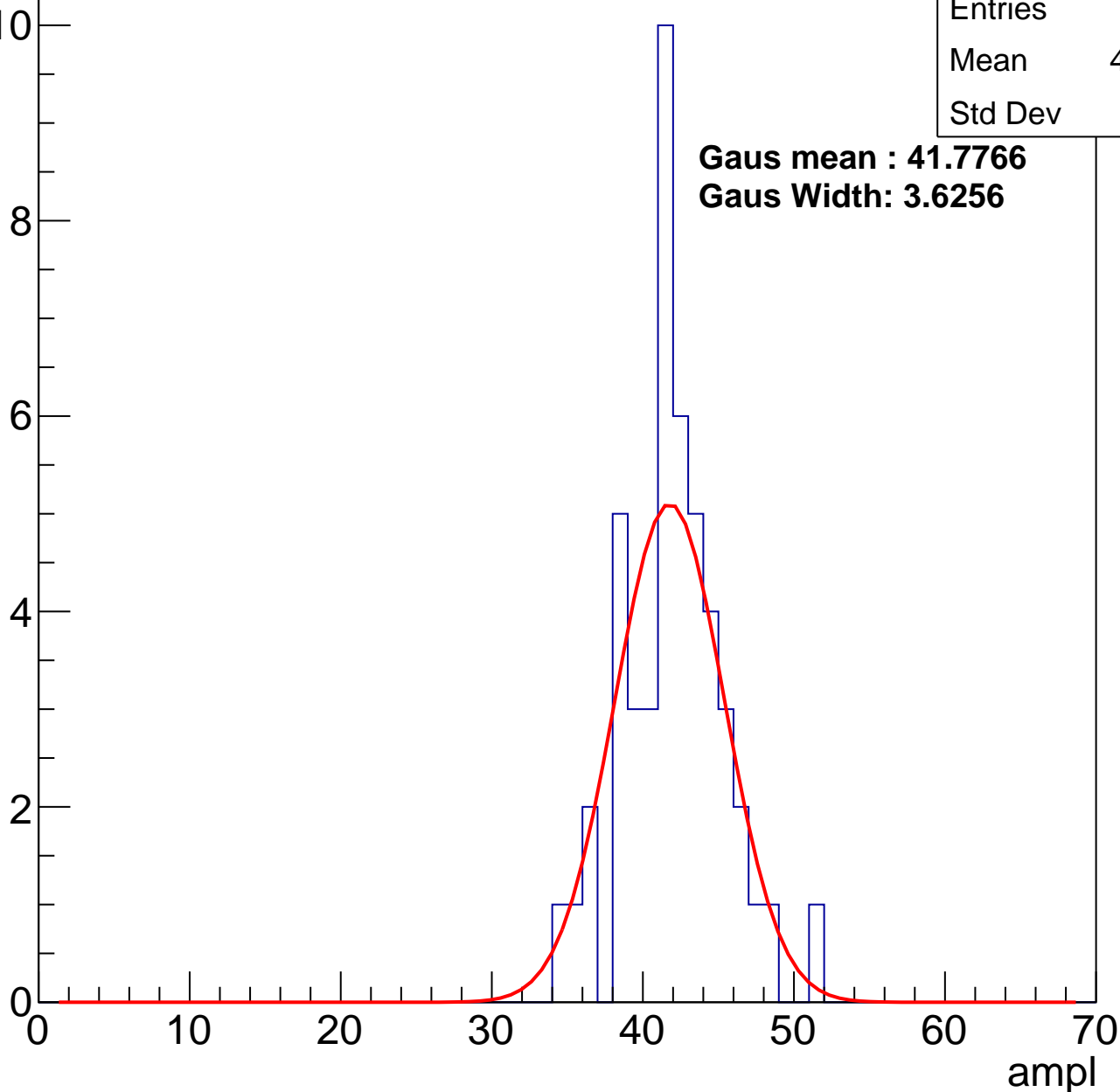
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	41.54
Std Dev	3.31

Gaus mean : 41.7766

Gaus Width: 3.6256

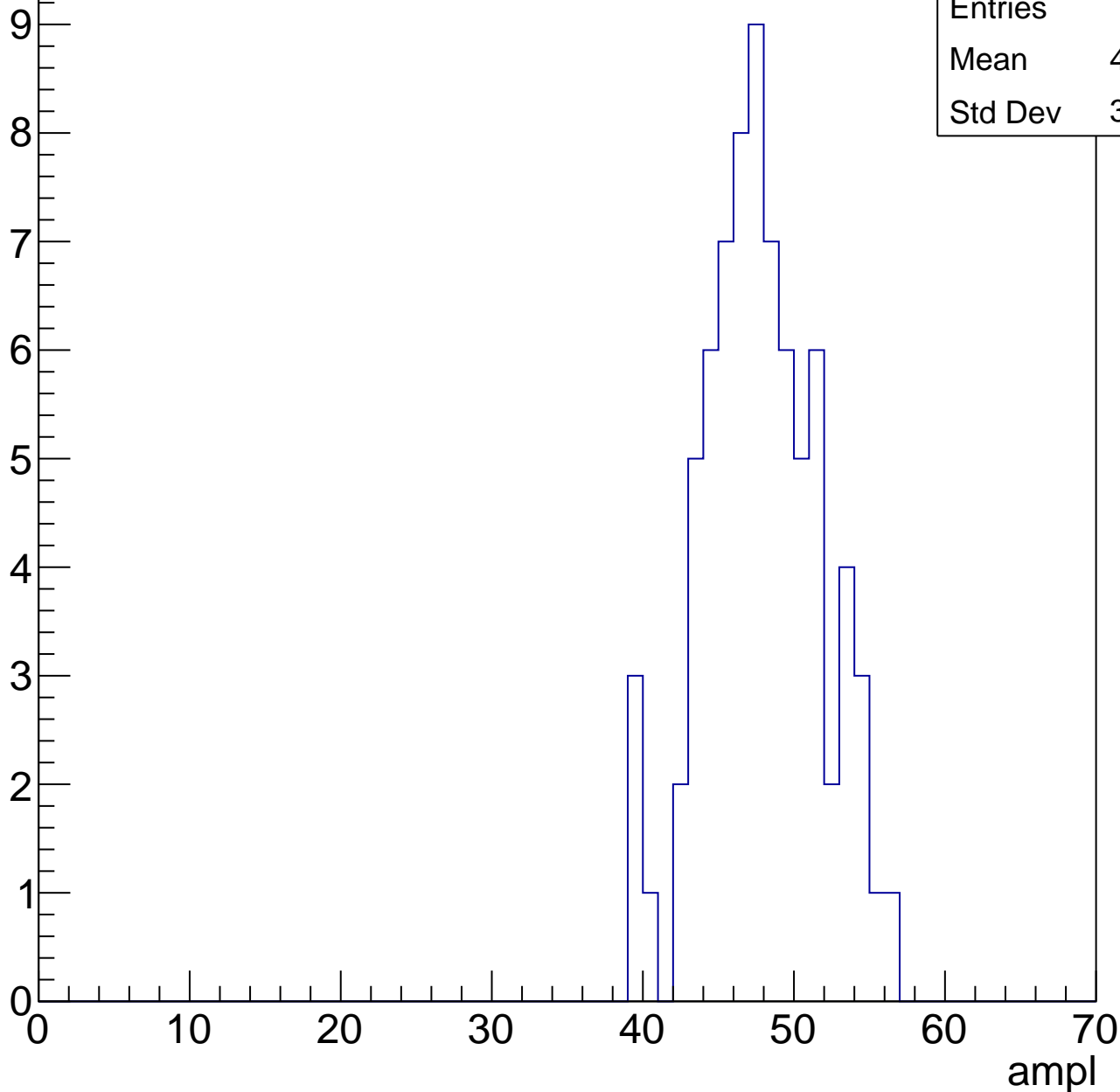


B1L102S, U11-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	47.38
Std Dev	3.818

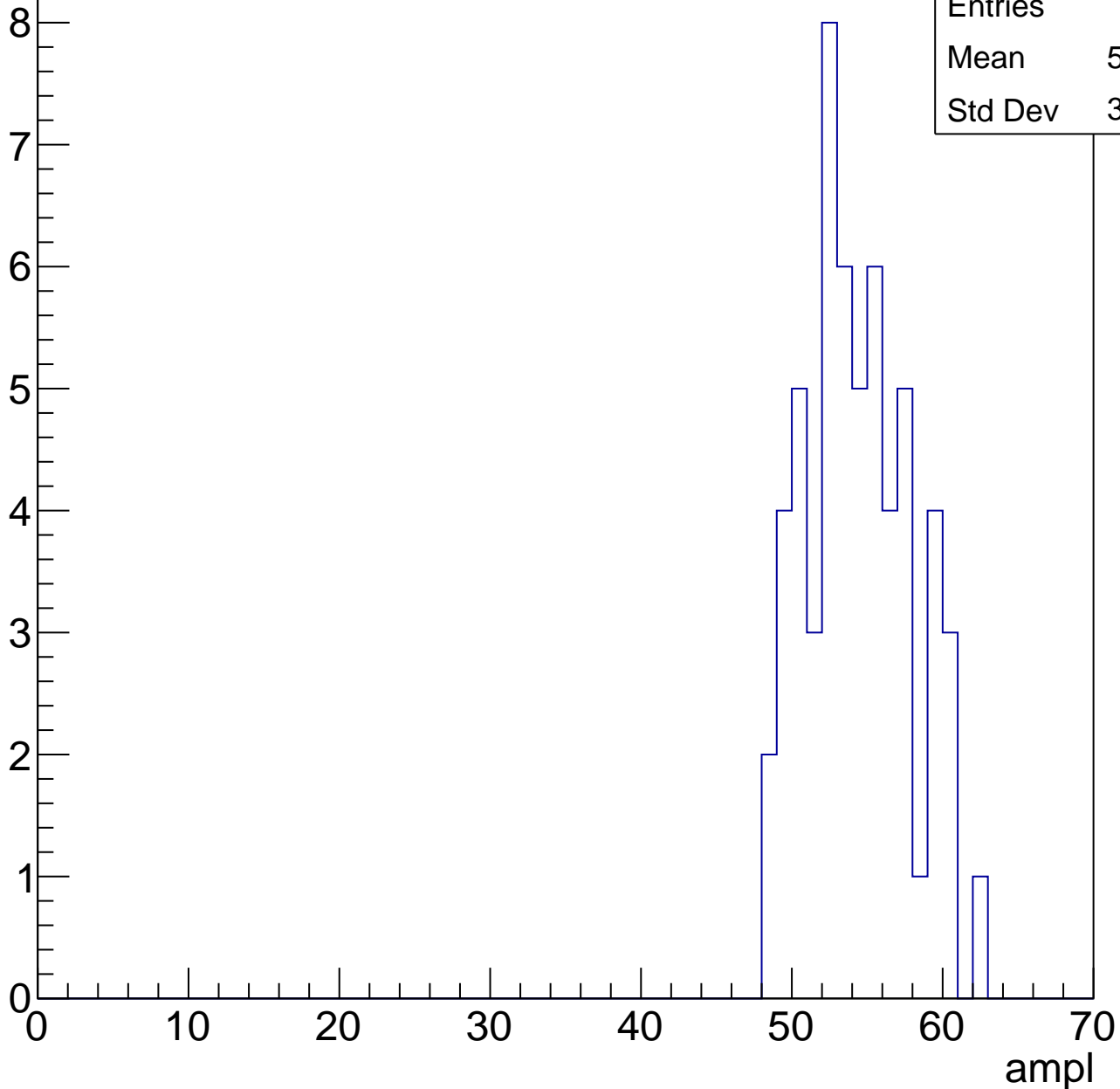


B1L102S, U11-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.93
Std Dev	3.433

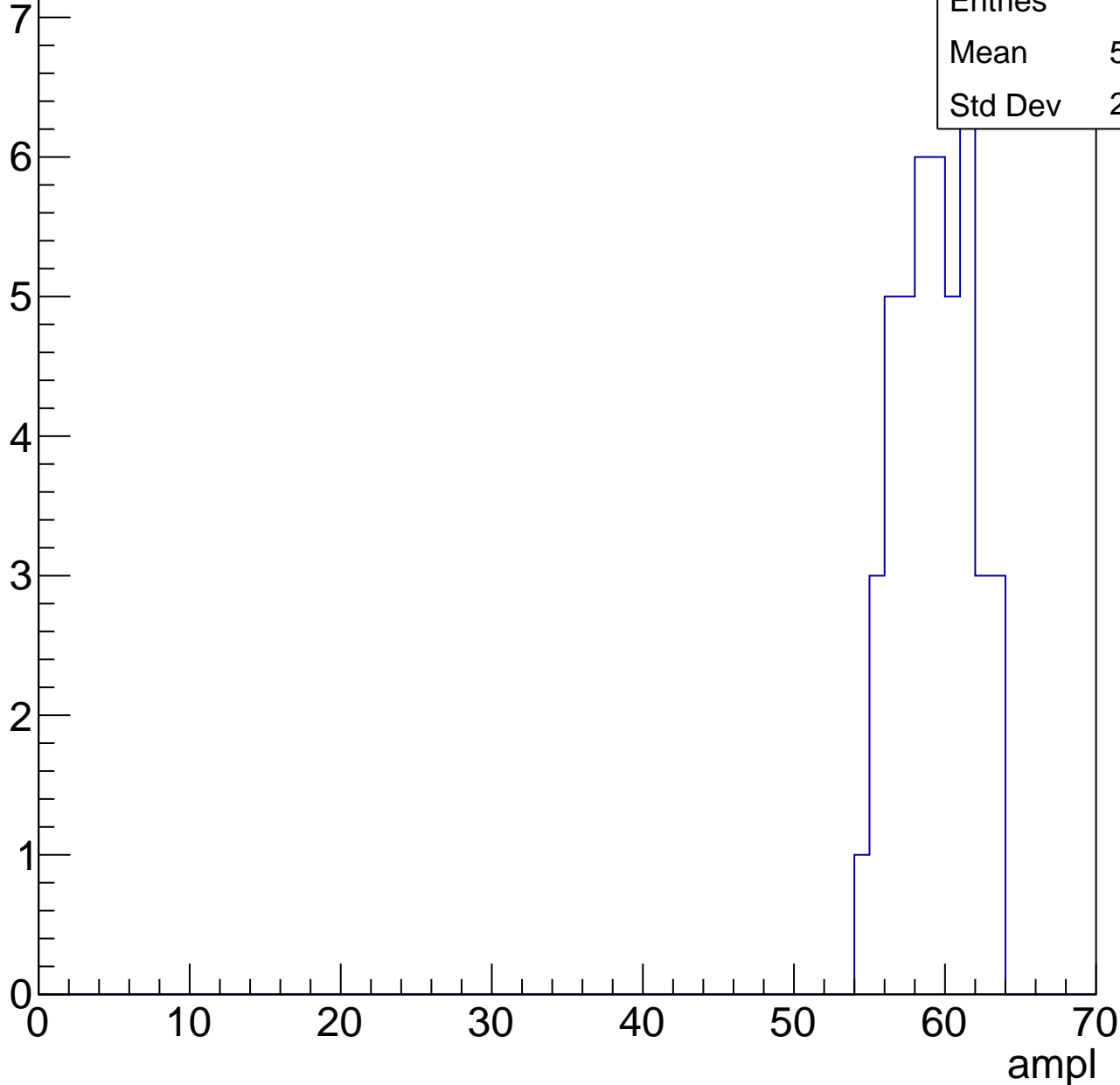


B1L102S, U11-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.82
Std Dev	2.386

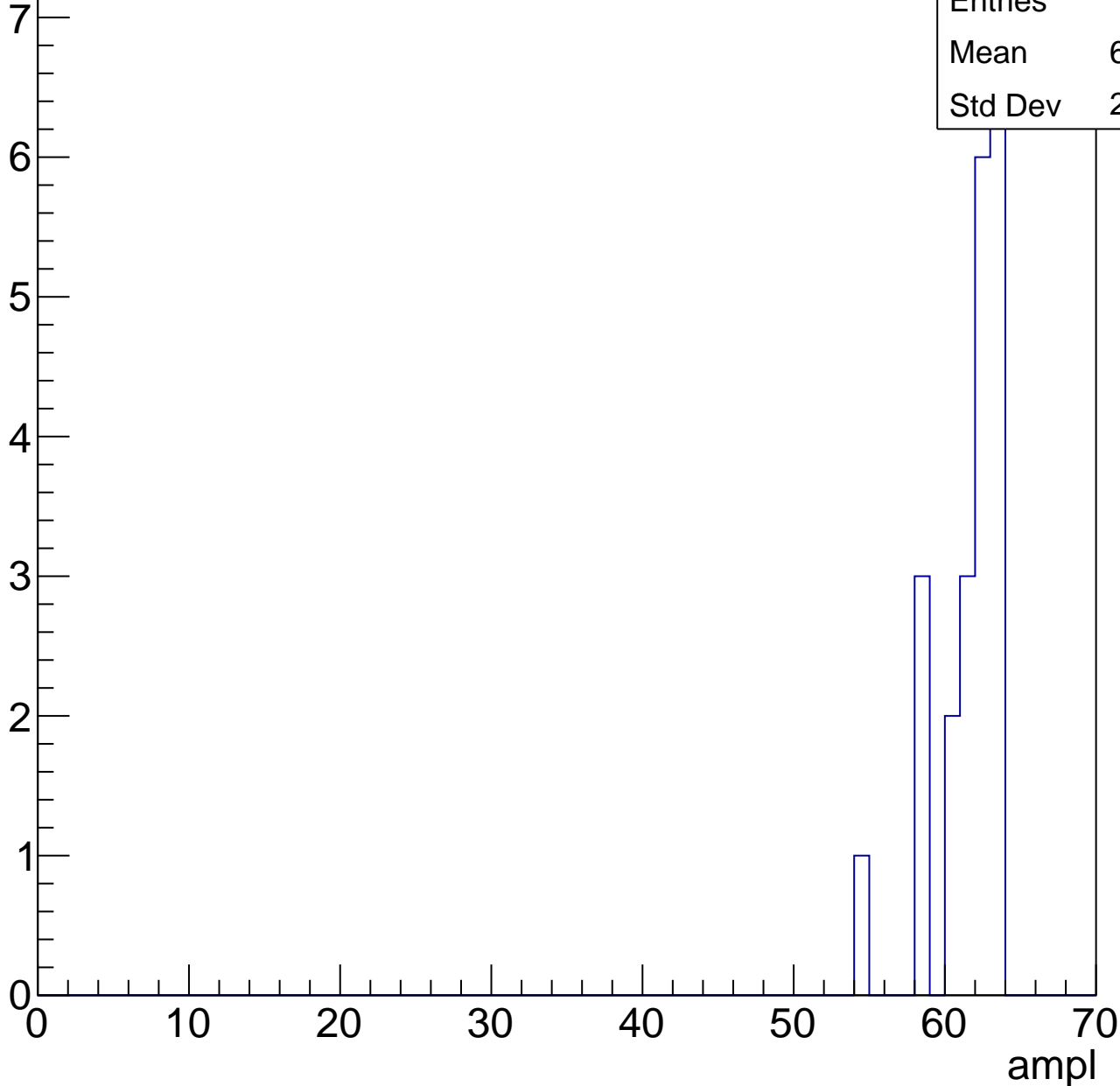


B1L102S, U11-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	61.09
Std Dev	2.254



B1L102S, U11-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch69, adc0

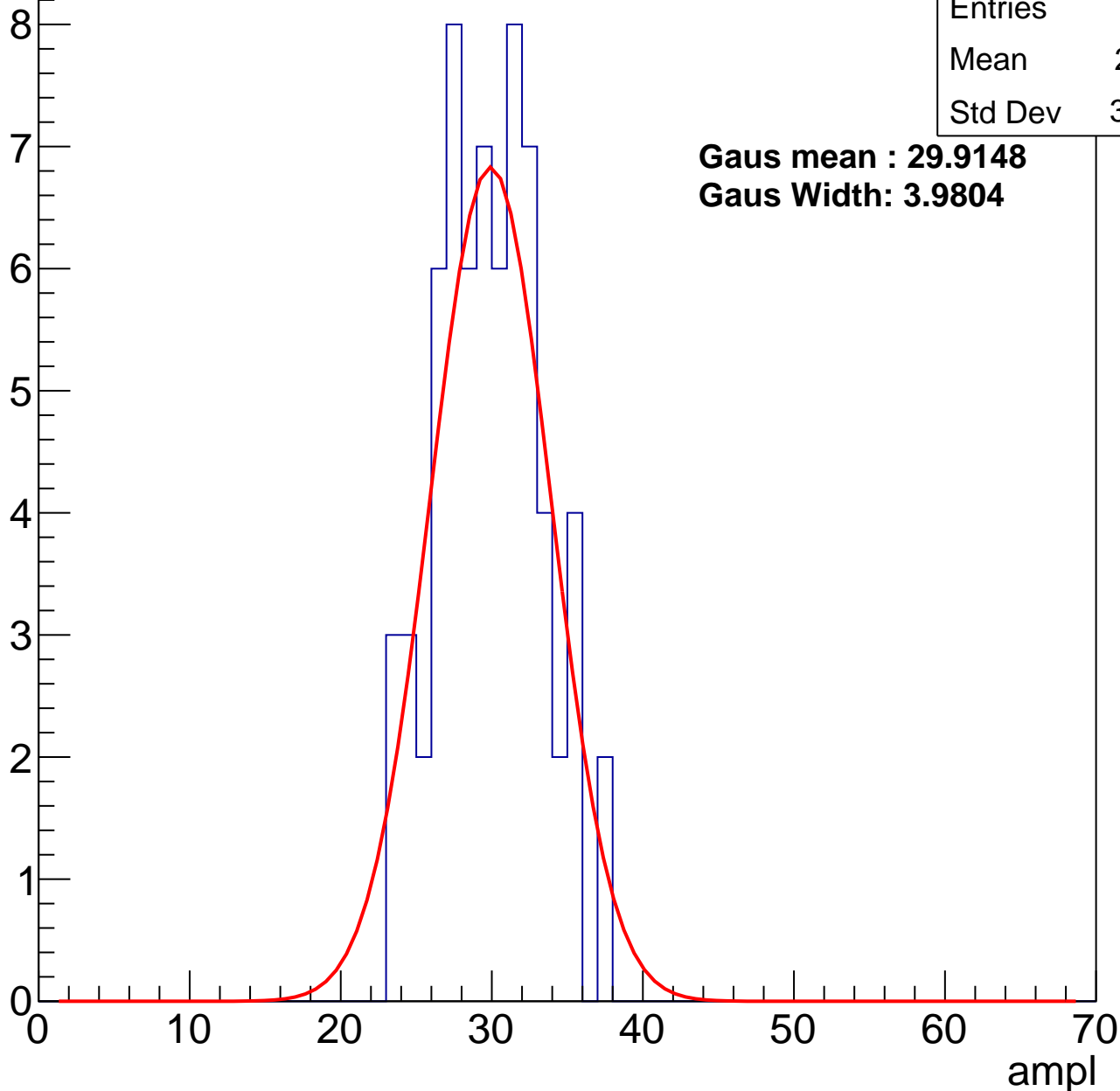
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.41
Std Dev	3.392

Gaus mean : 29.9148

Gaus Width: 3.9804



B1L102S, U11-ch69, adc1

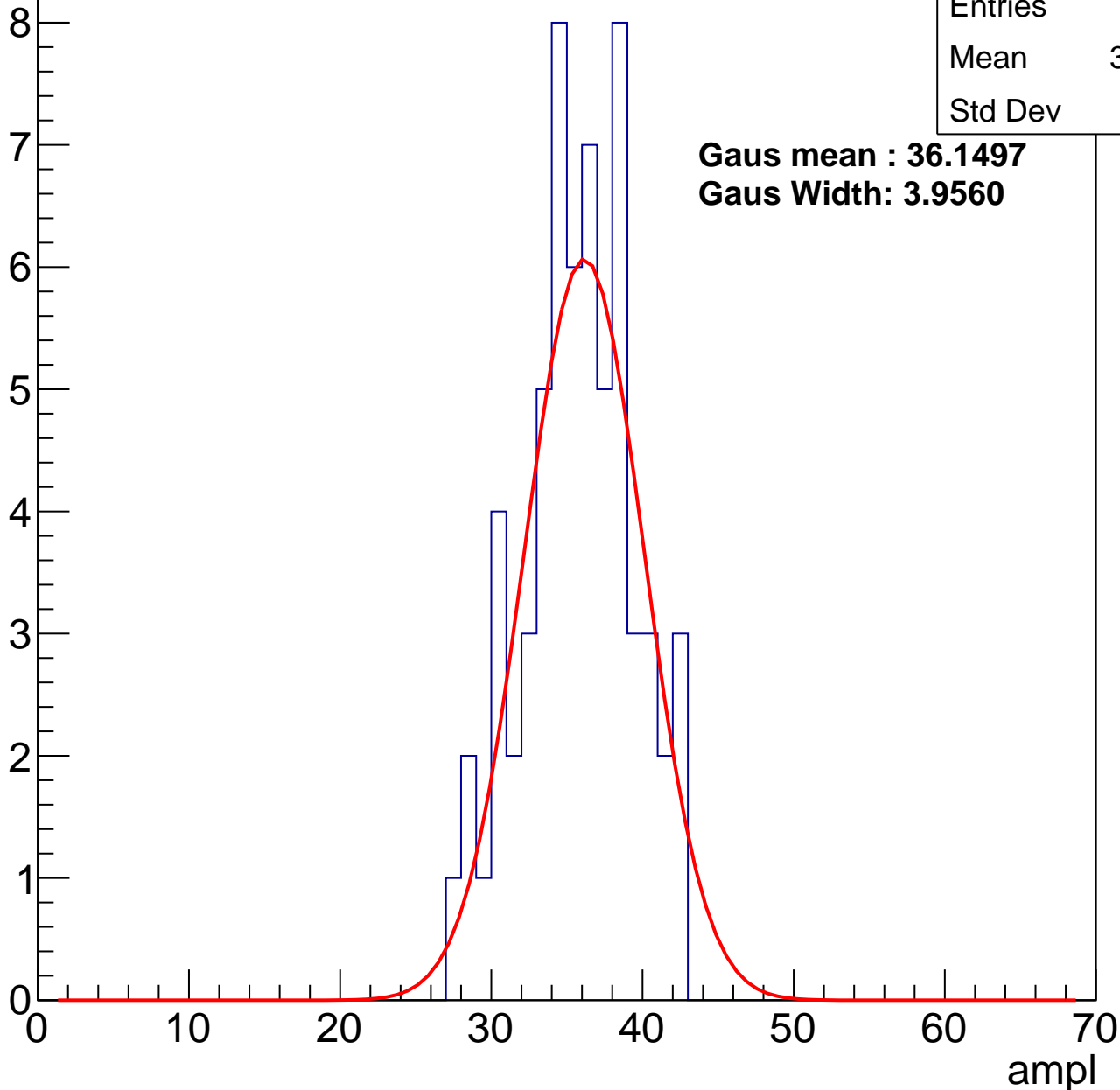
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.29
Std Dev	3.61

Gaus mean : 36.1497

Gaus Width: 3.9560



B1L102S, U11-ch69, adc2

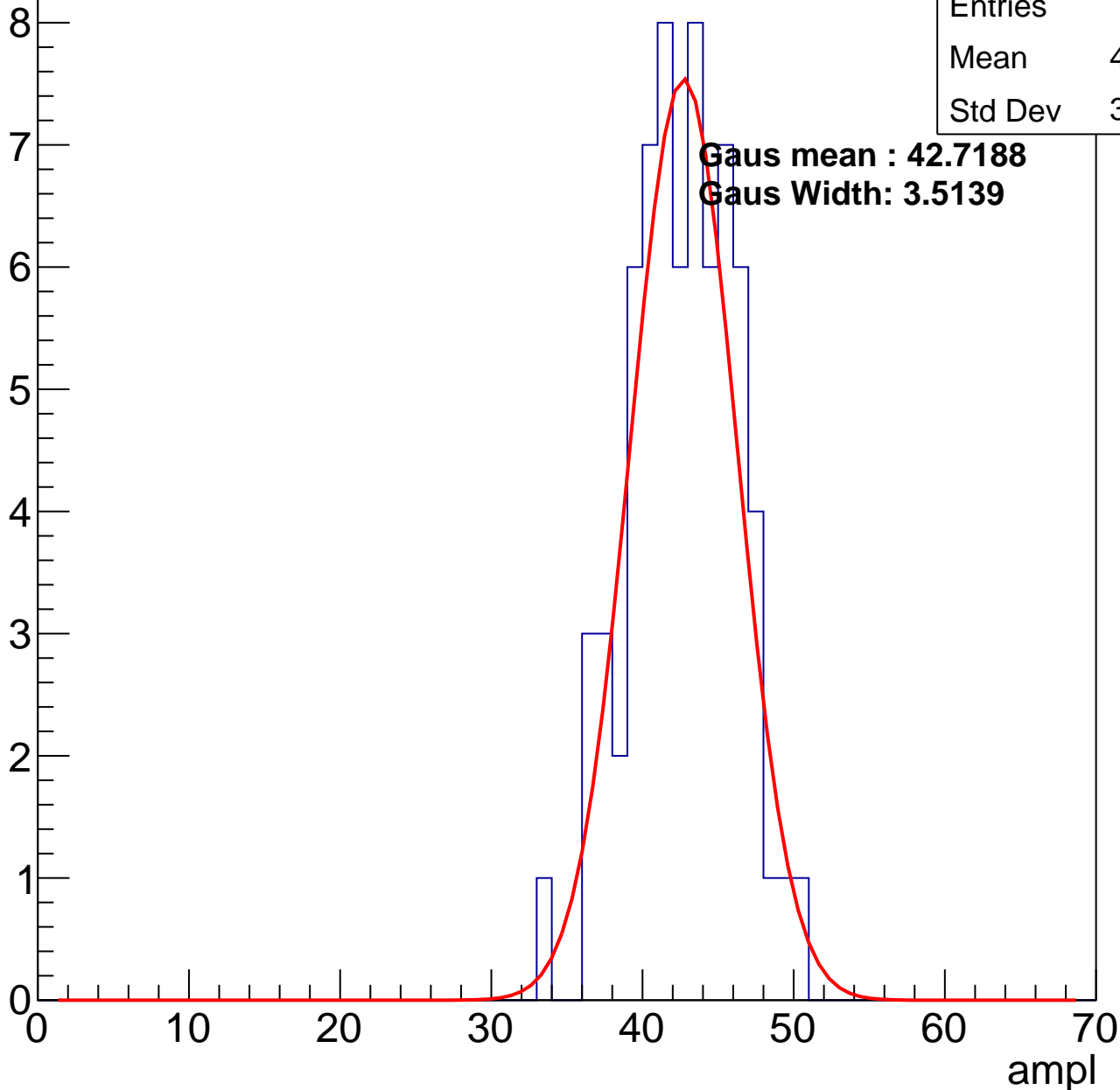
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.23
Std Dev	3.427

Gaus mean : 42.7188

Gaus Width: 3.5139

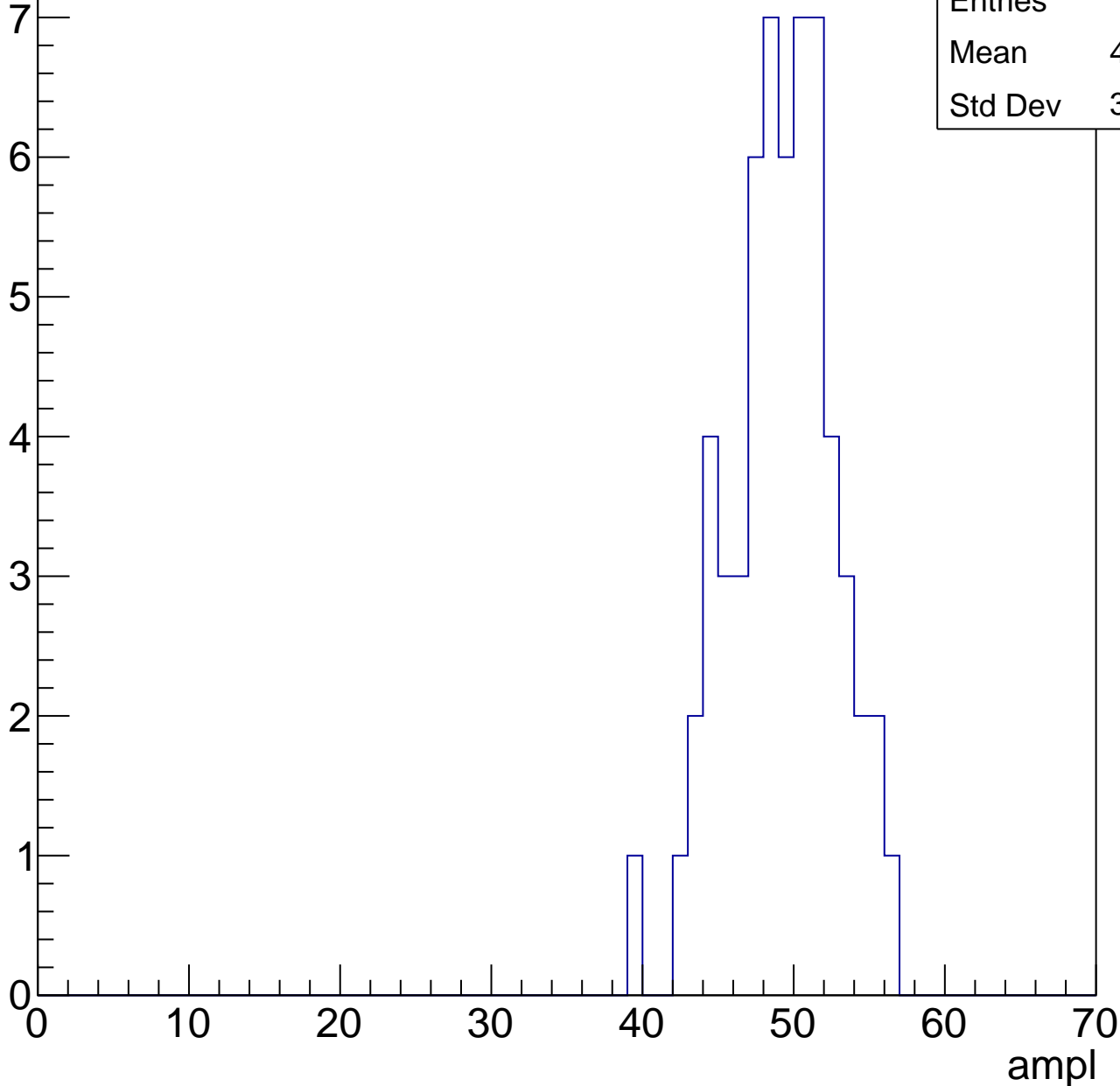


B1L102S, U11-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.75
Std Dev	3.477

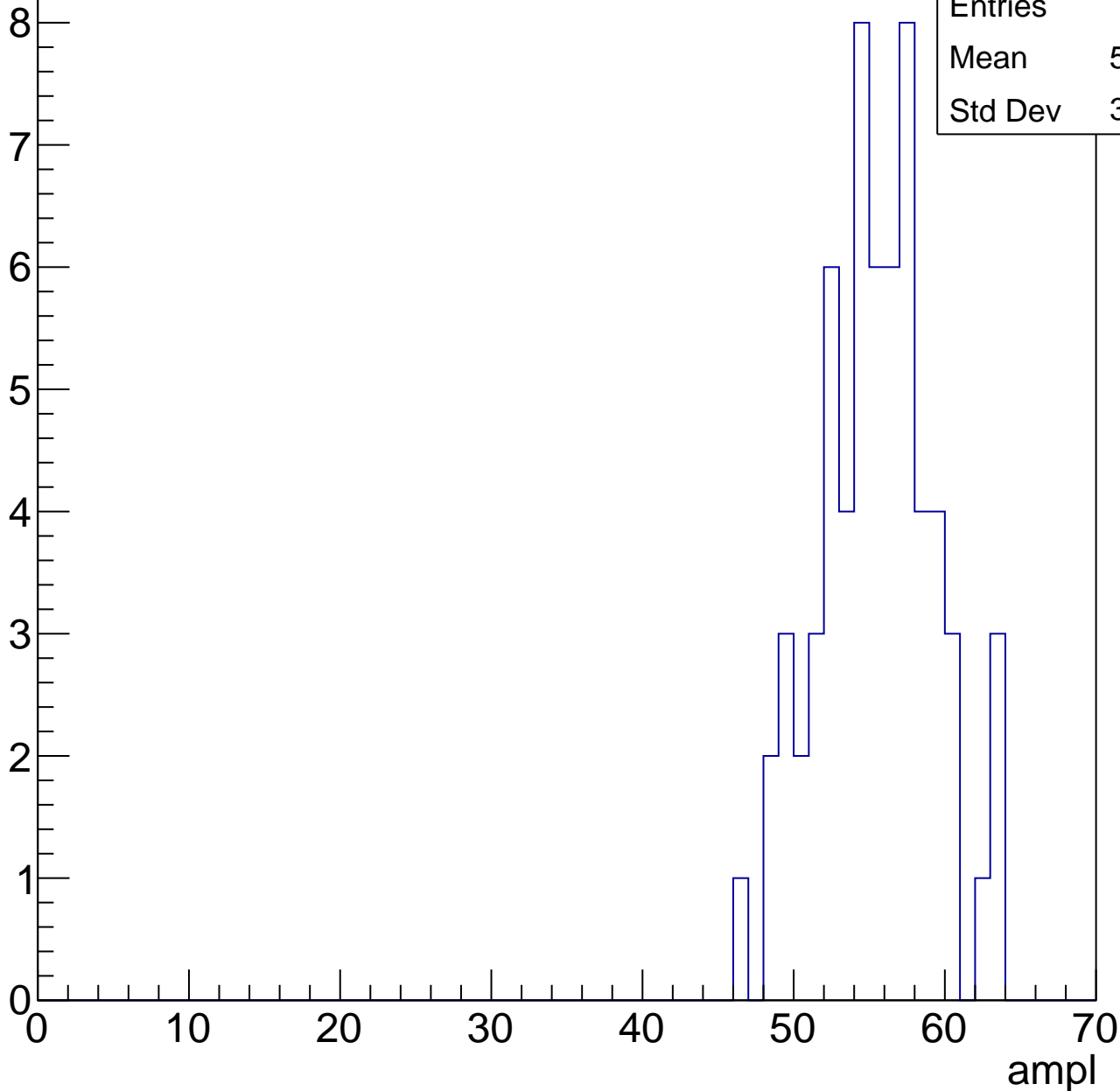


B1L102S, U11-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.98
Std Dev	3.785

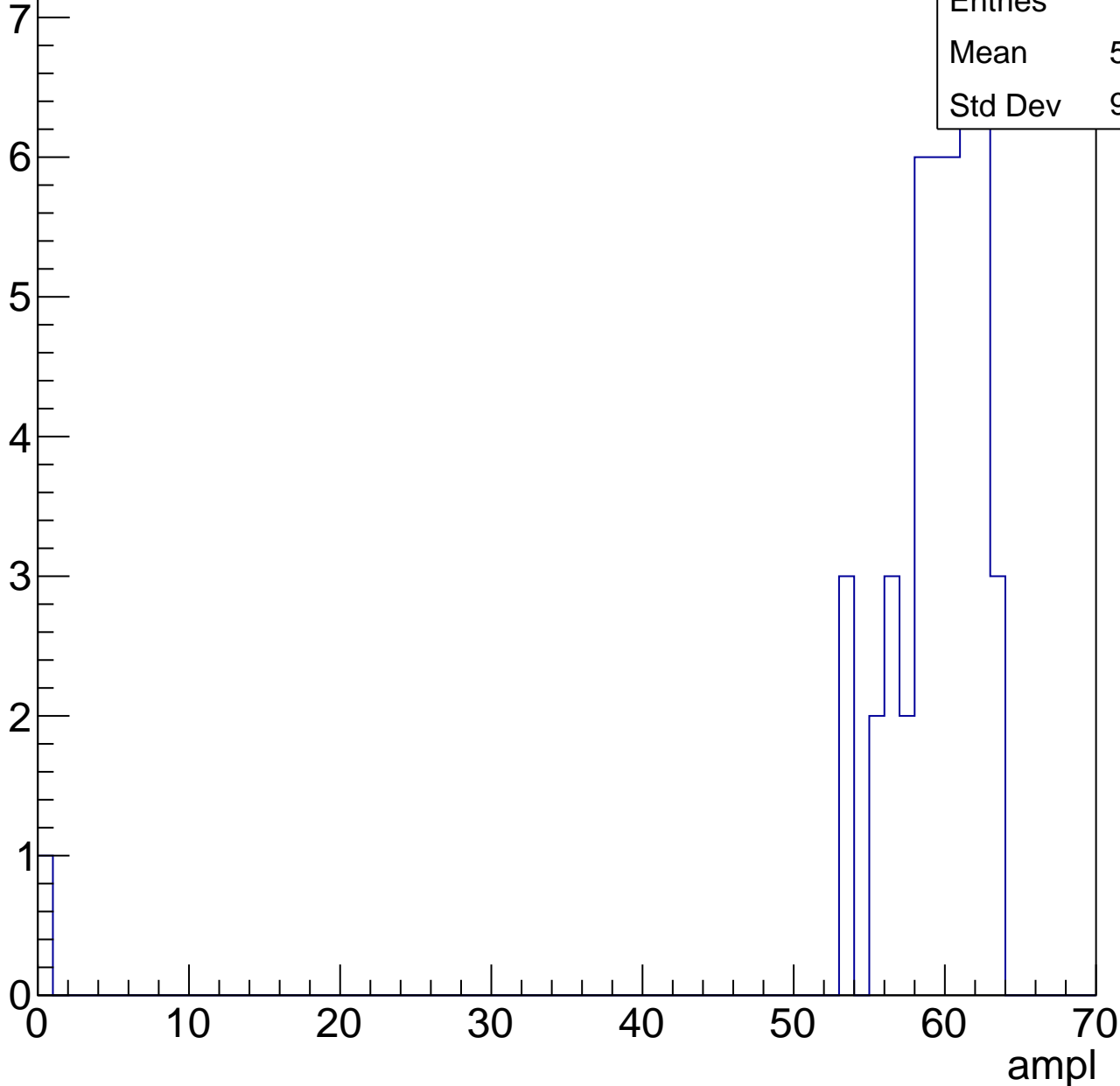


B1L102S, U11-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

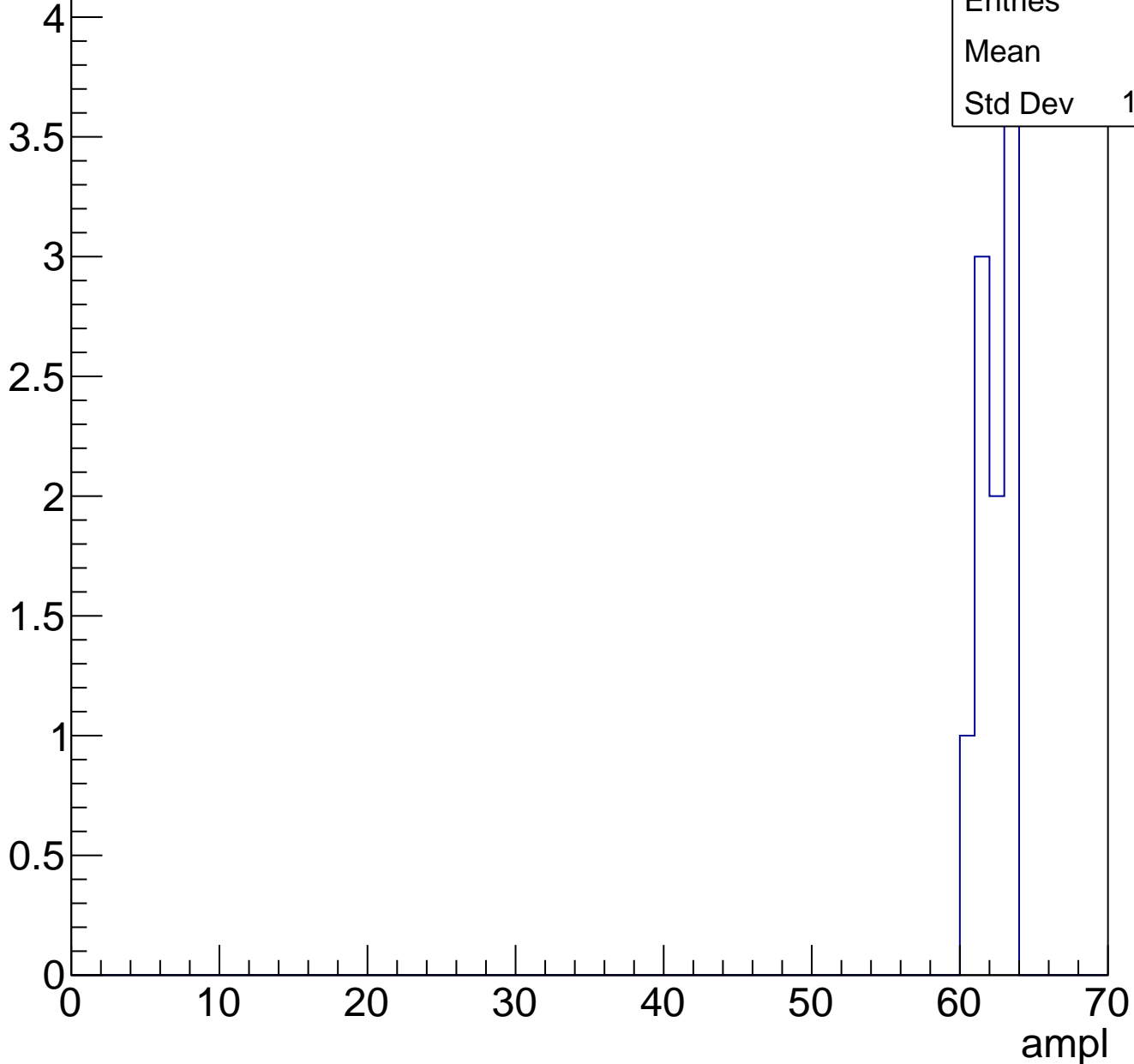
Entries	46
Mean	57.89
Std Dev	9.029



B1L102S, U11-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch70, adc0

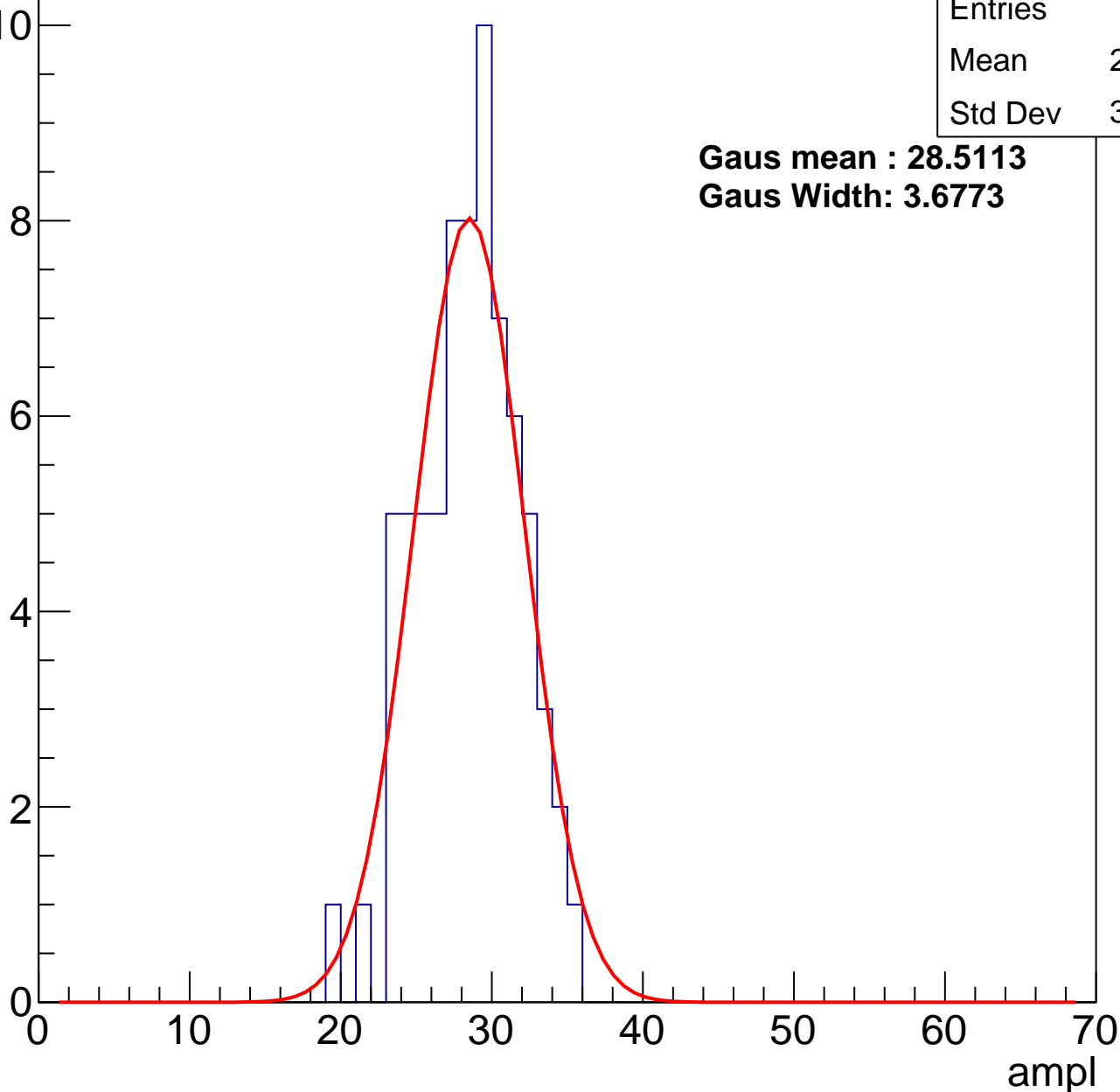
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.03
Std Dev	3.283

Gaus mean : 28.5113

Gaus Width: 3.6773



B1L102S, U11-ch70, adc1

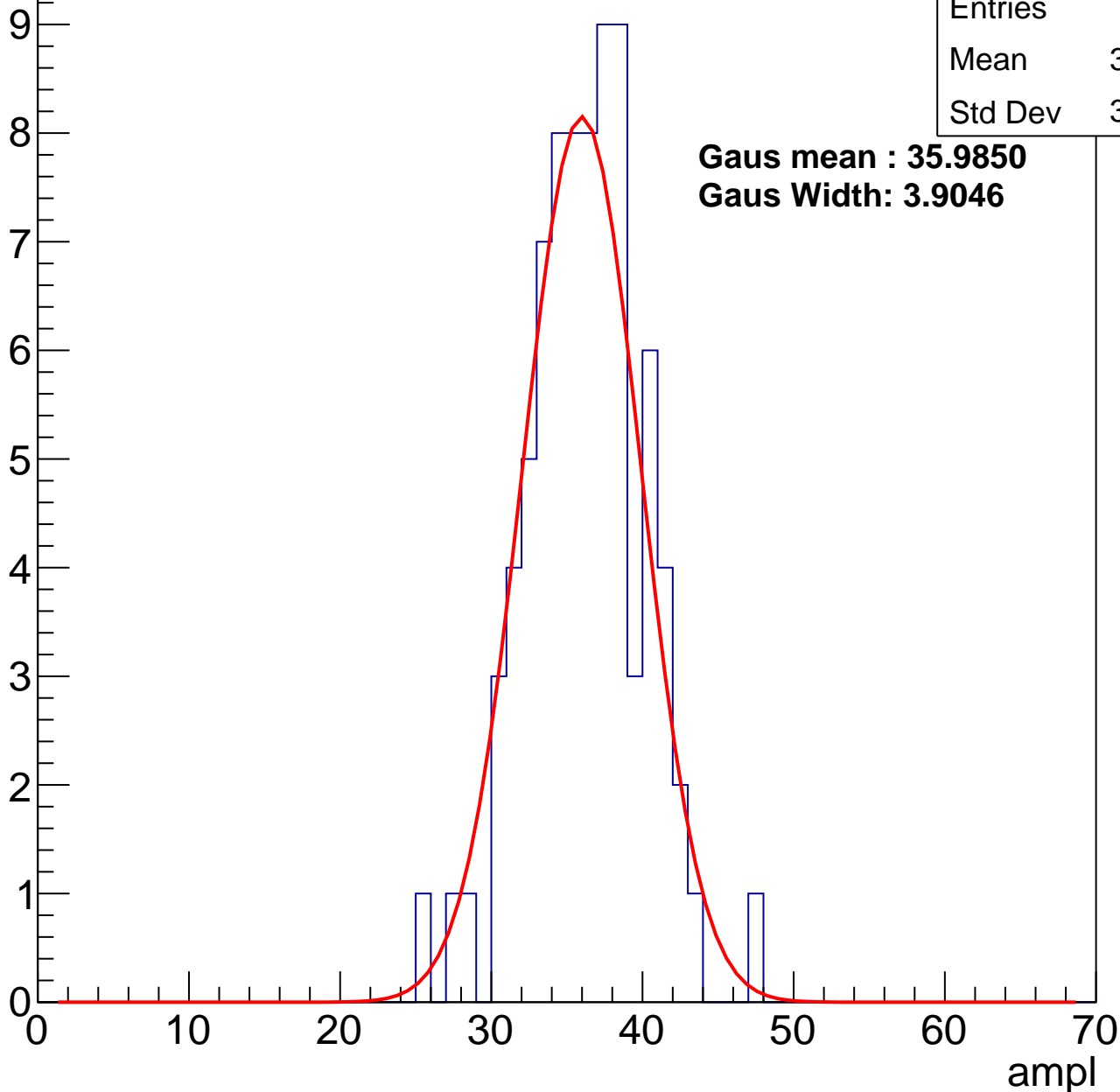
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.74
Std Dev	3.787

Gaus mean : 35.9850

Gaus Width: 3.9046



B1L102S, U11-ch70, adc2

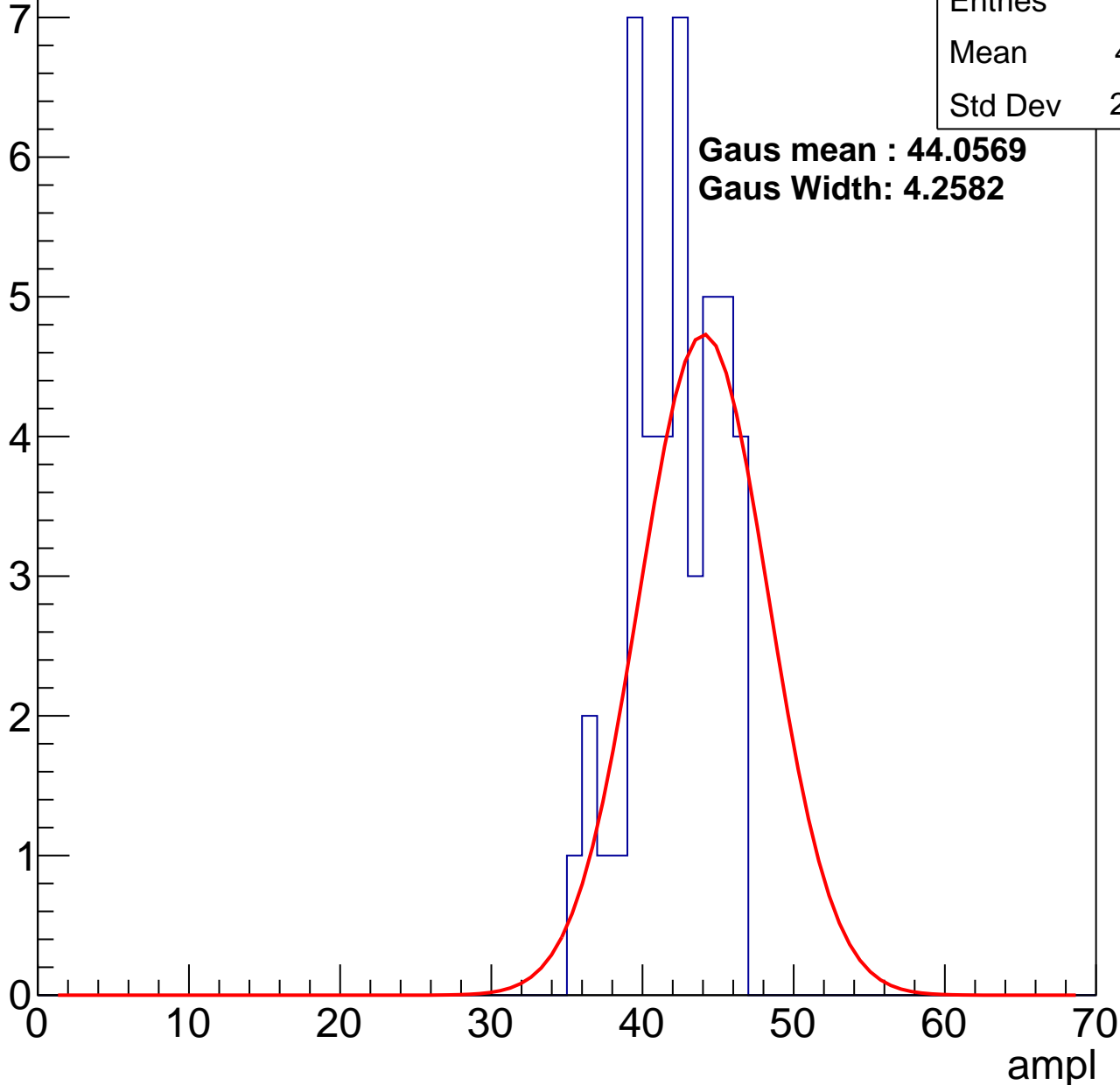
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	41.61
Std Dev	2.902

Gaus mean : 44.0569

Gaus Width: 4.2582

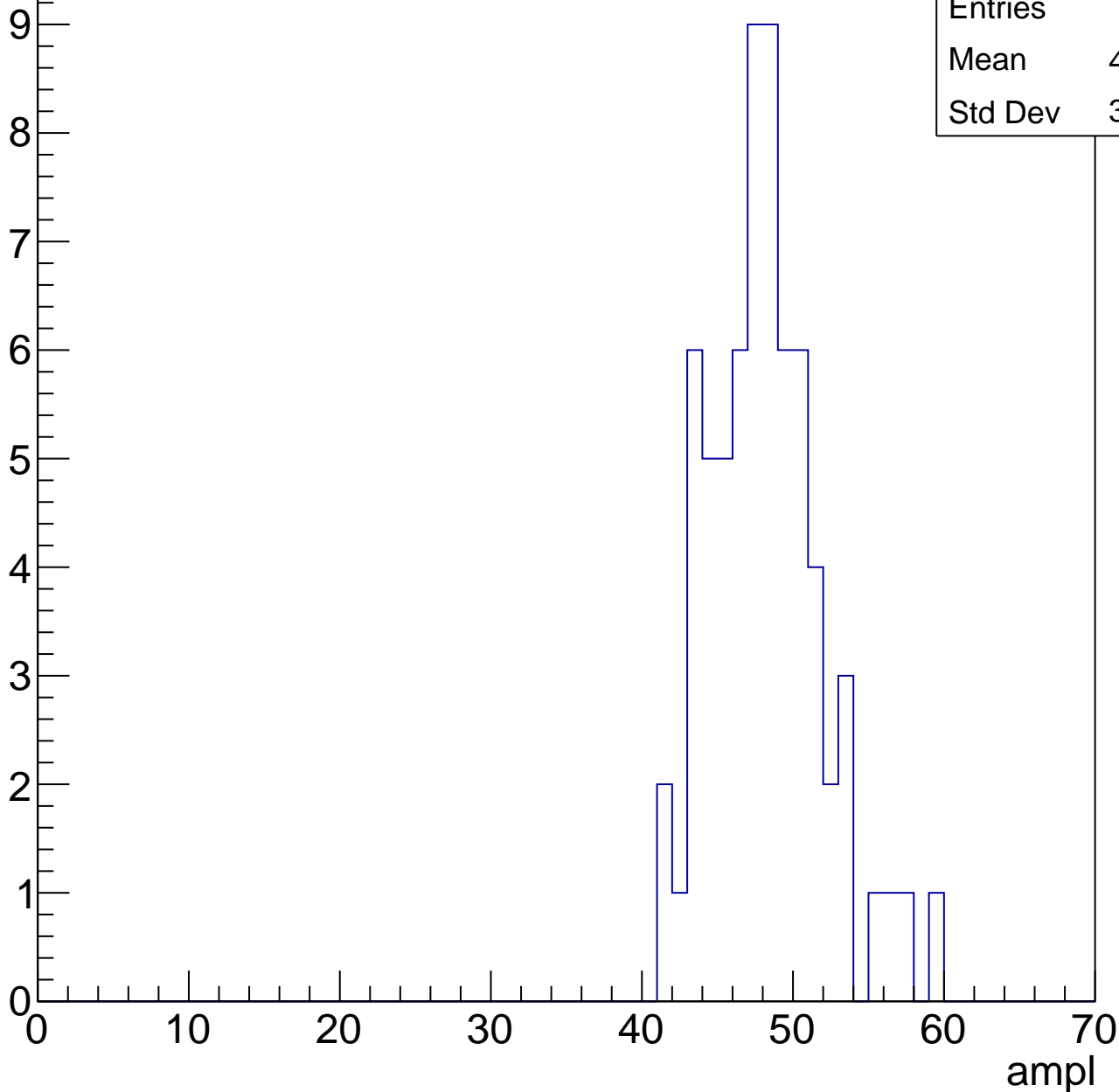


B1L102S, U11-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	47.74
Std Dev	3.689

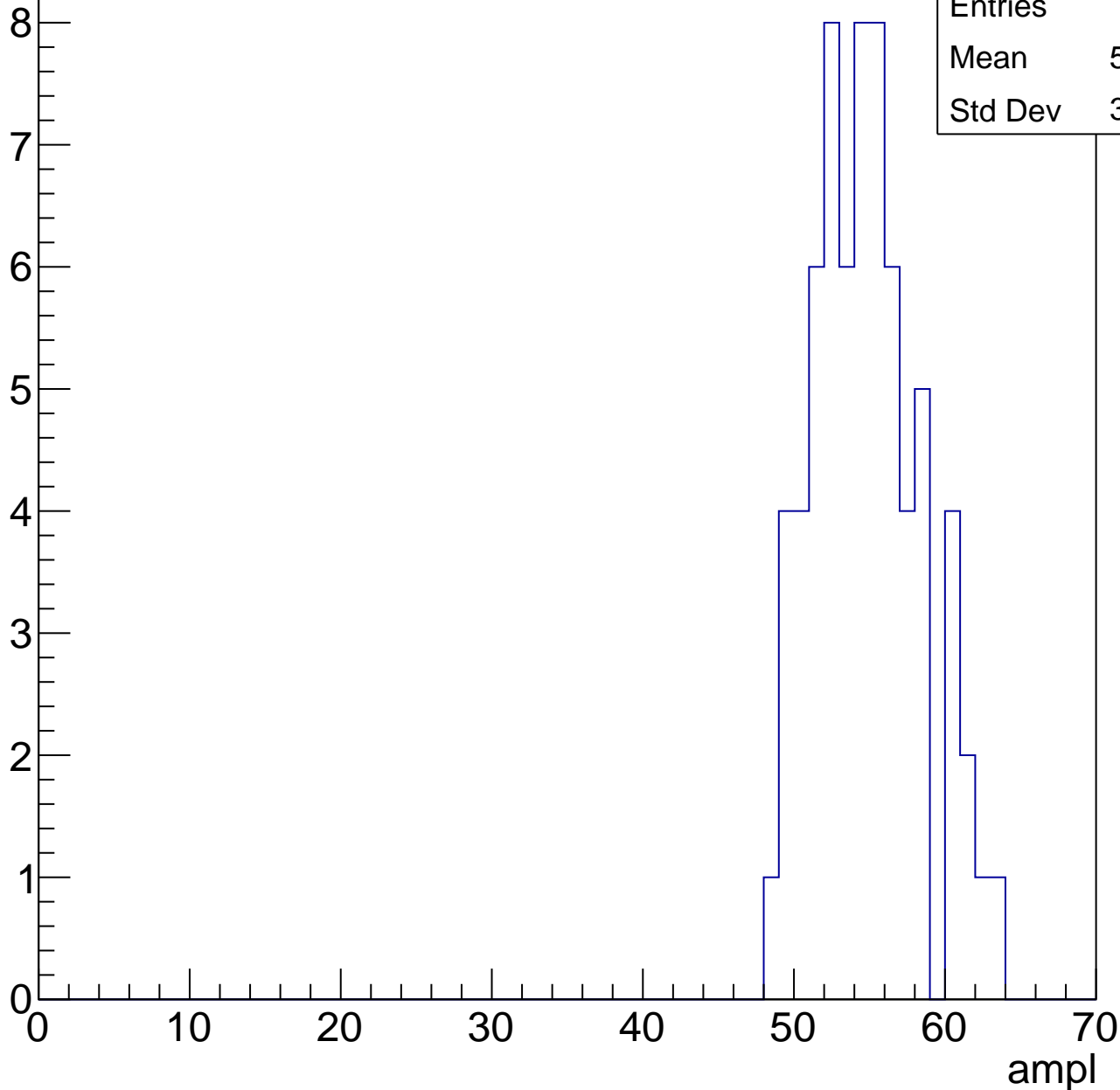


B1L102S, U11-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.37
Std Dev	3.468



B1L102S, U11-ch70, adc5

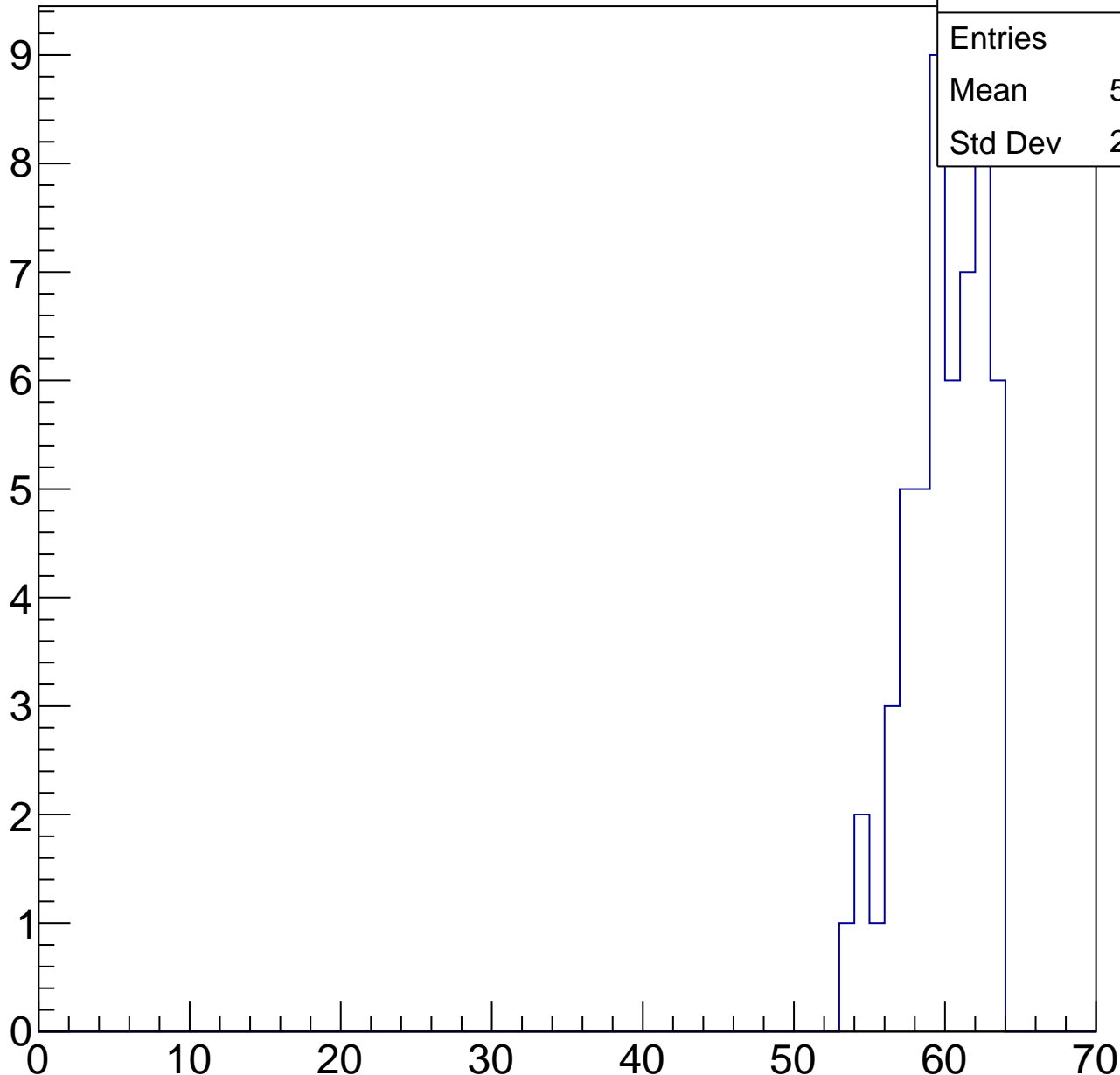
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.45
Std Dev	2.552

ampl



B1L102S, U11-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

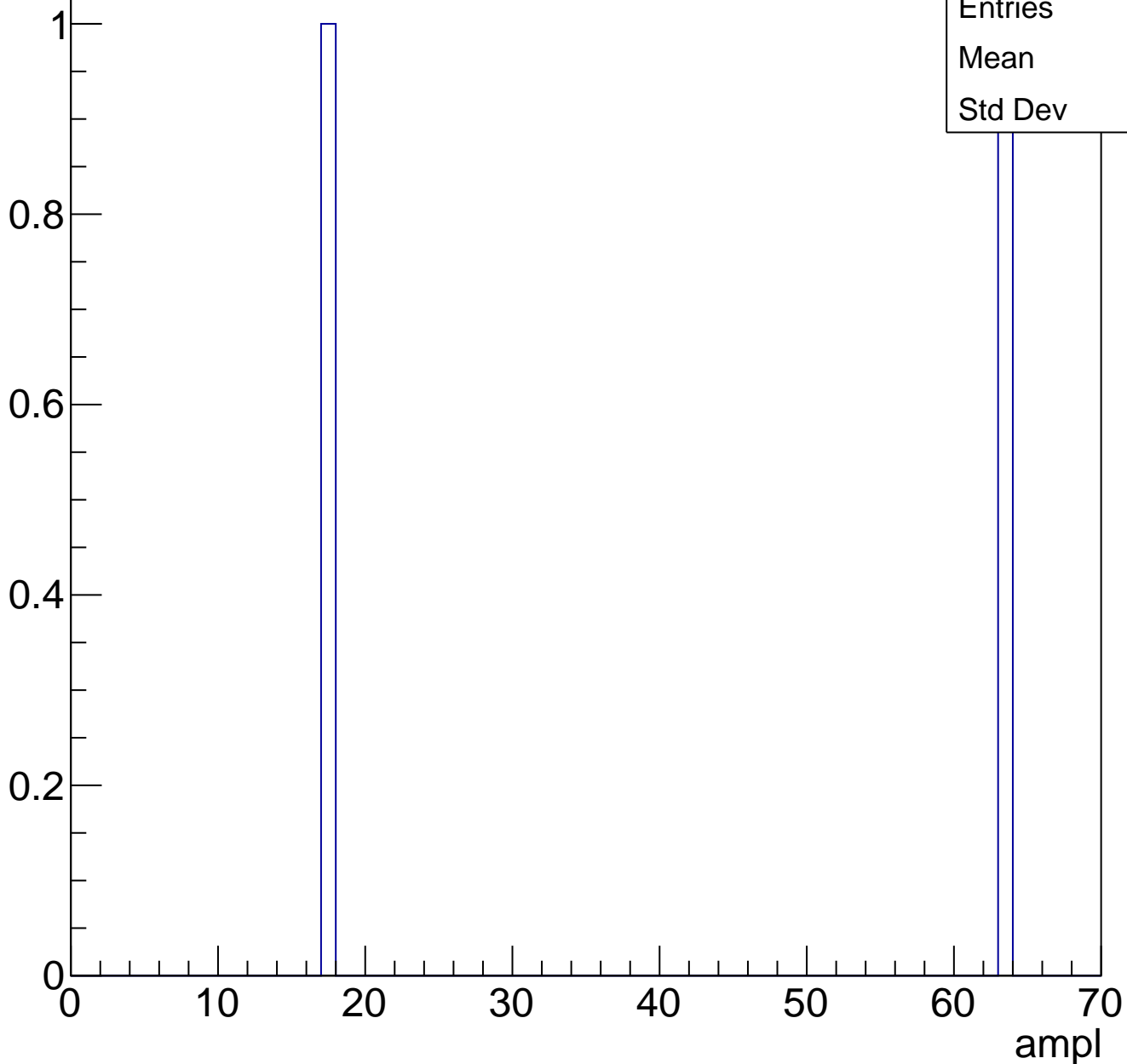
Entry



B1L102S, U11-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	40
Std Dev	23

B1L102S, U11-ch71, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	27.87
Std Dev	3.609

Gaus mean : 28.6945

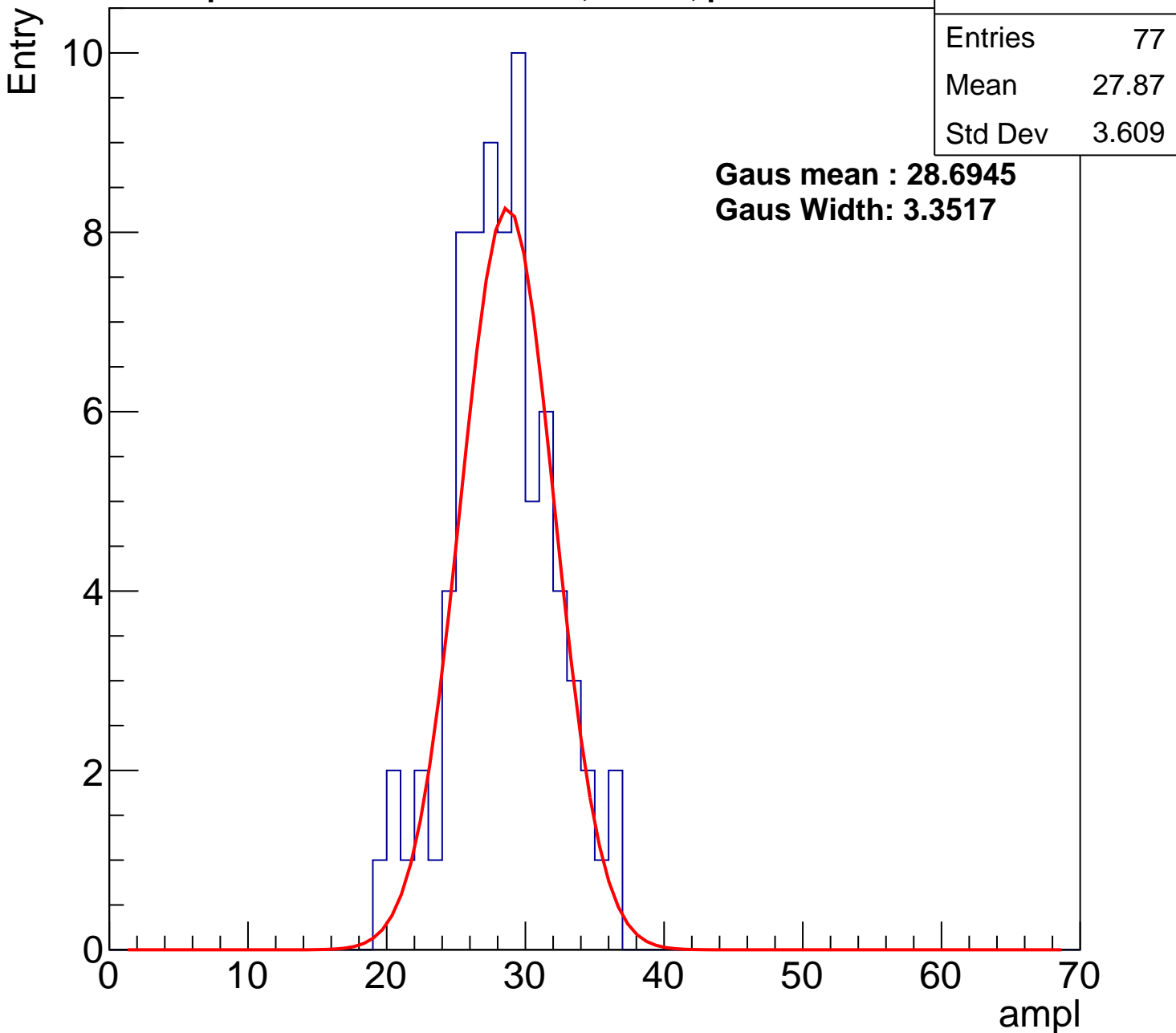
Gaus Width: 3.3517

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch71, adc1

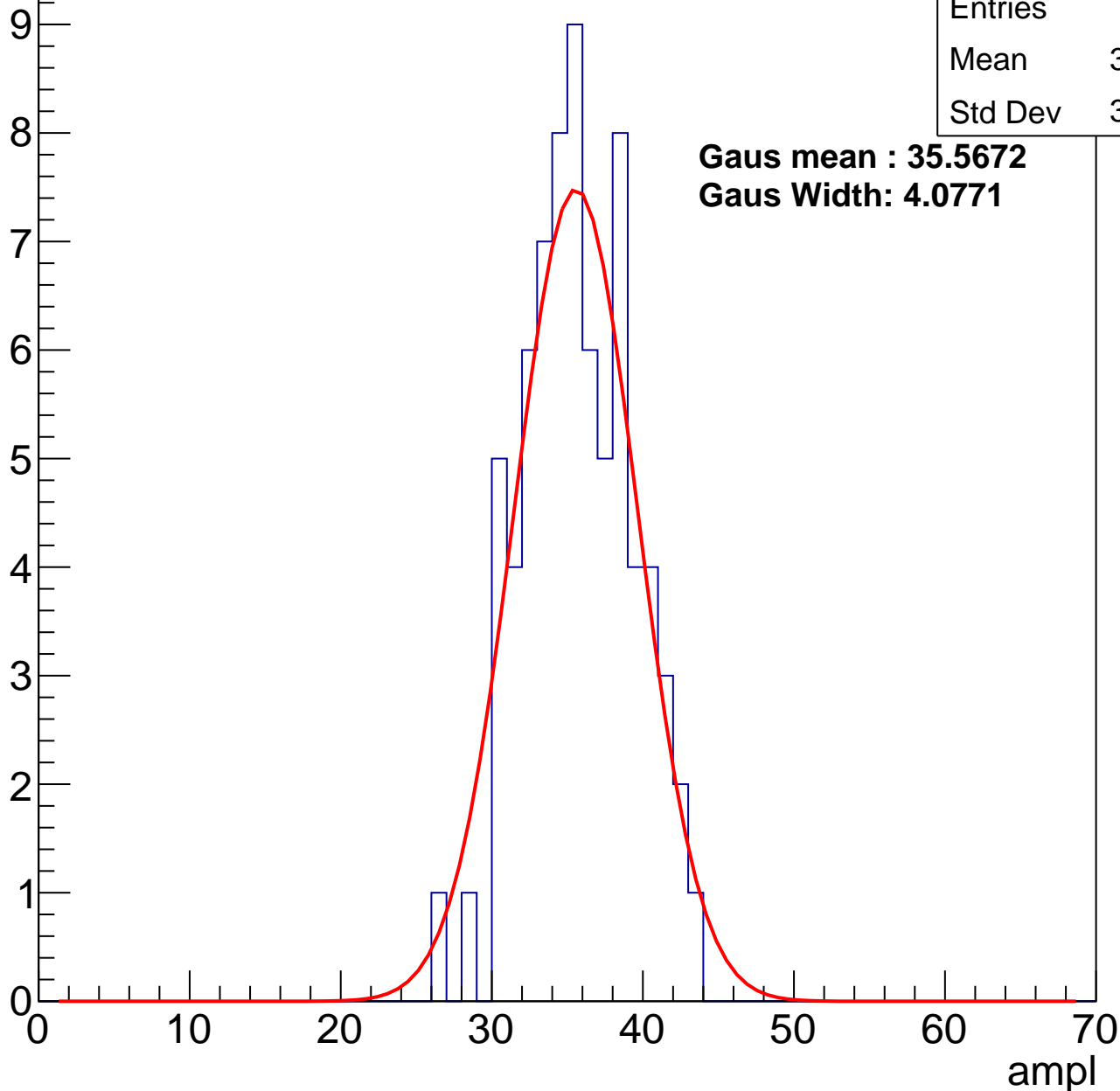
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.26
Std Dev	3.557

Gaus mean : 35.5672

Gaus Width: 4.0771



B1L102S, U11-ch71, adc2

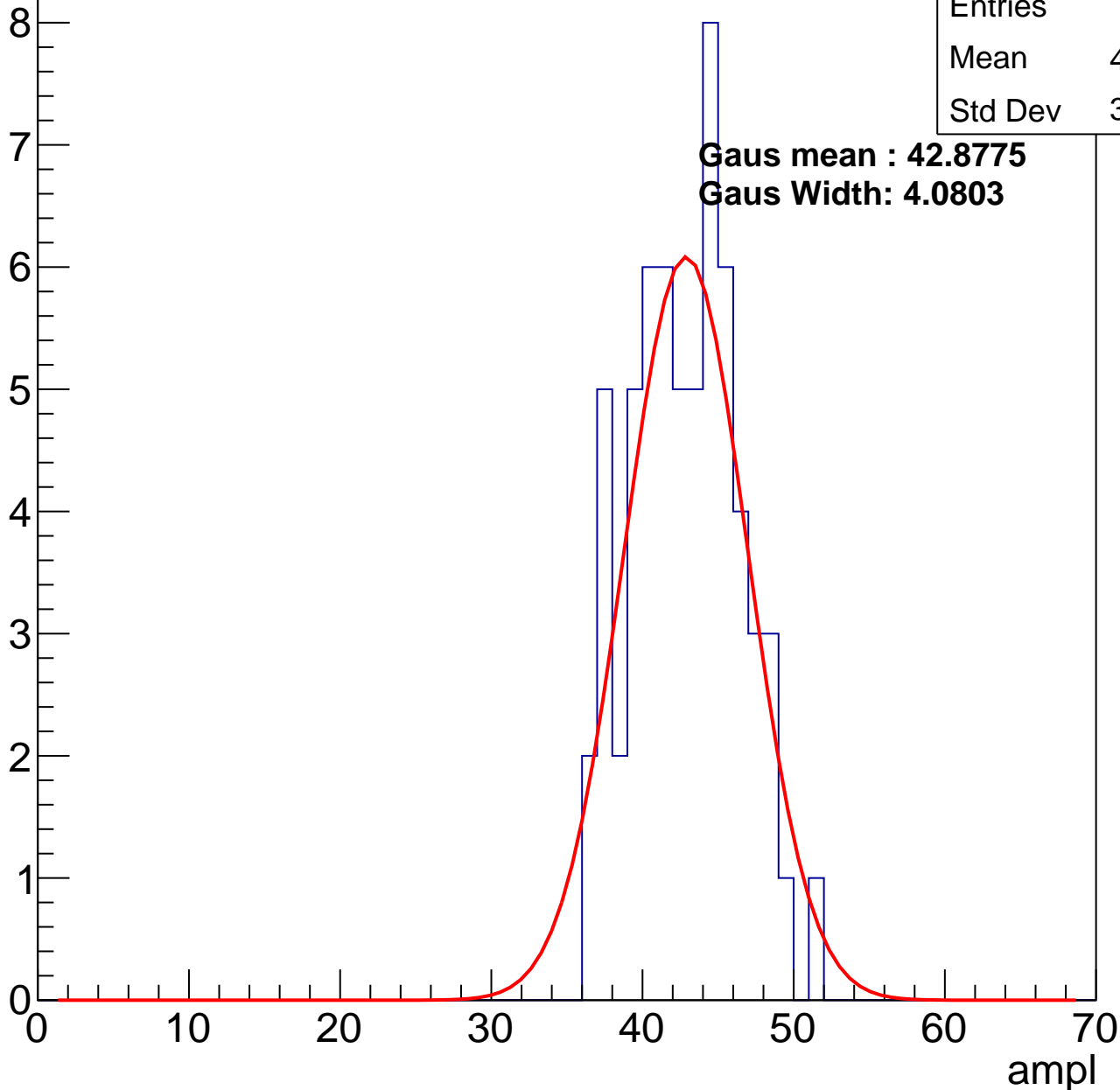
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.42
Std Dev	3.504

Gaus mean : 42.8775

Gaus Width: 4.0803

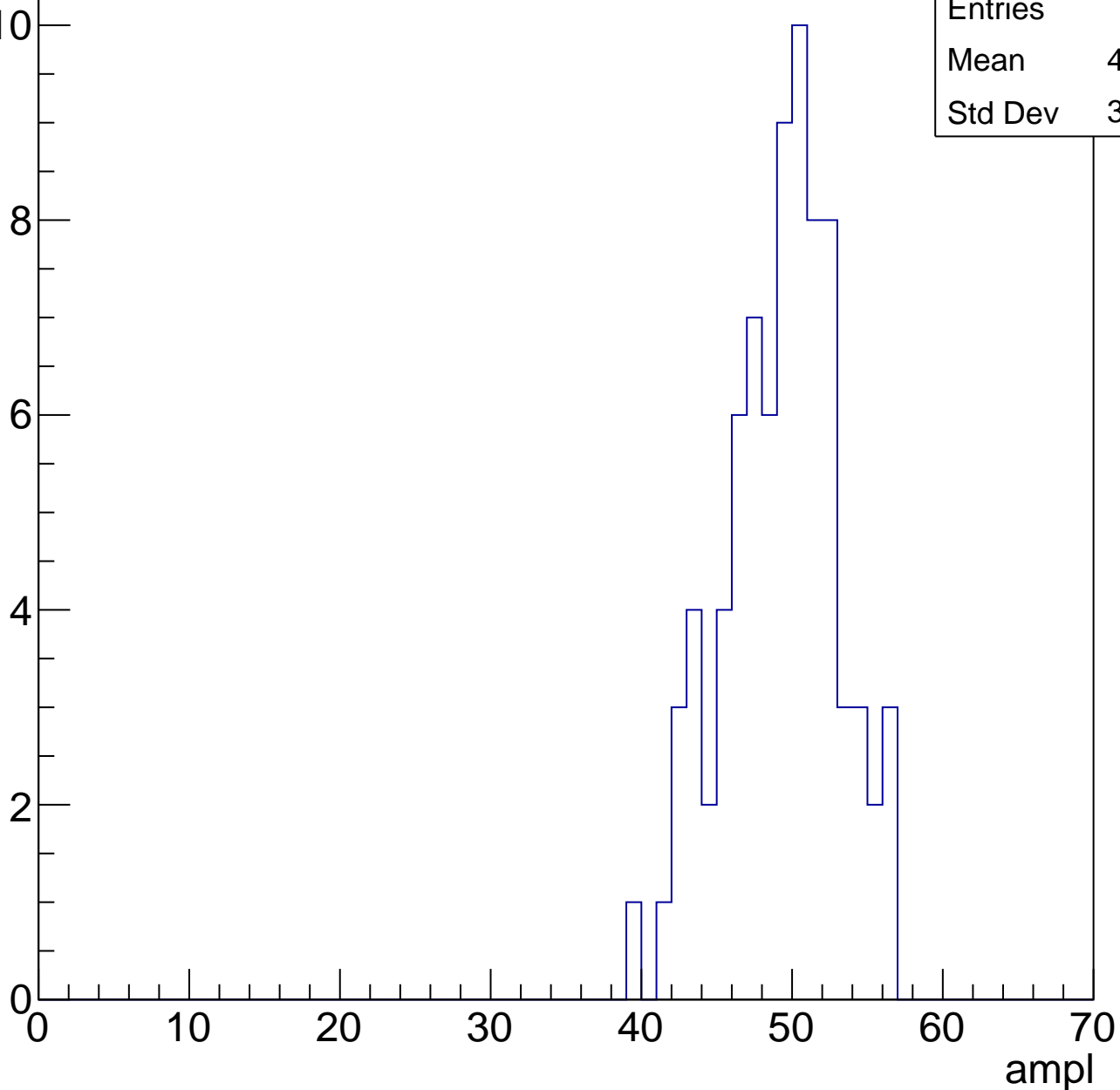


B1L102S, U11-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	48.79
Std Dev	3.724

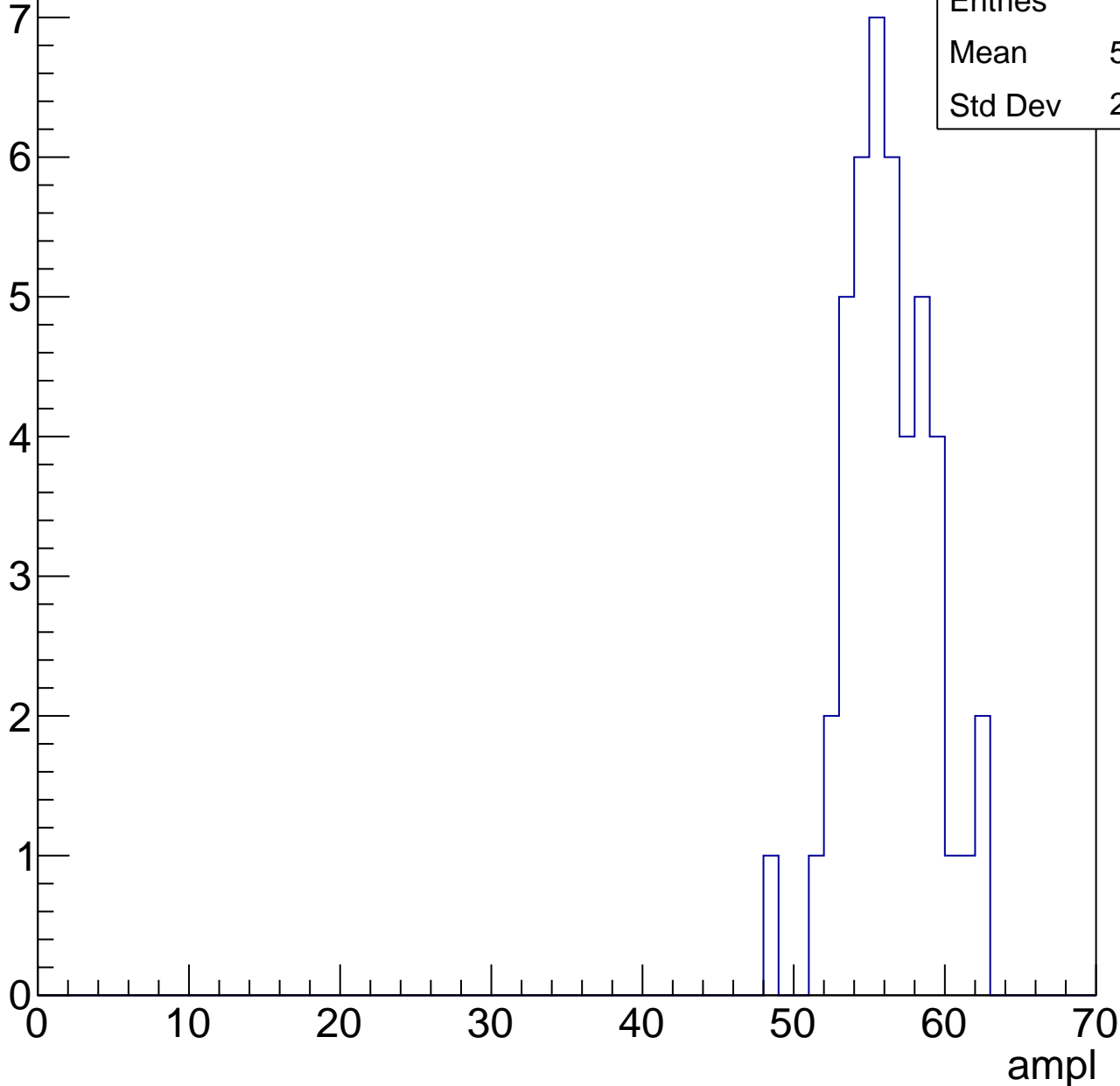


B1L102S, U11-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	55.82
Std Dev	2.862



B1L102S, U11-ch71, adc5

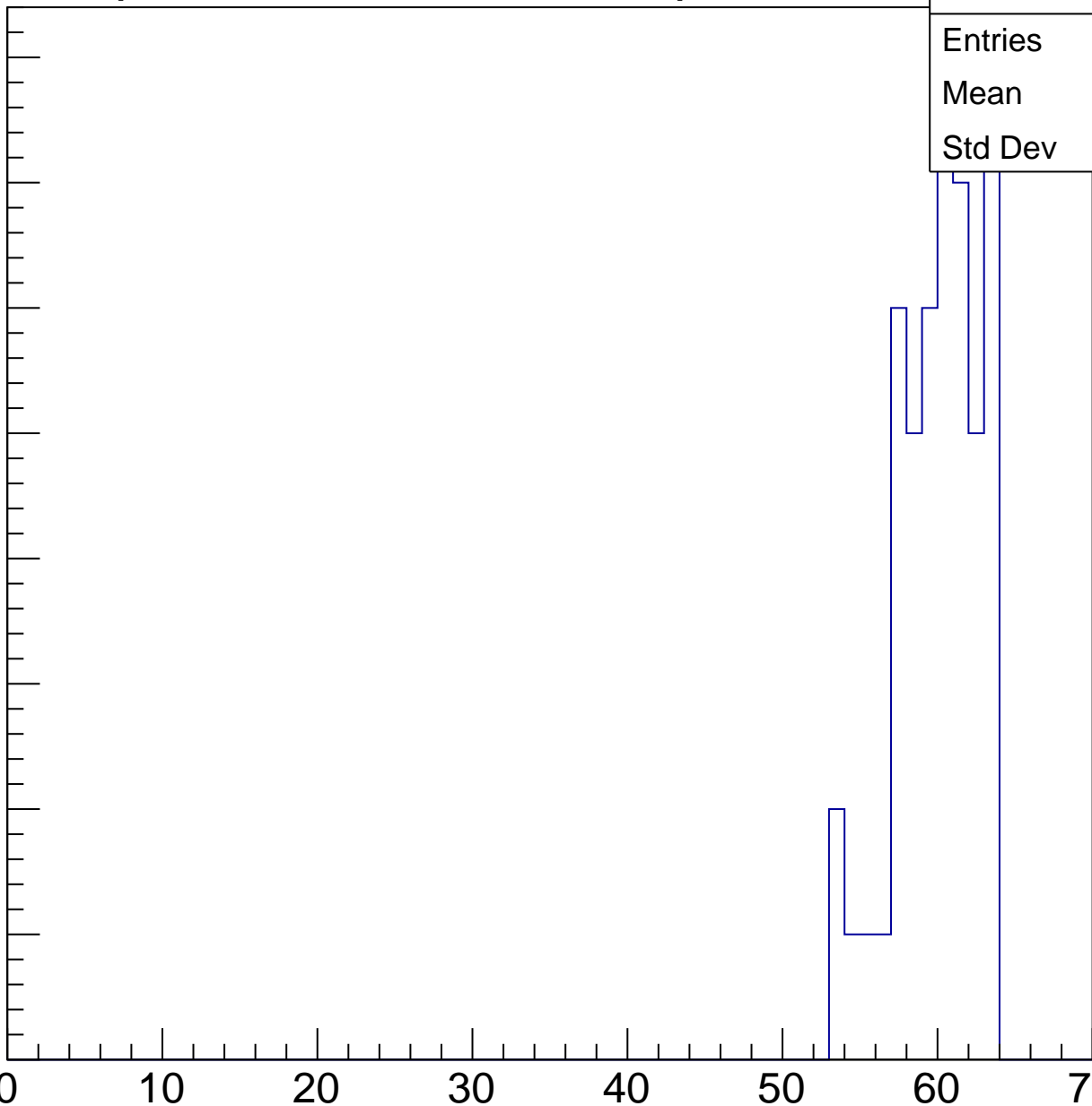
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.56
Std Dev	2.624

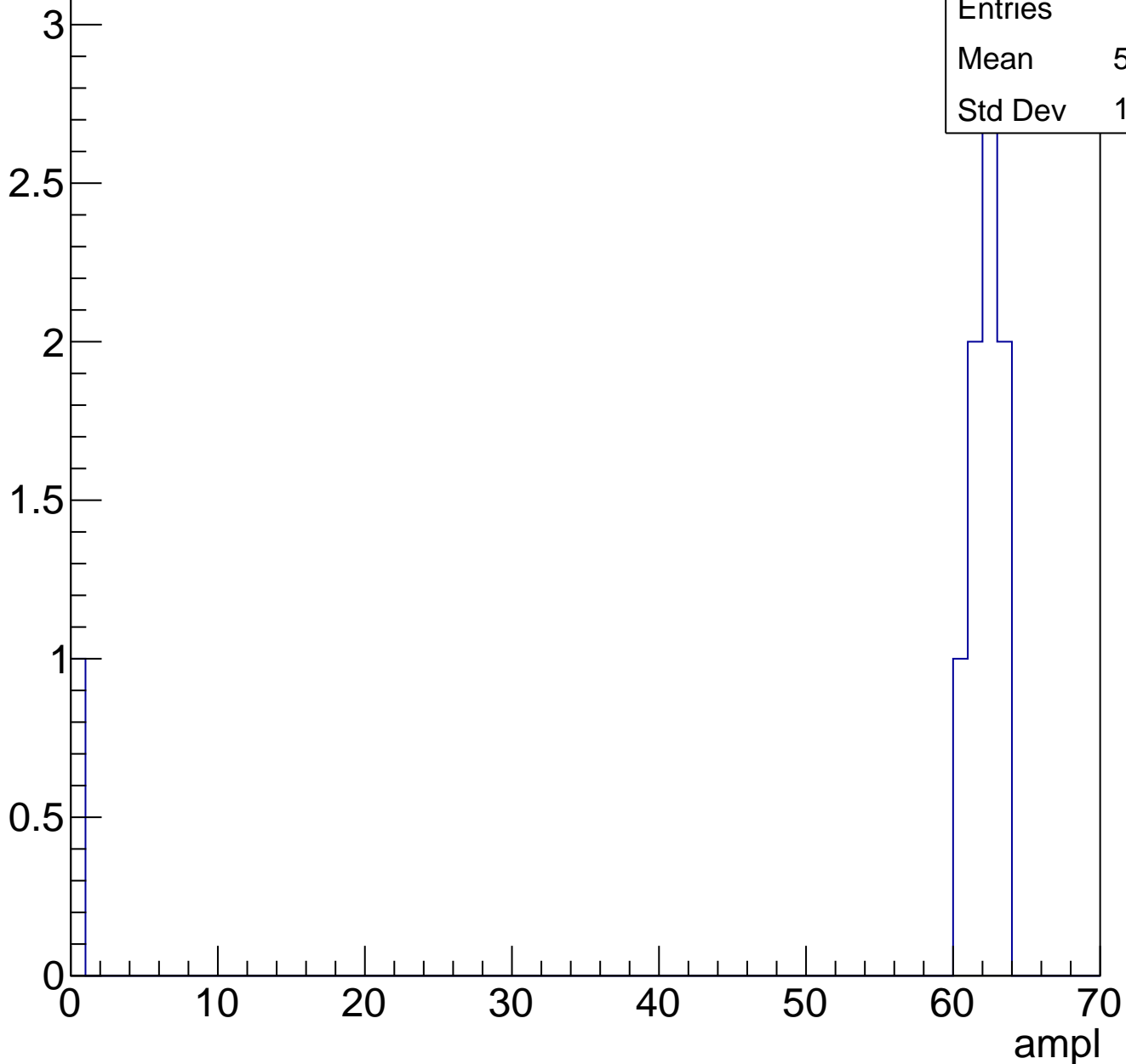
ampl



B1L102S, U11-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch72, adc0

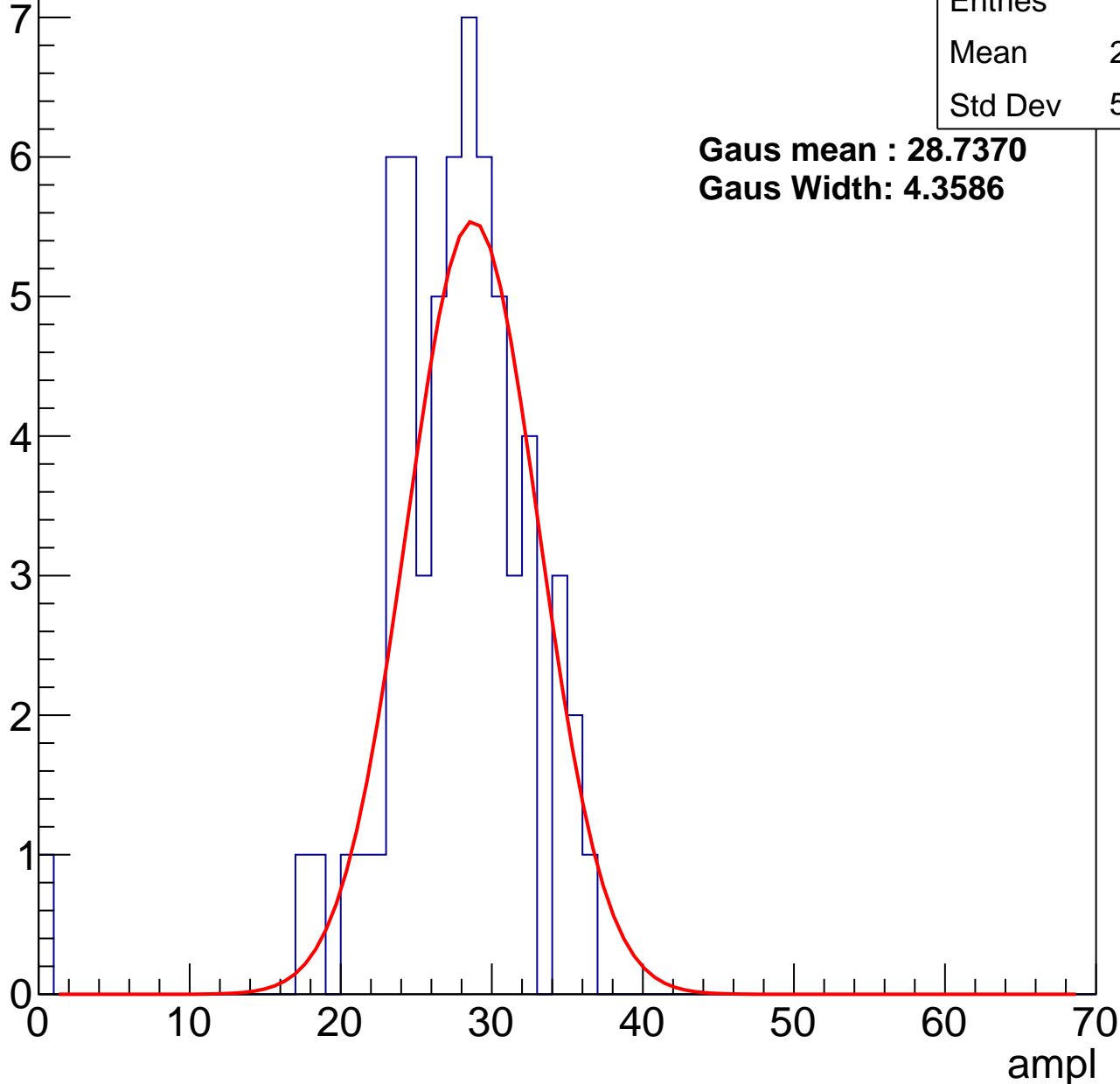
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	26.92
Std Dev	5.295

Gaus mean : 28.7370

Gaus Width: 4.3586



B1L102S, U11-ch72, adc1

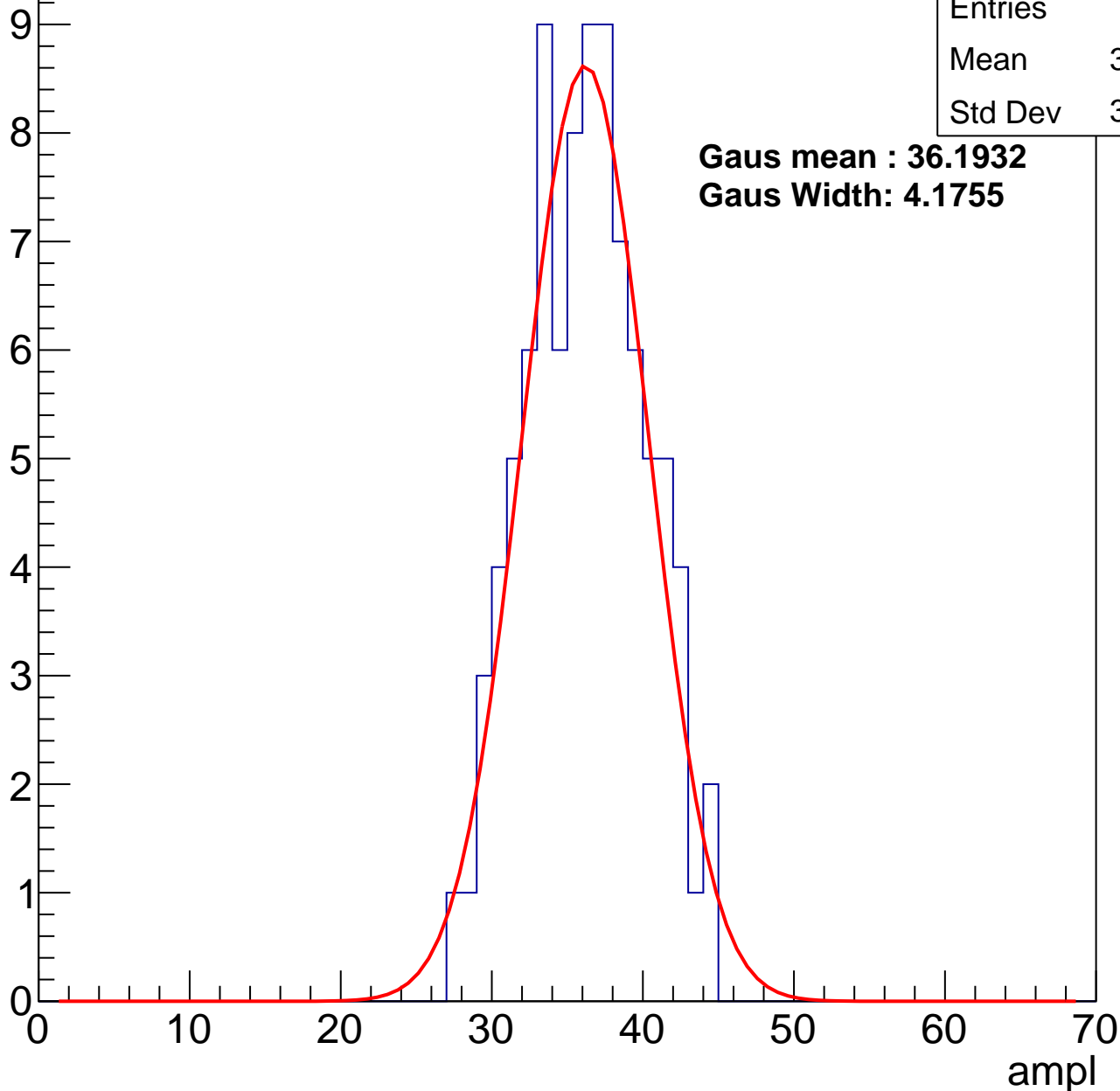
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	91
Mean	35.73
Std Dev	3.894

Gaus mean : 36.1932

Gaus Width: 4.1755



B1L102S, U11-ch72, adc2

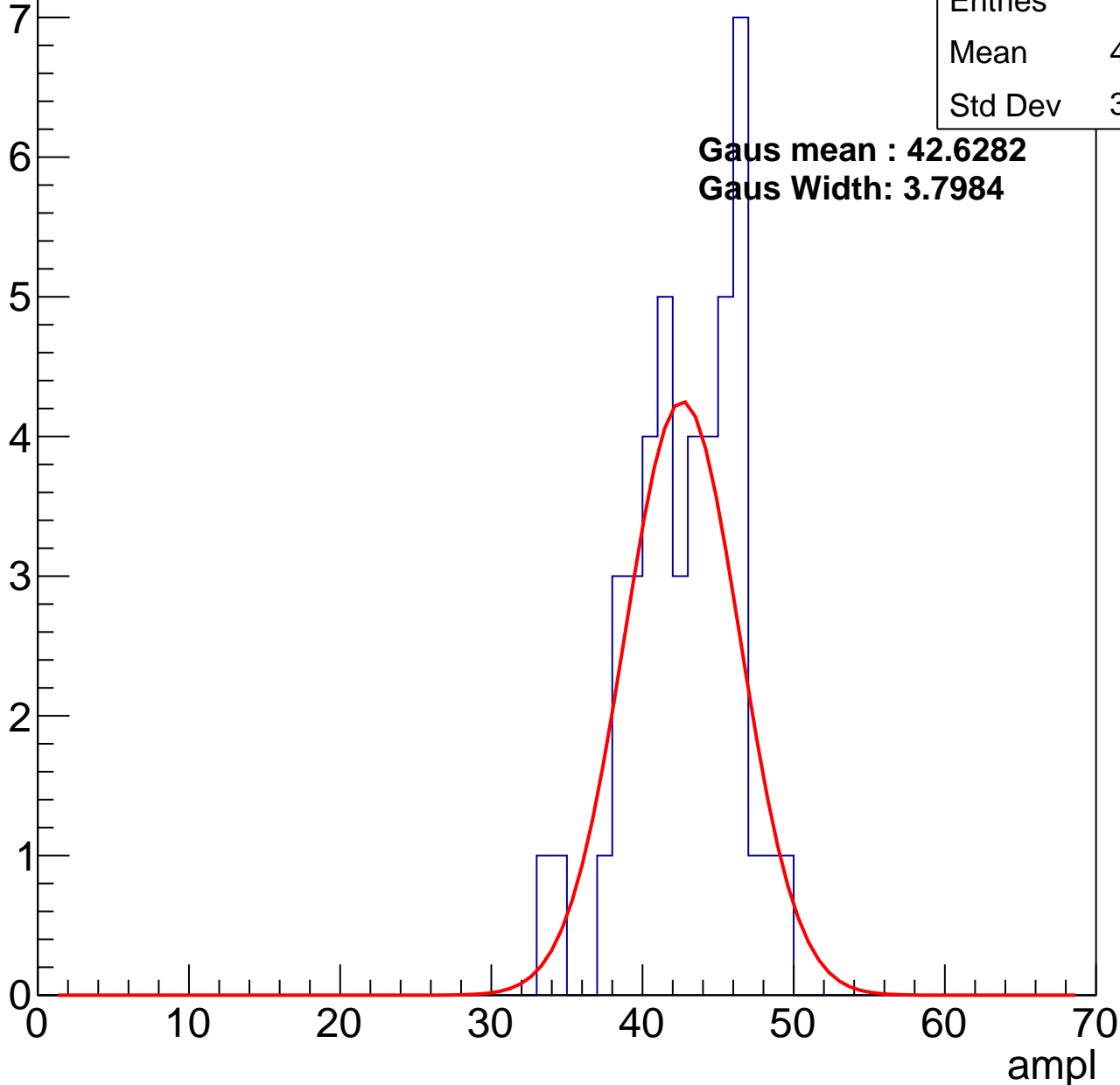
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	42.39
Std Dev	3.524

Gaus mean : 42.6282

Gaus Width: 3.7984

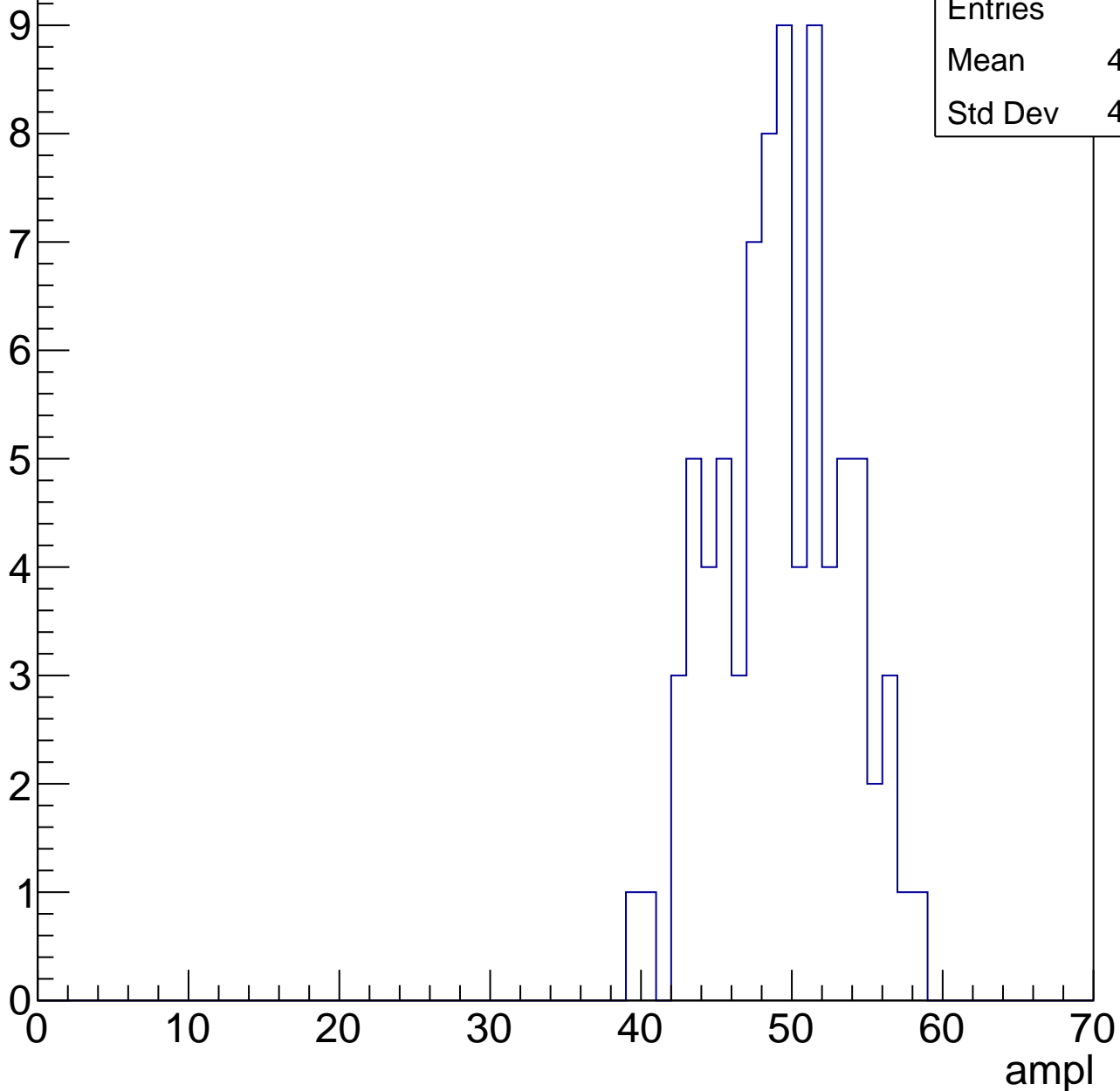


B1L102S, U11-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	48.85
Std Dev	4.175

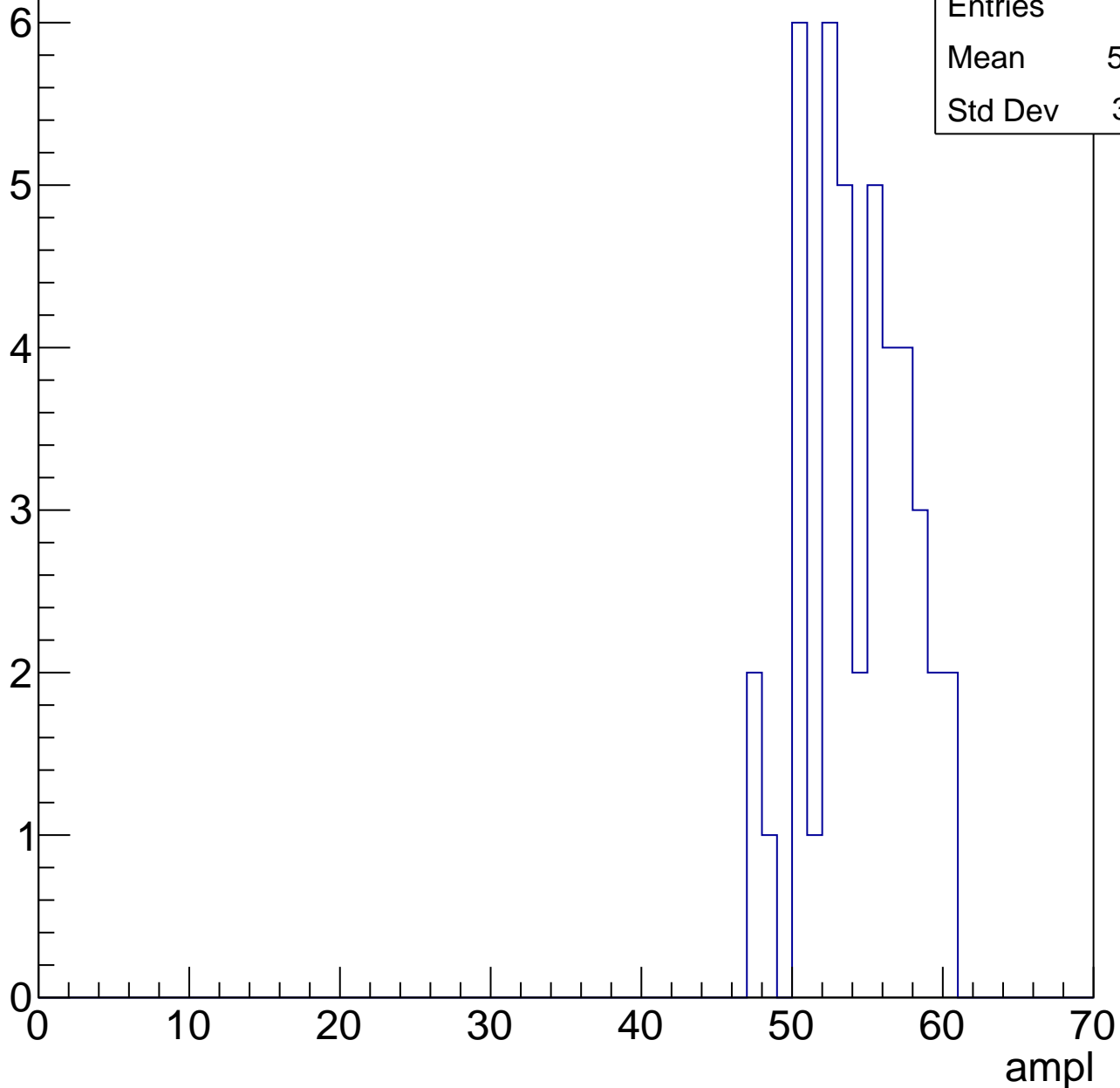


B1L102S, U11-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

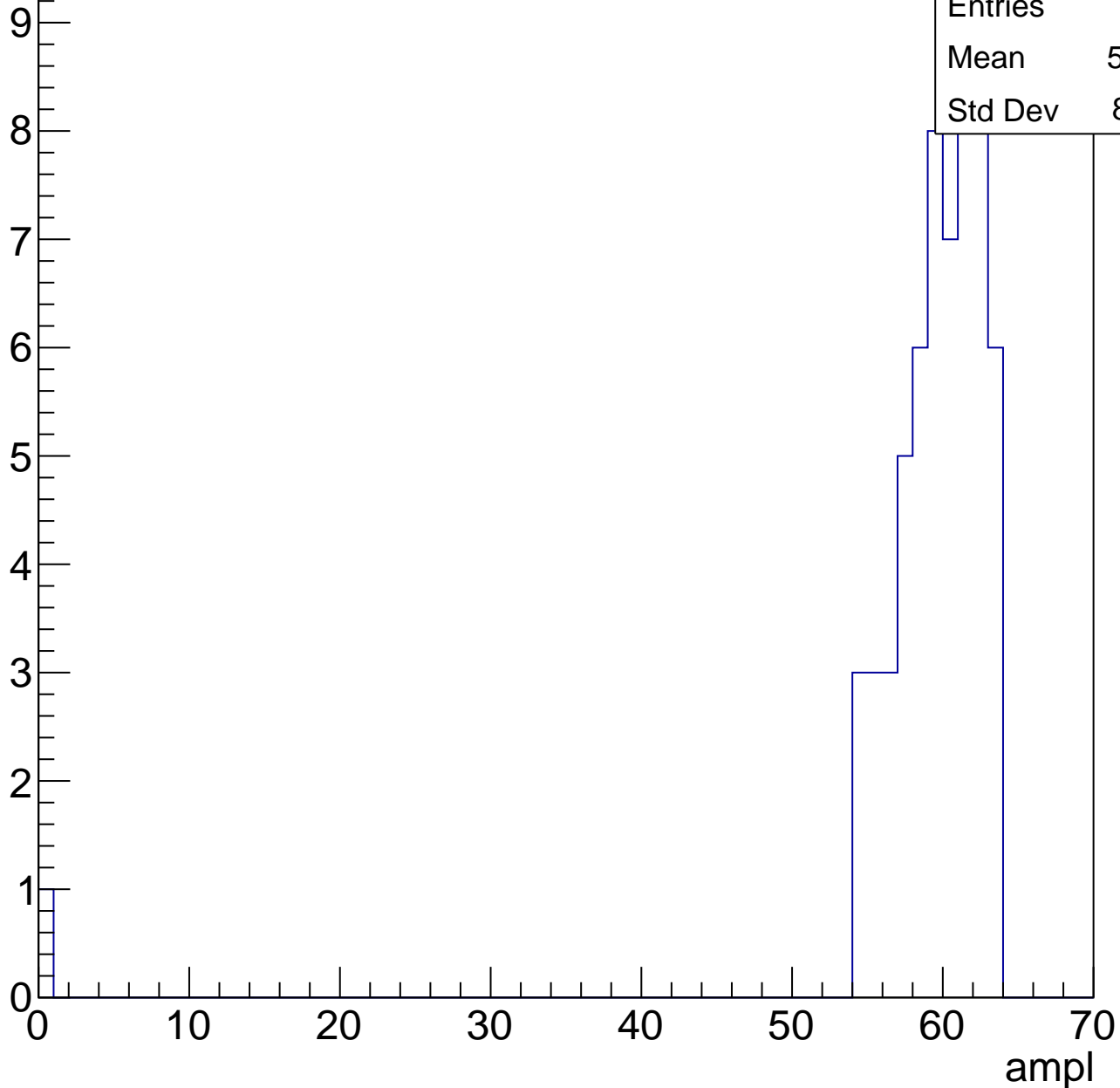
Entries	43
Mean	53.88
Std Dev	3.391



B1L102S, U11-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

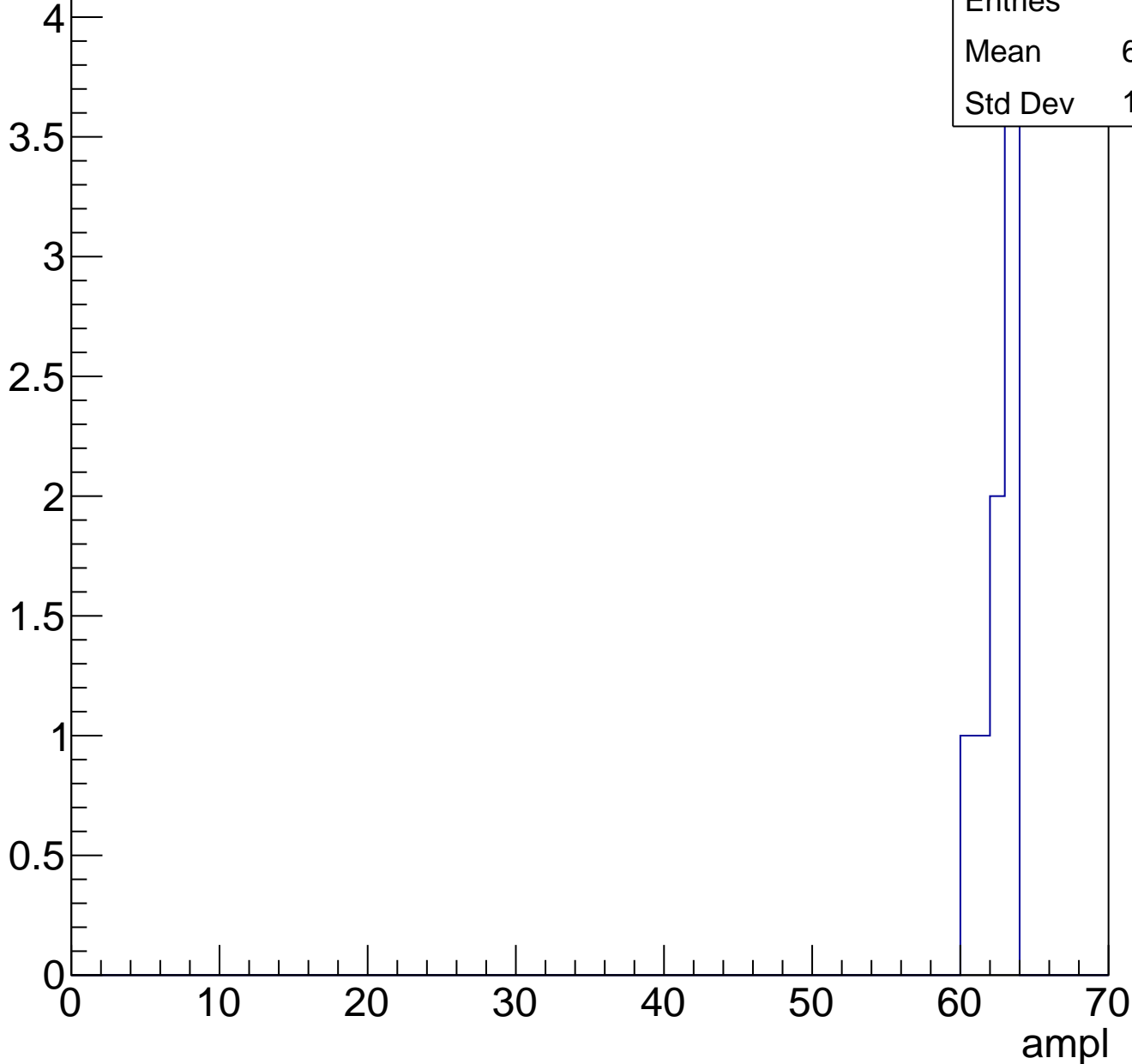
Entry



B1L102S, U11-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch73, adc0

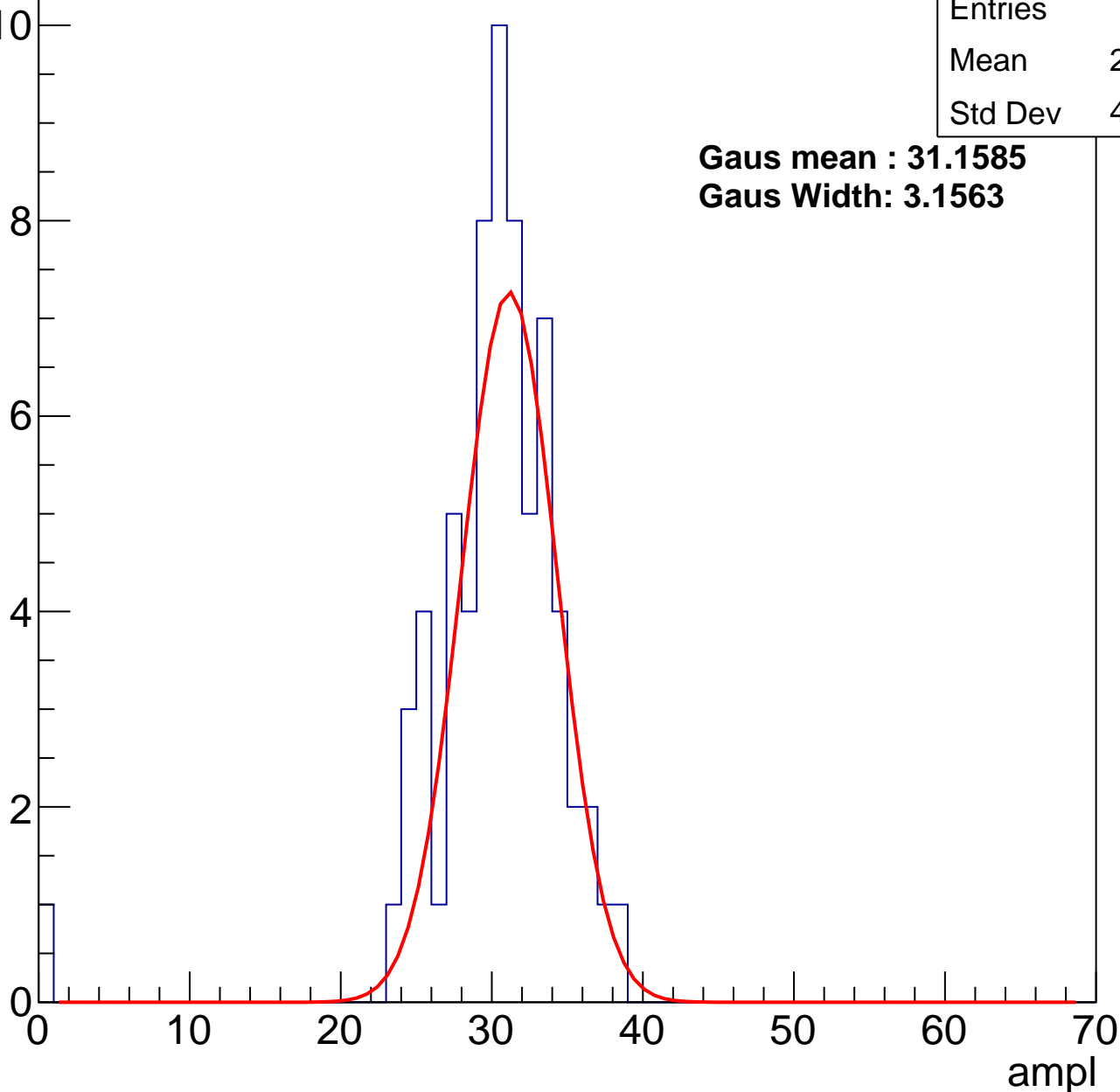
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.73
Std Dev	4.928

Gaus mean : 31.1585

Gaus Width: 3.1563



B1L102S, U11-ch73, adc1

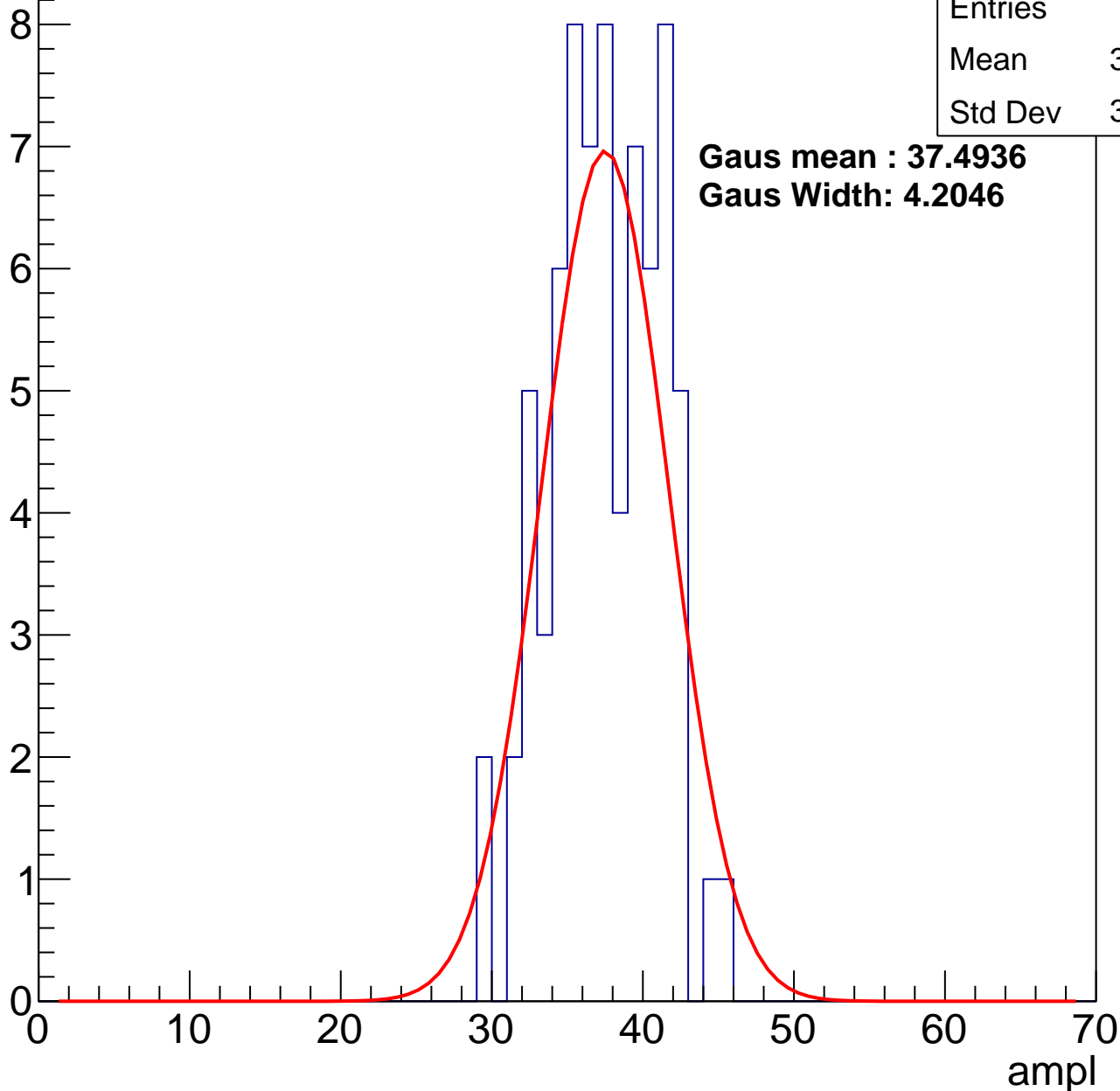
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.03
Std Dev	3.546

Gaus mean : 37.4936

Gaus Width: 4.2046



B1L102S, U11-ch73, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	44.34
Std Dev	3.822

Gaus mean : 45.0721

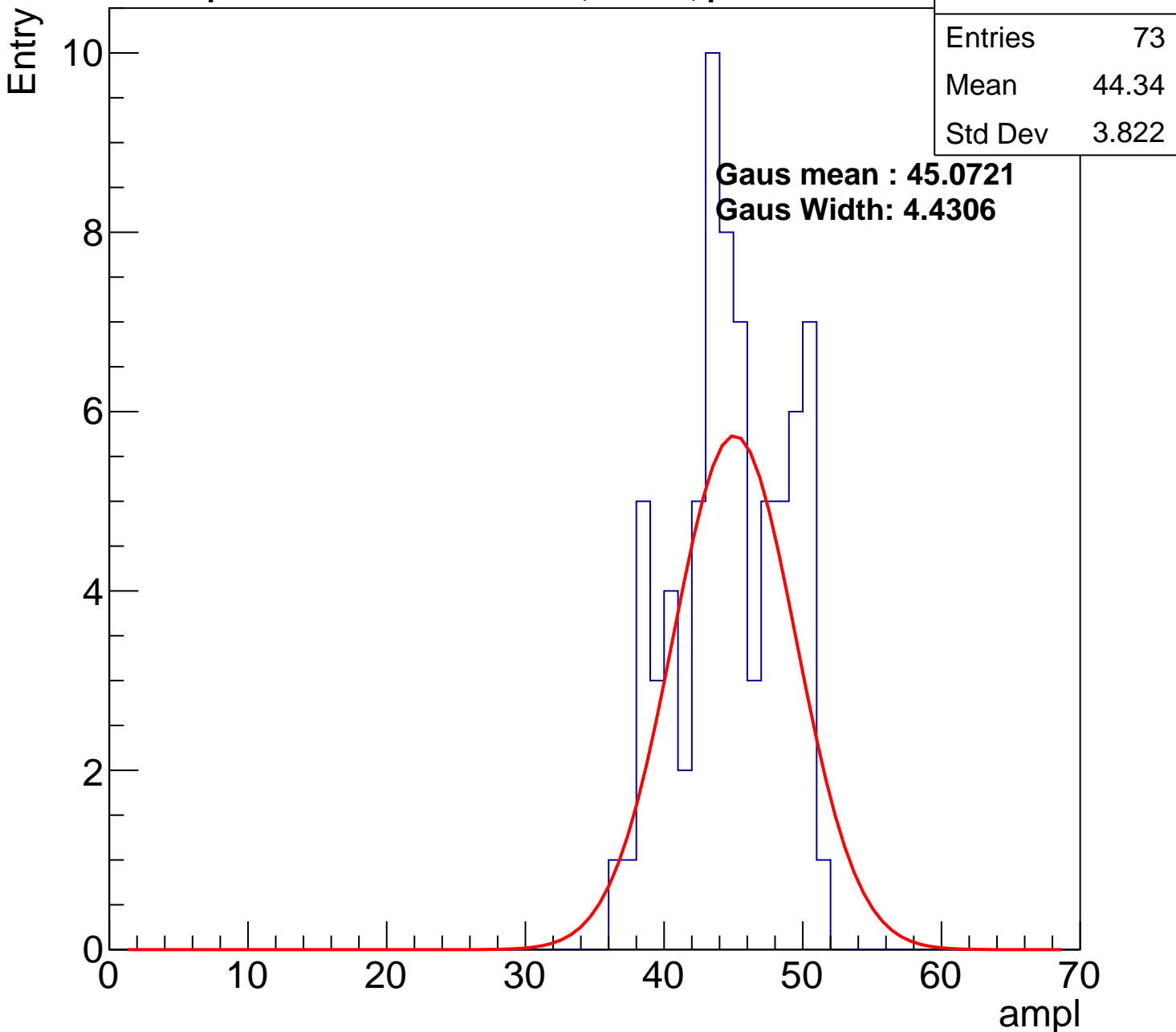
Gaus Width: 4.4306

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

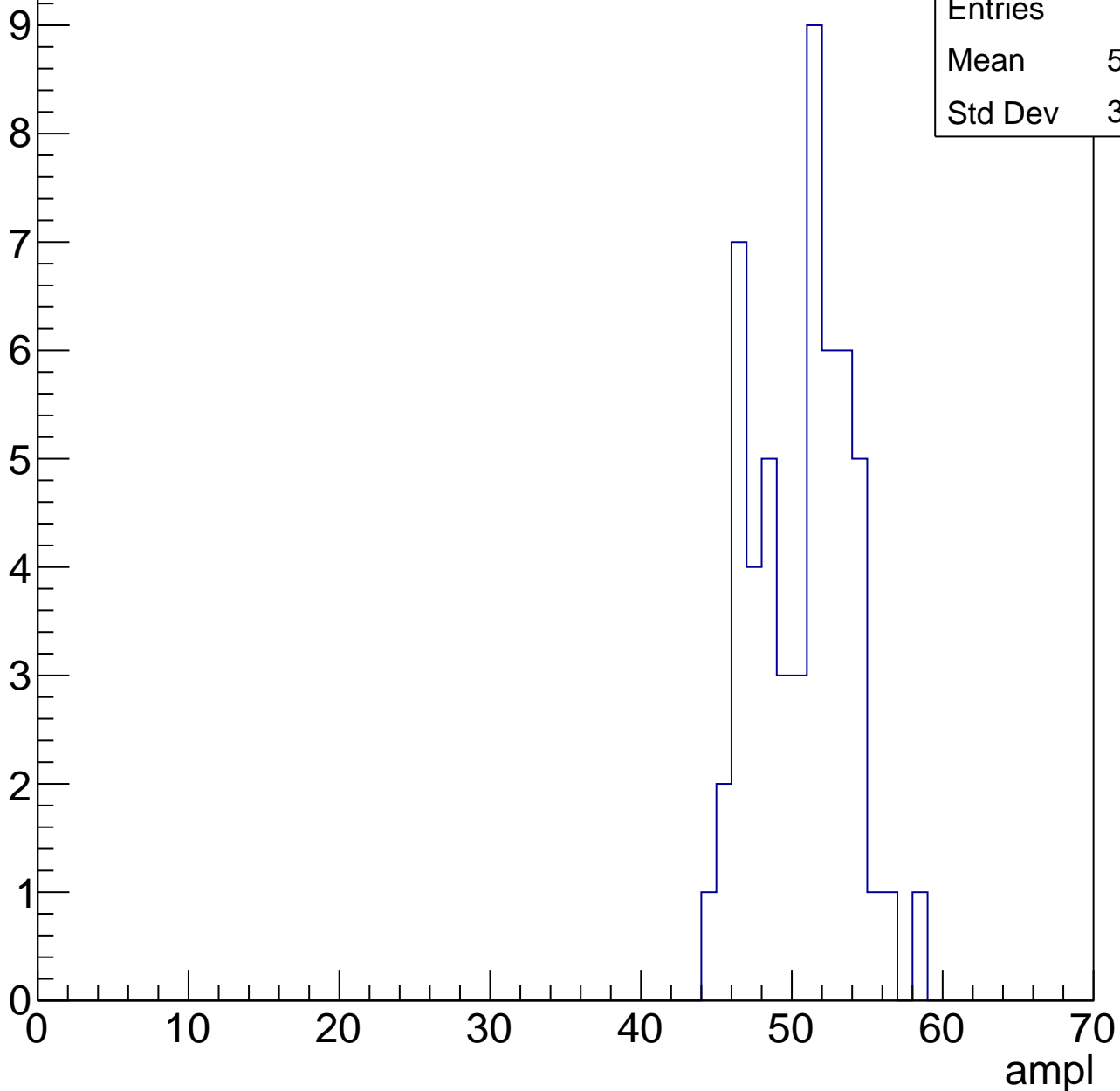


B1L102S, U11-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	50.17
Std Dev	3.184

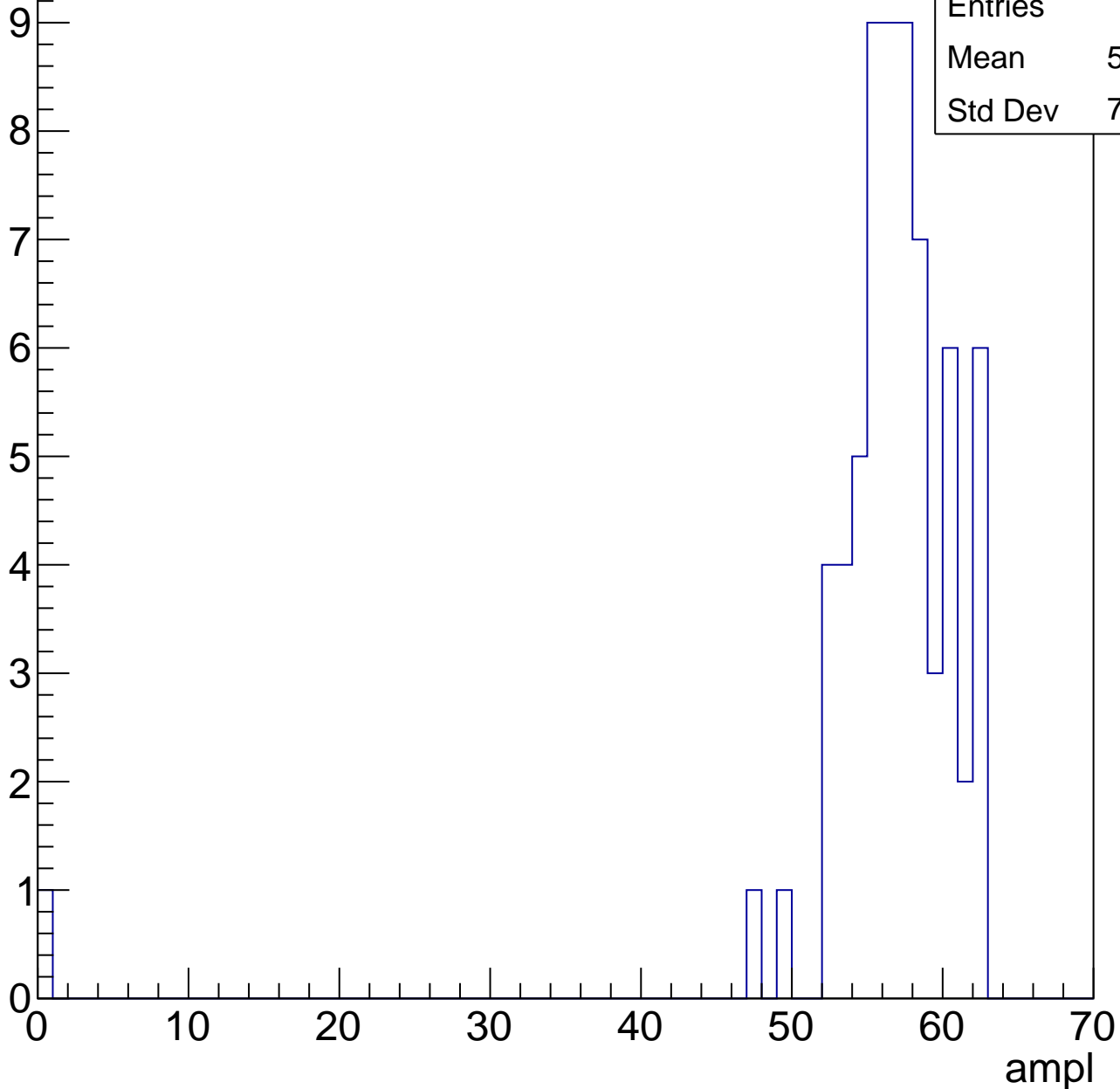


B1L102S, U11-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

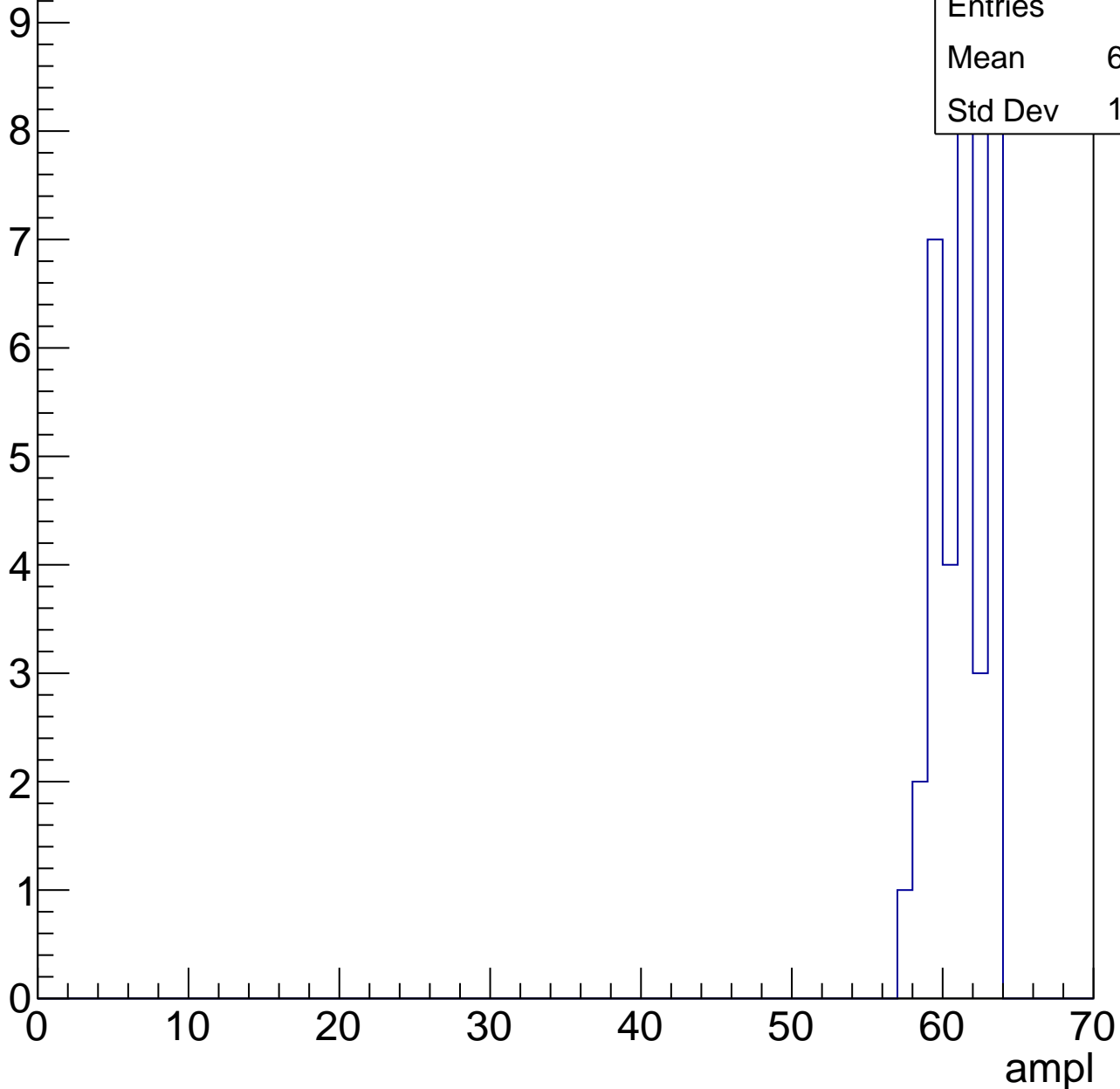
Entries	67
Mean	55.75
Std Dev	7.548



B1L102S, U11-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

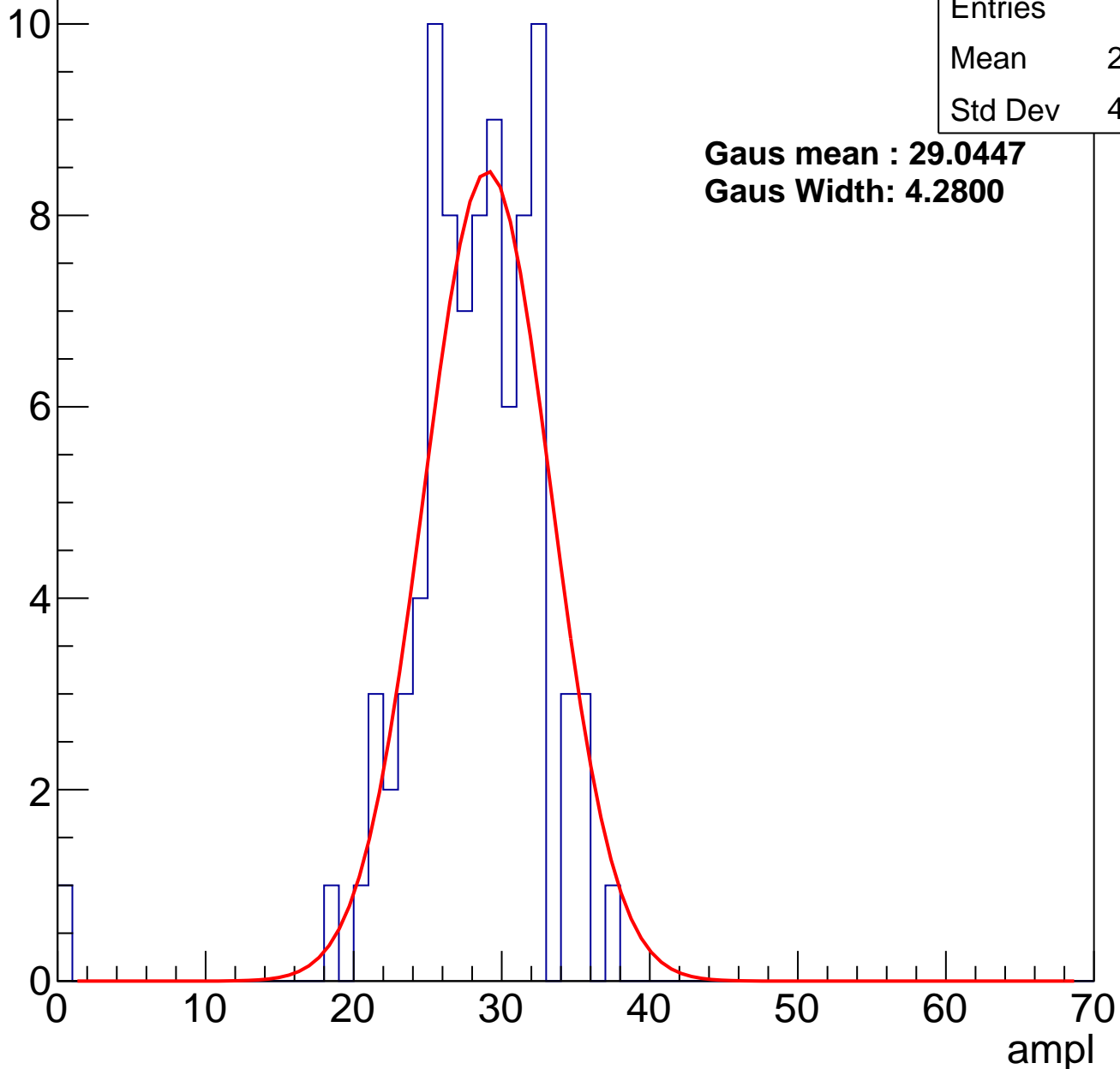
B1L102S, U11-ch74, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	27.66
Std Dev	4.798

Gaus mean : 29.0447
Gaus Width: 4.2800

Entry



B1L102S, U11-ch74, adc1

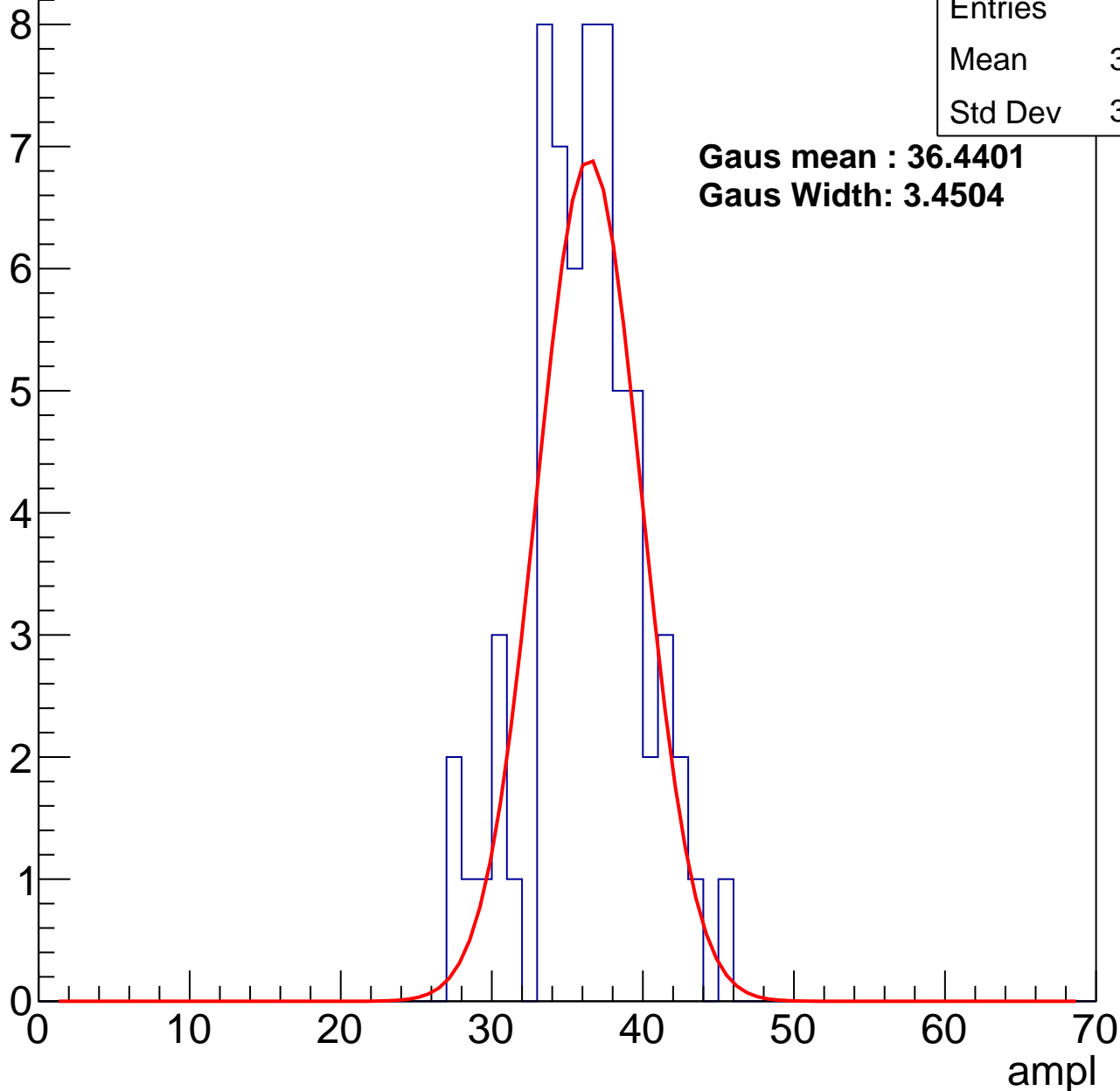
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.75
Std Dev	3.733

Gaus mean : 36.4401

Gaus Width: 3.4504

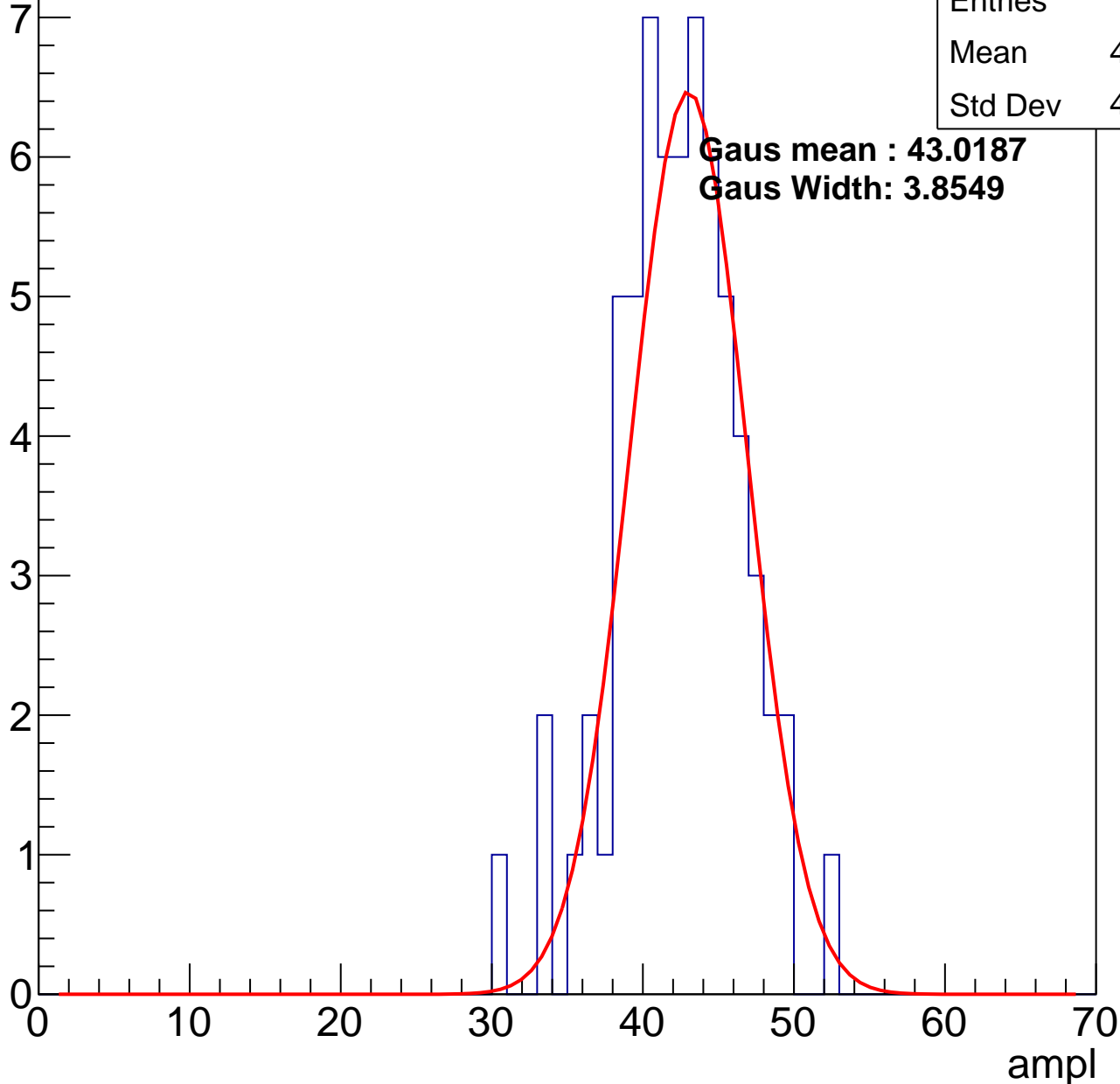


B1L102S, U11-ch74, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.88
Std Dev	4.073

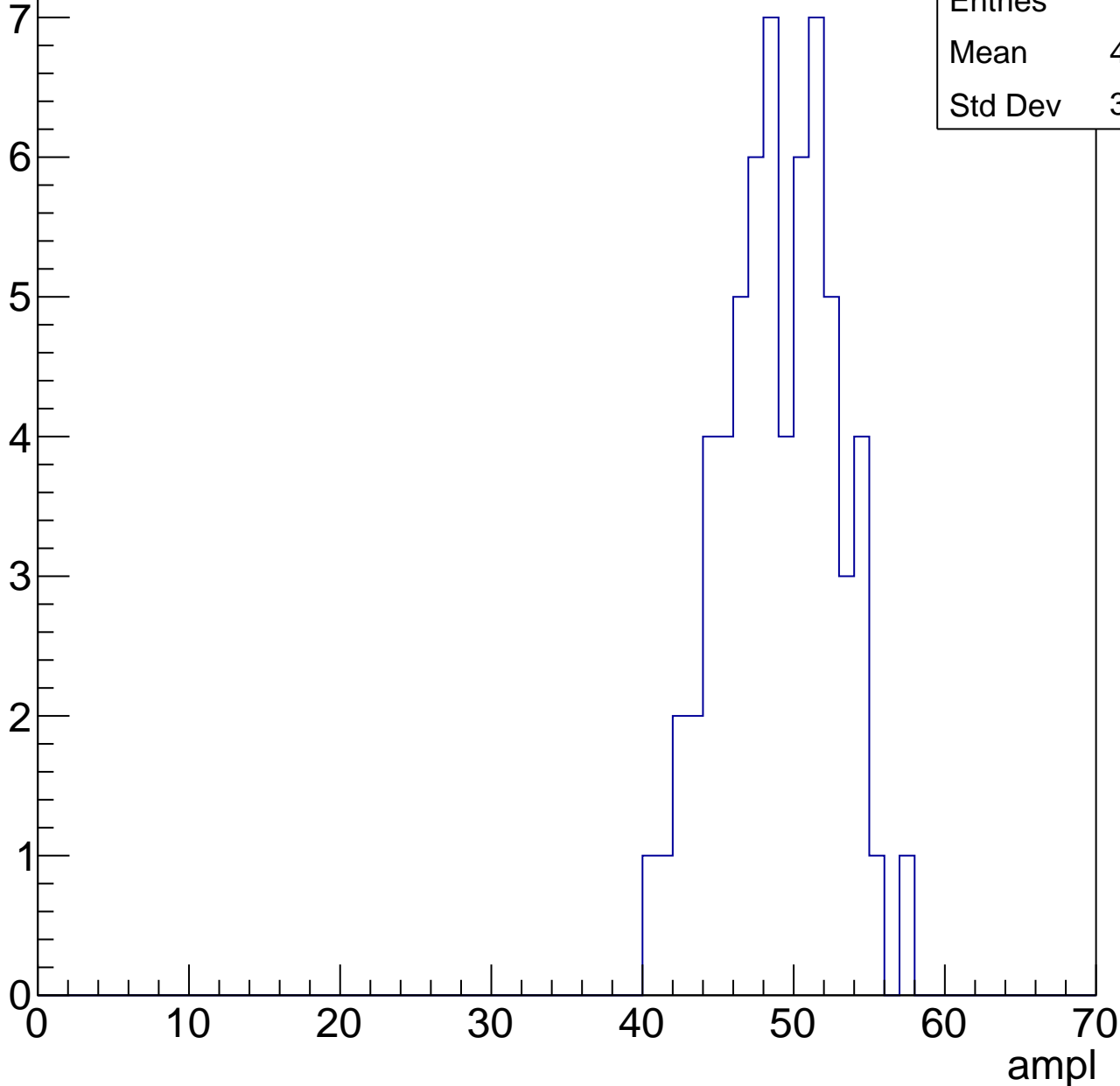


B1L102S, U11-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.49
Std Dev	3.707

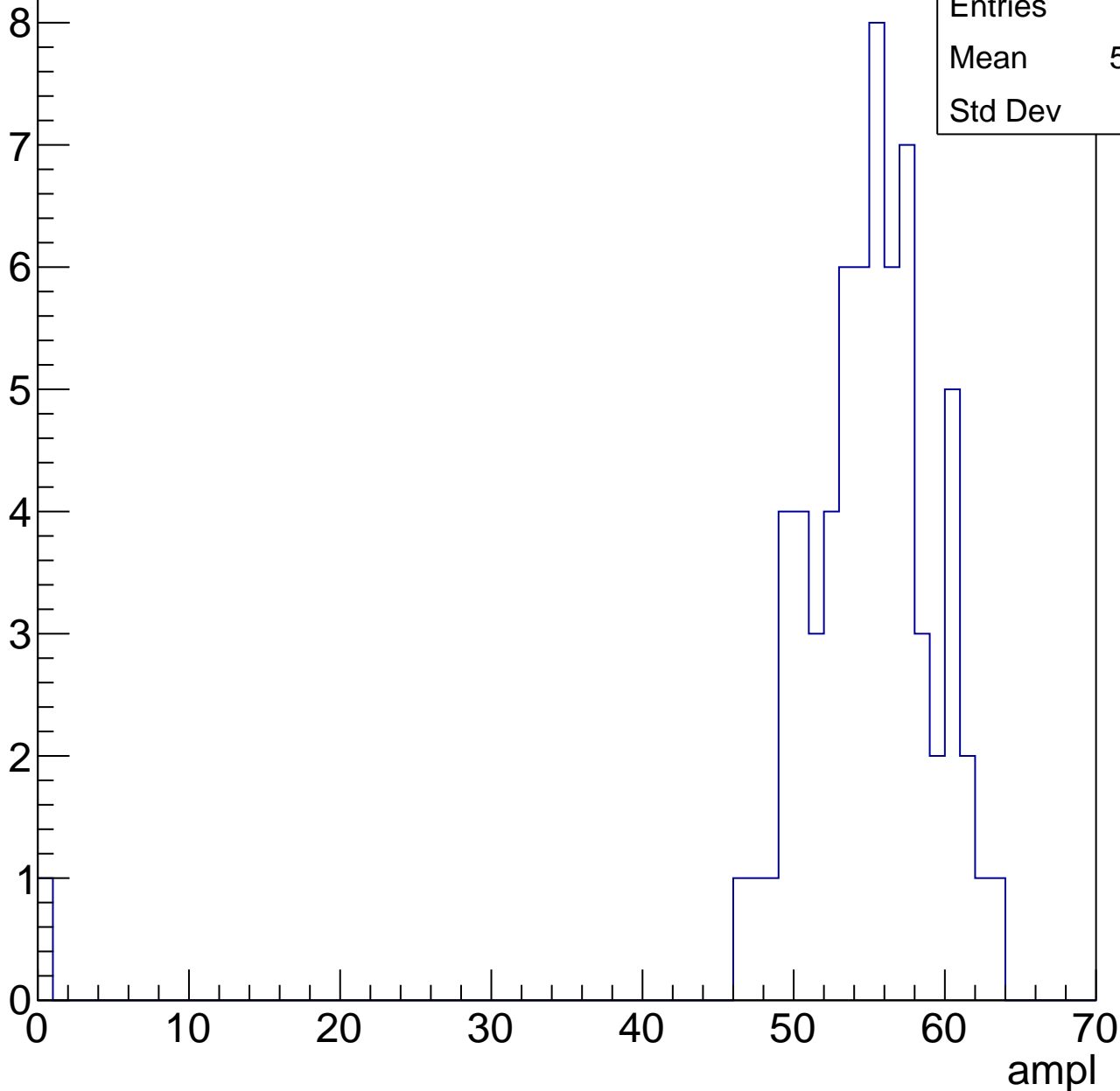


B1L102S, U11-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.85
Std Dev	7.68

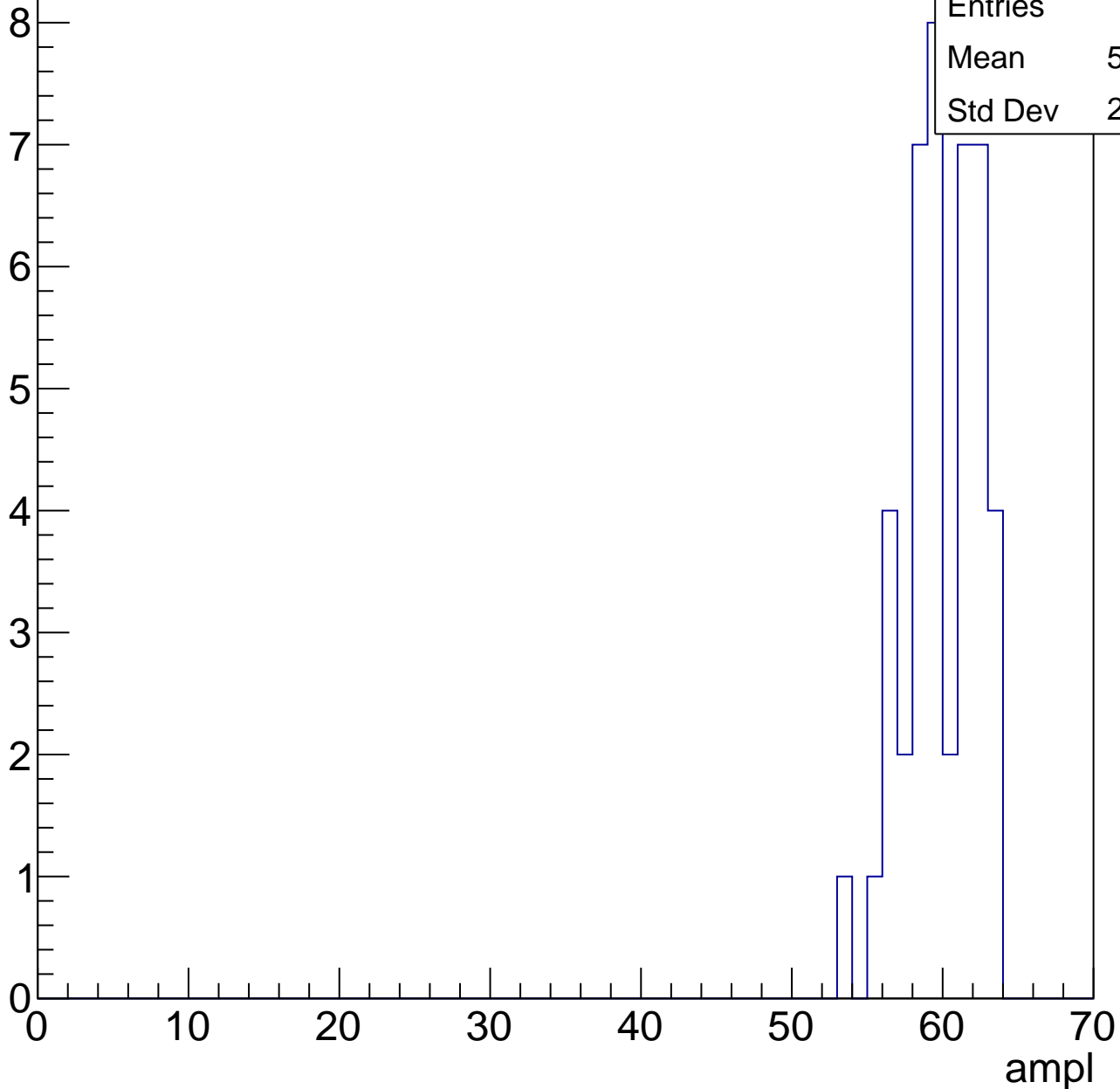


B1L102S, U11-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.47
Std Dev	2.415

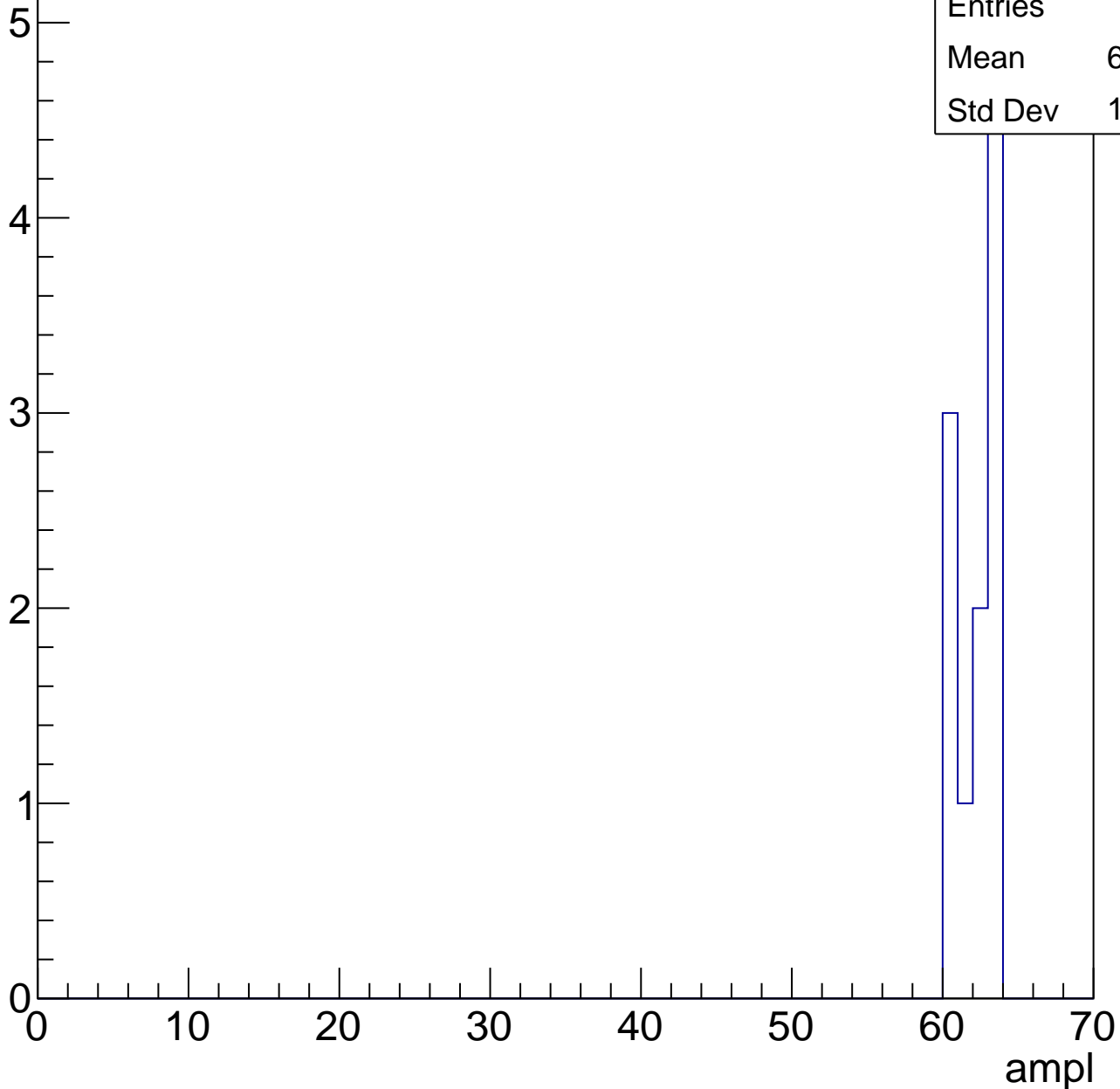


B1L102S, U11-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.82
Std Dev	1.266



B1L102S, U11-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch75, adc0

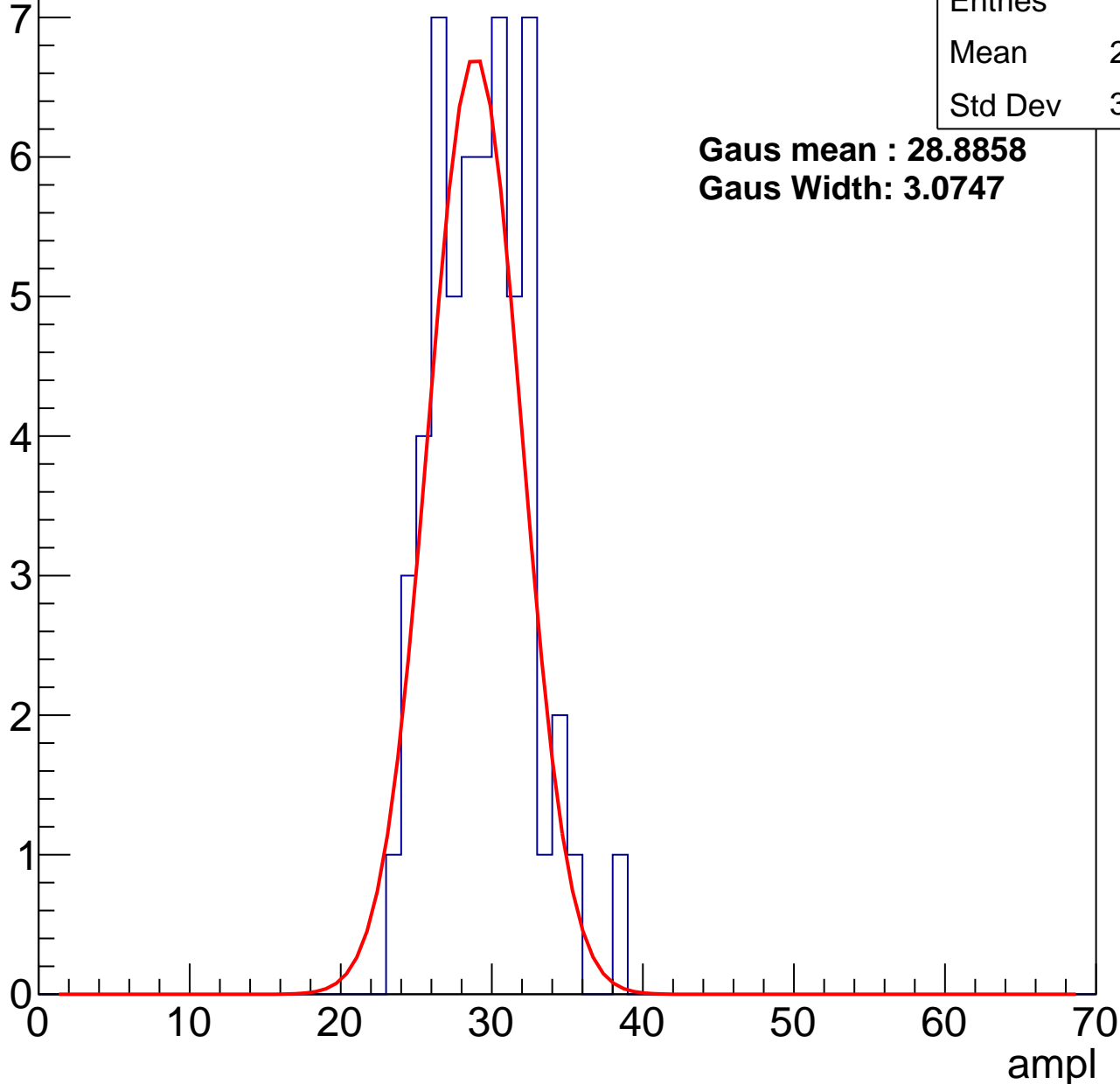
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.88
Std Dev	3.094

Gaus mean : 28.8858

Gaus Width: 3.0747



B1L102S, U11-ch75, adc1

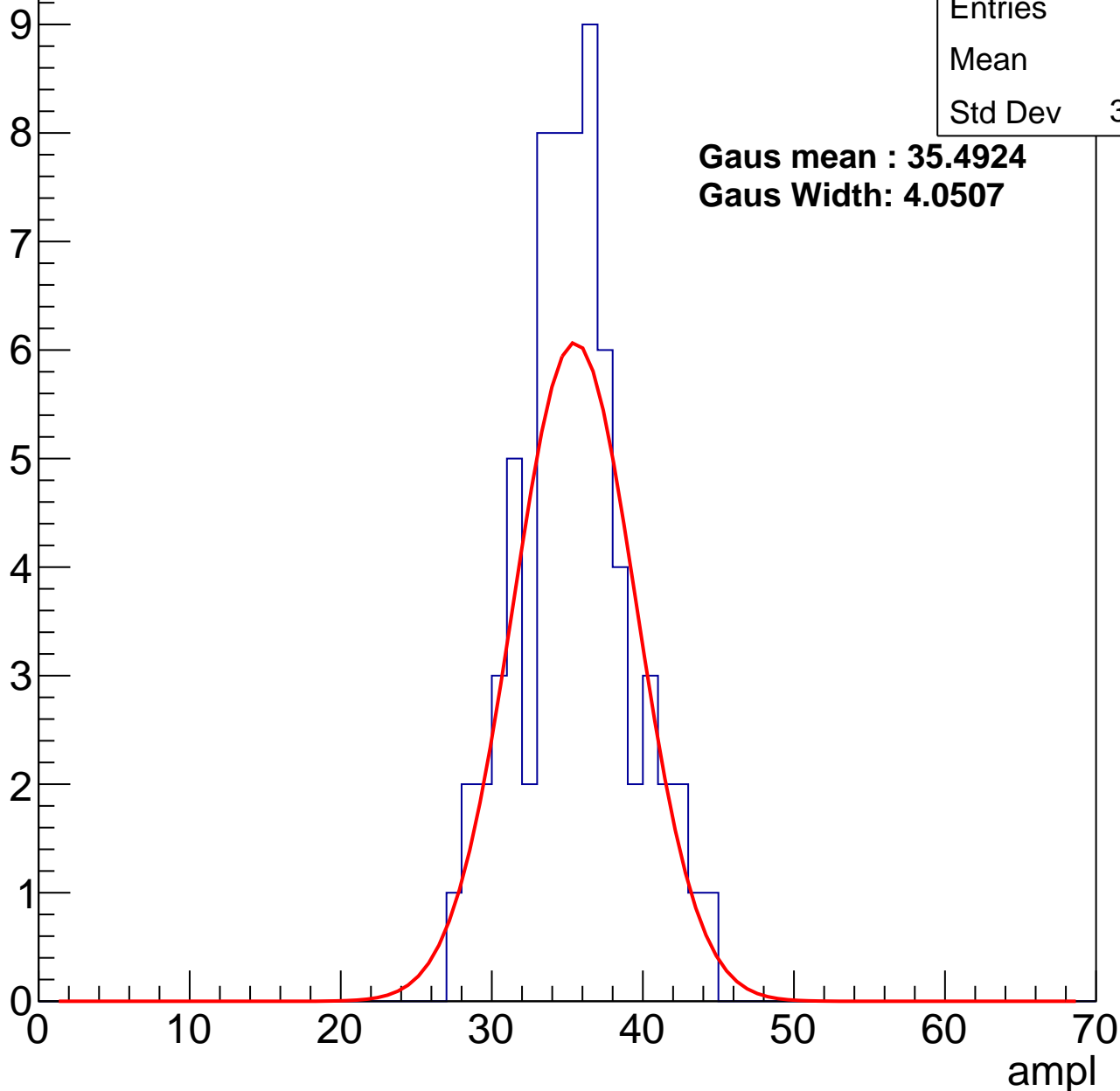
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35
Std Dev	3.687

Gaus mean : 35.4924

Gaus Width: 4.0507



B1L102S, U11-ch75, adc2

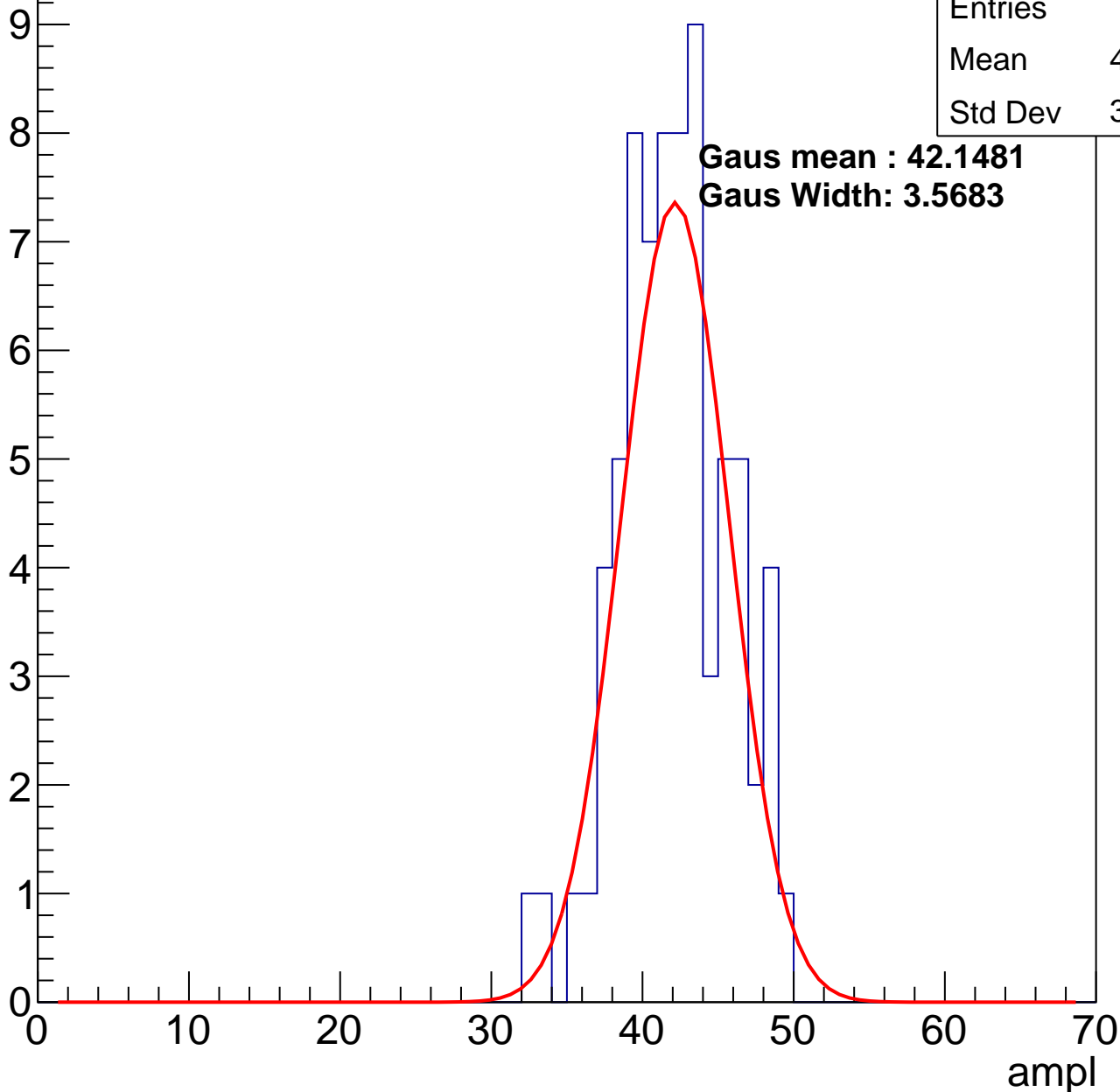
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.63
Std Dev	3.575

Gaus mean : 42.1481

Gaus Width: 3.5683



B1L102S, U11-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

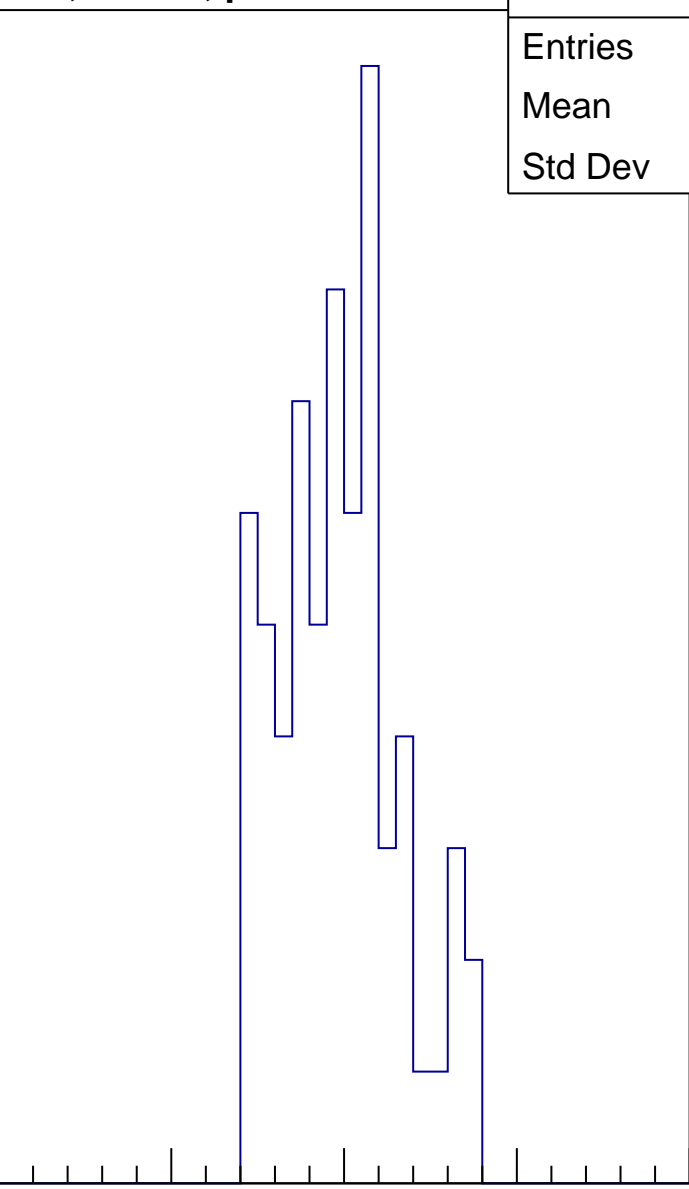
Entries	65
Mean	49.28
Std Dev	3.435

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

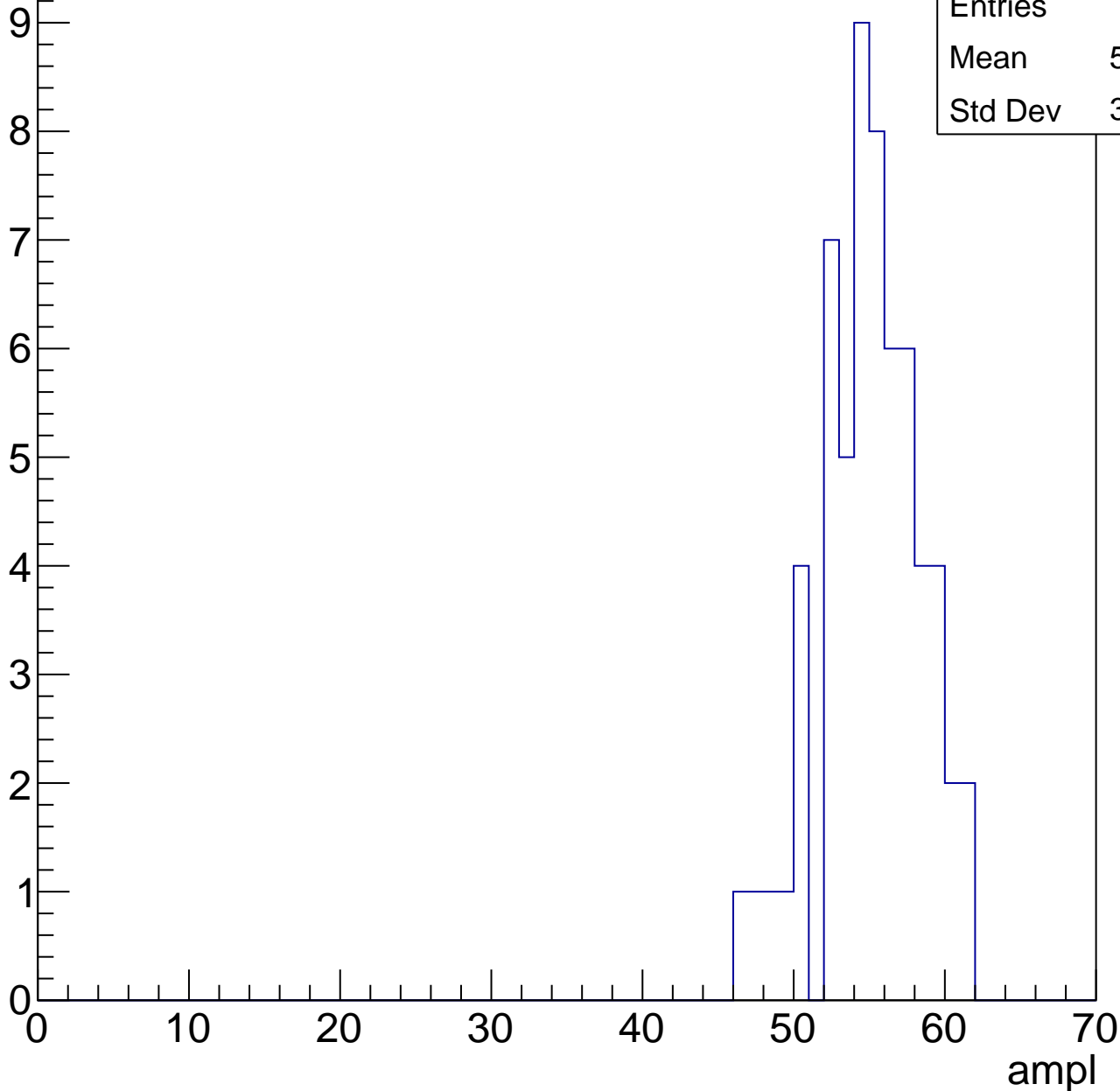


B1L102S, U11-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.64
Std Dev	3.304

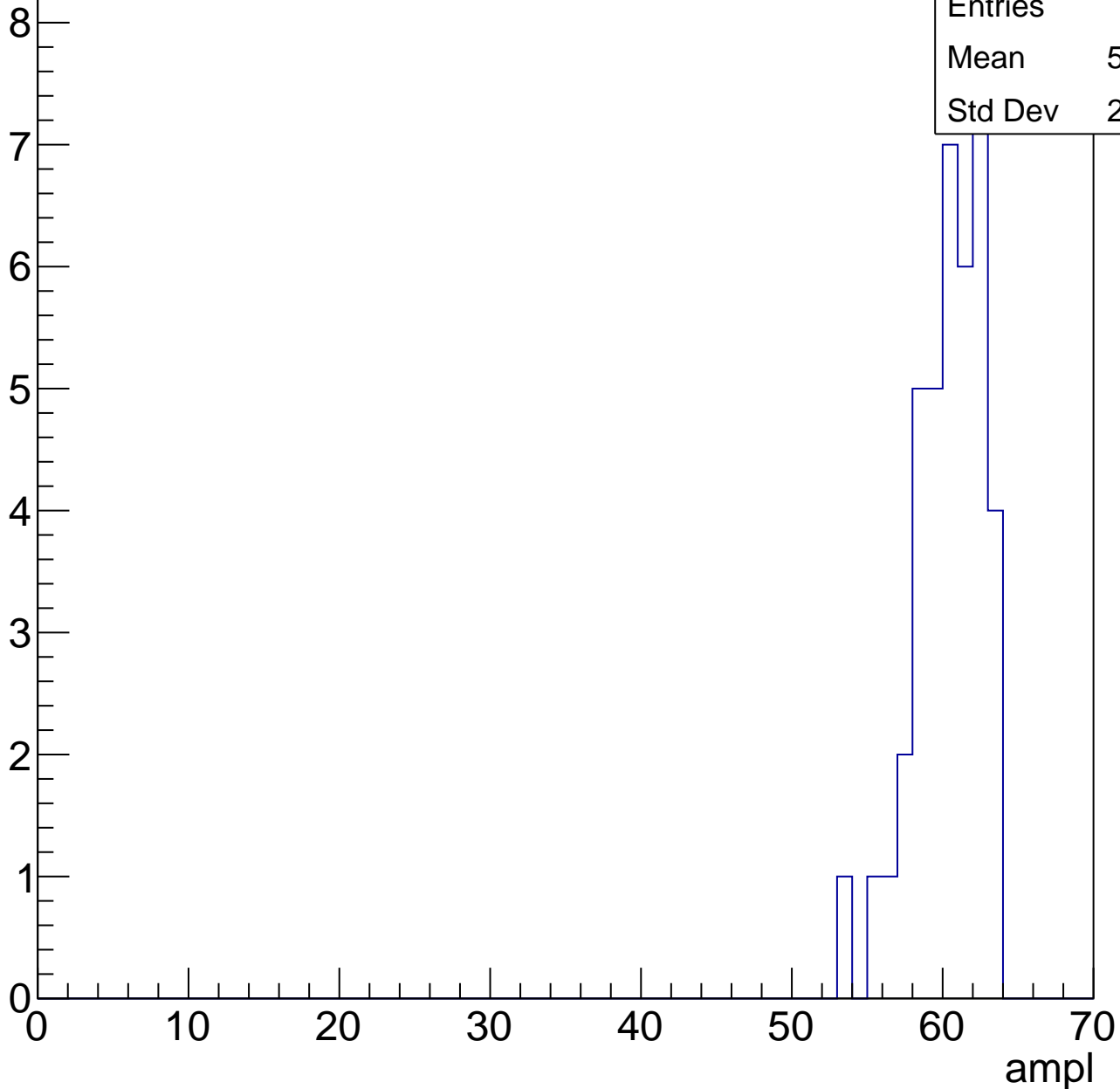


B1L102S, U11-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	59.92
Std Dev	2.274

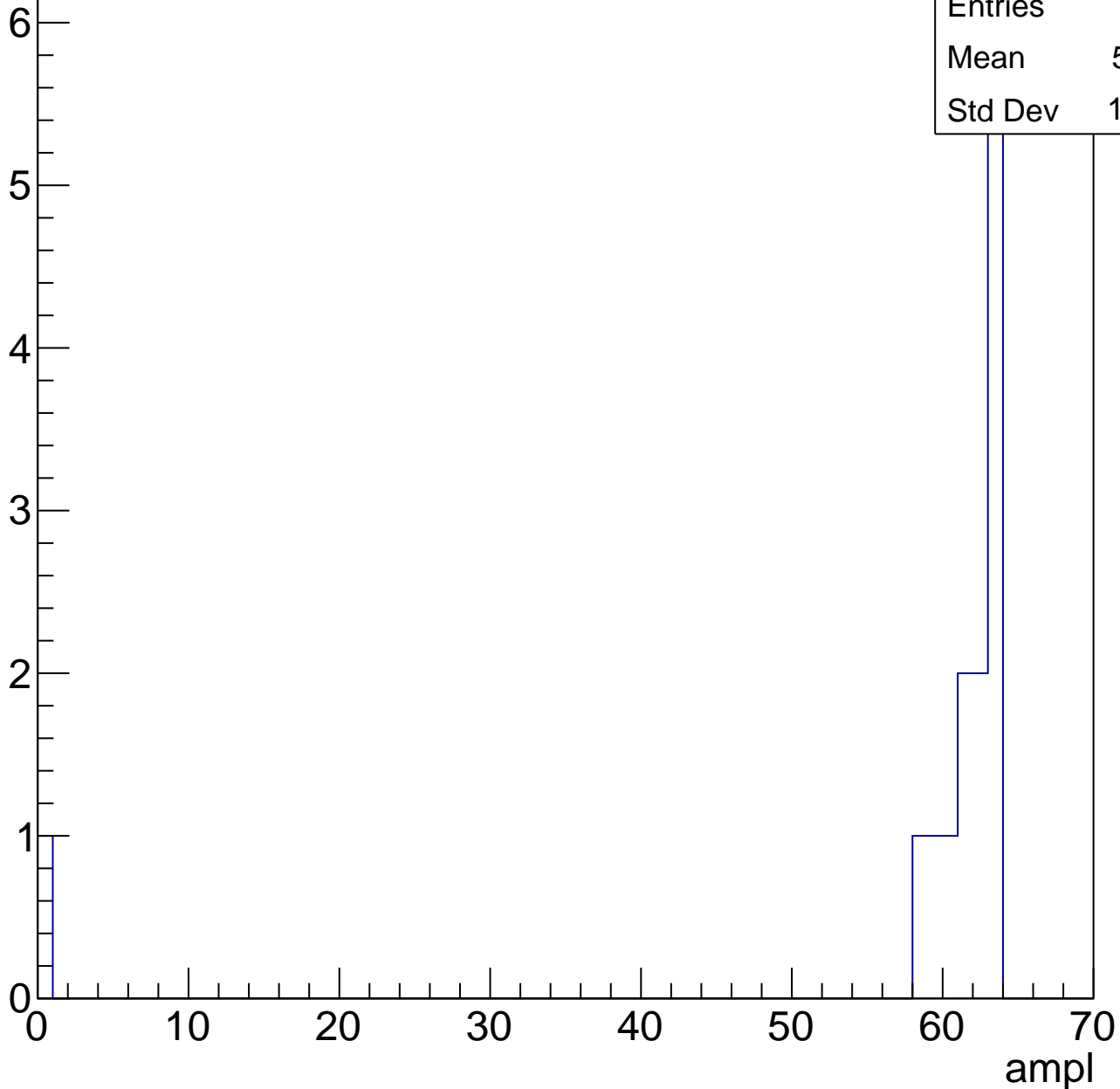


B1L102S, U11-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.21
Std Dev	15.95



B1L102S, U11-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U11-ch76, adc0

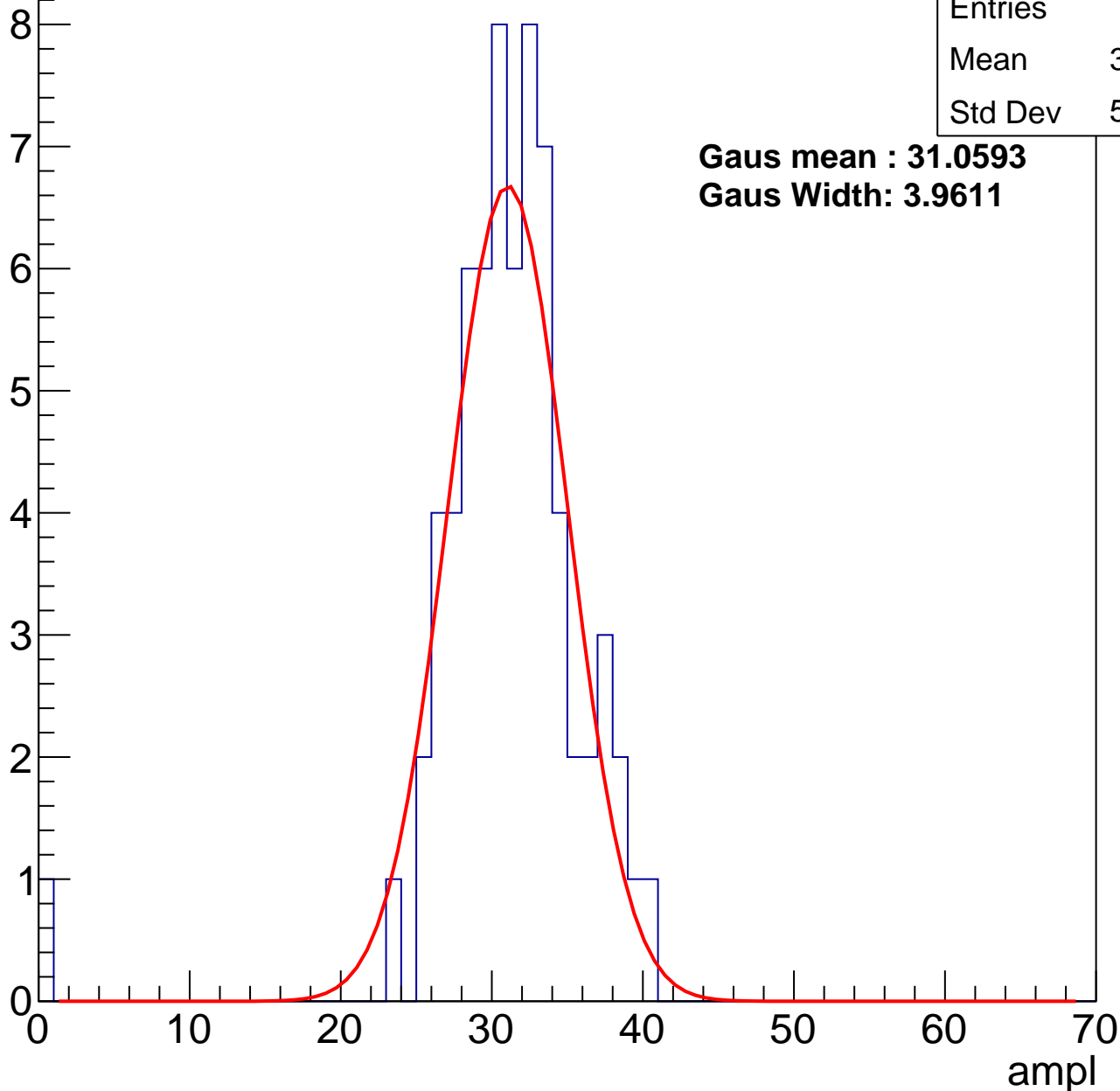
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	30.65
Std Dev	5.207

Gaus mean : 31.0593

Gaus Width: 3.9611



B1L102S, U11-ch76, adc1

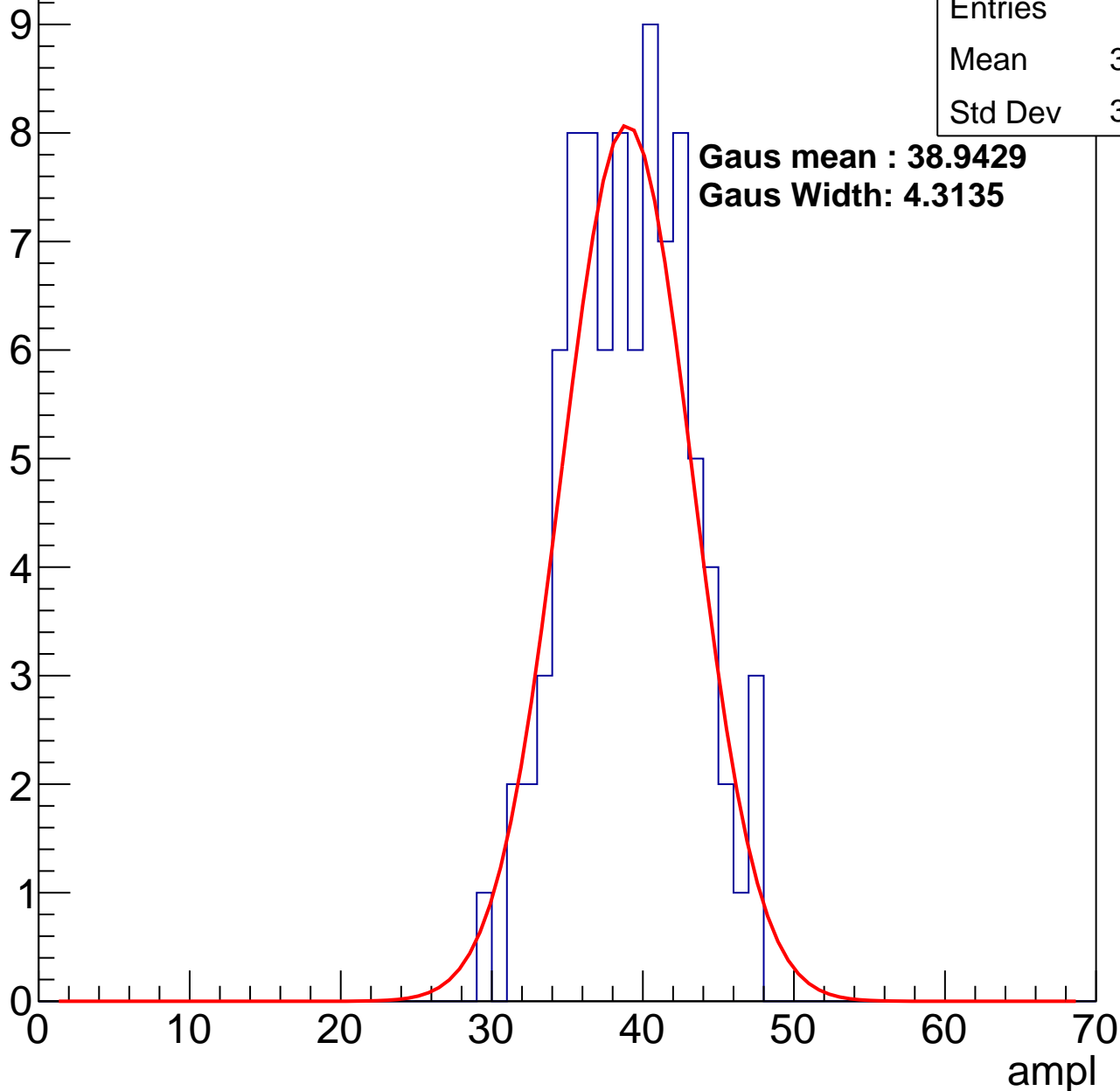
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	38.62
Std Dev	3.973

Gaus mean : 38.9429

Gaus Width: 4.3135



B1L102S, U11-ch76, adc2

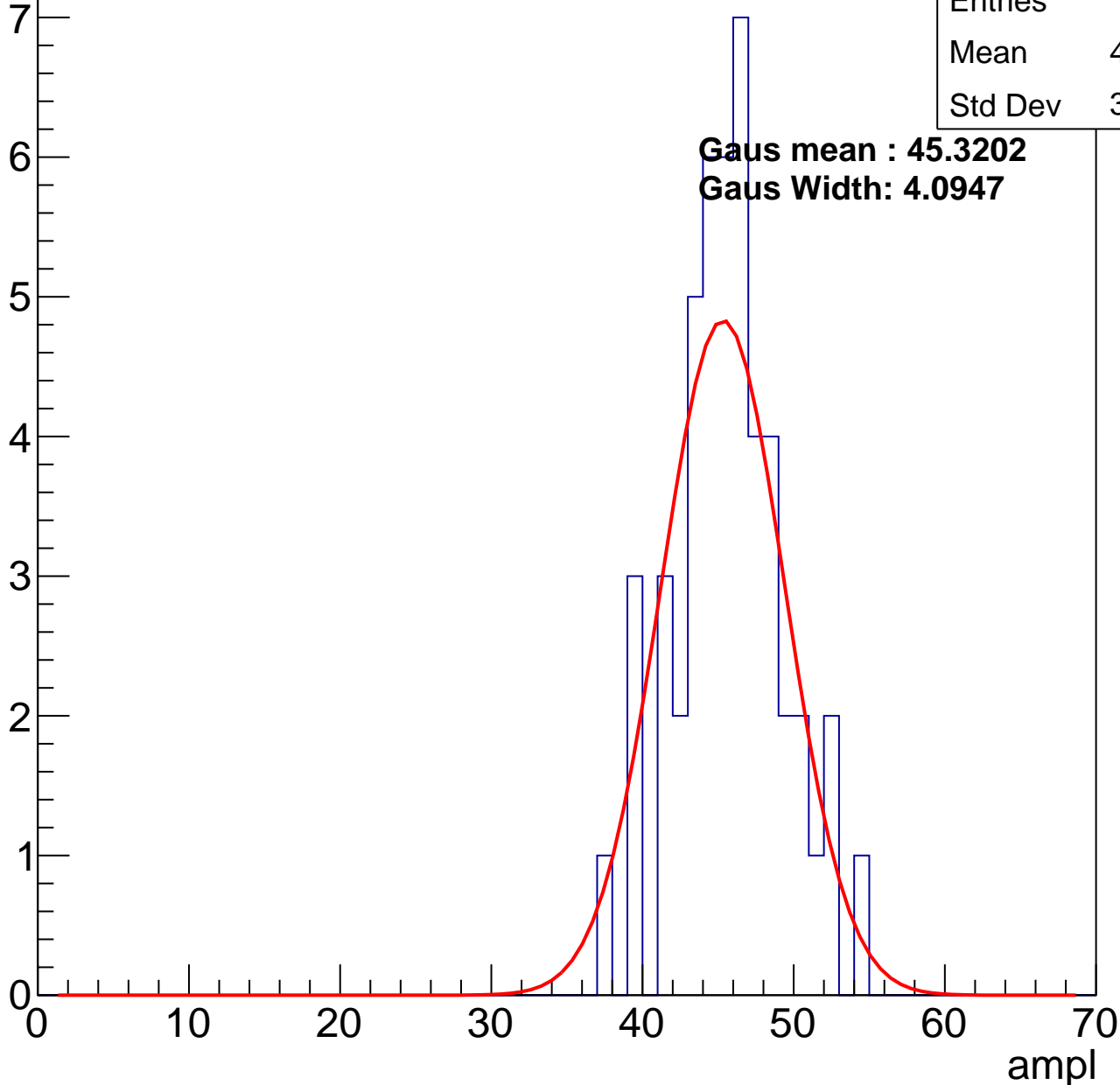
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	45.29
Std Dev	3.546

Gaus mean : 45.3202

Gaus Width: 4.0947

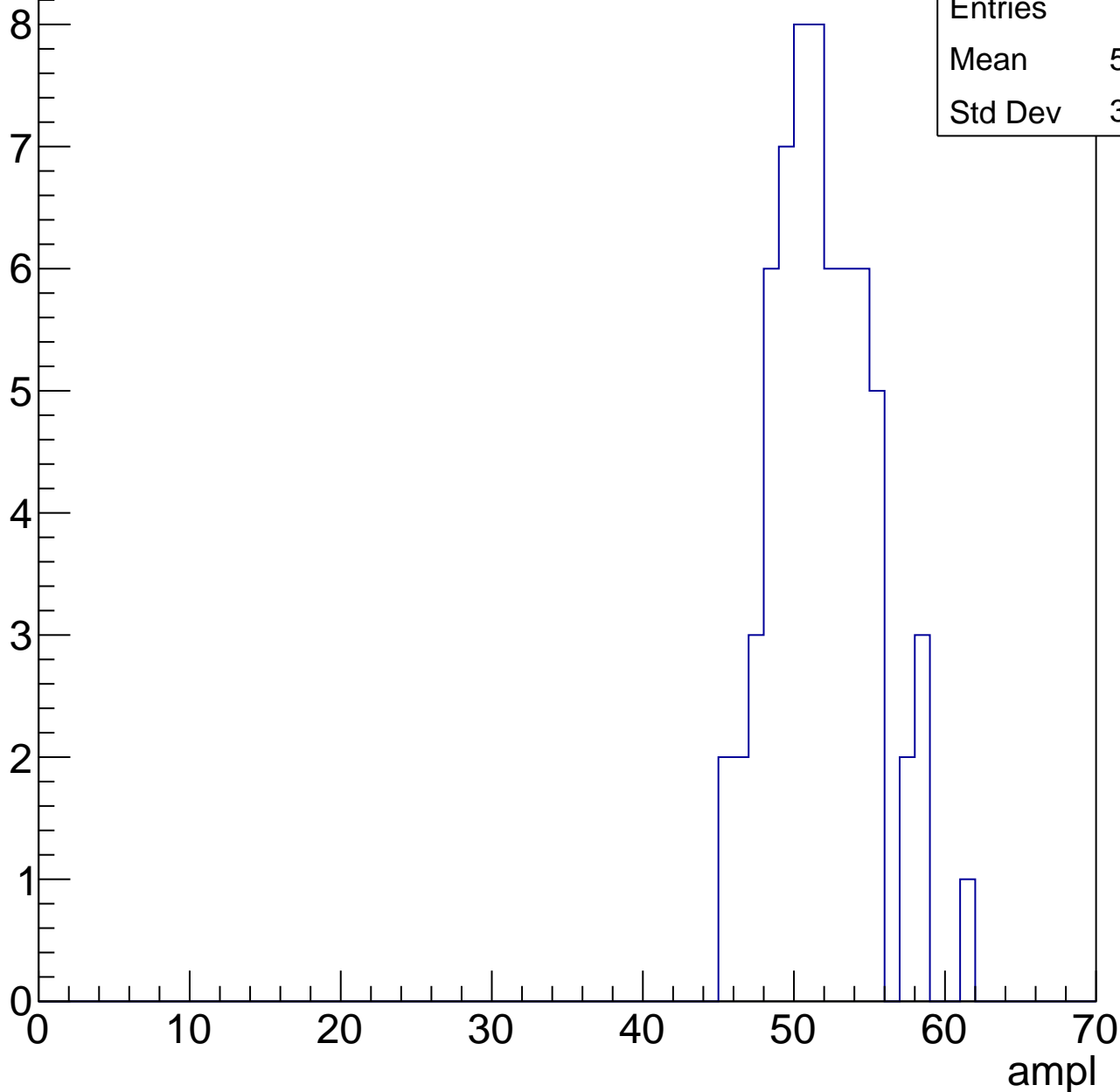


B1L102S, U11-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	51.38
Std Dev	3.359

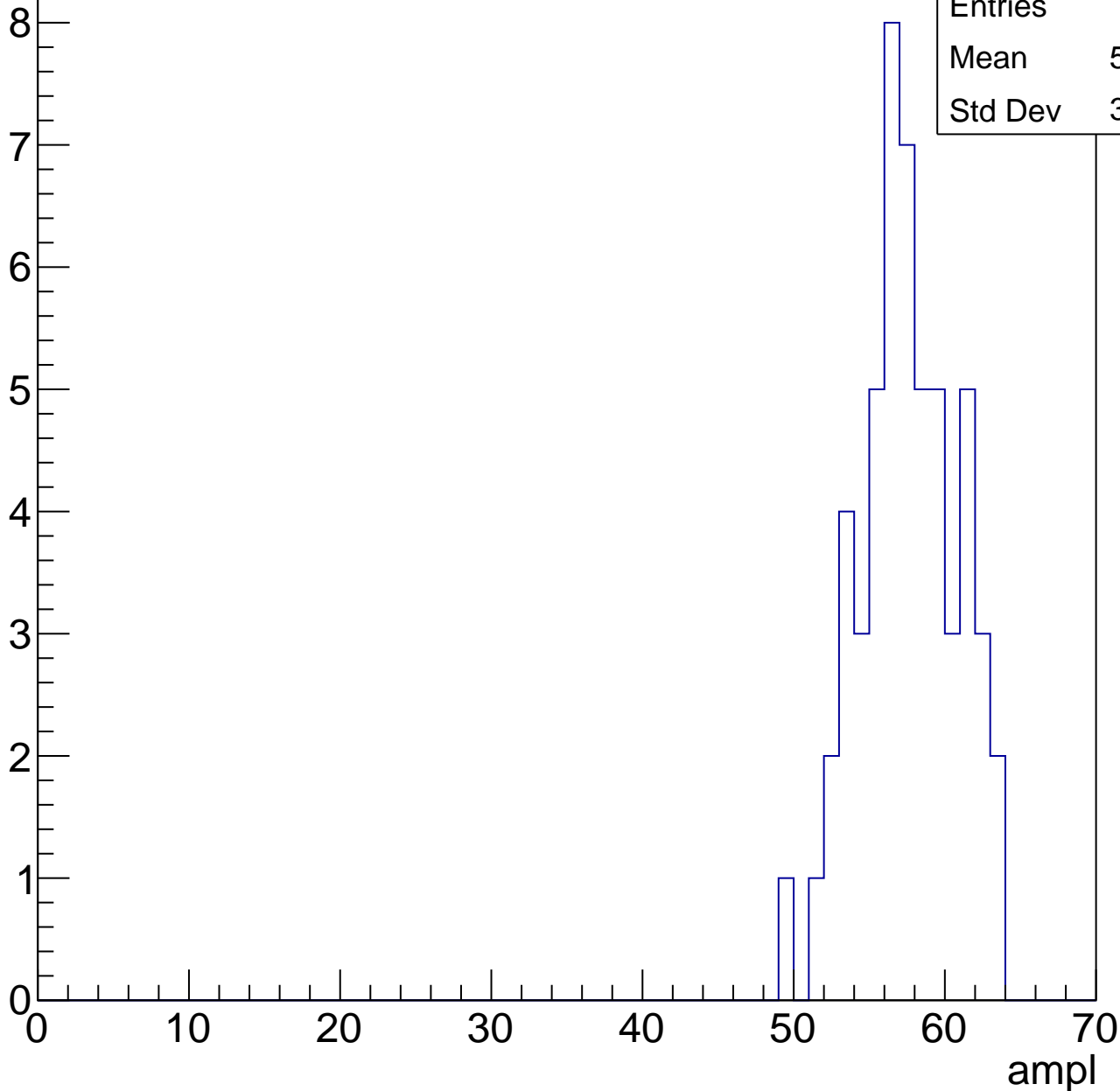


B1L102S, U11-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

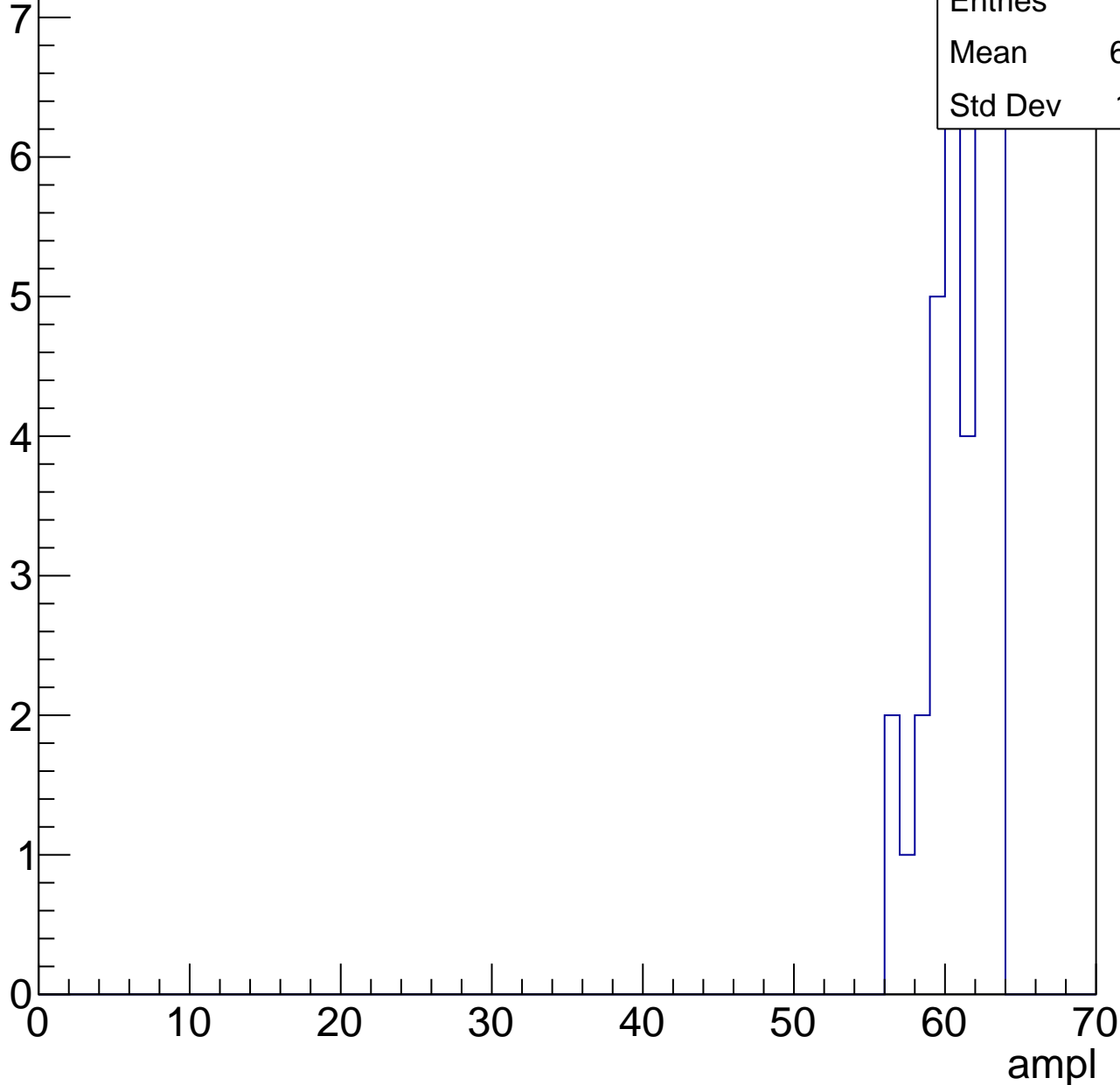
Entries	54
Mean	57.07
Std Dev	3.185



B1L102S, U11-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	35
Mean	60.54
Std Dev	1.991

B1L102S, U11-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch77, adc0

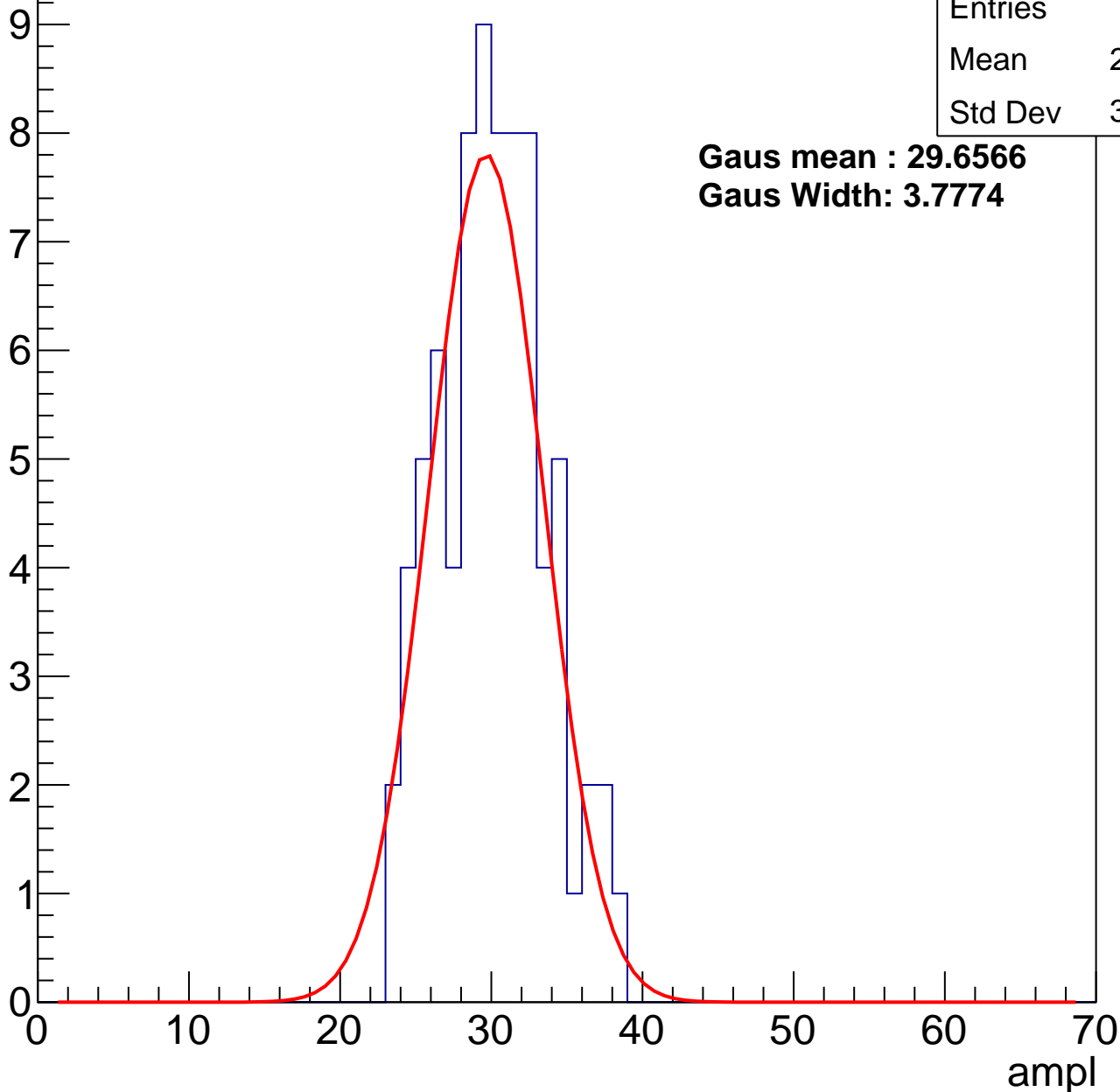
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.62
Std Dev	3.502

Gaus mean : 29.6566

Gaus Width: 3.7774



B1L102S, U11-ch77, adc1

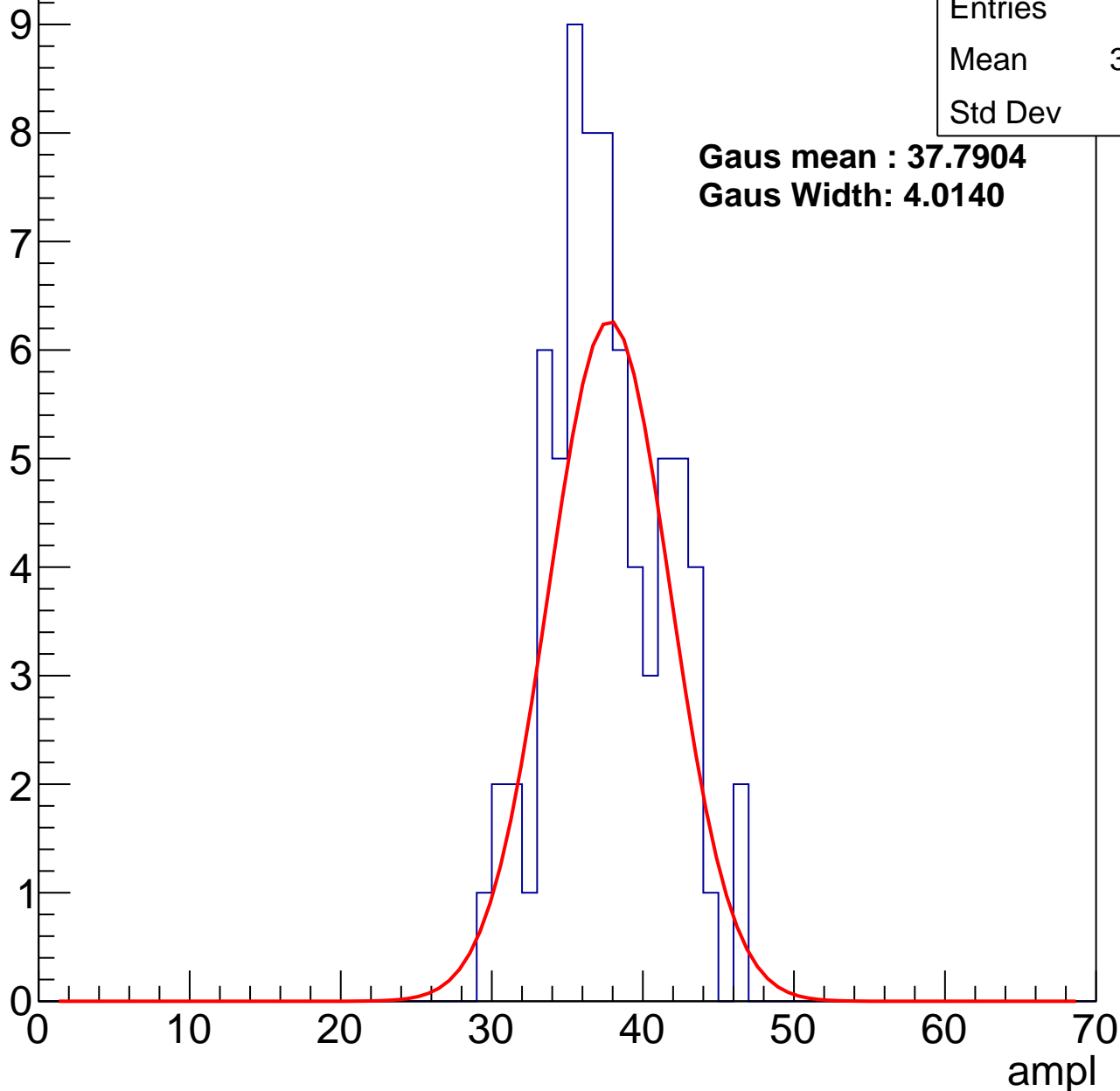
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.18
Std Dev	3.82

Gaus mean : 37.7904

Gaus Width: 4.0140



B1L102S, U11-ch77, adc2

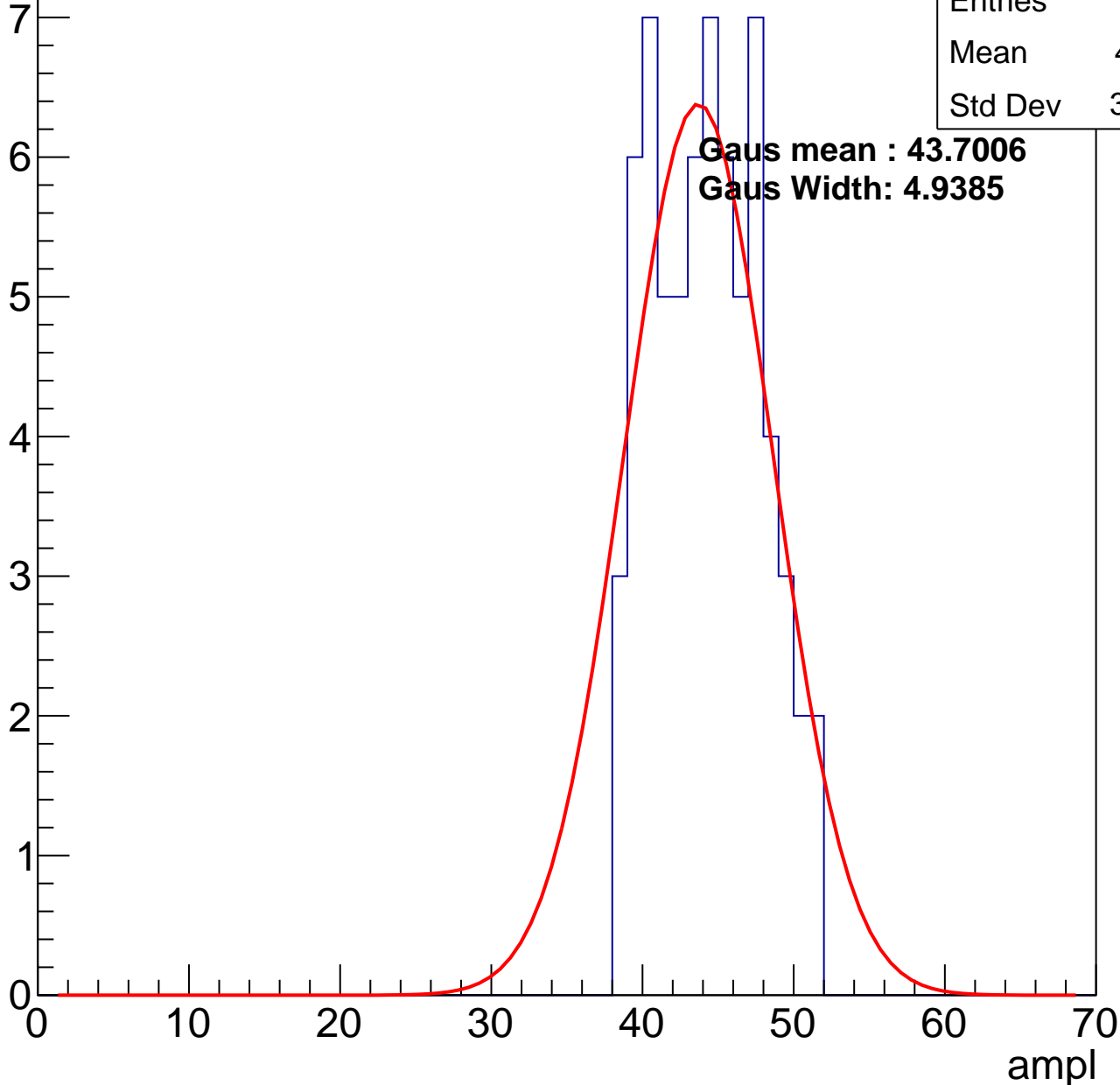
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.81
Std Dev	3.507

Gaus mean : 43.7006

Gaus Width: 4.9385

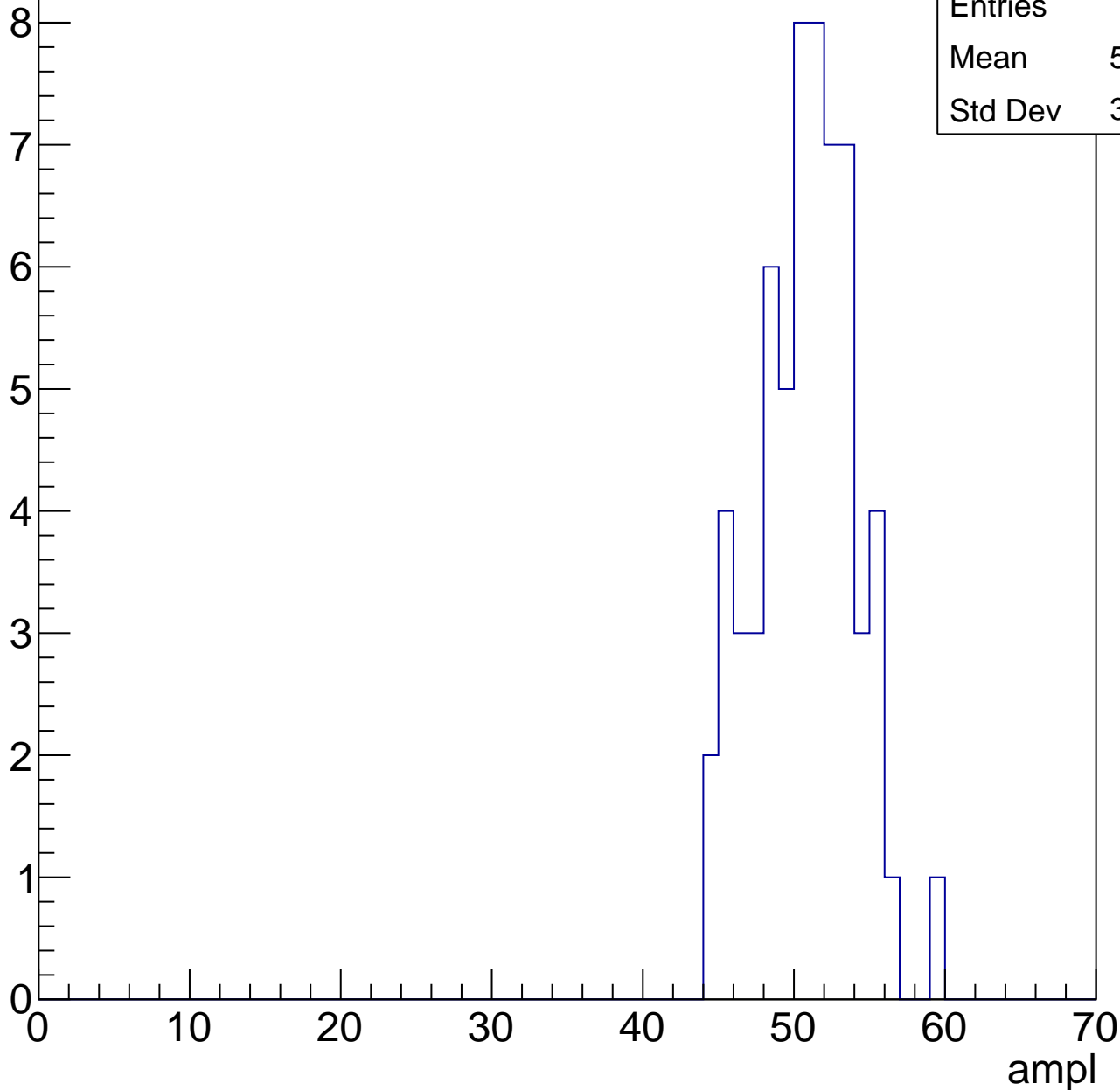


B1L102S, U11-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.32
Std Dev	3.197

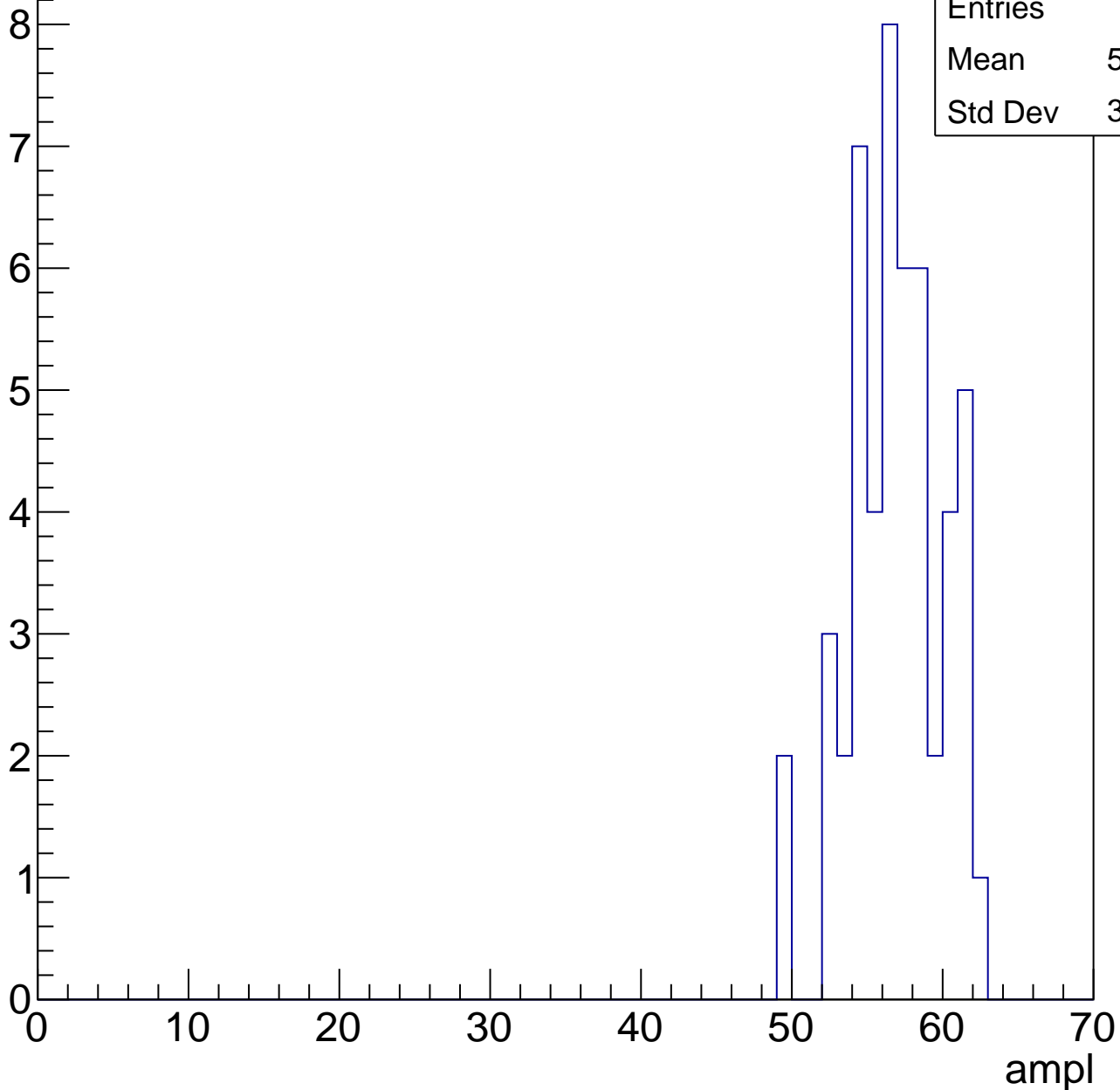


B1L102S, U11-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.42
Std Dev	3.034

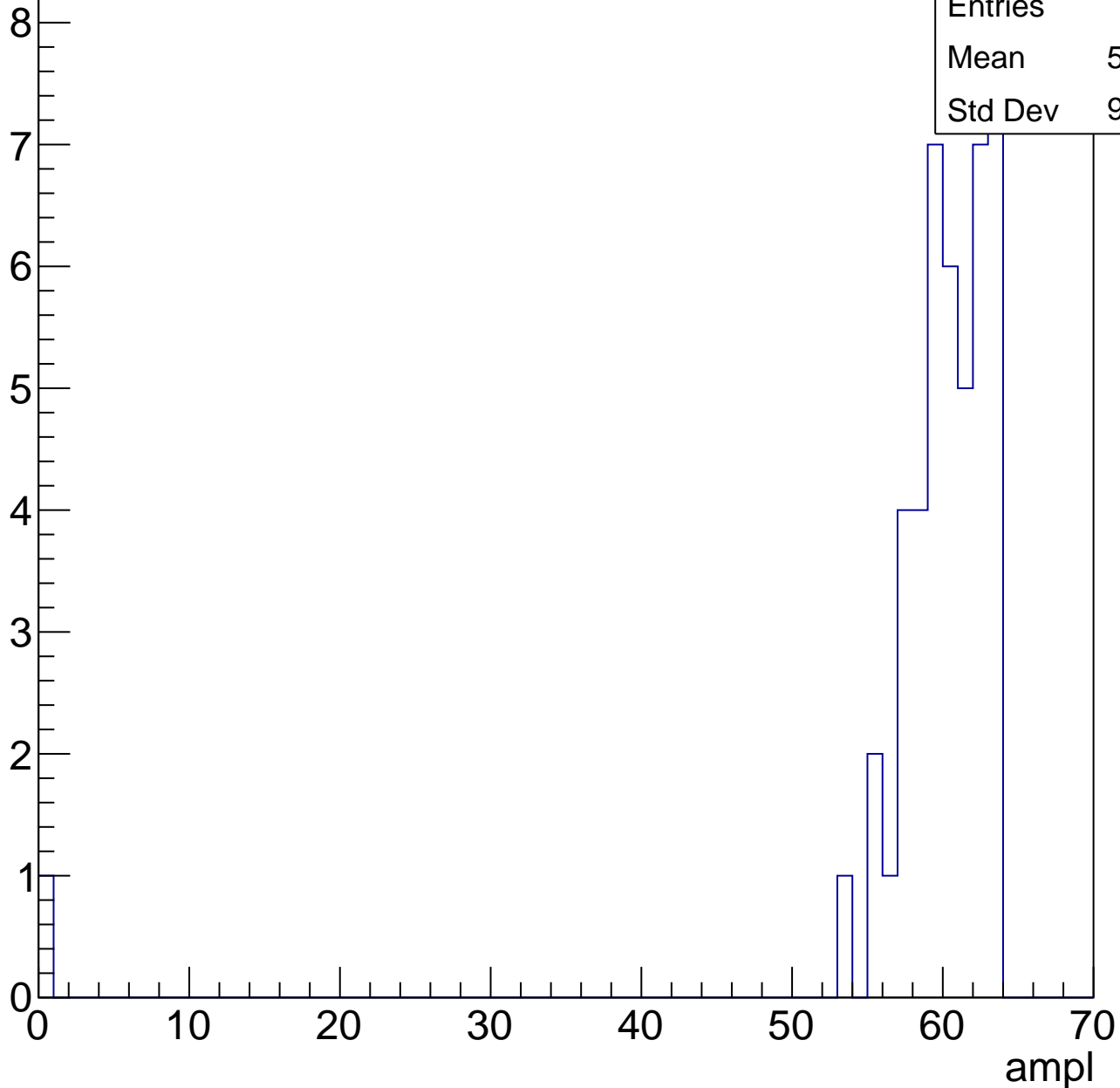


B1L102S, U11-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.59
Std Dev	9.074



B1L102S, U11-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L102S, U11-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch78, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	29.27
Std Dev	5.051

Gaus mean : 30.6653

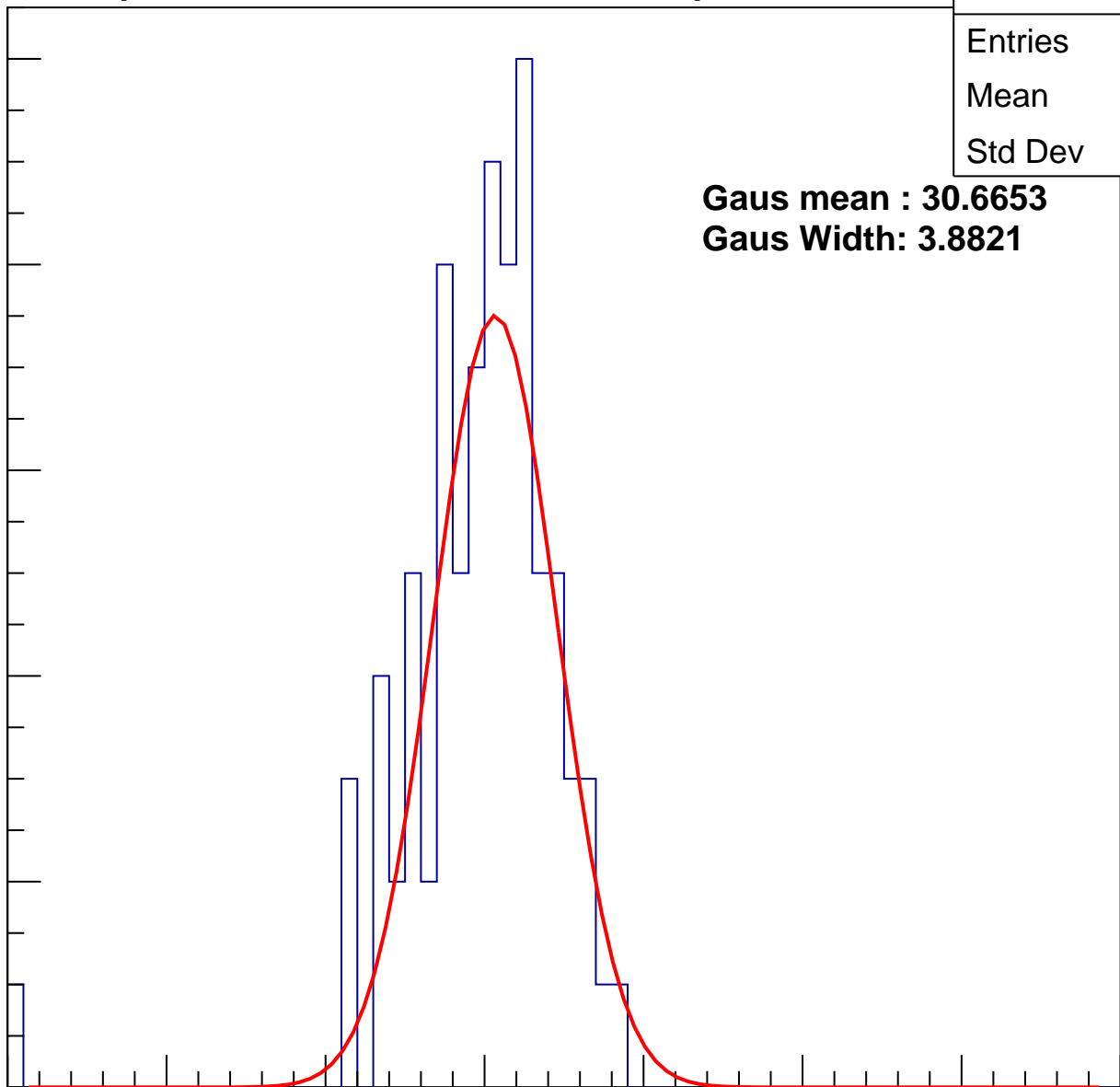
Gaus Width: 3.8821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch78, adc1

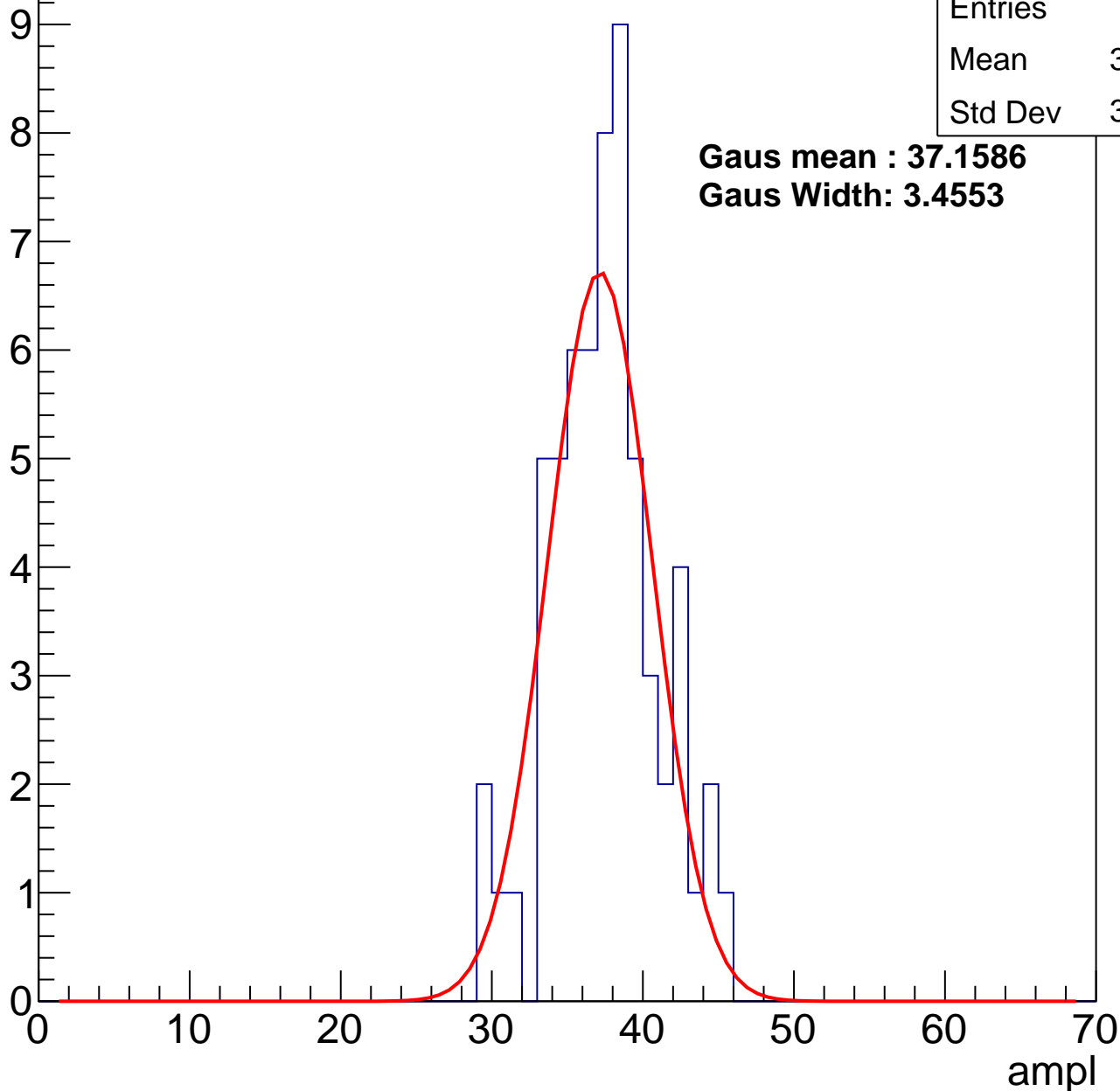
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	37.03
Std Dev	3.516

Gaus mean : 37.1586

Gaus Width: 3.4553



B1L102S, U11-ch78, adc2

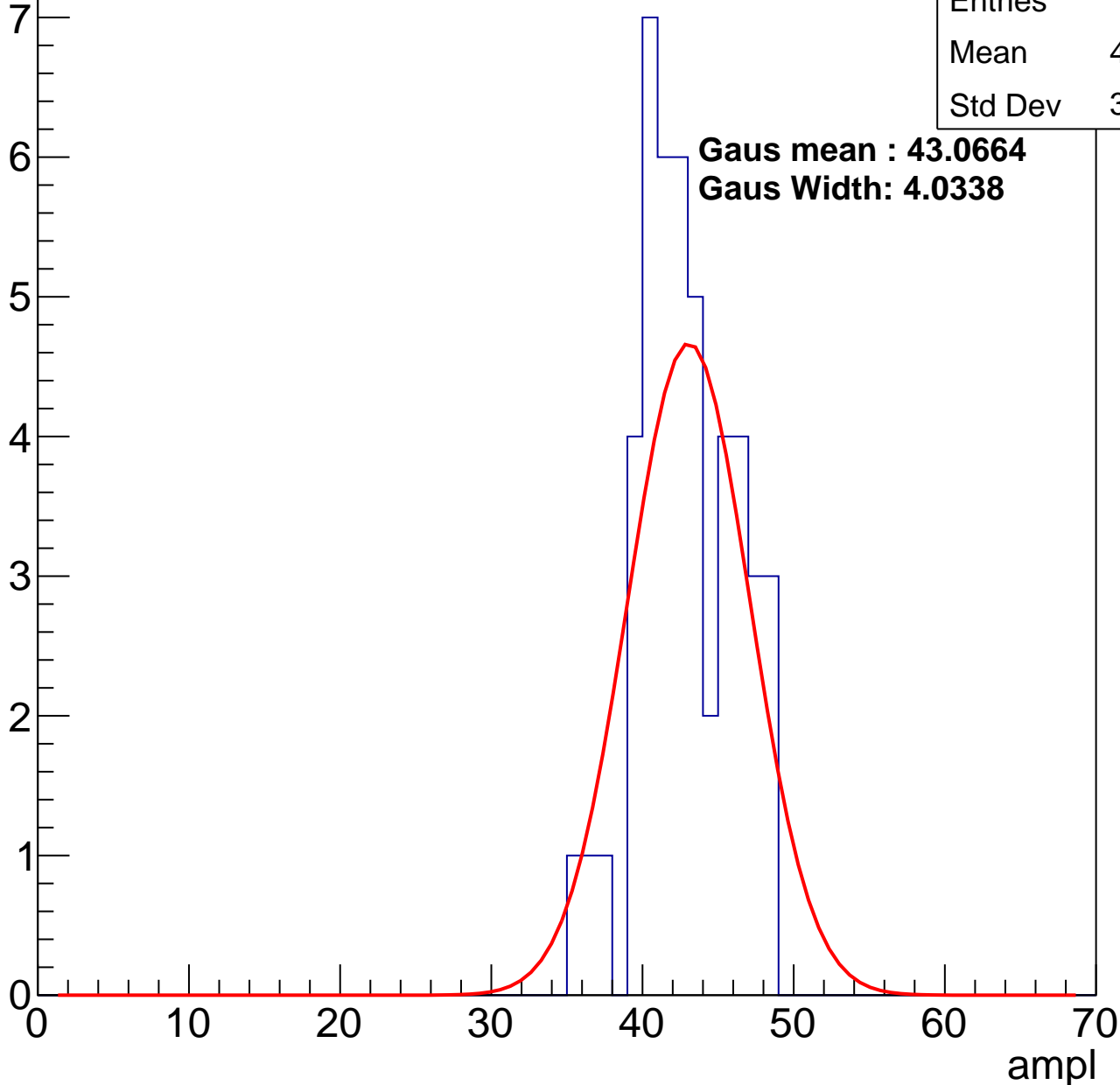
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	42.43
Std Dev	3.154

Gaus mean : 43.0664

Gaus Width: 4.0338

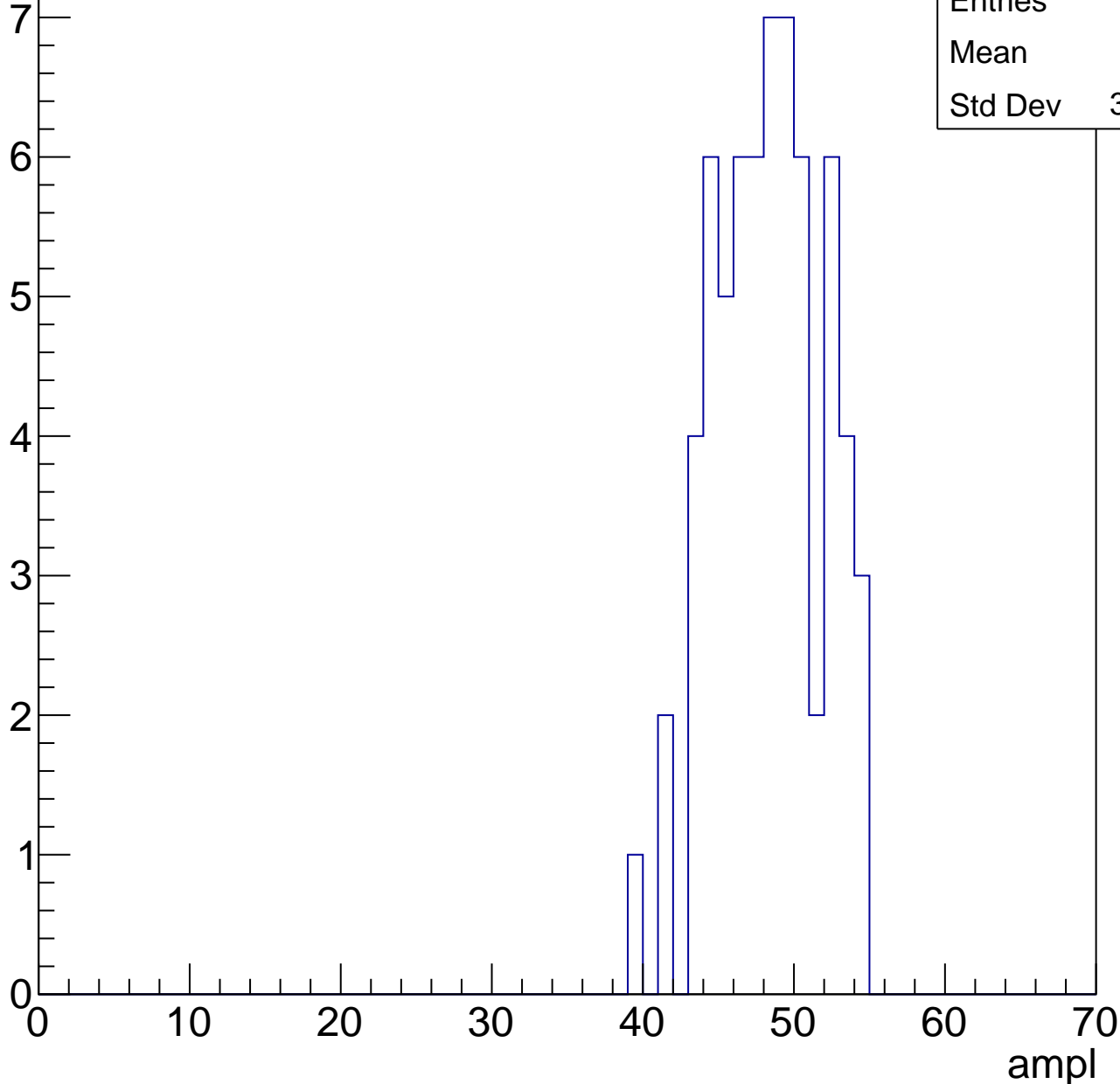


B1L102S, U11-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.8
Std Dev	3.509



B1L102S, U11-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

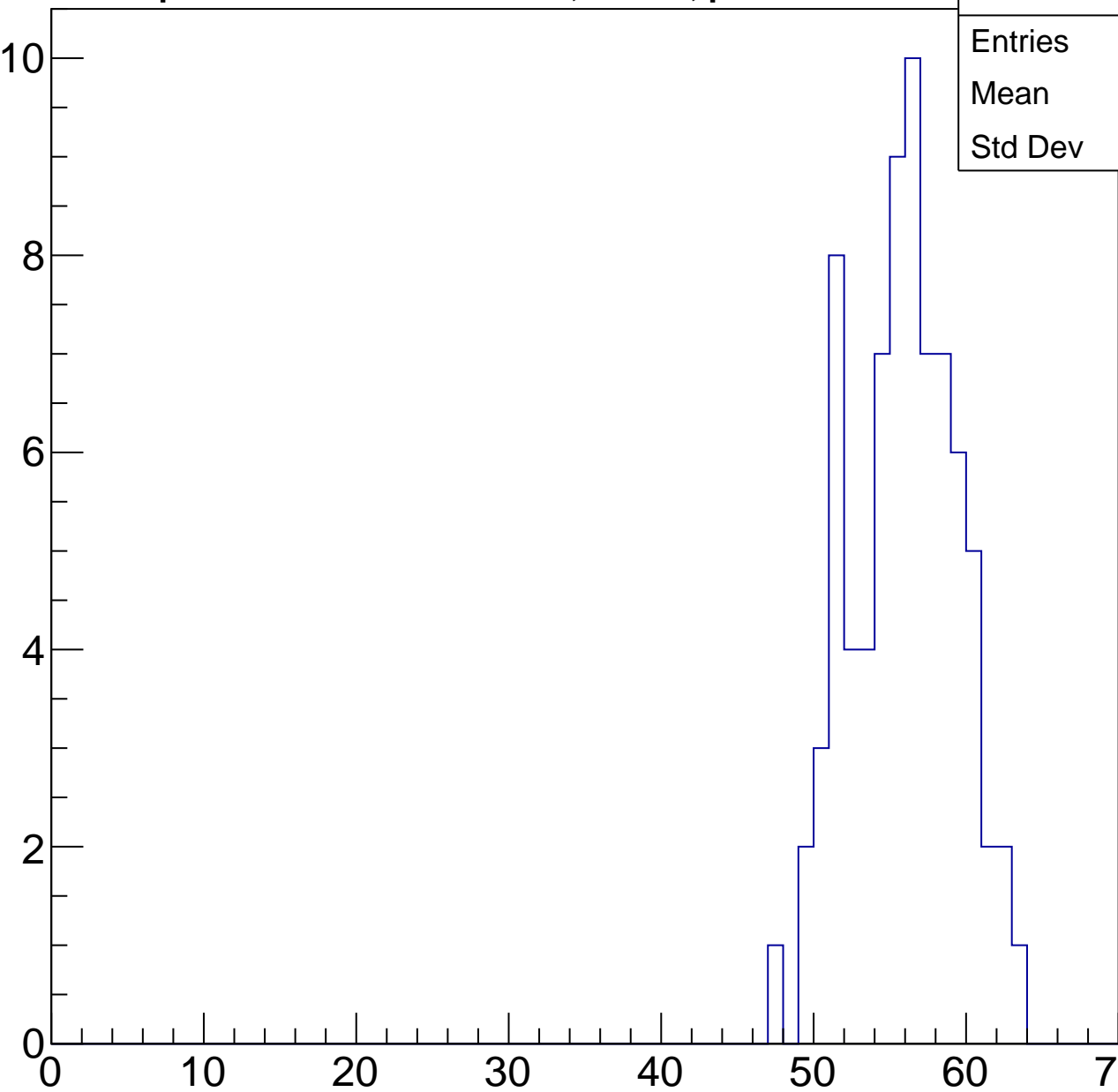
Entries	78
Mean	55.44
Std Dev	3.47

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

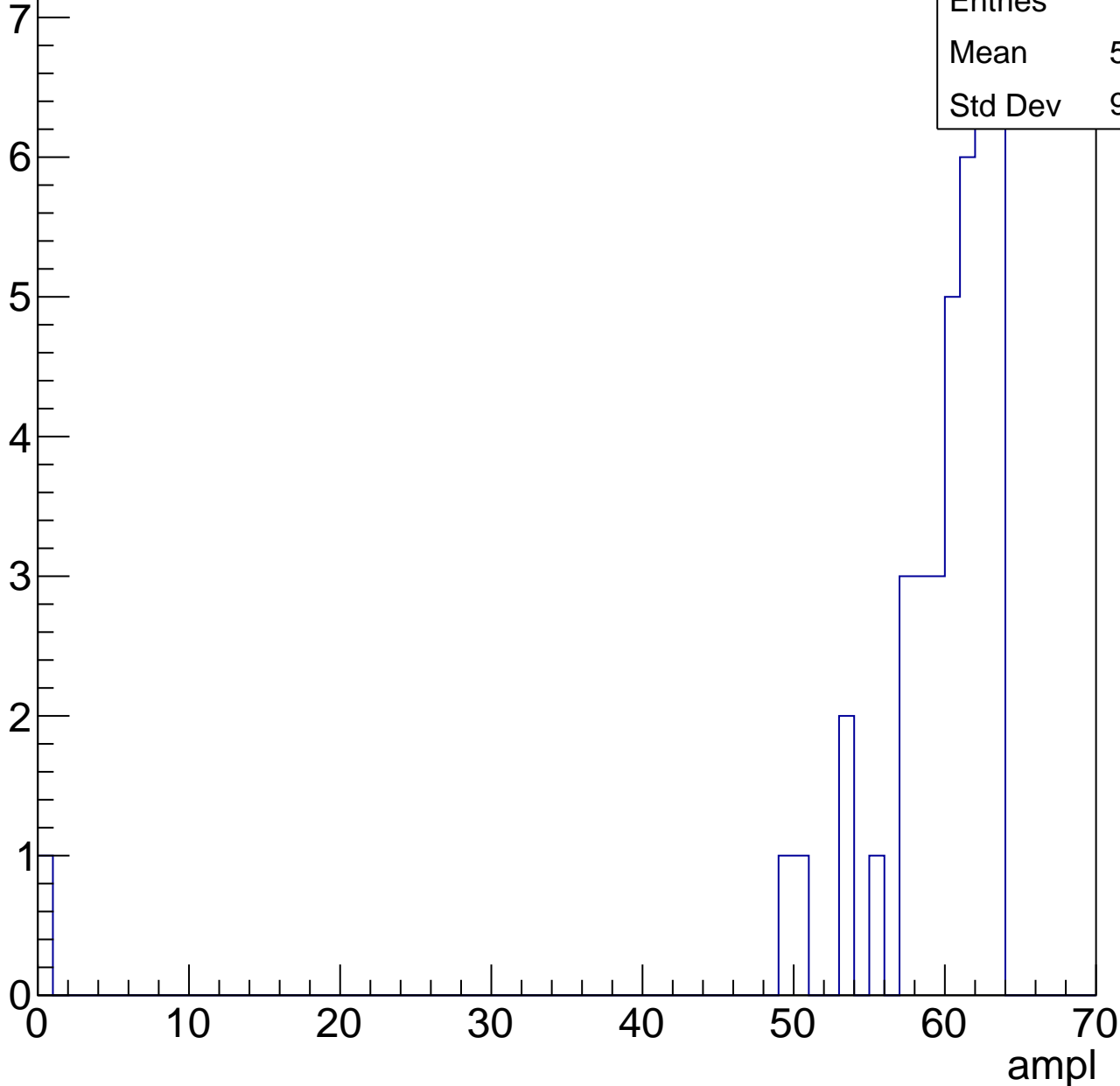


B1L102S, U11-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

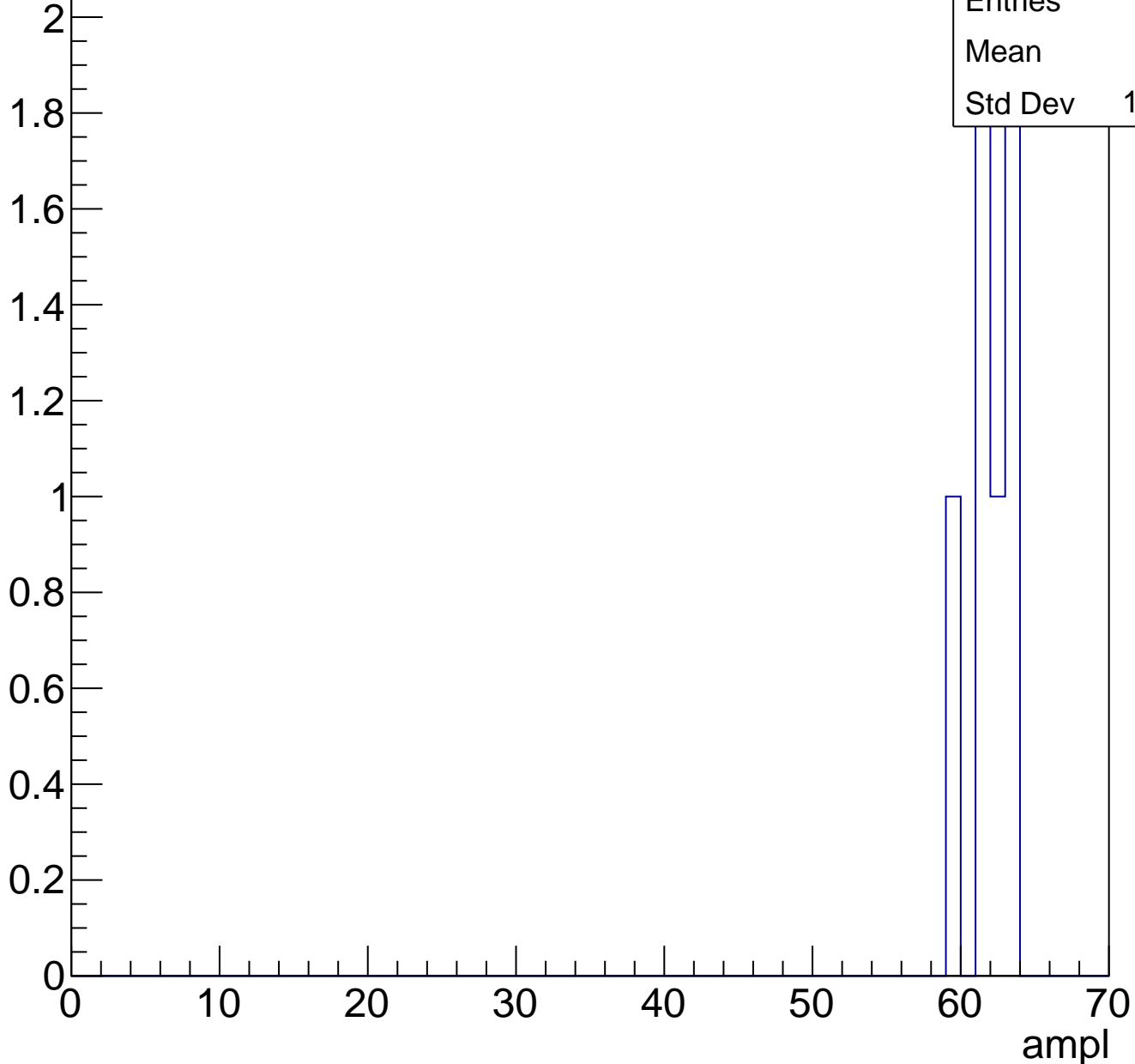
Entries	40
Mean	58.08
Std Dev	9.918



B1L102S, U11-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U11-ch79, adc0

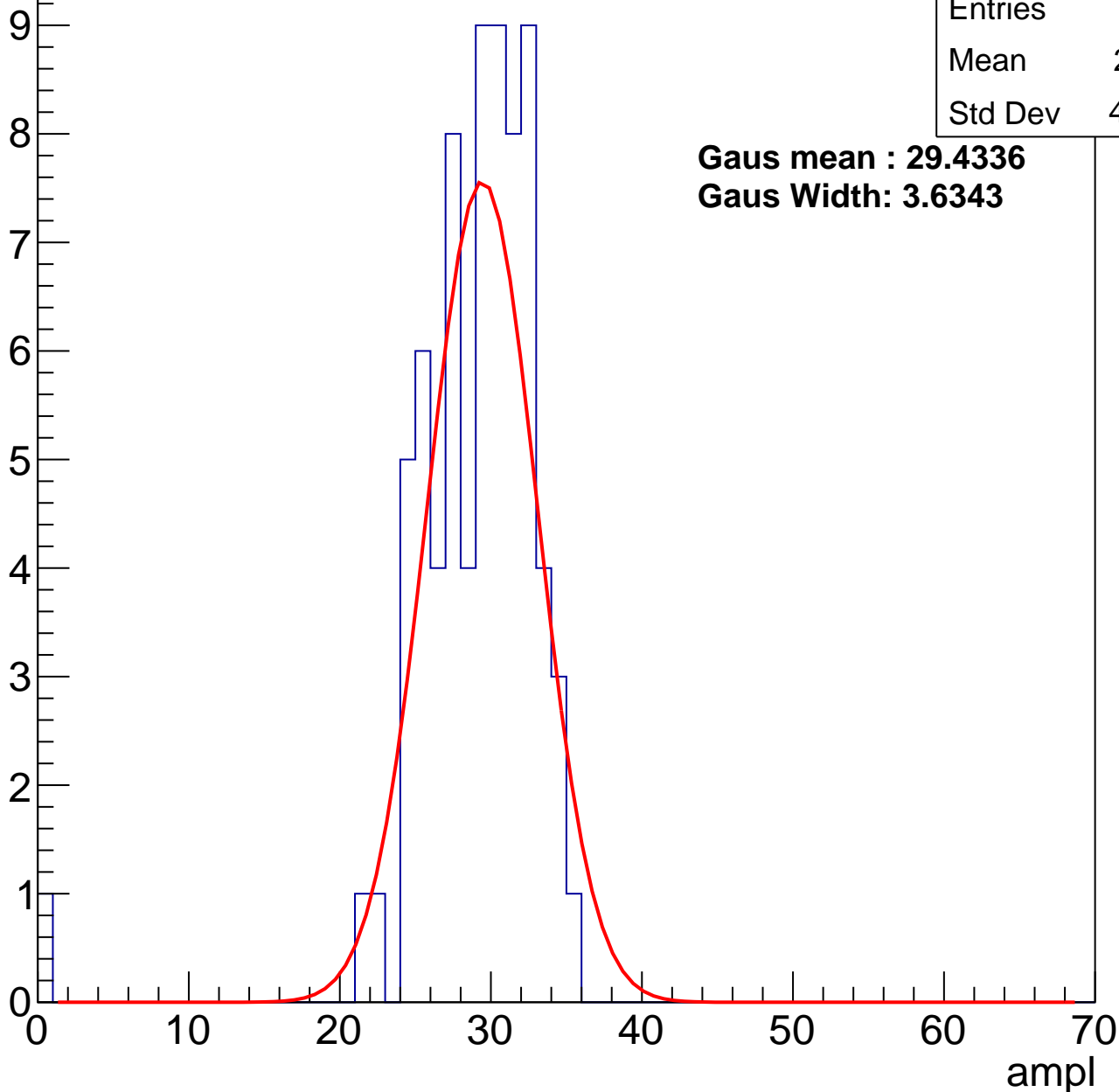
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.51
Std Dev	4.572

Gaus mean : 29.4336

Gaus Width: 3.6343



B1L102S, U11-ch79, adc1

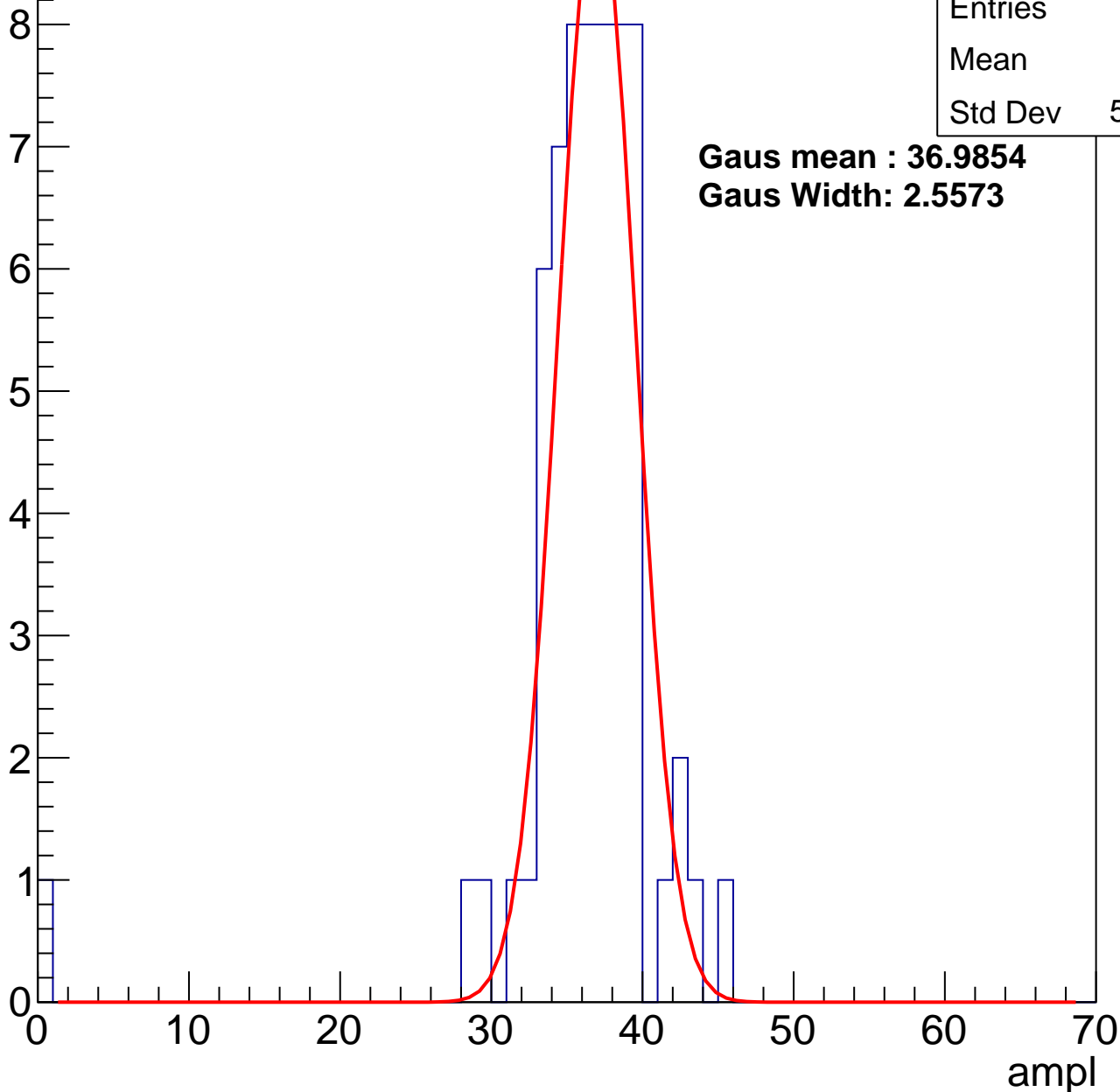
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.7
Std Dev	5.453

Gaus mean : 36.9854

Gaus Width: 2.5573



B1L102S, U11-ch79, adc2

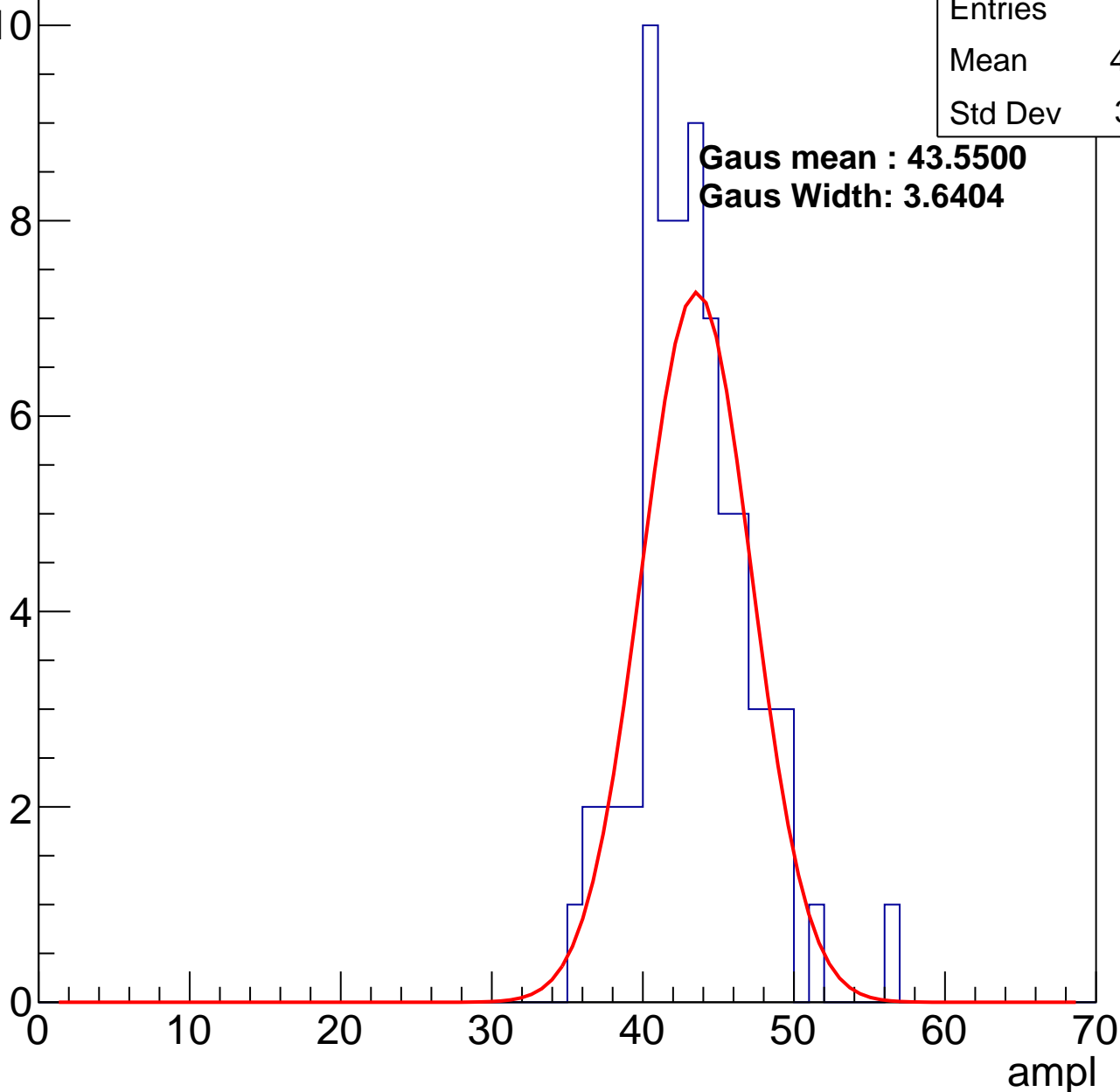
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.89
Std Dev	3.691

Gaus mean : 43.5500

Gaus Width: 3.6404

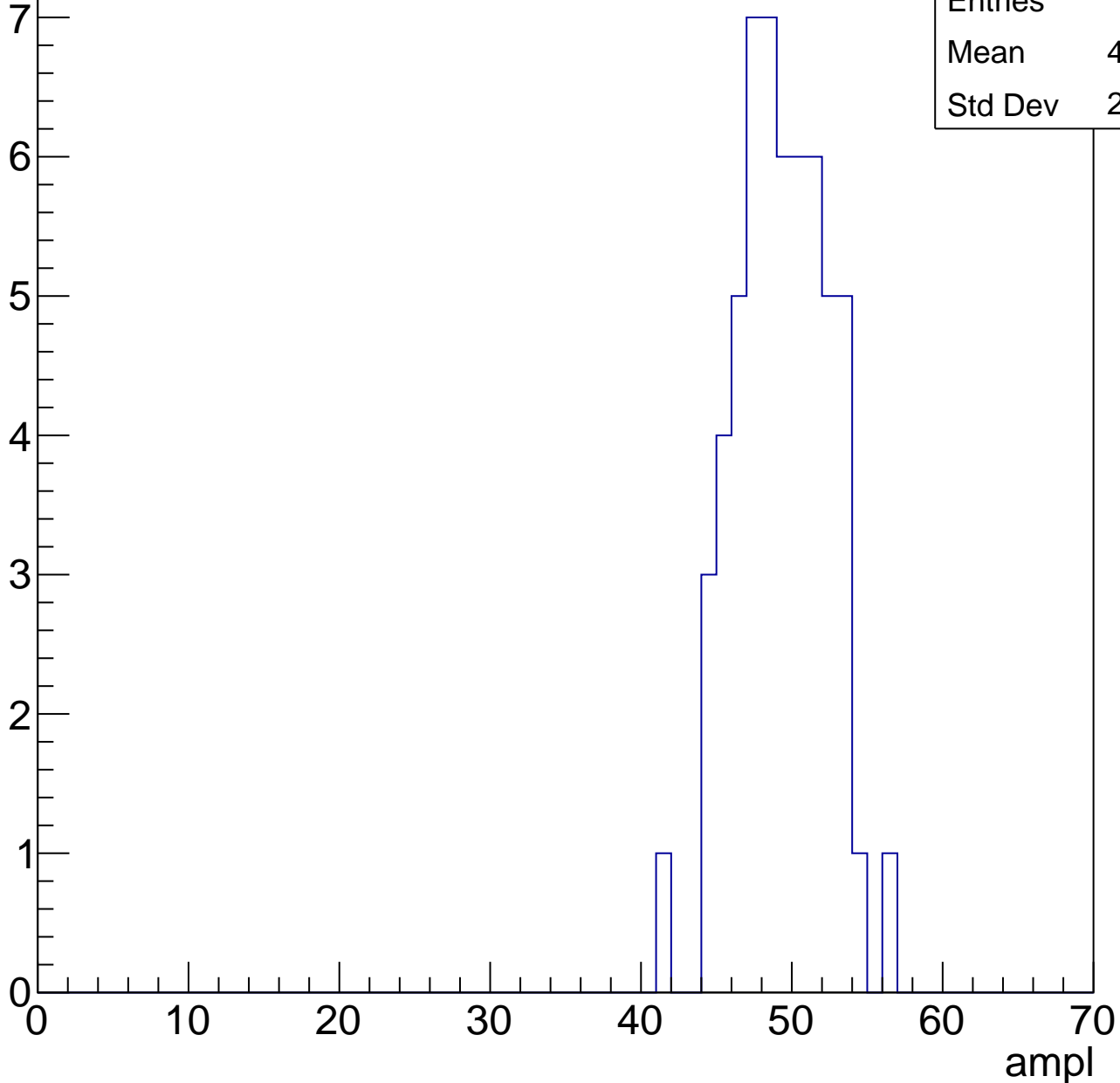


B1L102S, U11-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.82
Std Dev	2.992

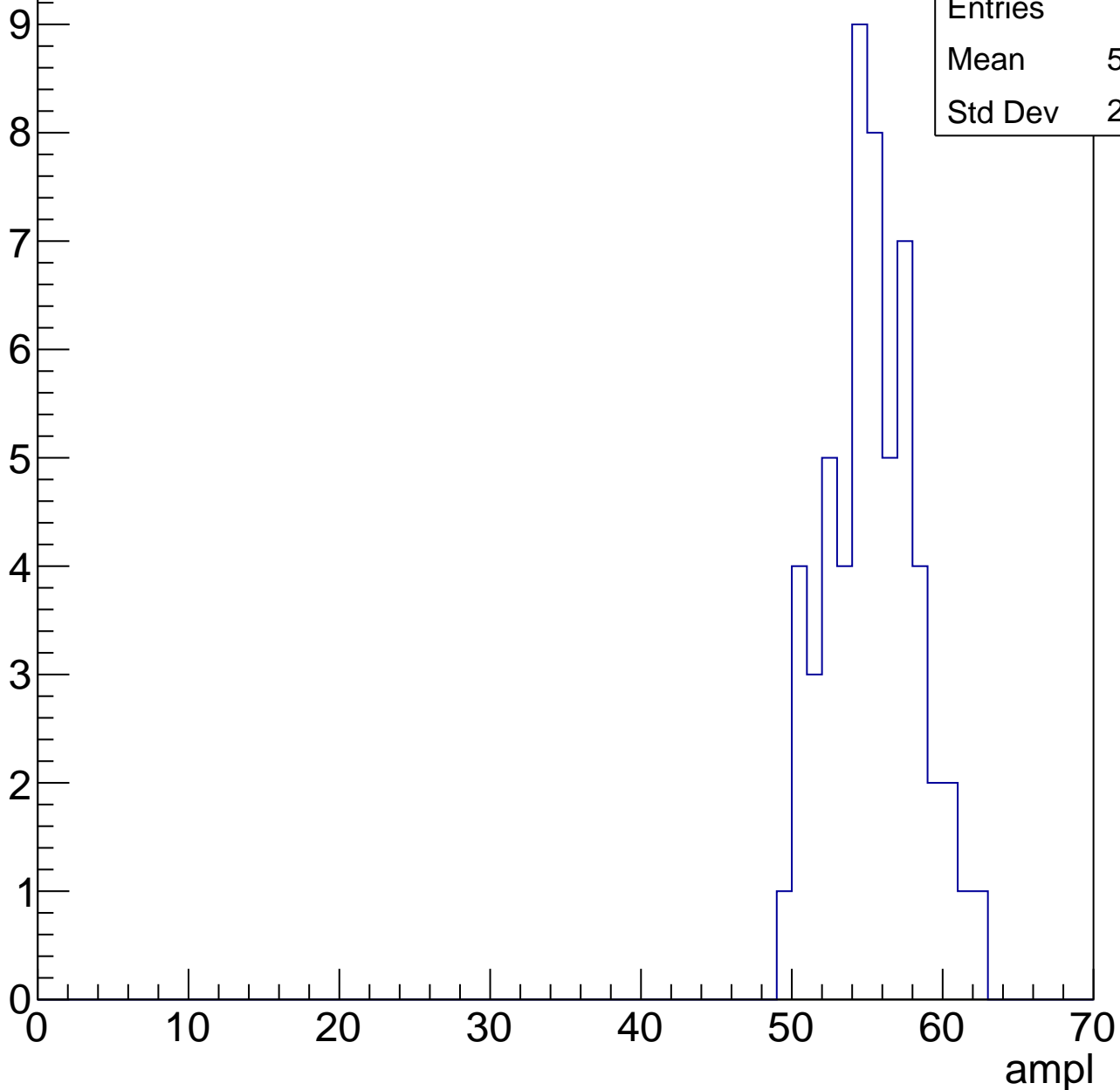


B1L102S, U11-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

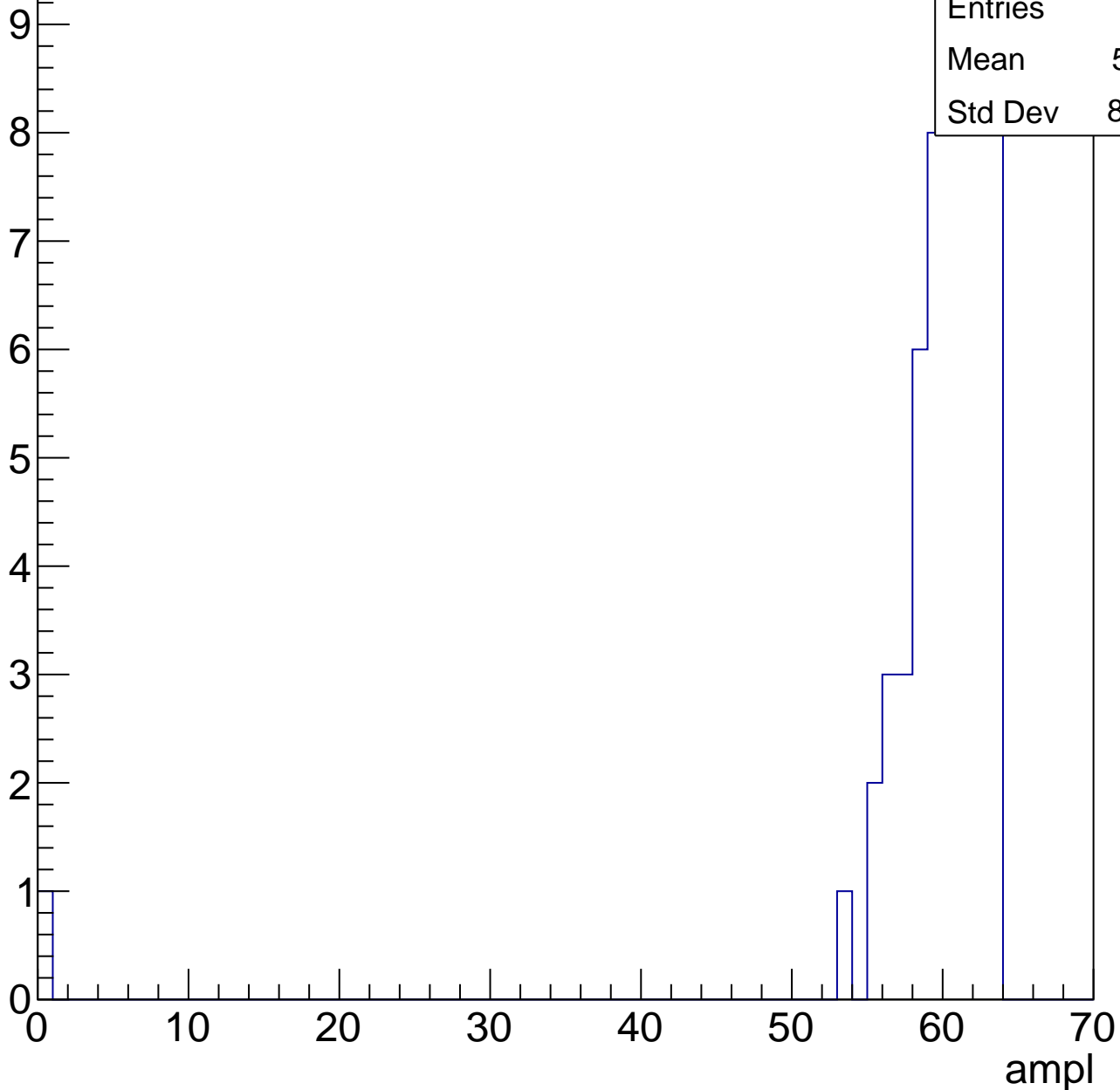
Entries	56
Mean	54.86
Std Dev	2.955



B1L102S, U11-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch80, adc0

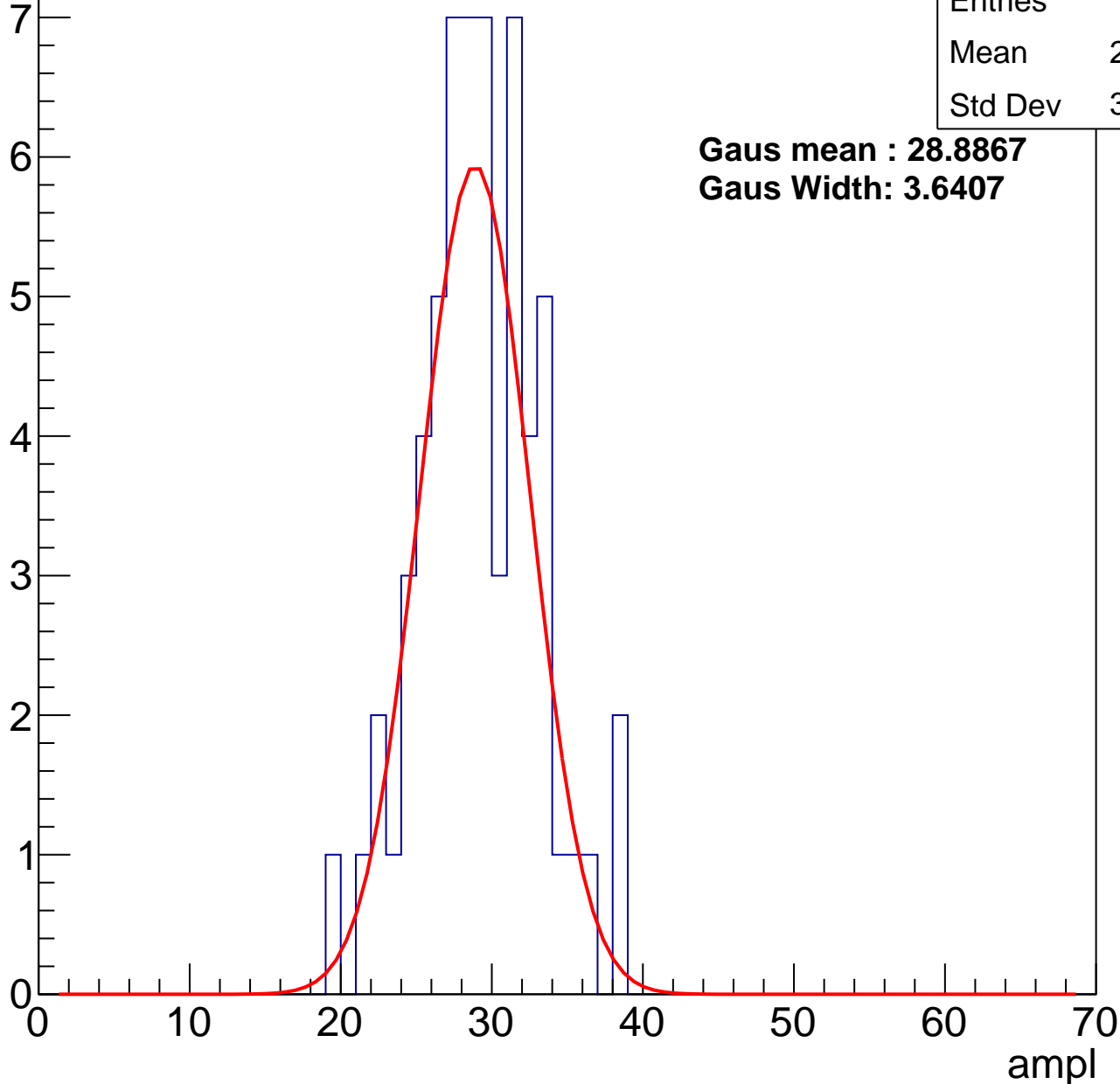
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.68
Std Dev	3.872

Gaus mean : 28.8867

Gaus Width: 3.6407



B1L102S, U11-ch80, adc1

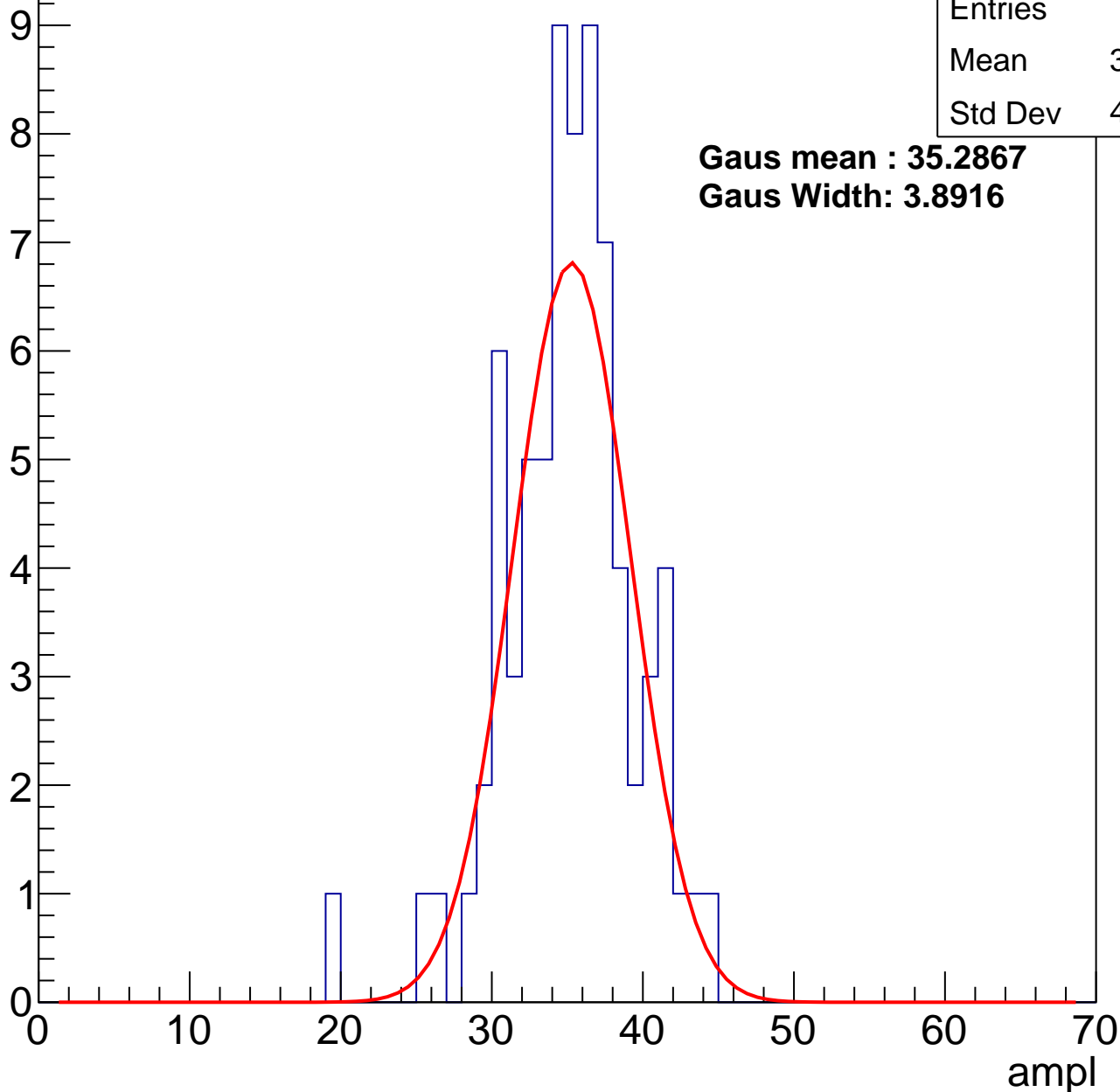
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.68
Std Dev	4.256

Gaus mean : 35.2867

Gaus Width: 3.8916



B1L102S, U11-ch80, adc2

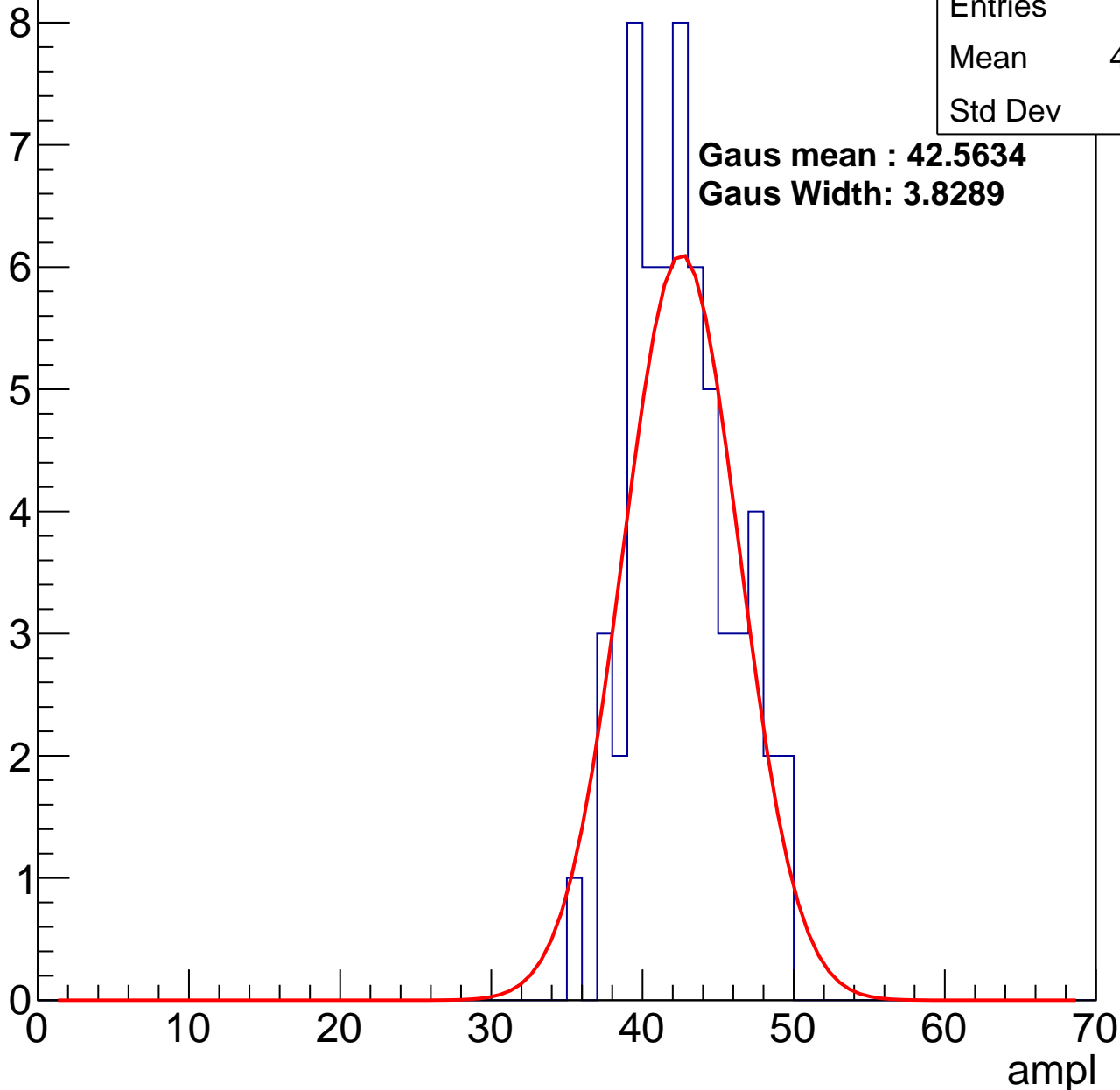
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.19
Std Dev	3.26

Gaus mean : 42.5634

Gaus Width: 3.8289

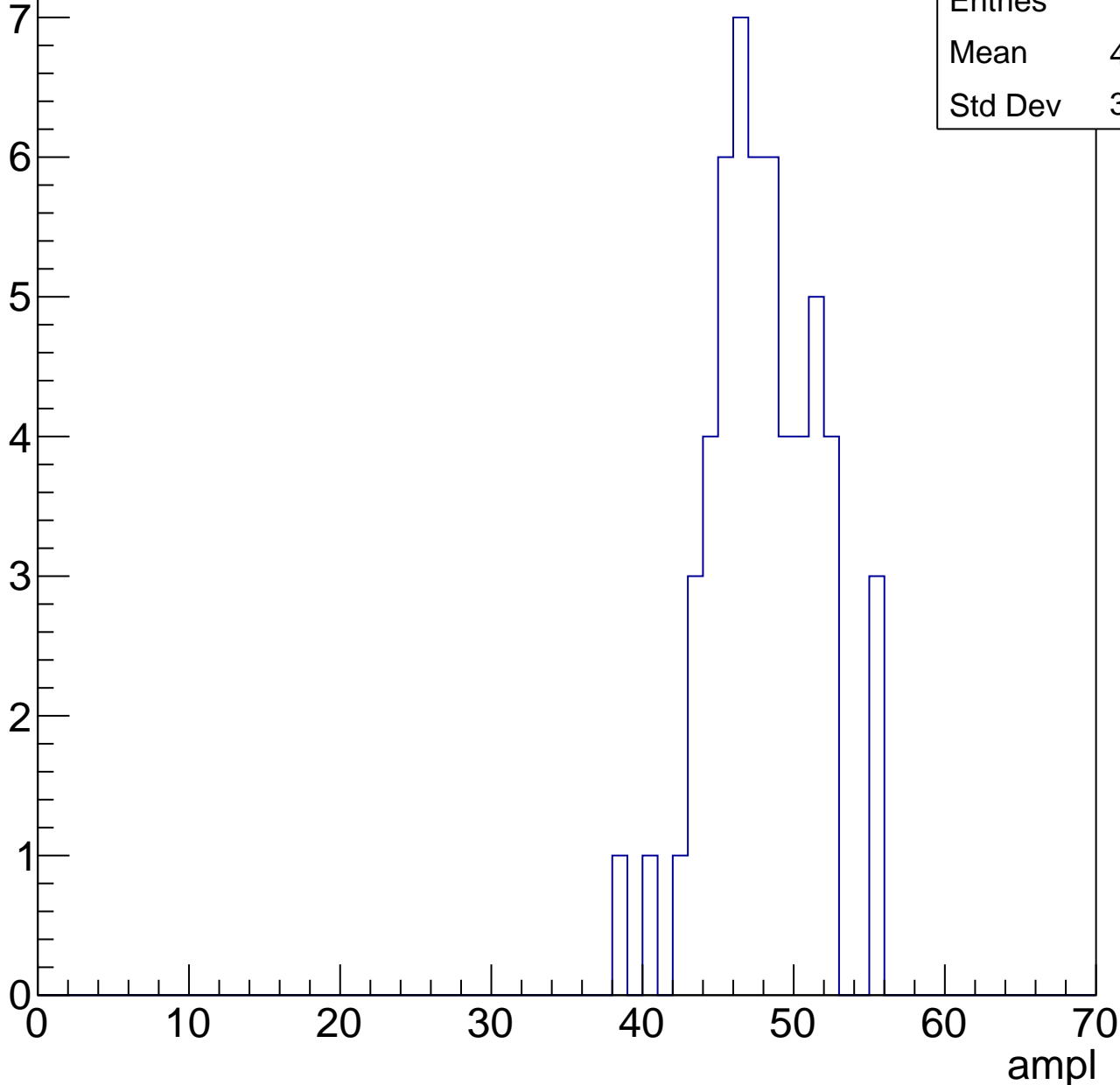


B1L102S, U11-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	47.47
Std Dev	3.536



B1L102S, U11-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	53.75
Std Dev	3.809

Entry

10

8

6

4

2

0

0

10

20

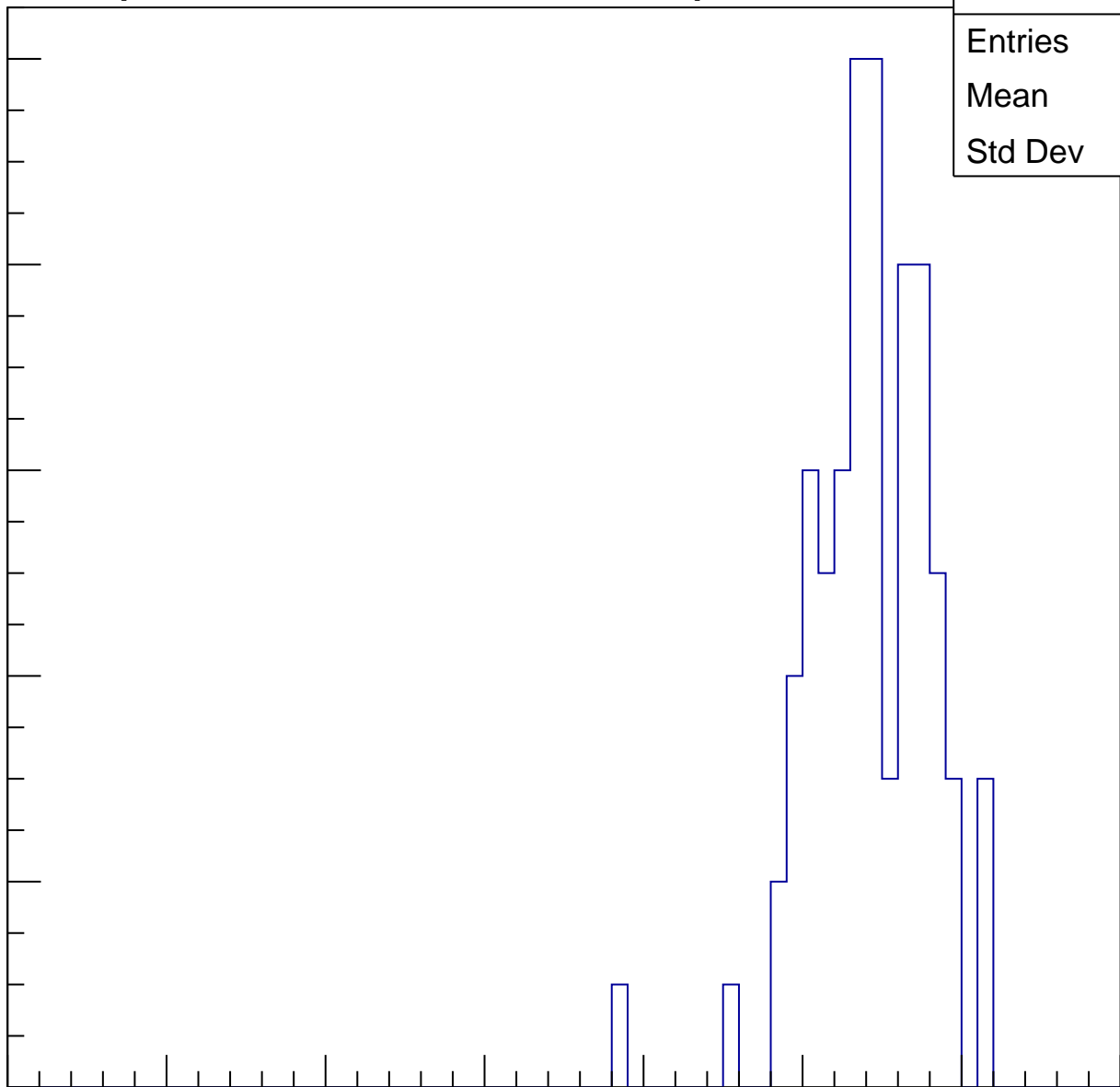
30

40

50

60

ampl



B1L102S, U11-ch80, adc5

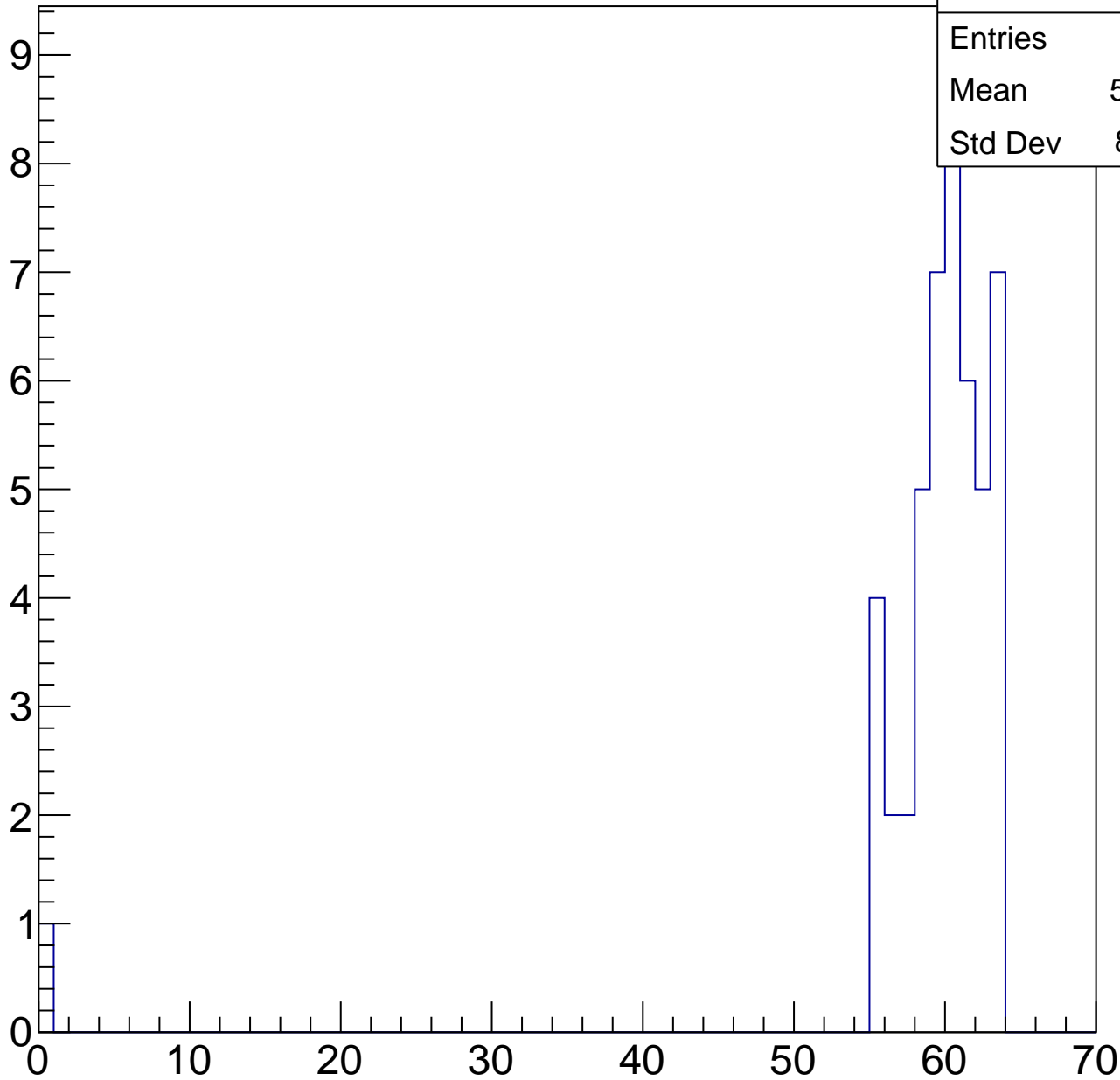
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.46
Std Dev	8.841

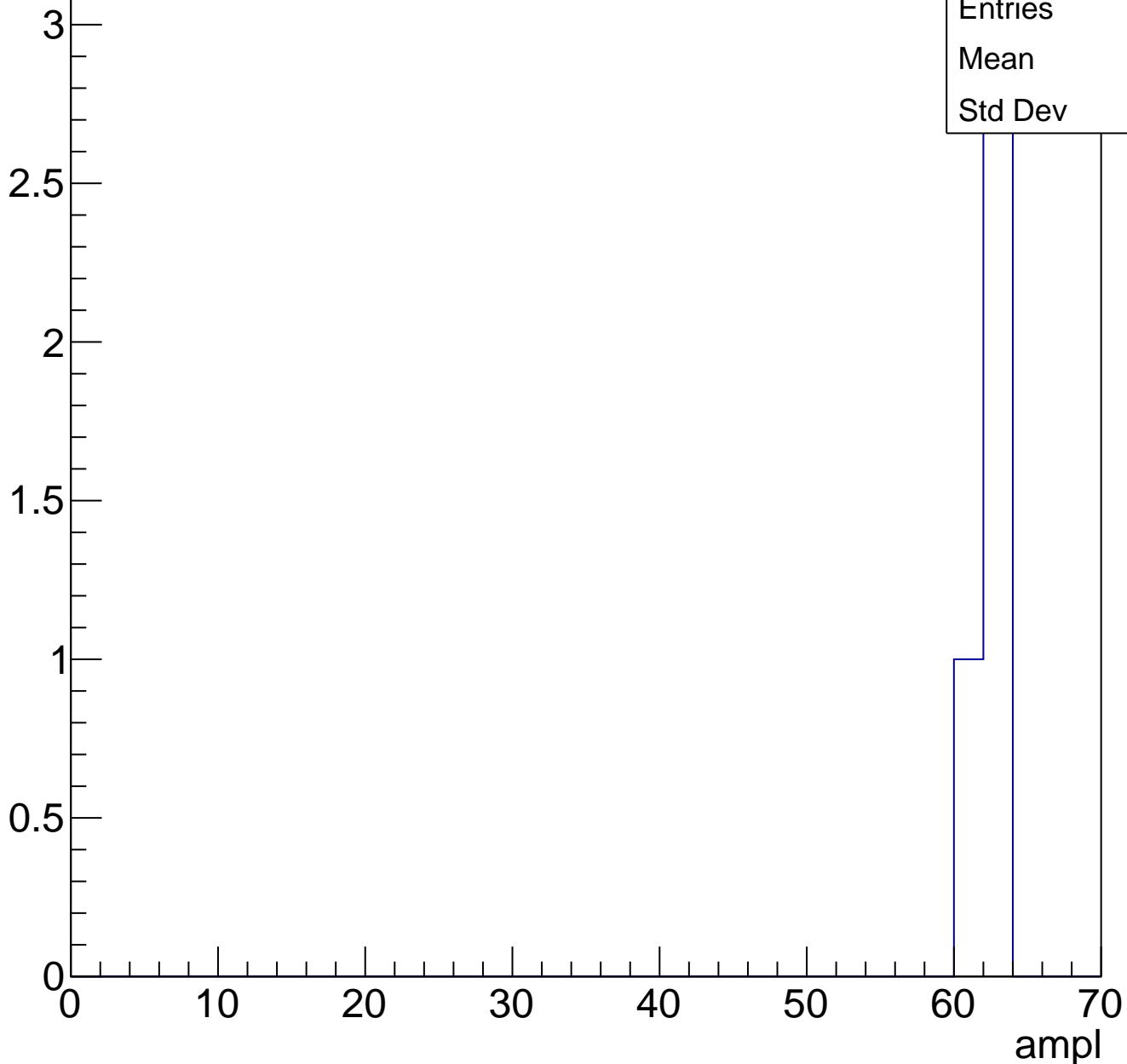
ampl



B1L102S, U11-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

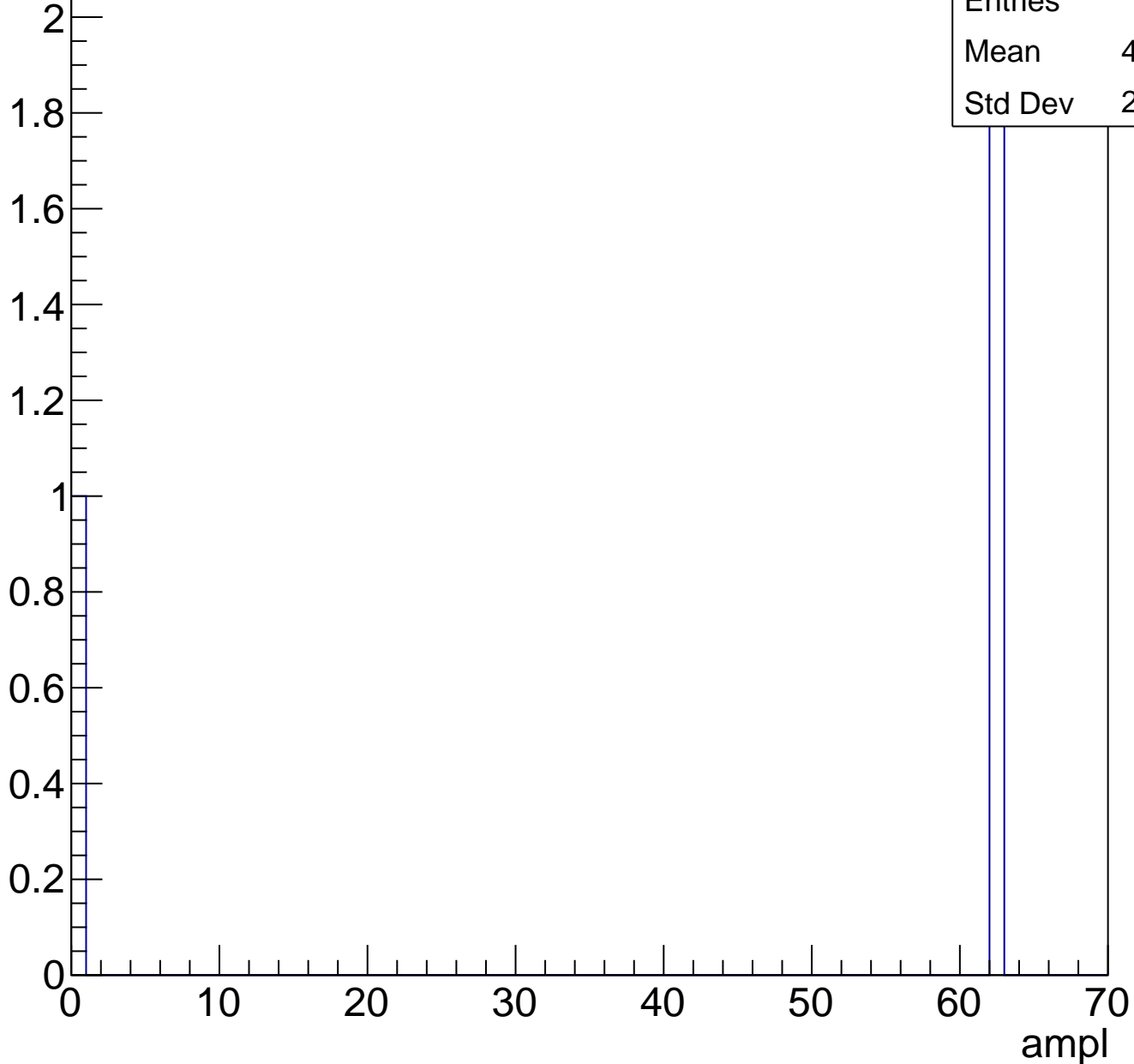
Entry



B1L102S, U11-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch81, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	27.51
Std Dev	5.631

Gaus mean : 28.7204

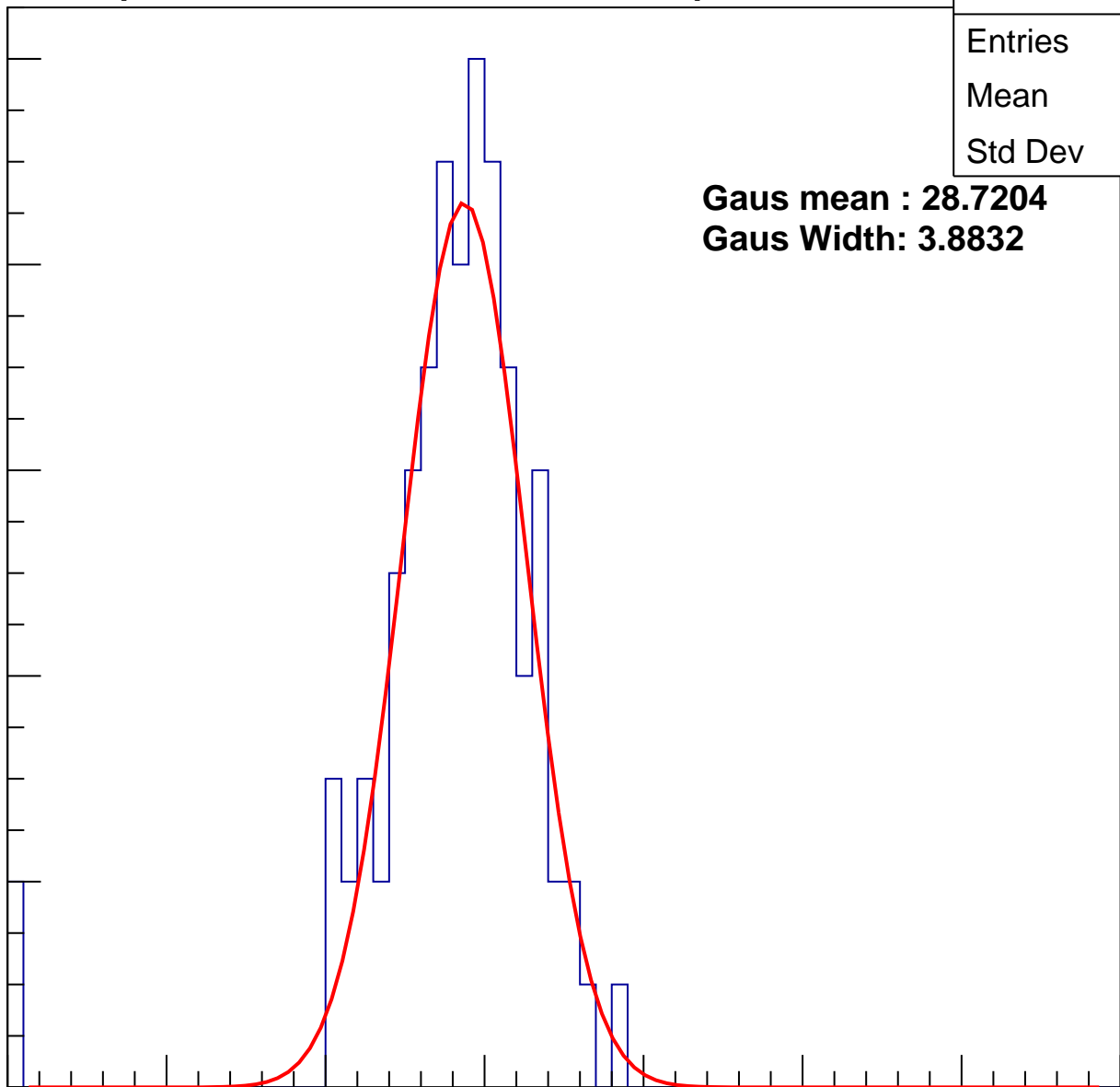
Gaus Width: 3.8832

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch81, adc1

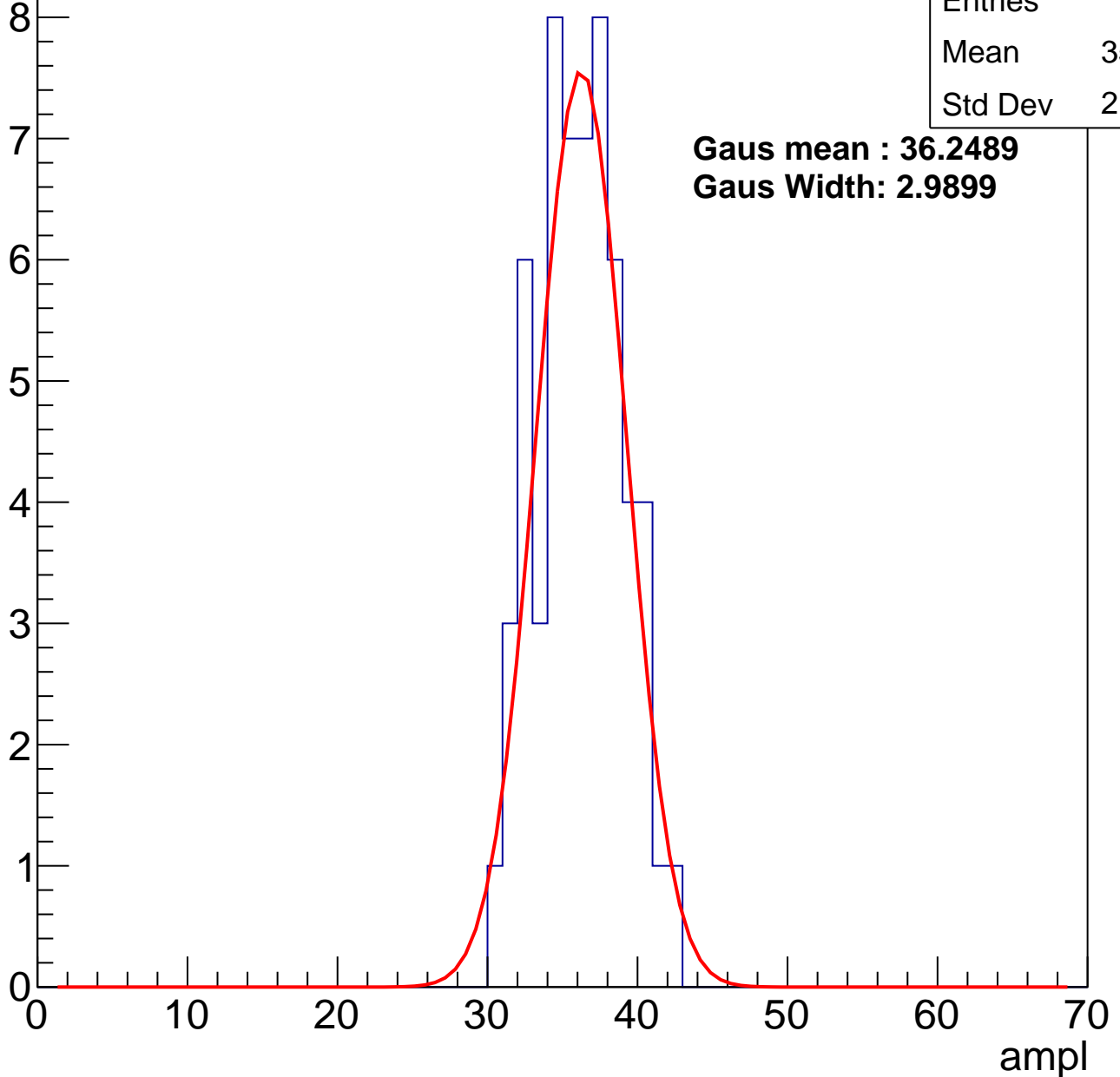
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.69
Std Dev	2.794

Gaus mean : 36.2489

Gaus Width: 2.9899



B1L102S, U11-ch81, adc2

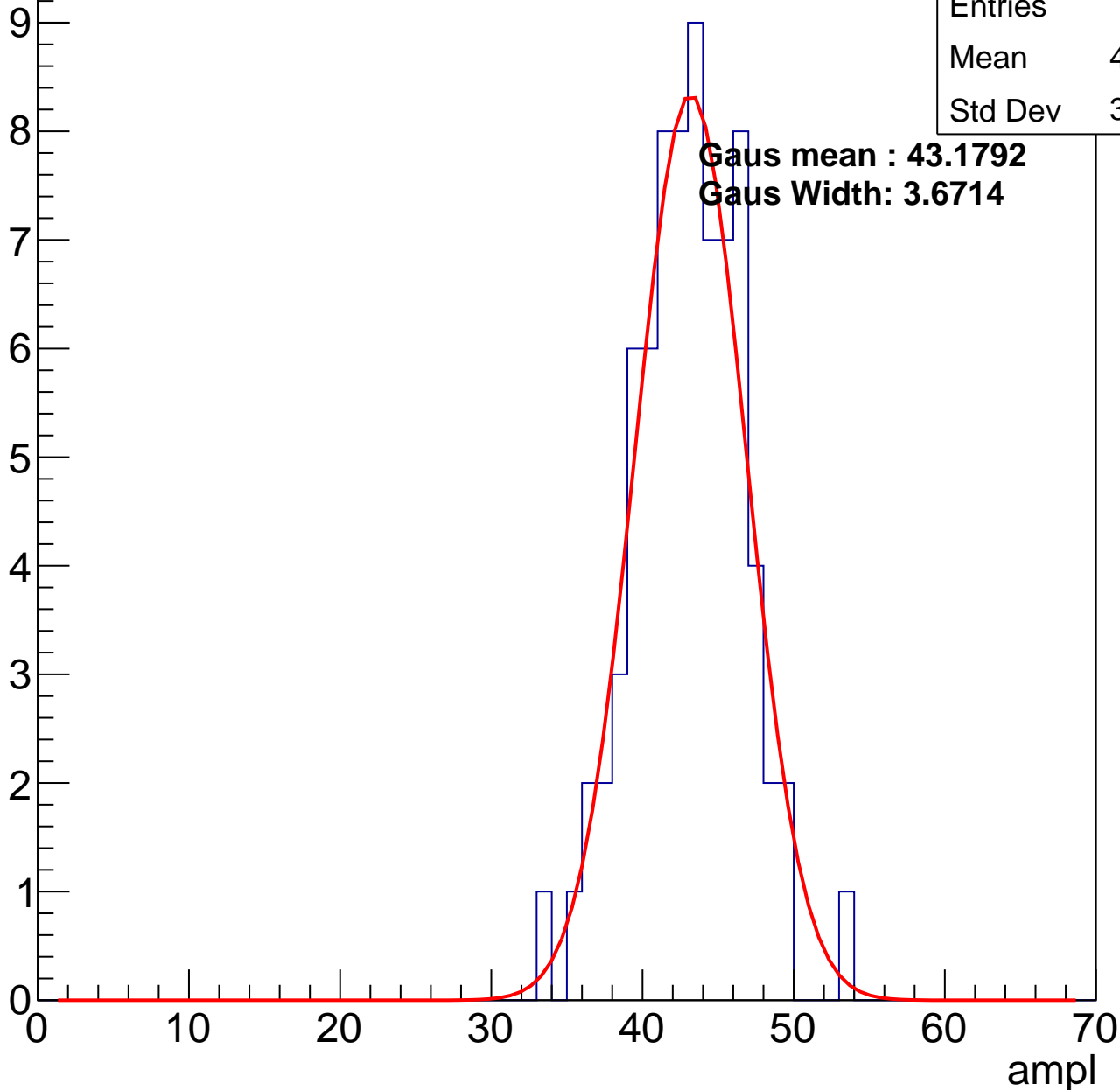
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.58
Std Dev	3.565

Gaus mean : 43.1792

Gaus Width: 3.6714

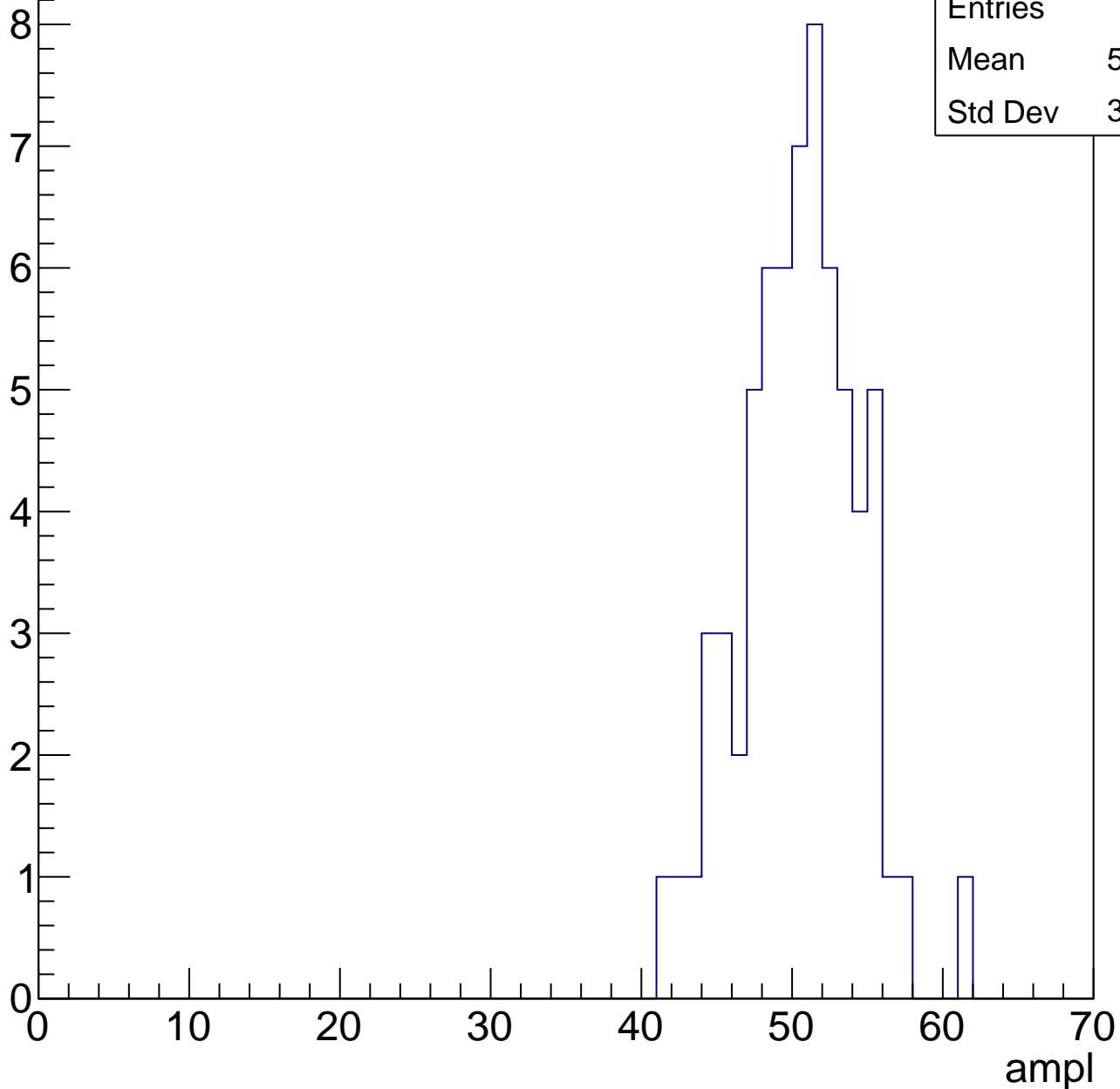


B1L102S, U11-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.03
Std Dev	3.806

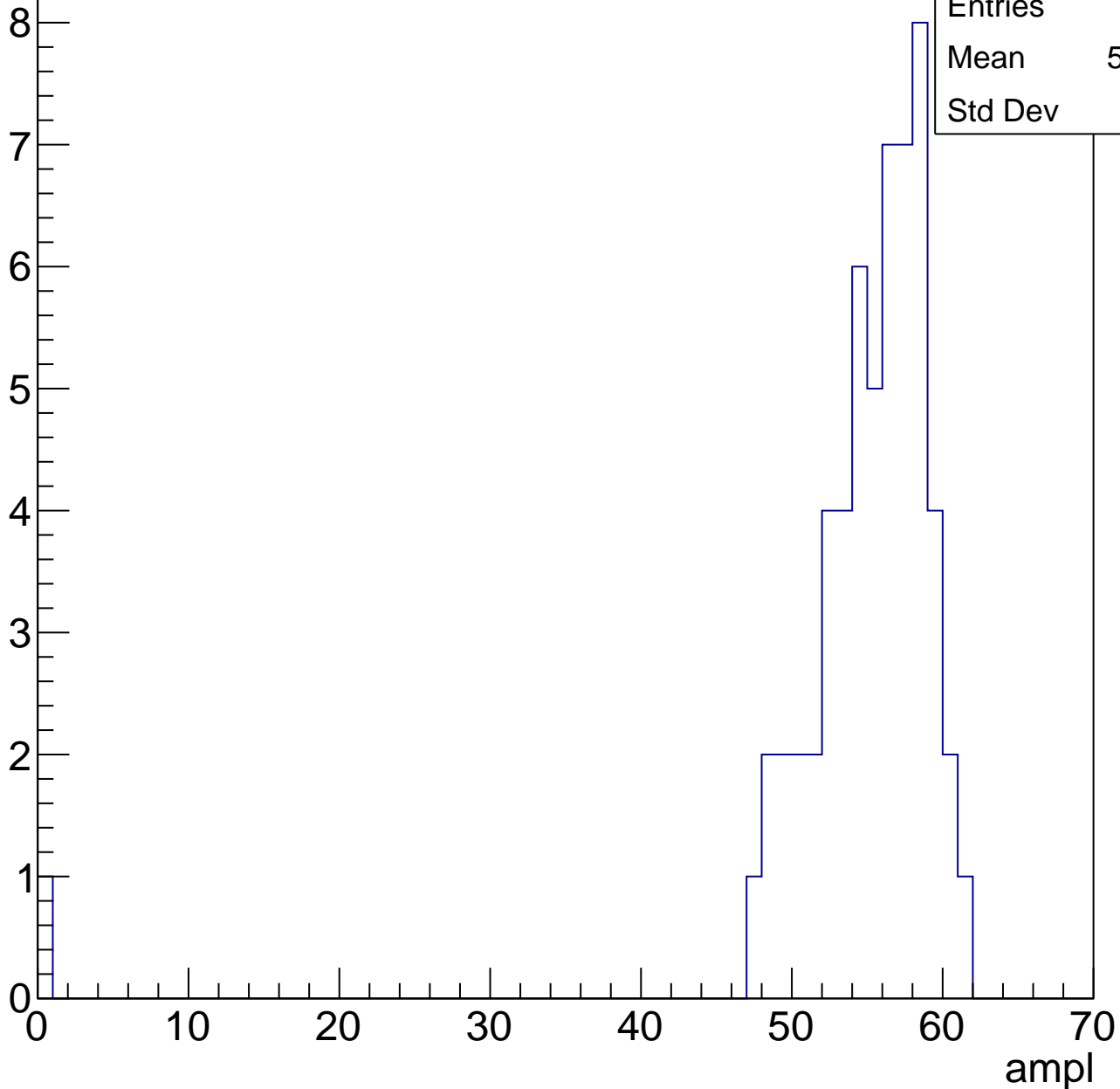


B1L102S, U11-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.03
Std Dev	7.88

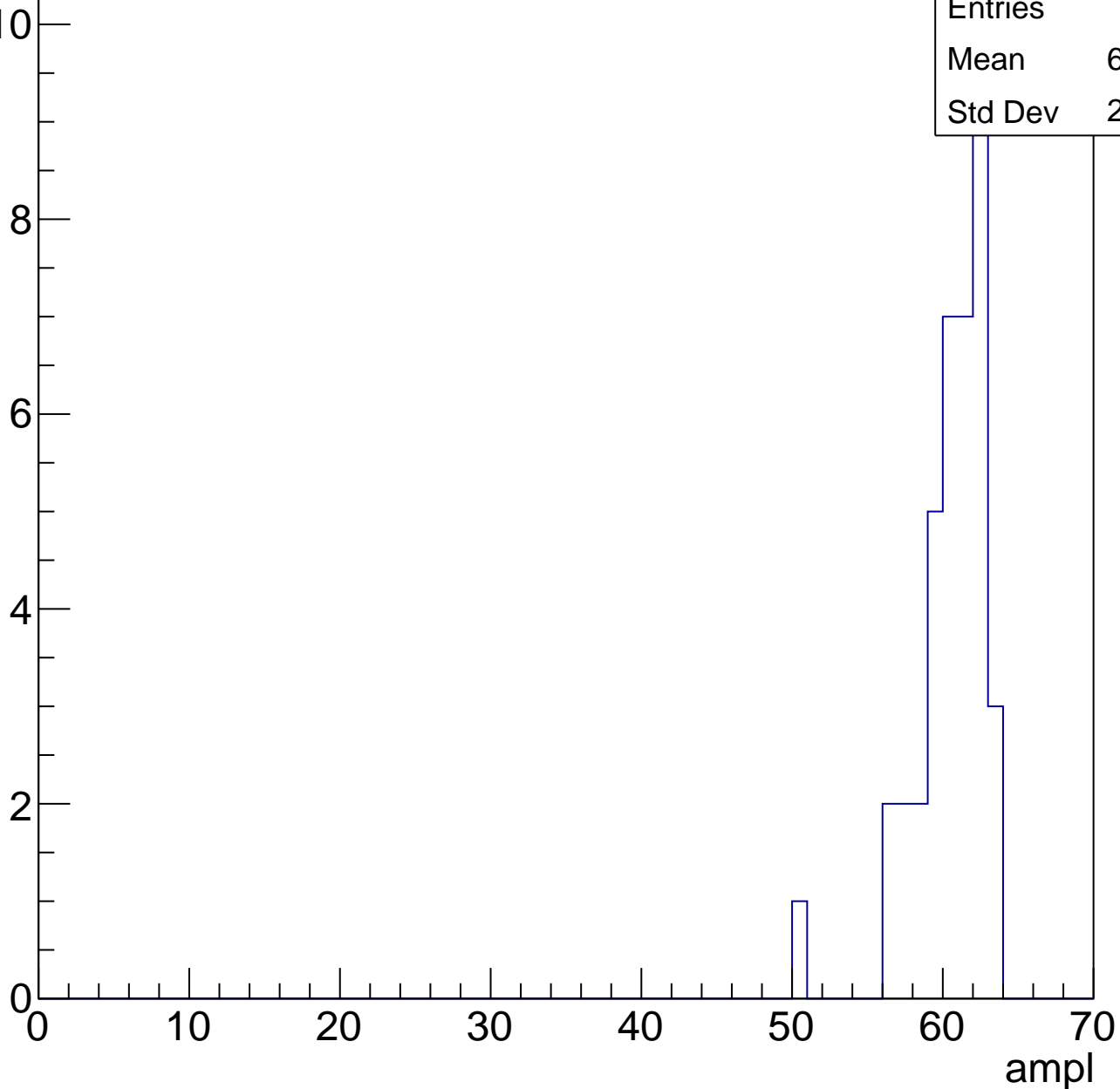


B1L102S, U11-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	60.08
Std Dev	2.464

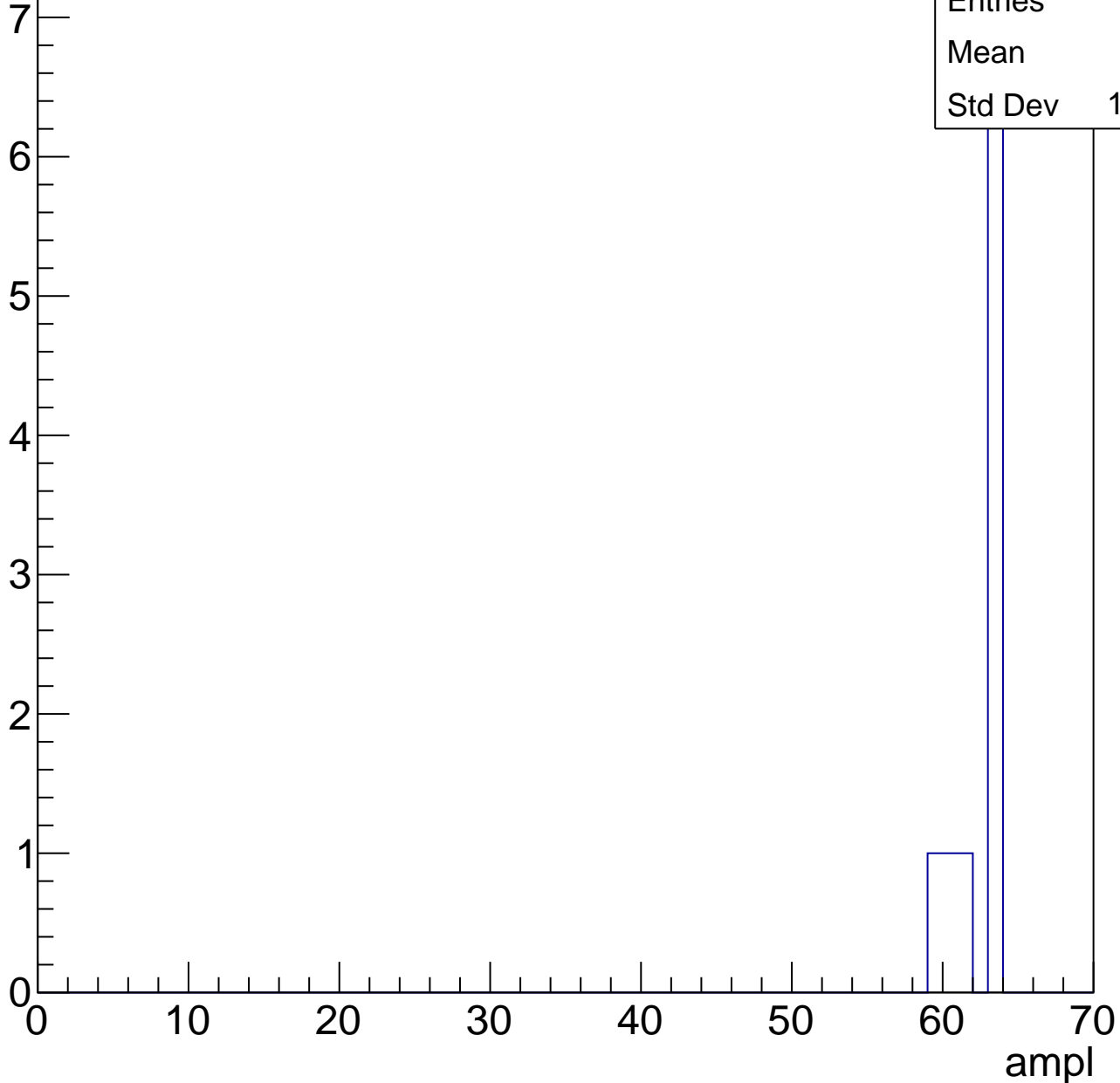


B1L102S, U11-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.1
Std Dev	1.446



B1L102S, U11-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch82, adc0

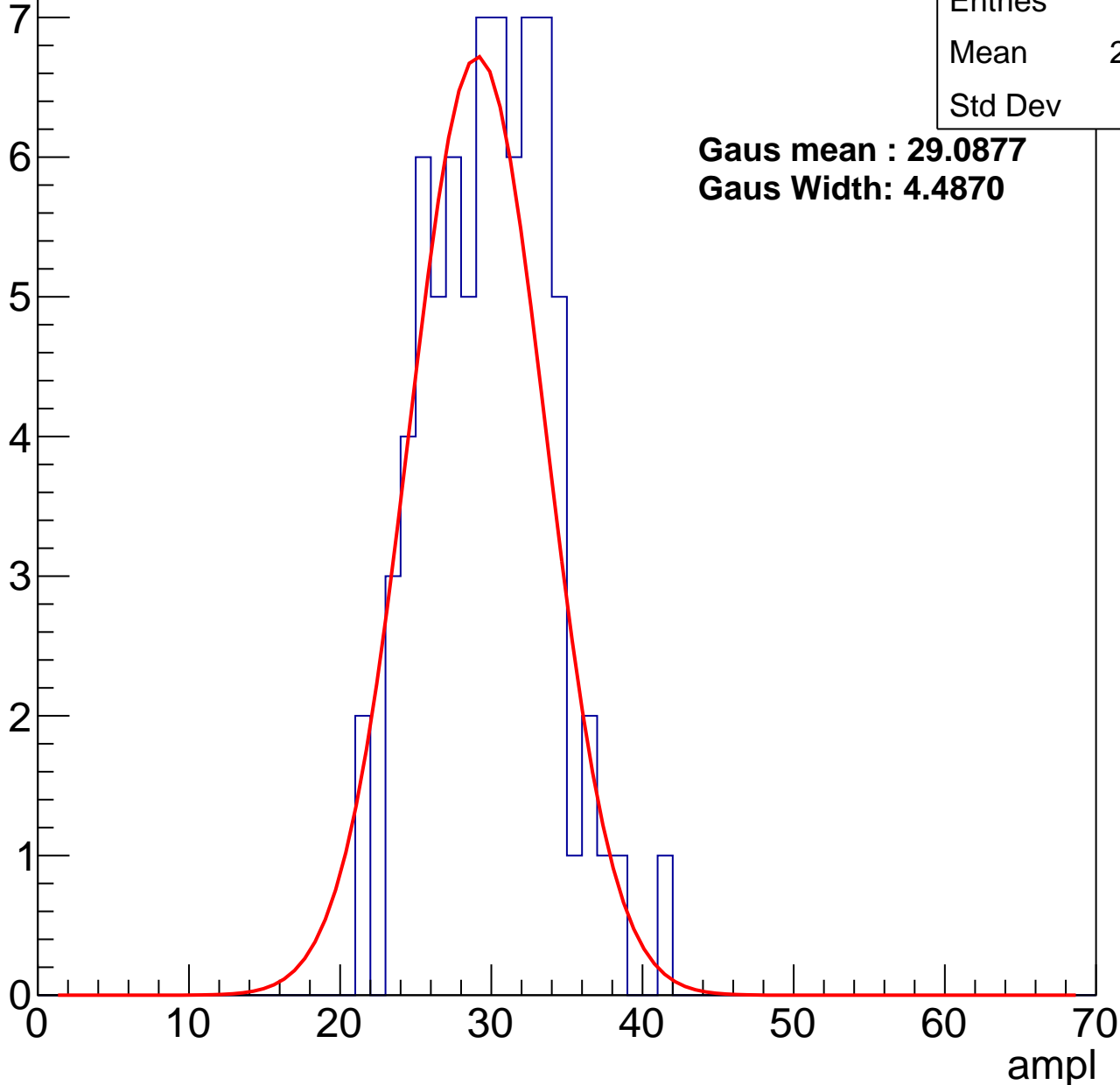
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.42
Std Dev	4.05

Gaus mean : 29.0877

Gaus Width: 4.4870



B1L102S, U11-ch82, adc1

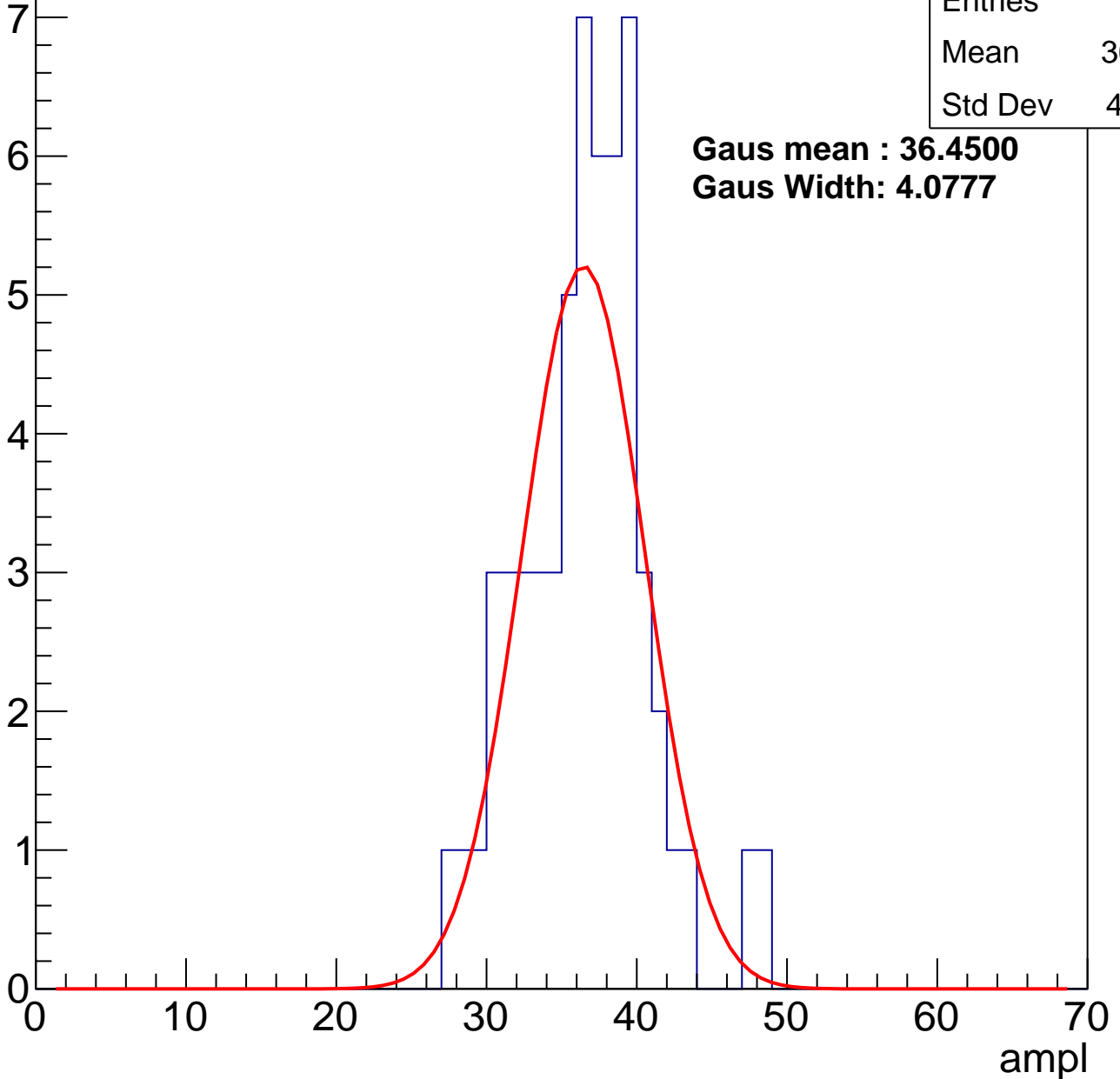
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.14
Std Dev	4.171

Gaus mean : 36.4500

Gaus Width: 4.0777



B1L102S, U11-ch82, adc2

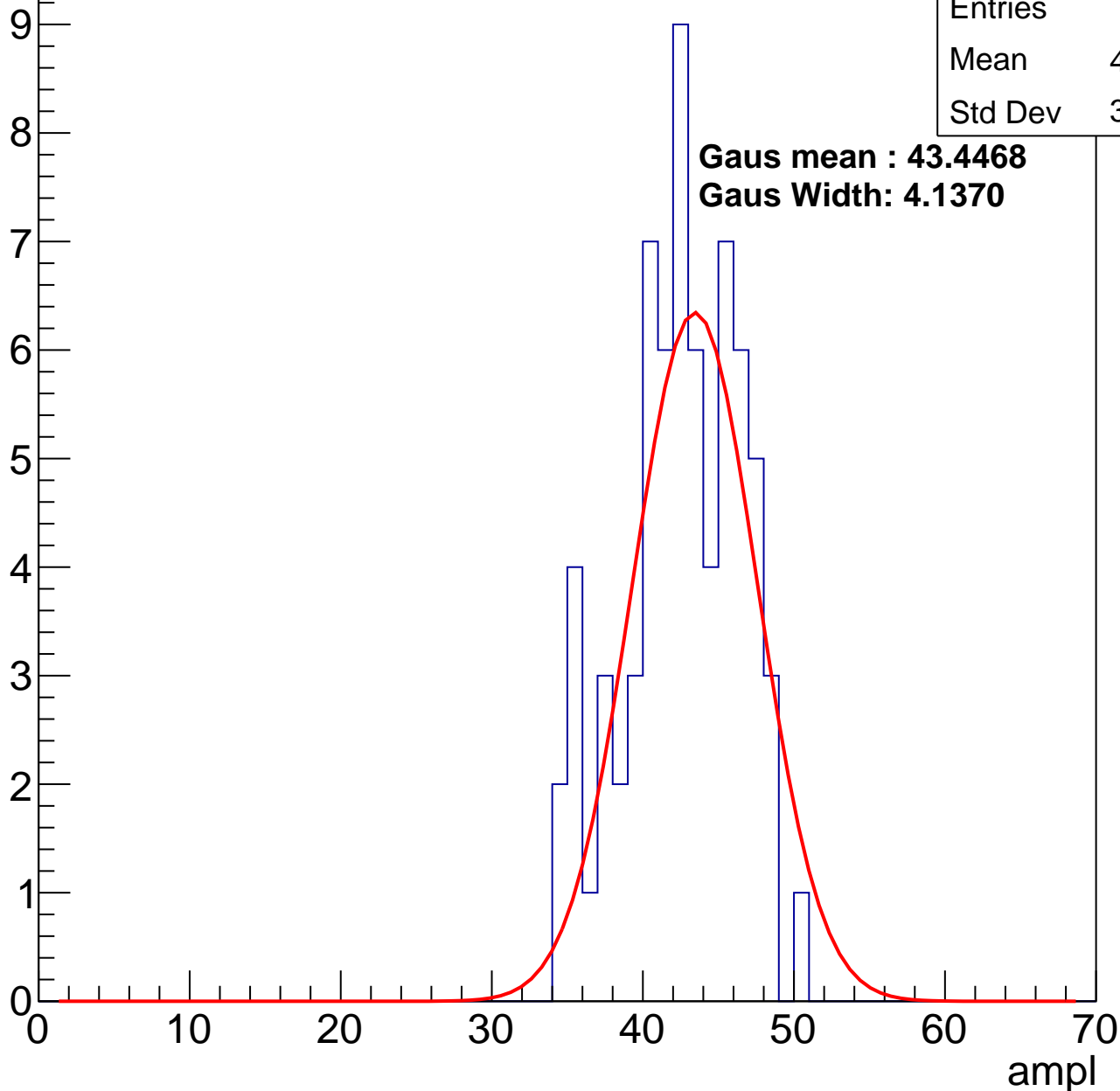
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.12
Std Dev	3.817

Gaus mean : 43.4468

Gaus Width: 4.1370

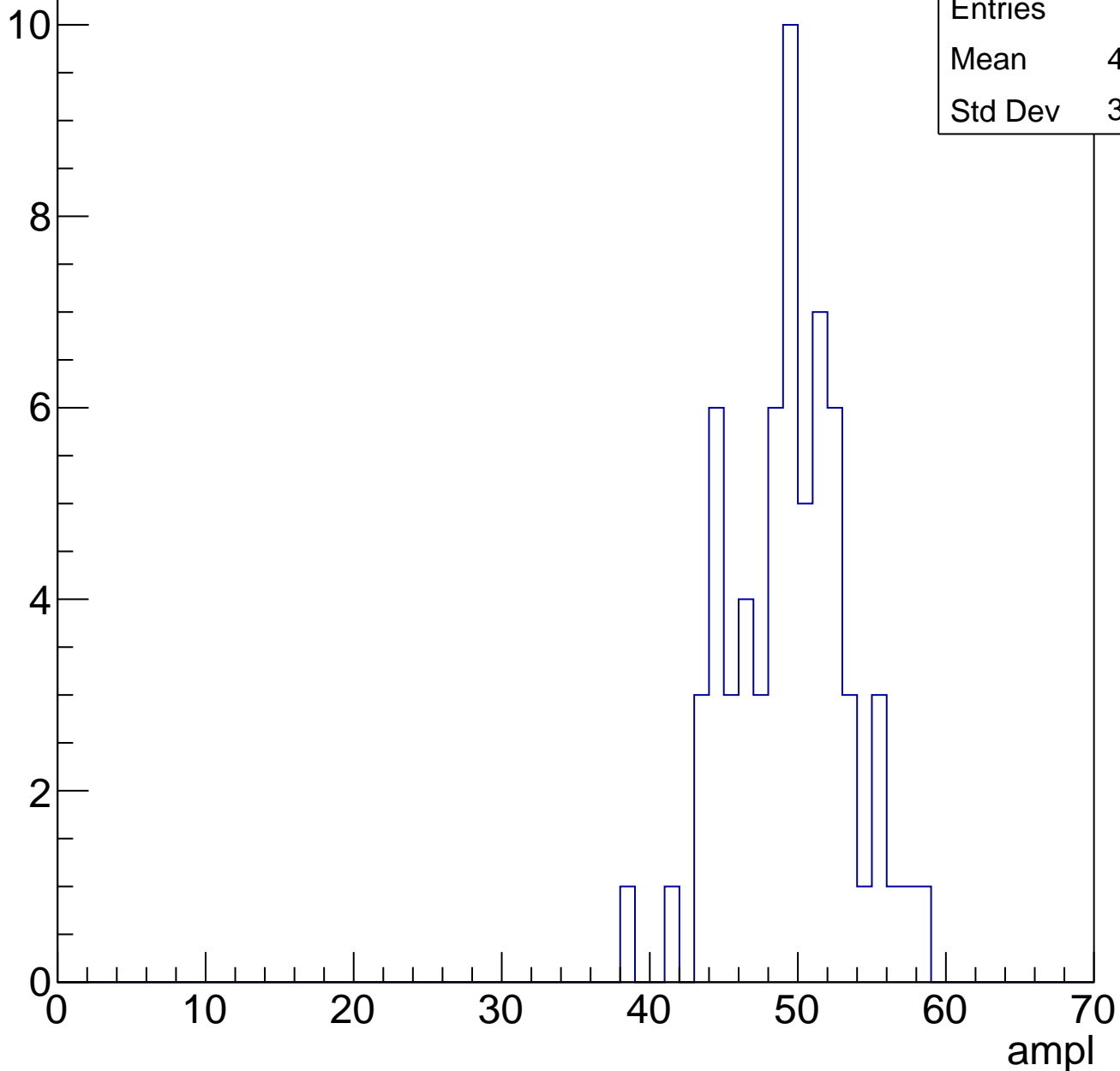


B1L102S, U11-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	48.89
Std Dev	3.935

Entry

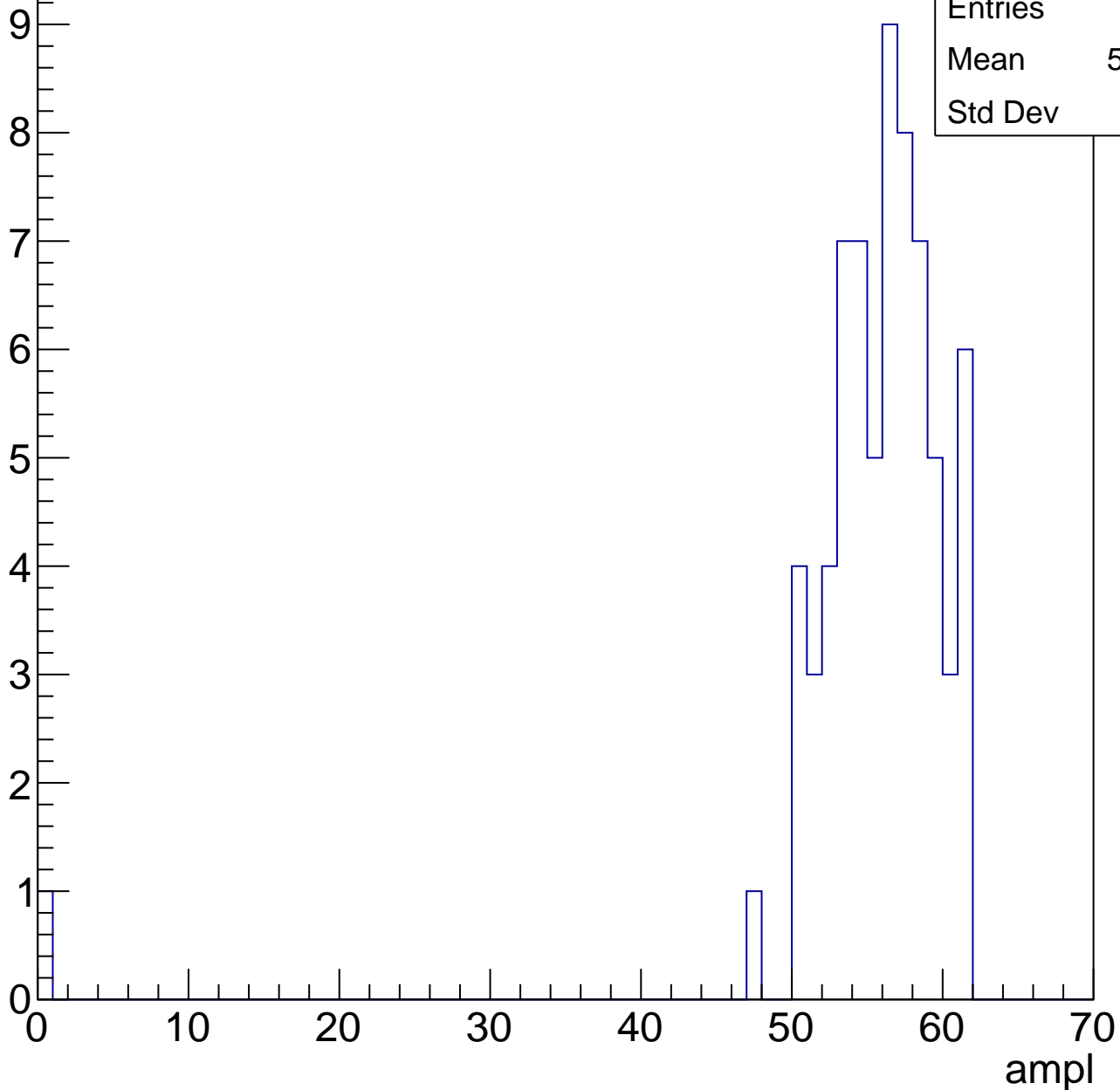


B1L102S, U11-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.84
Std Dev	7.35

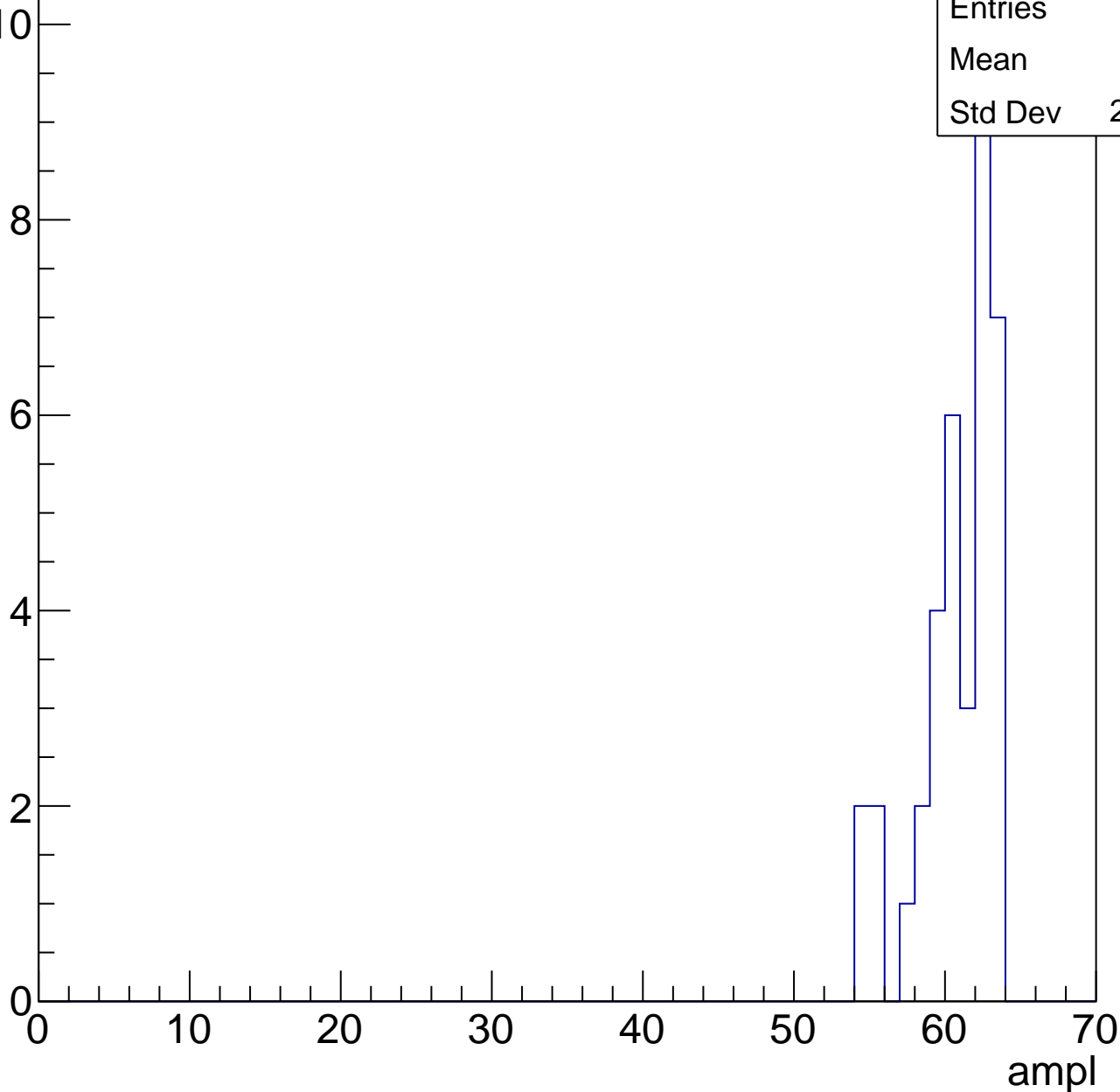


B1L102S, U11-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

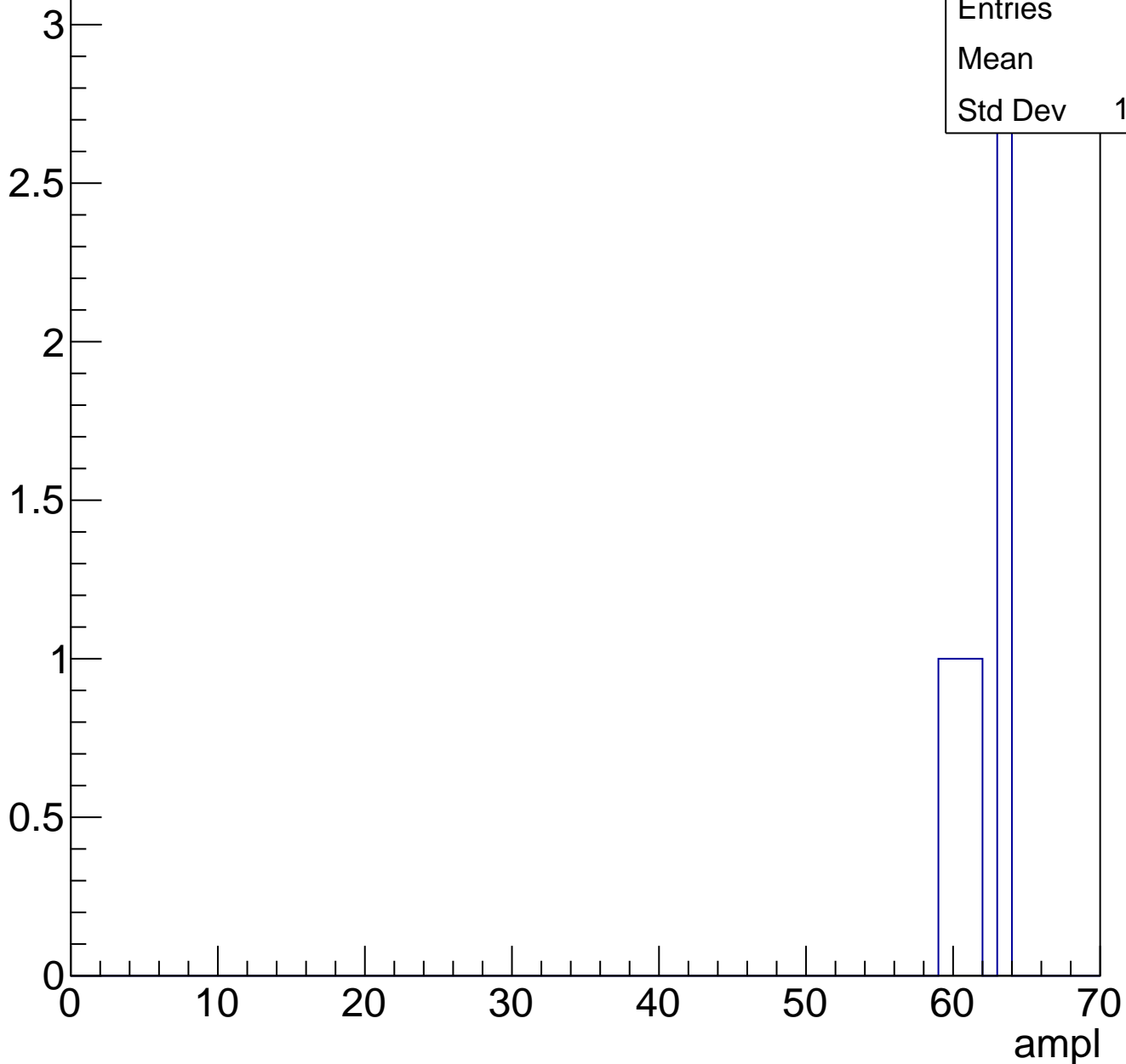
Entries	37
Mean	60.3
Std Dev	2.577



B1L102S, U11-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch83, adc0

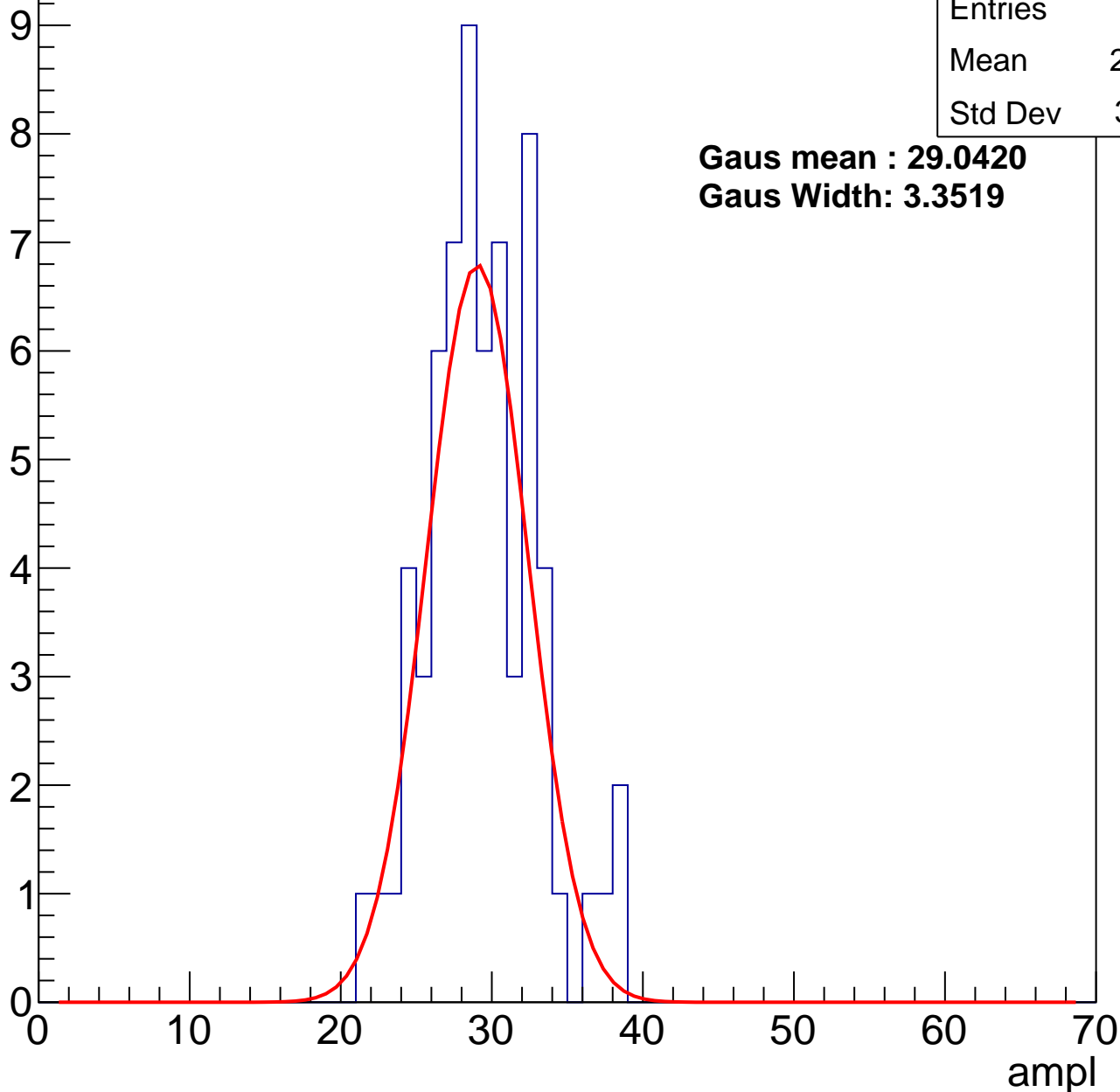
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.95
Std Dev	3.601

Gaus mean : 29.0420

Gaus Width: 3.3519



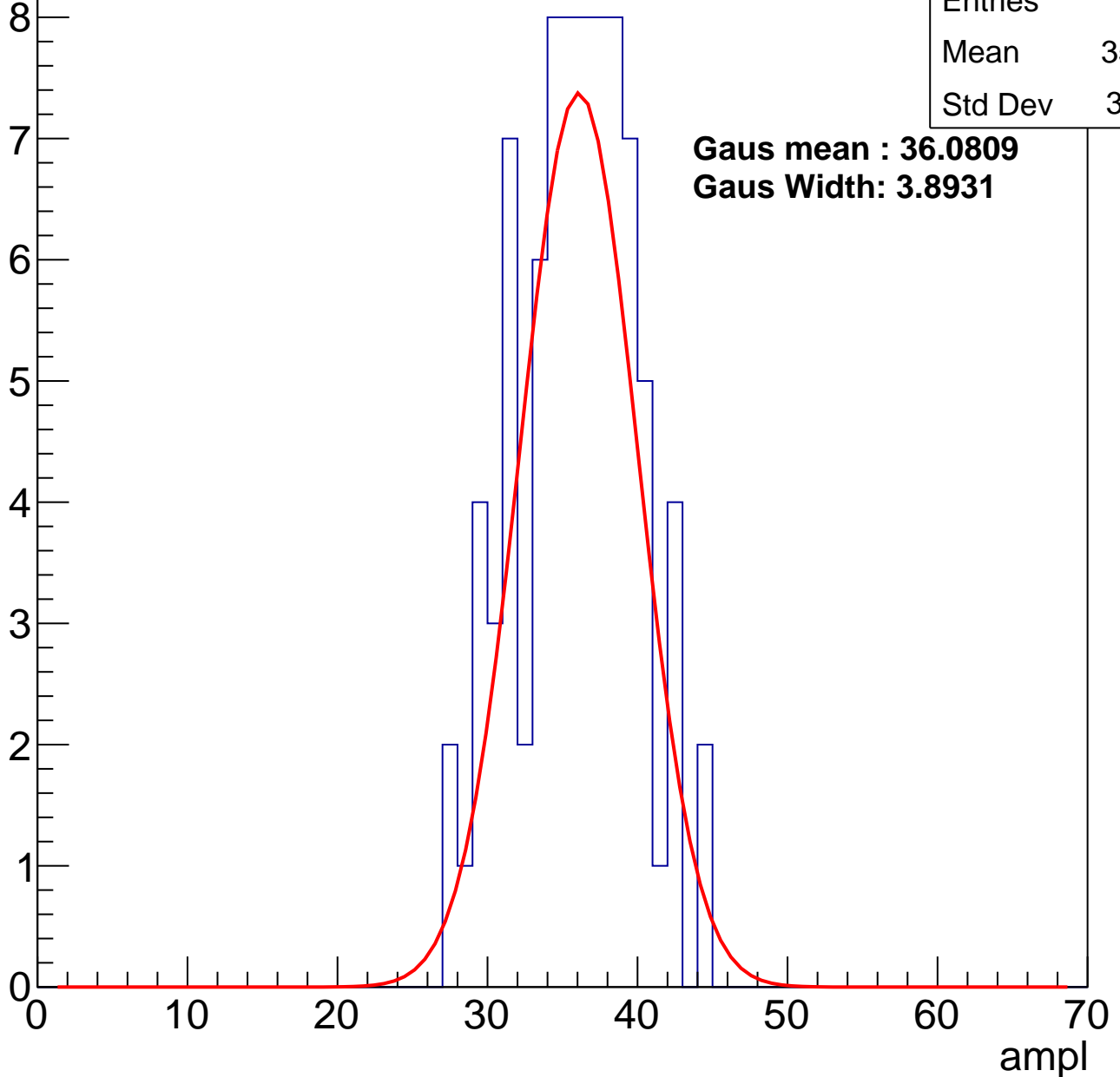
B1L102S, U11-ch83, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	35.44
Std Dev	3.911

Gaus mean : 36.0809
Gaus Width: 3.8931



B1L102S, U11-ch83, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	42.95
Std Dev	3.435

Gaus mean : 43.6906

Gaus Width: 3.5681

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

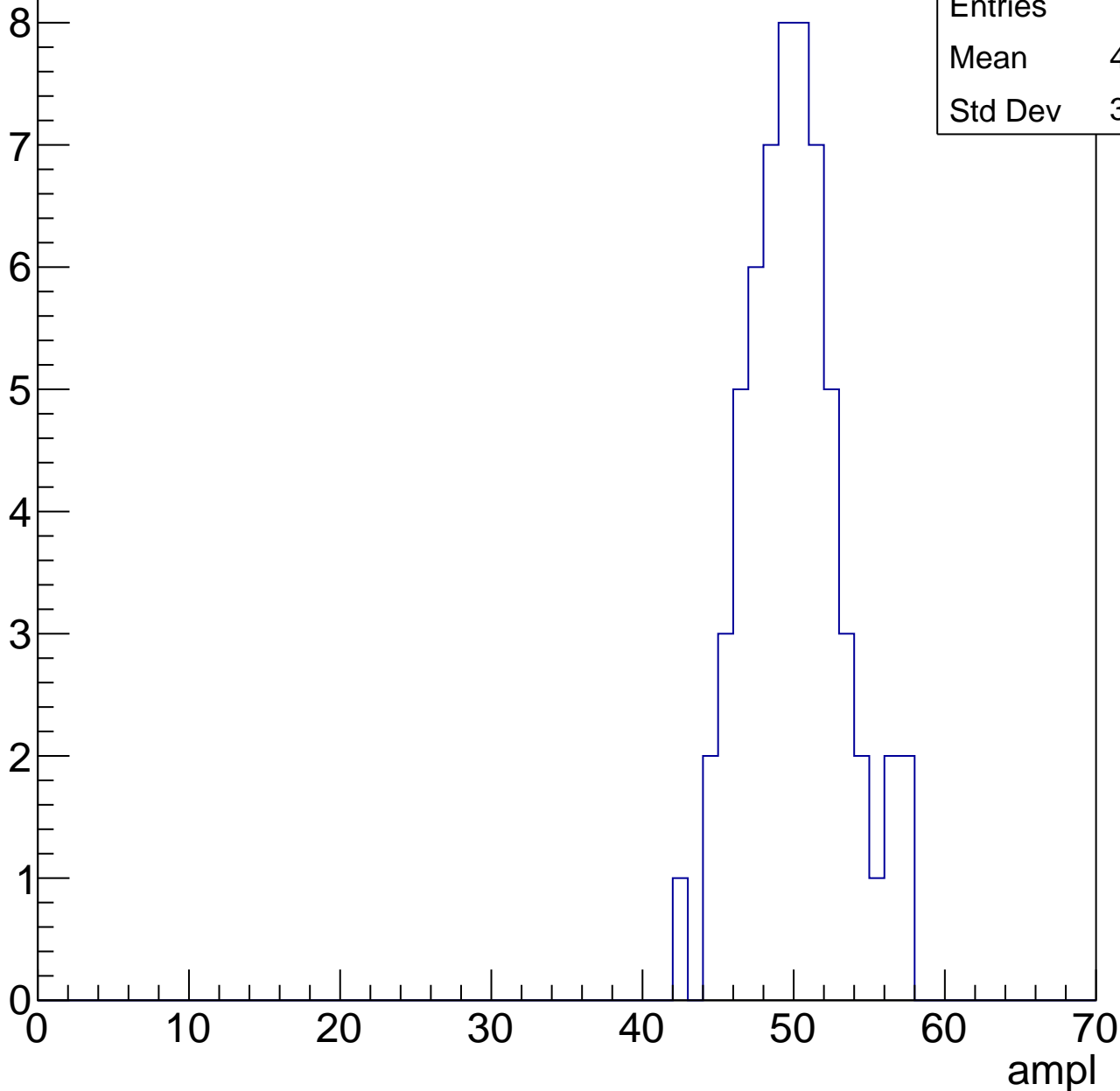
10

B1L102S, U11-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.52
Std Dev	3.232

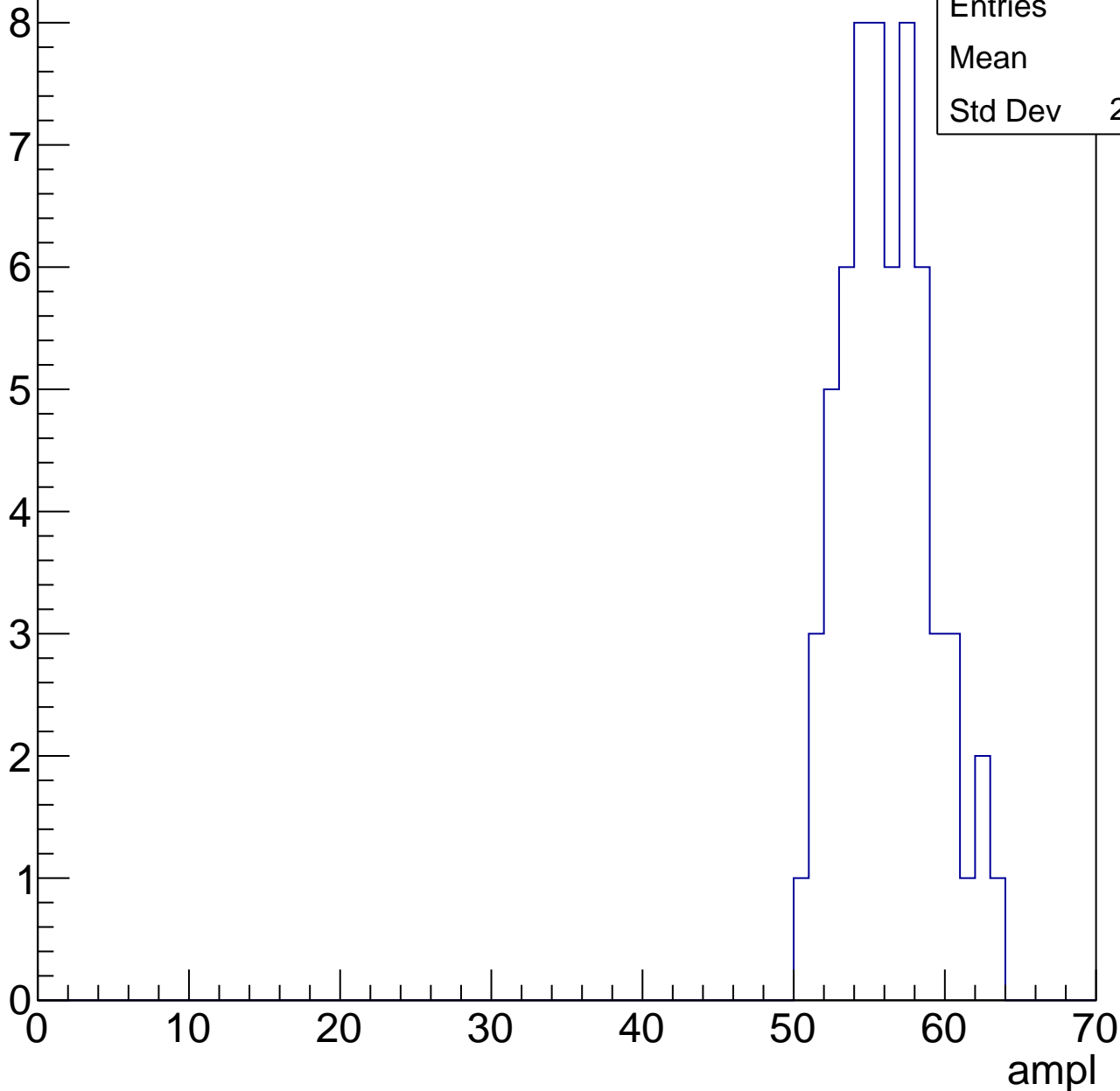


B1L102S, U11-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.7
Std Dev	2.955

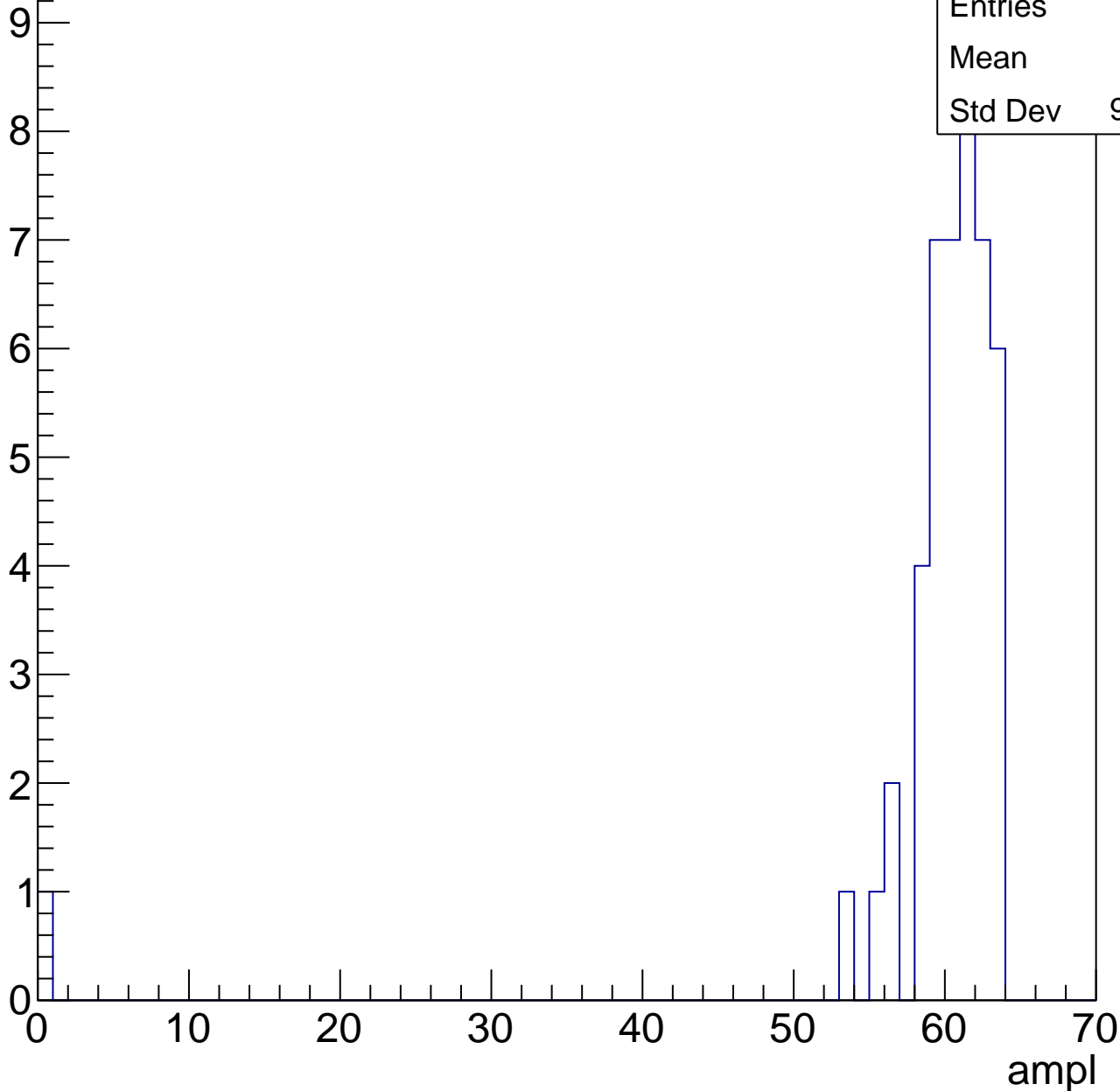


B1L102S, U11-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.8
Std Dev	9.135



B1L102S, U11-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch84, adc0

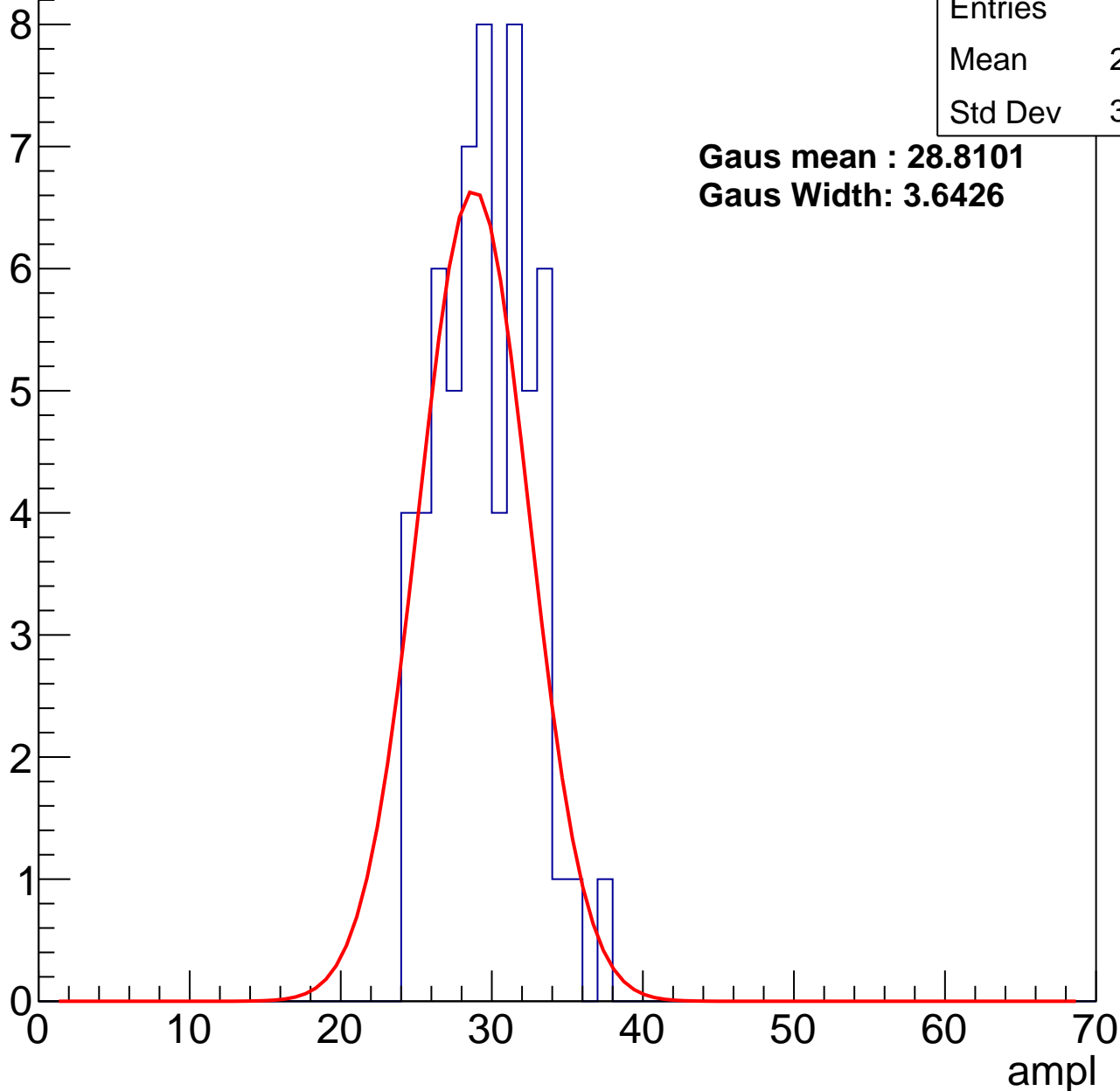
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.12
Std Dev	3.017

Gaus mean : 28.8101

Gaus Width: 3.6426



B1L102S, U11-ch84, adc1

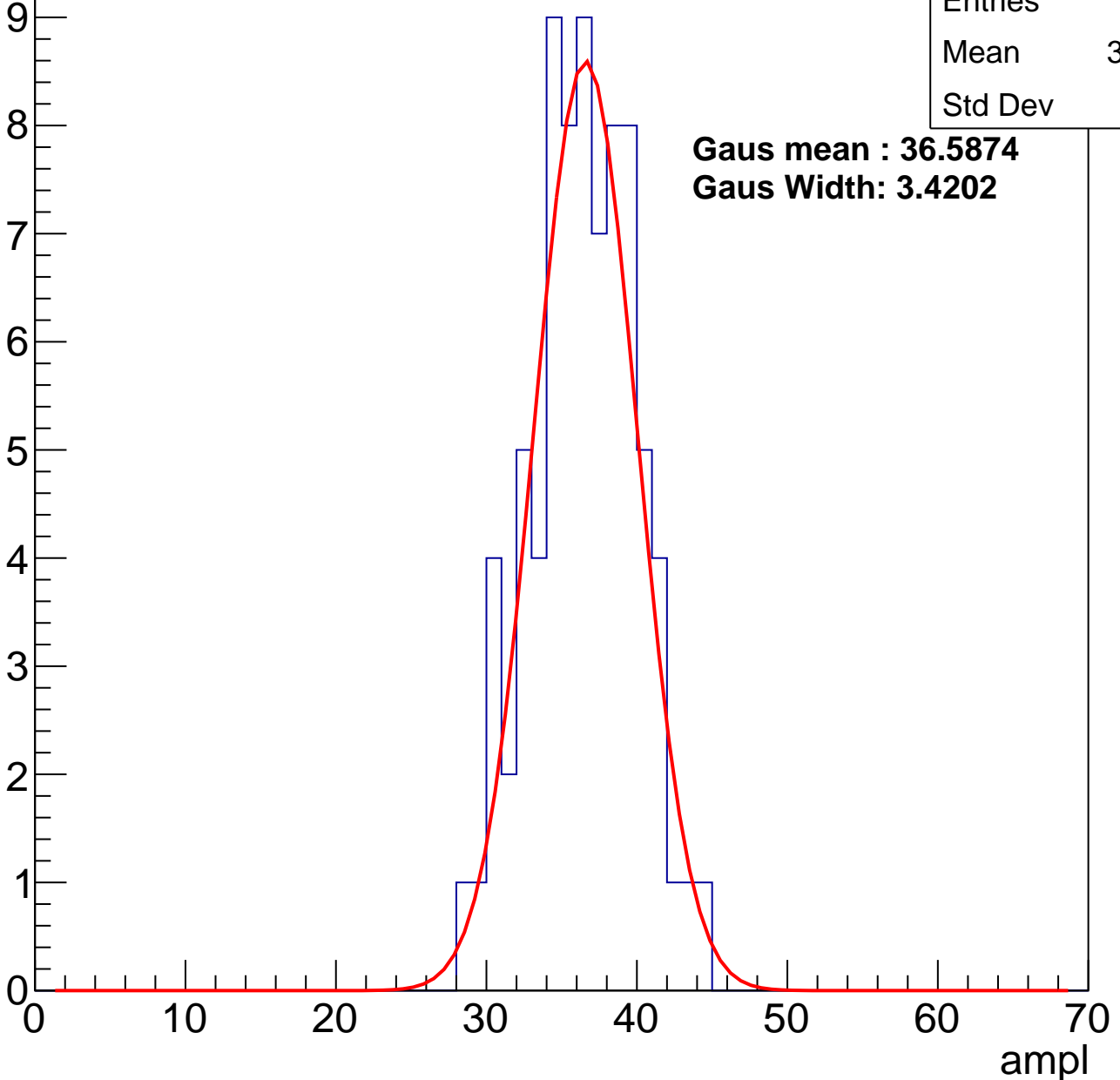
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.01
Std Dev	3.41

Gaus mean : 36.5874

Gaus Width: 3.4202



B1L102S, U11-ch84, adc2

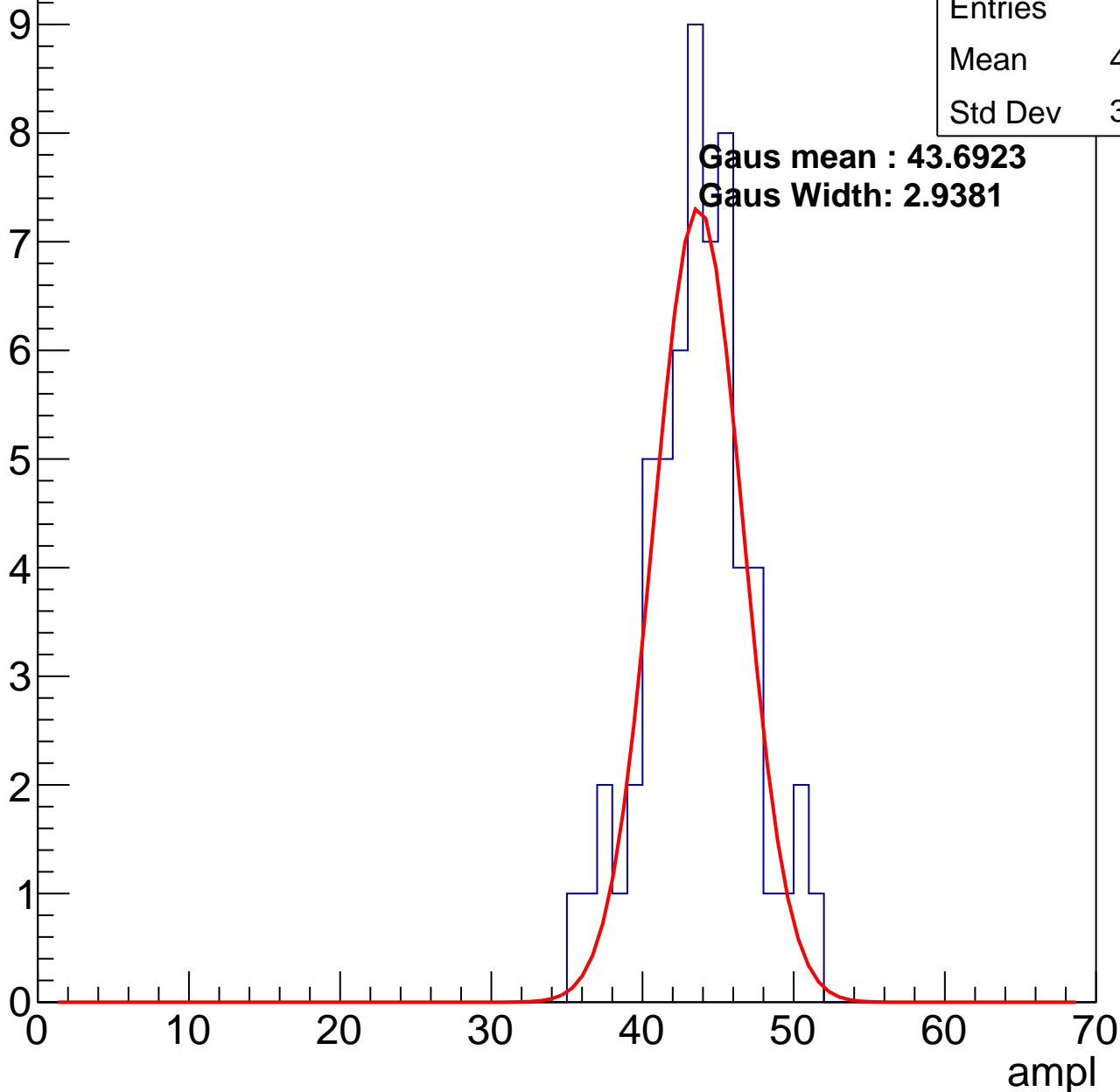
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.22
Std Dev	3.347

Gaus mean : 43.6923

Gaus Width: 2.9381

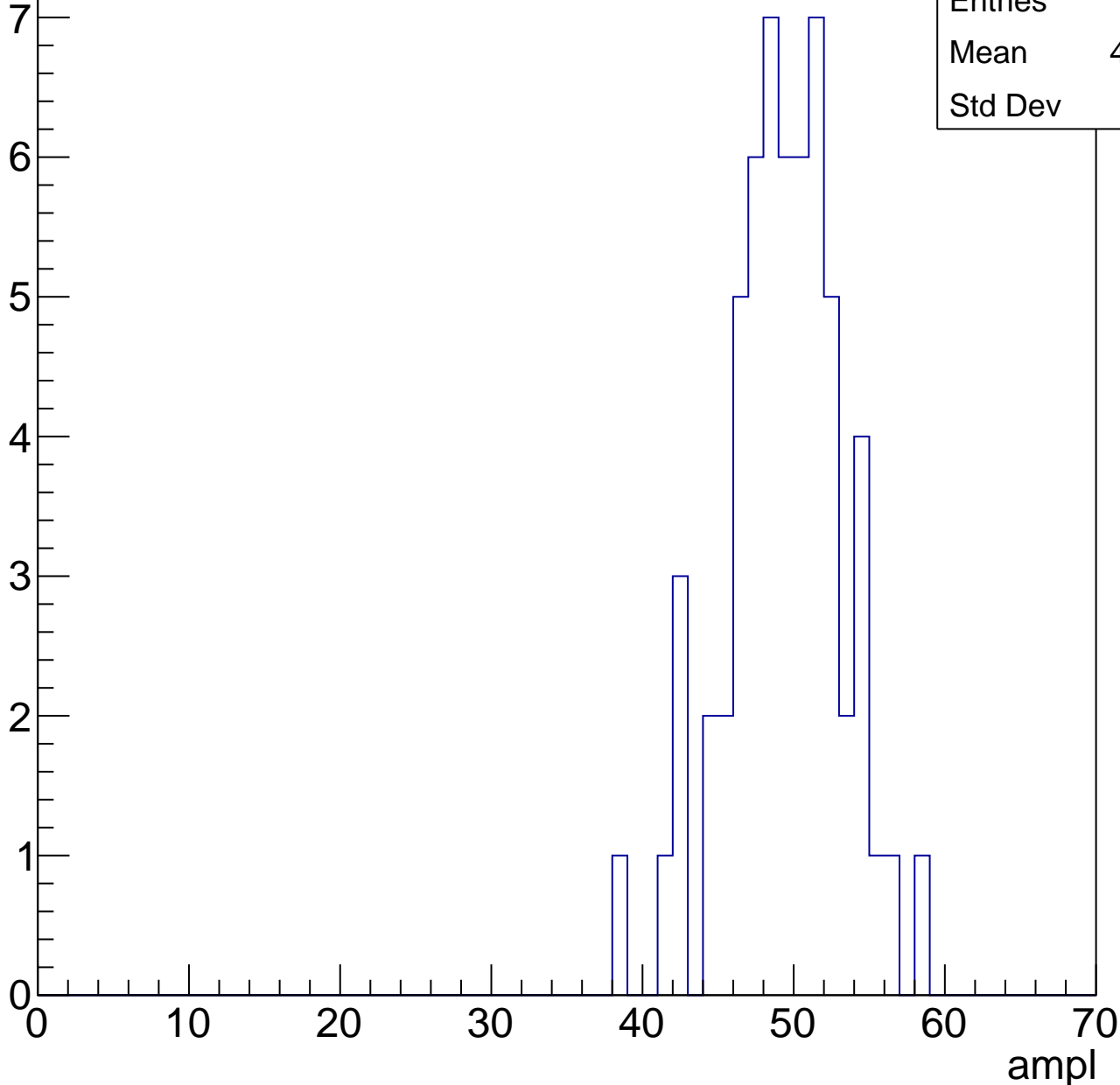


B1L102S, U11-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.88
Std Dev	3.8

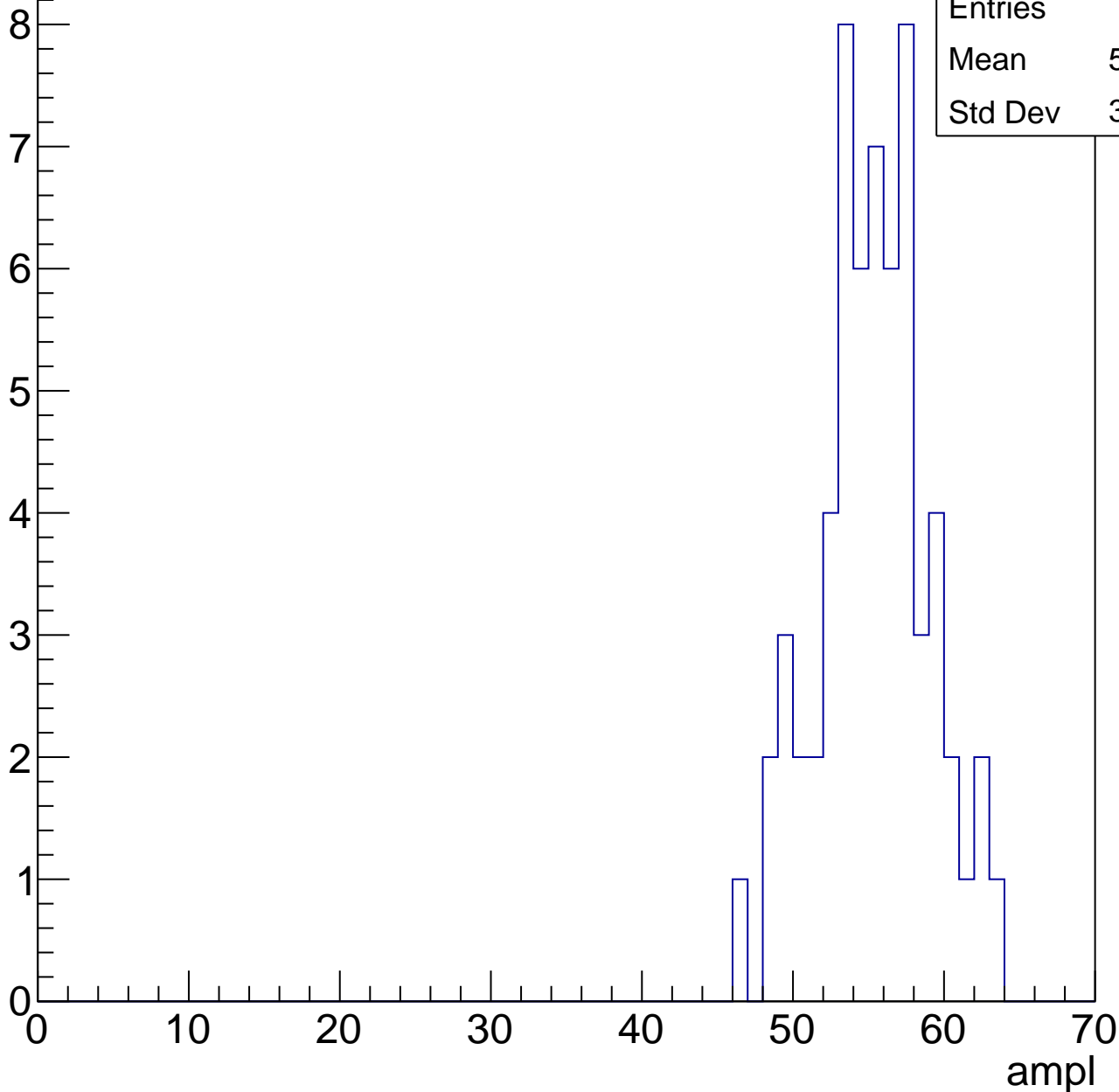


B1L102S, U11-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.87
Std Dev	3.634

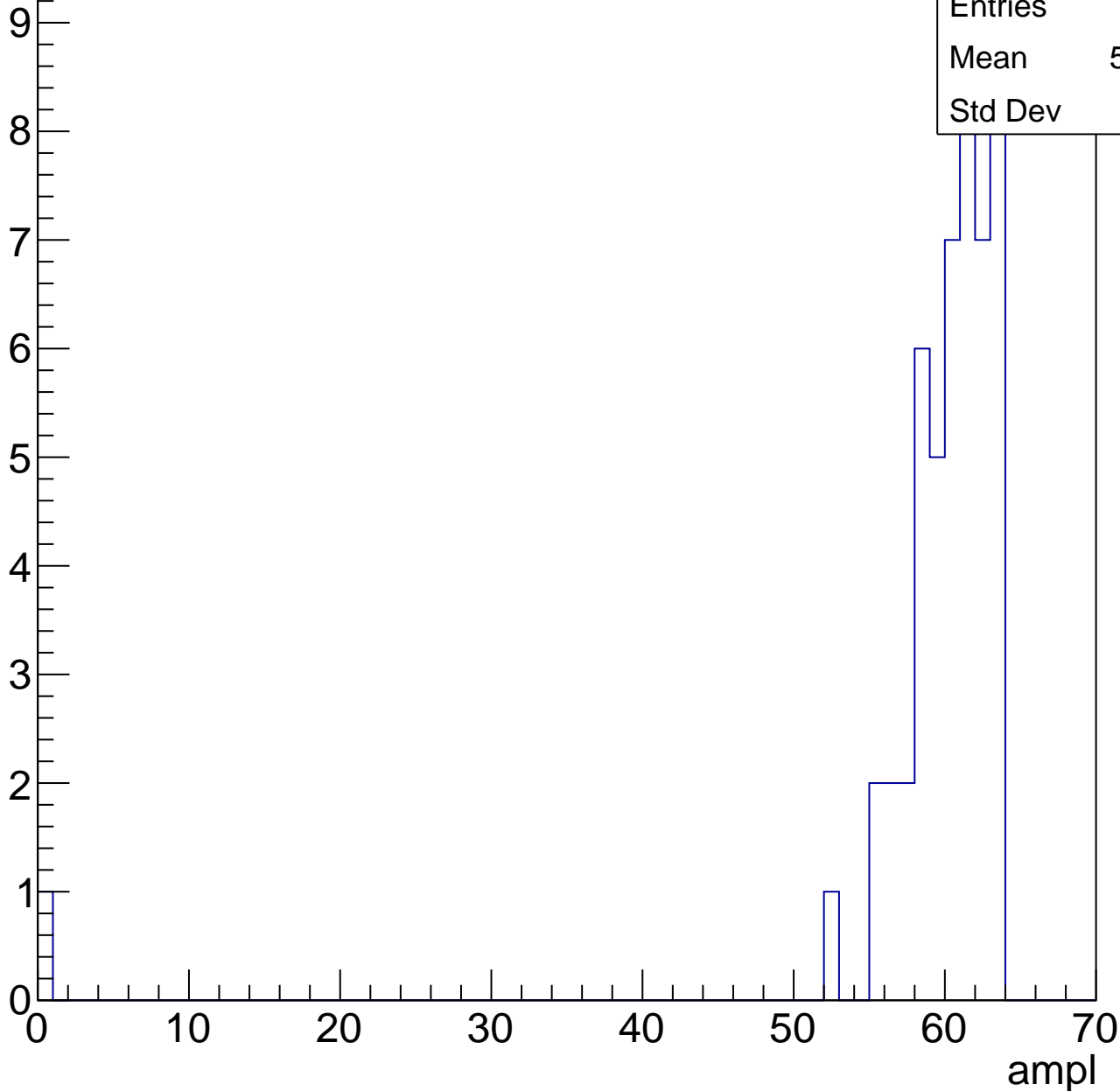


B1L102S, U11-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

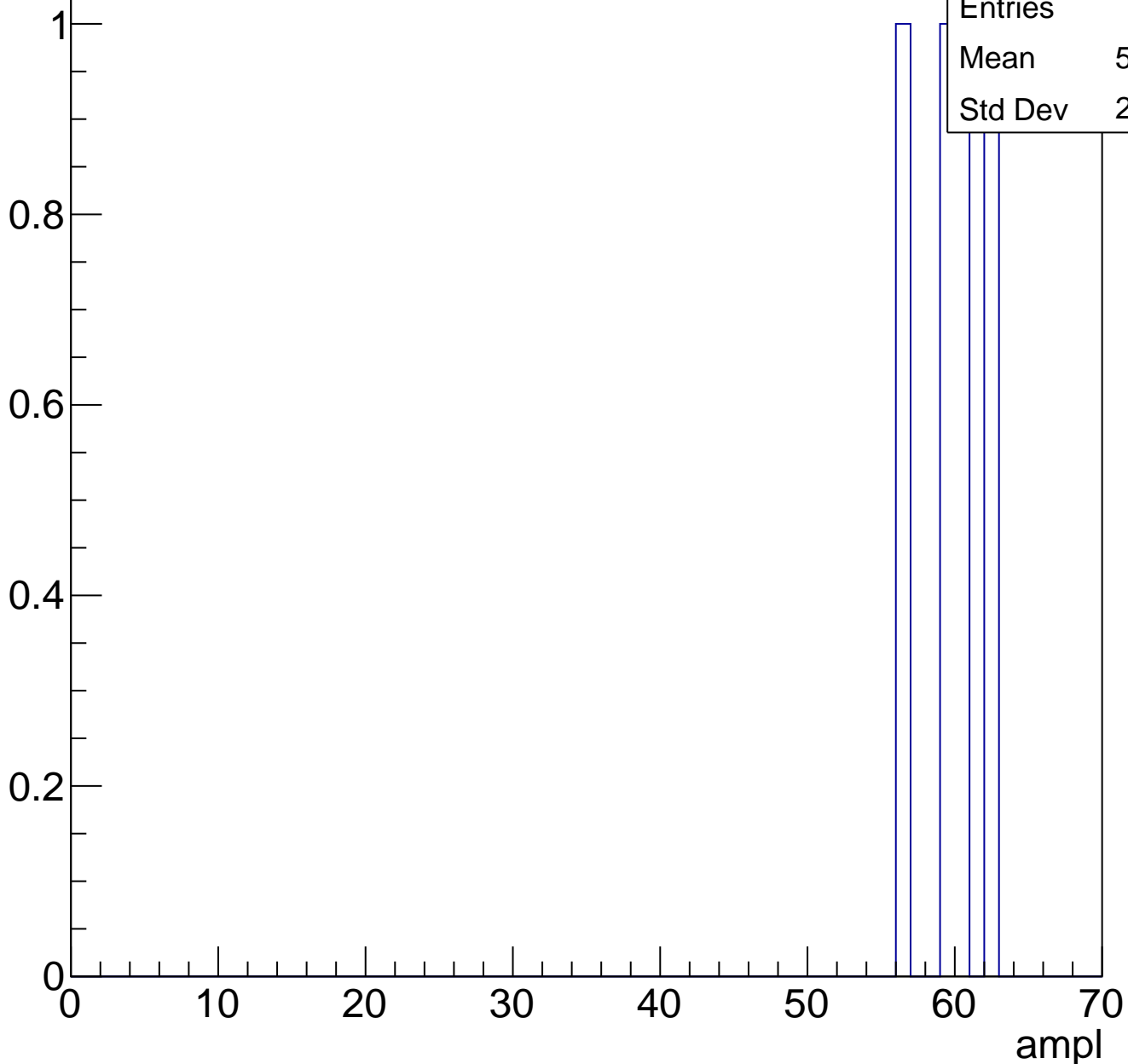
Entries	51
Mean	58.84
Std Dev	8.68



B1L102S, U11-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

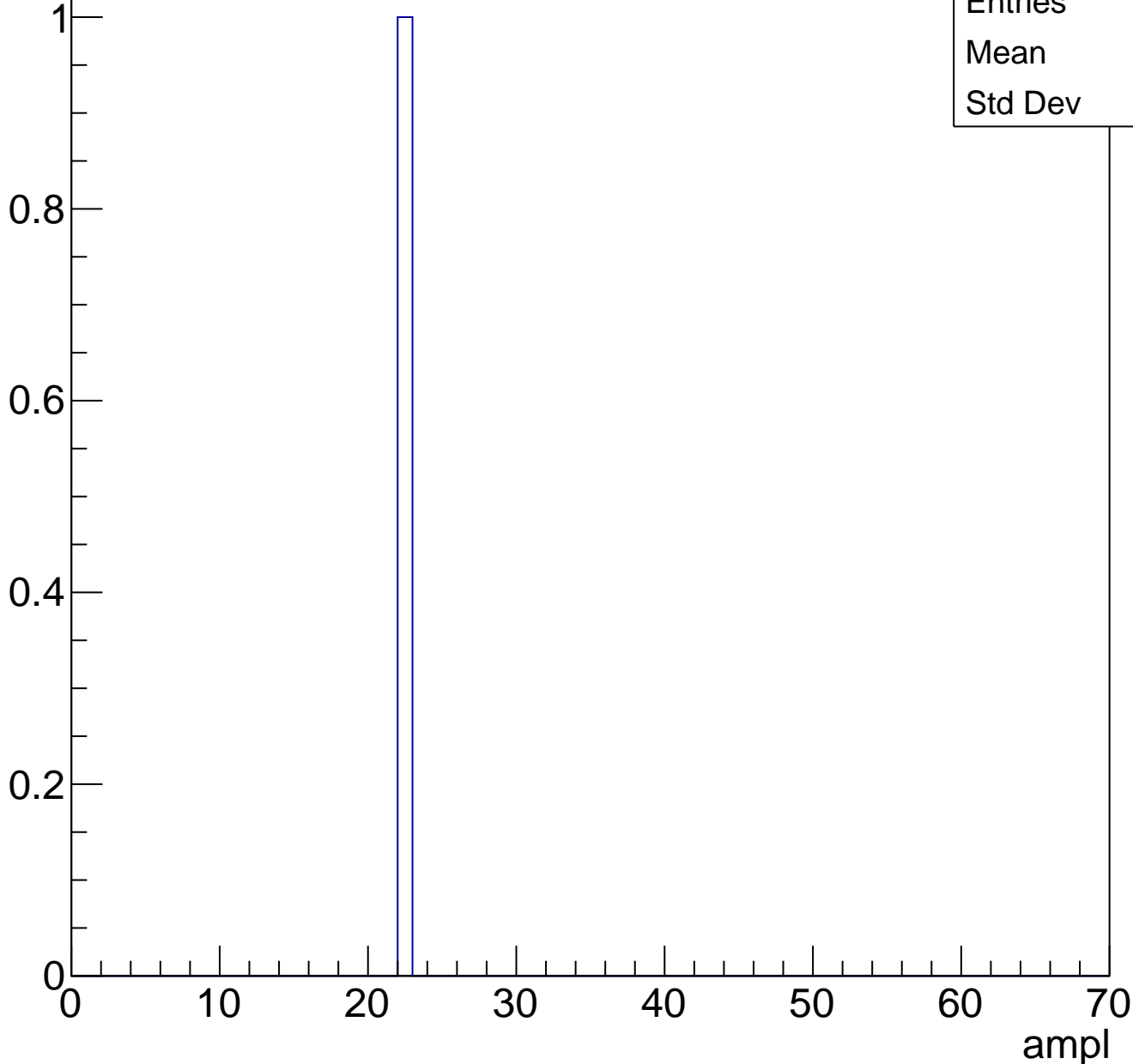
Entry



B1L102S, U11-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L102S, U11-ch85, adc0

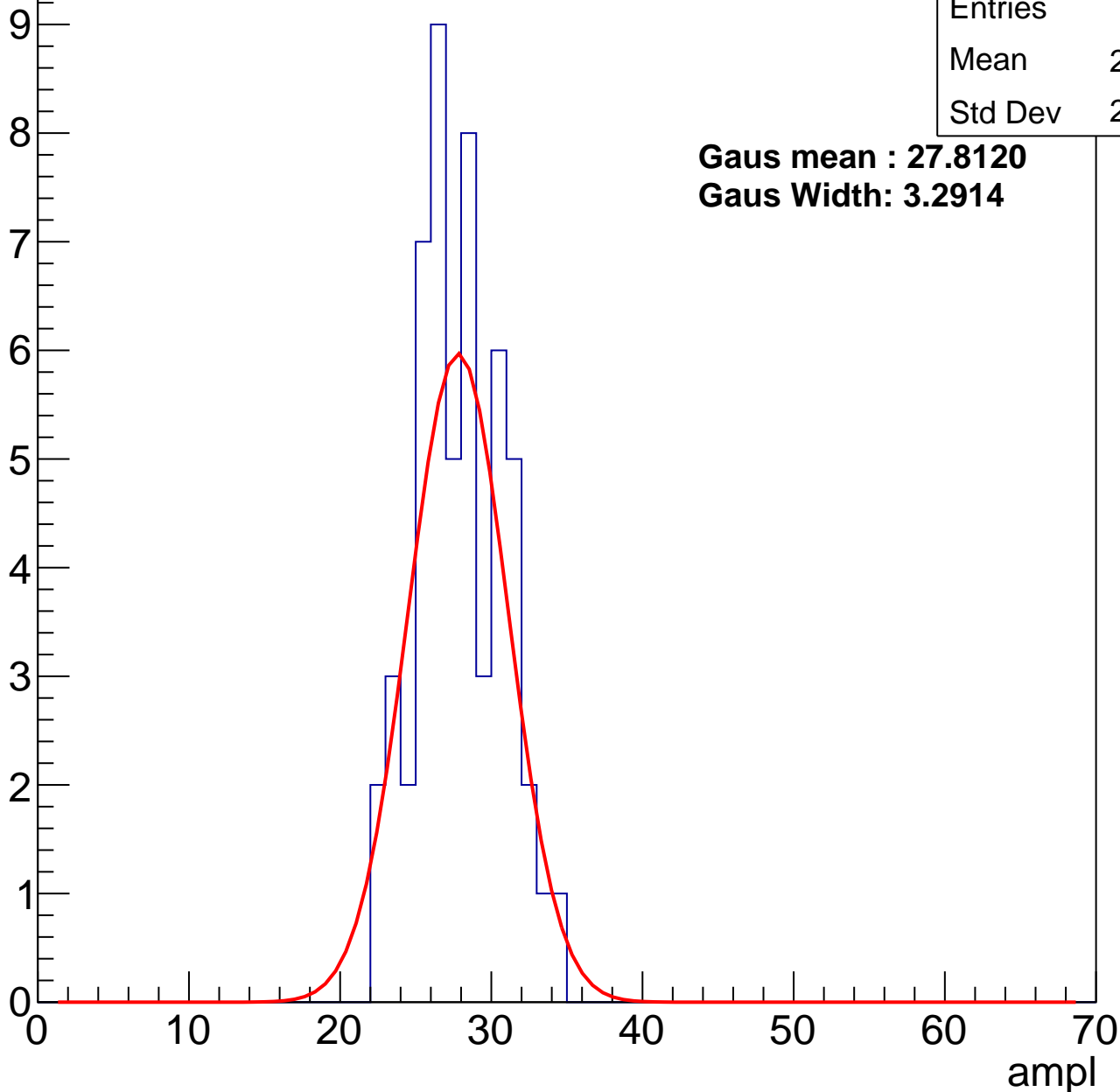
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	27.44
Std Dev	2.826

Gaus mean : 27.8120

Gaus Width: 3.2914



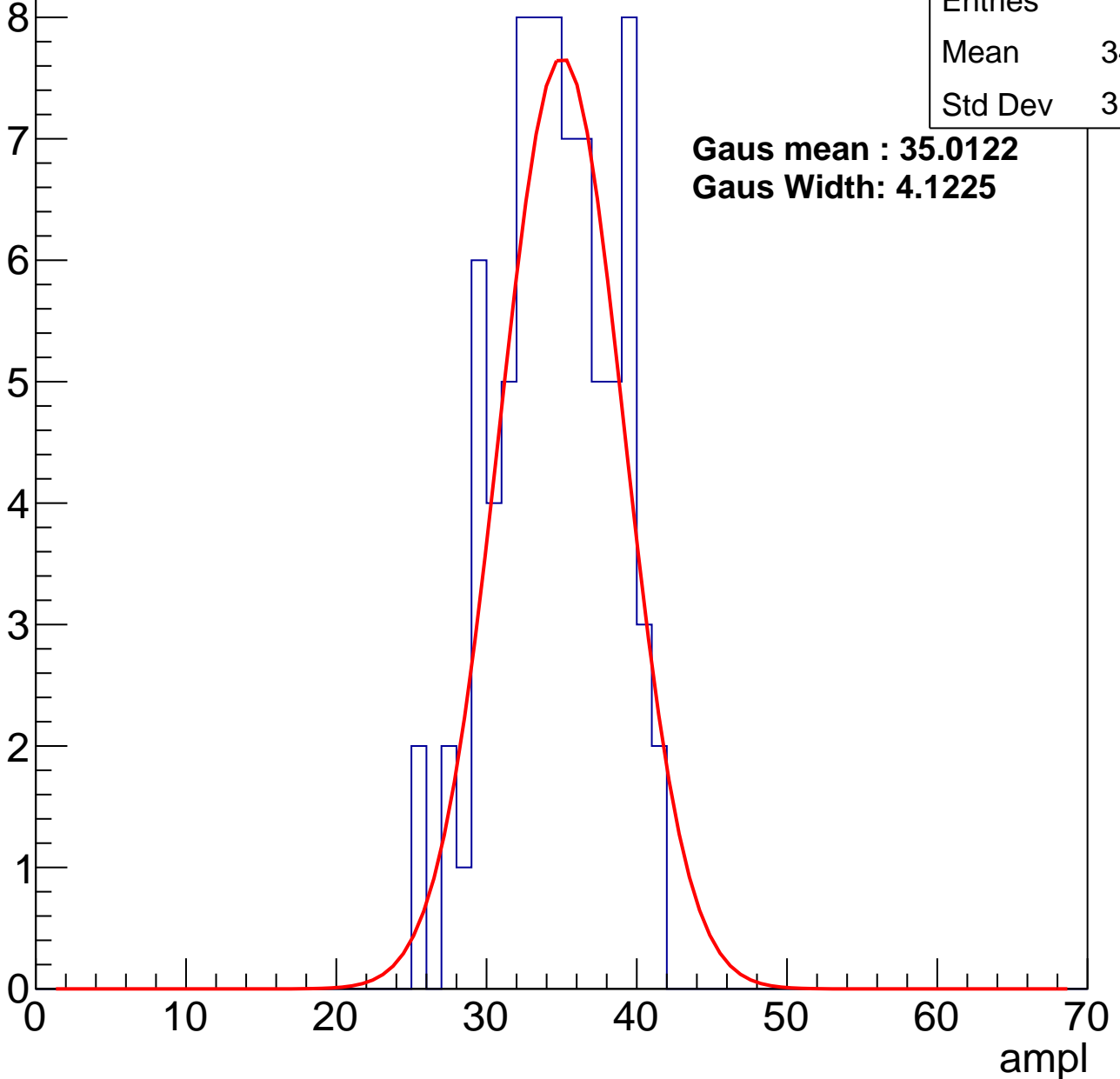
B1L102S, U11-ch85, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	34.06
Std Dev	3.785

Gaus mean : 35.0122
Gaus Width: 4.1225



B1L102S, U11-ch85, adc2

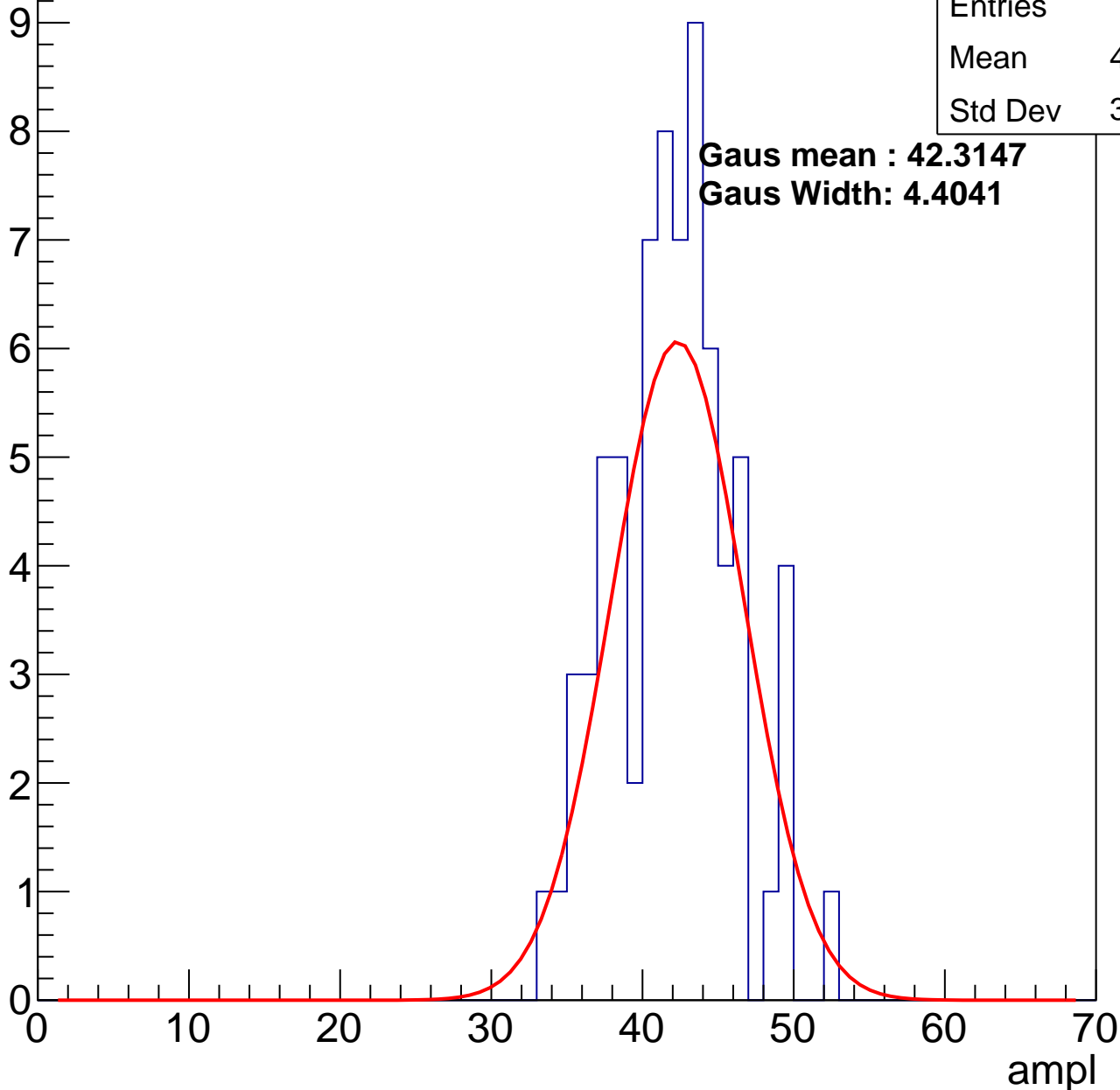
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.56
Std Dev	3.954

Gaus mean : 42.3147

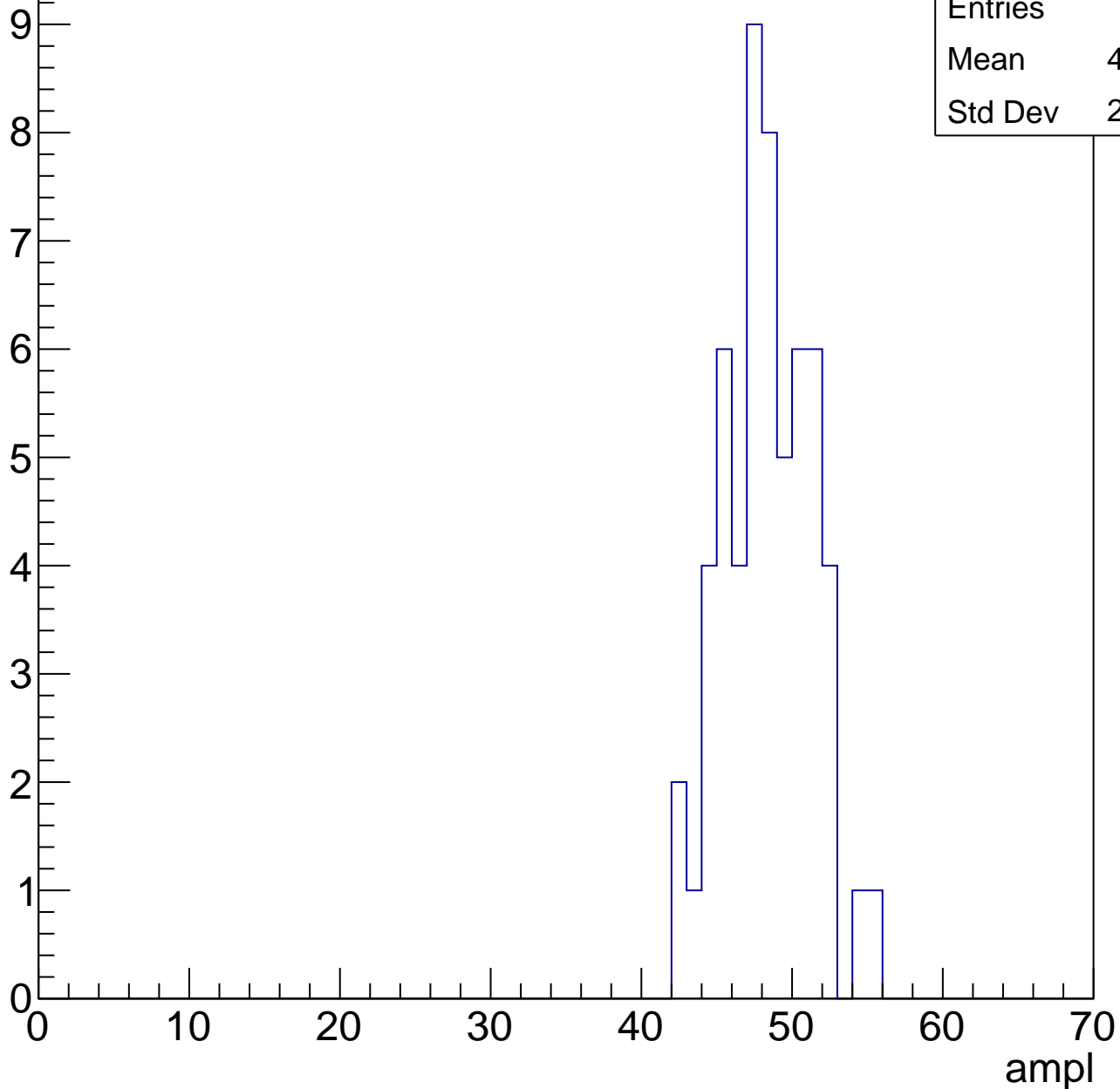
Gaus Width: 4.4041



B1L102S, U11-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

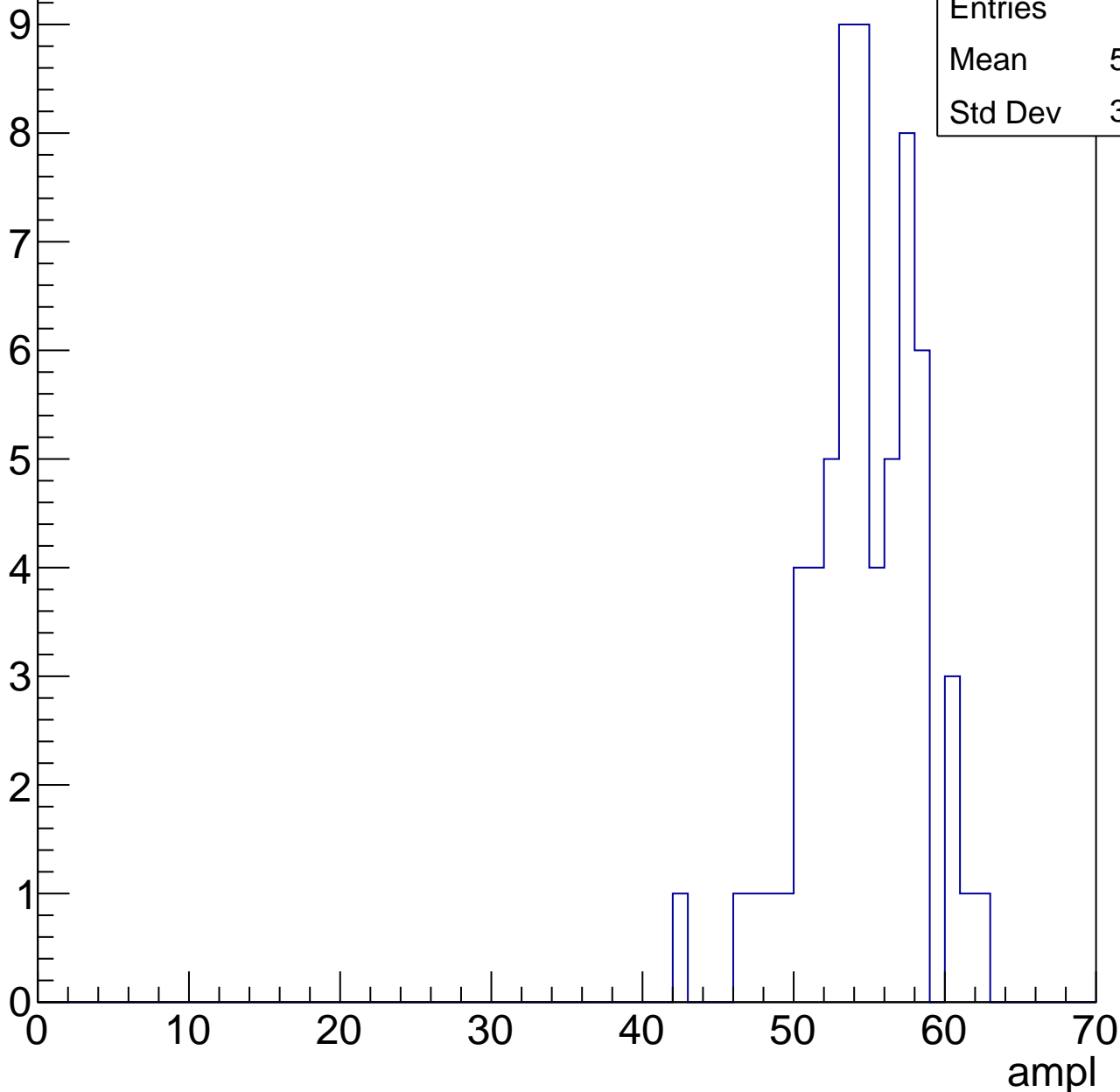


B1L102S, U11-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.16
Std Dev	3.662

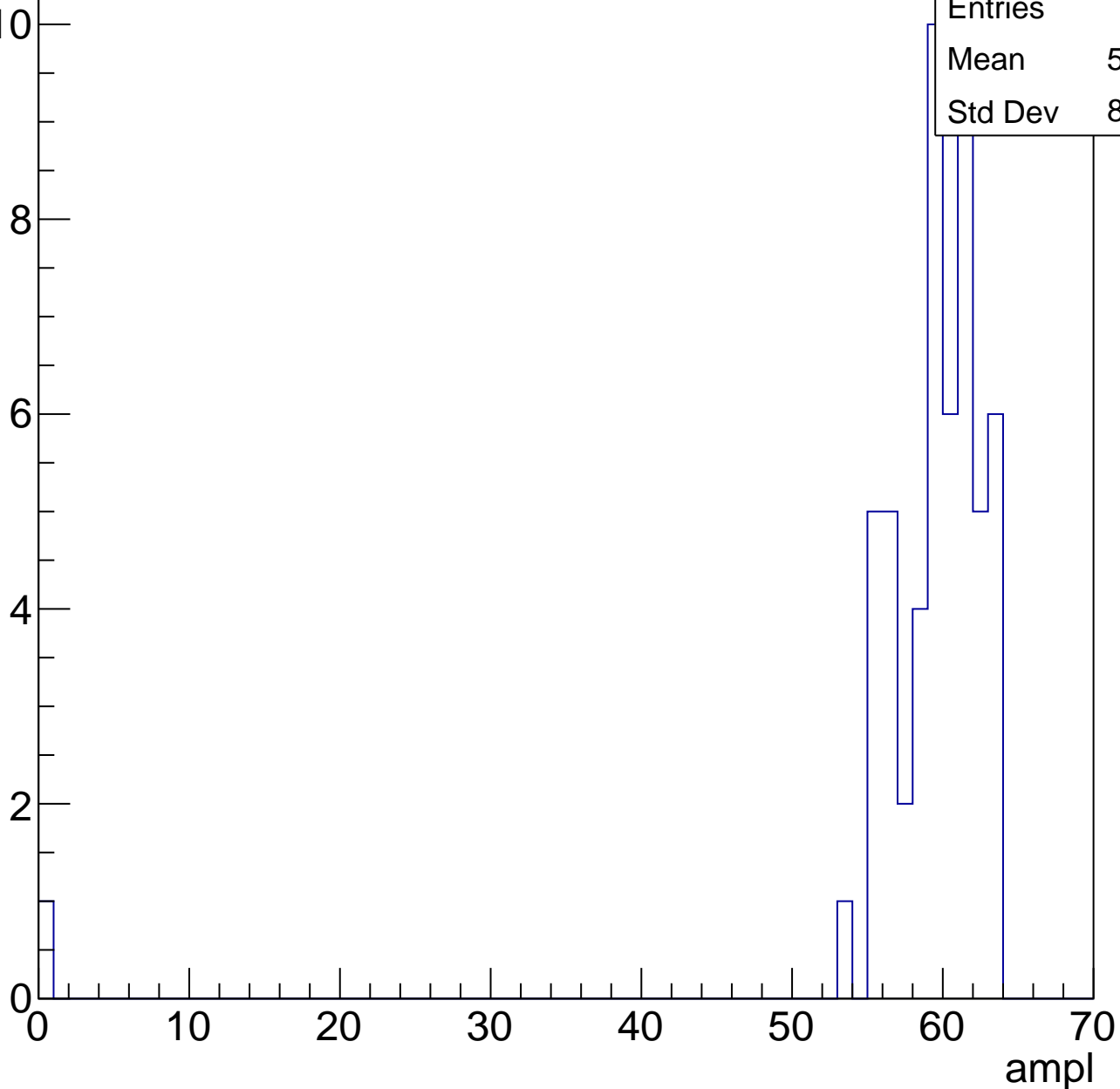


B1L102S, U11-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

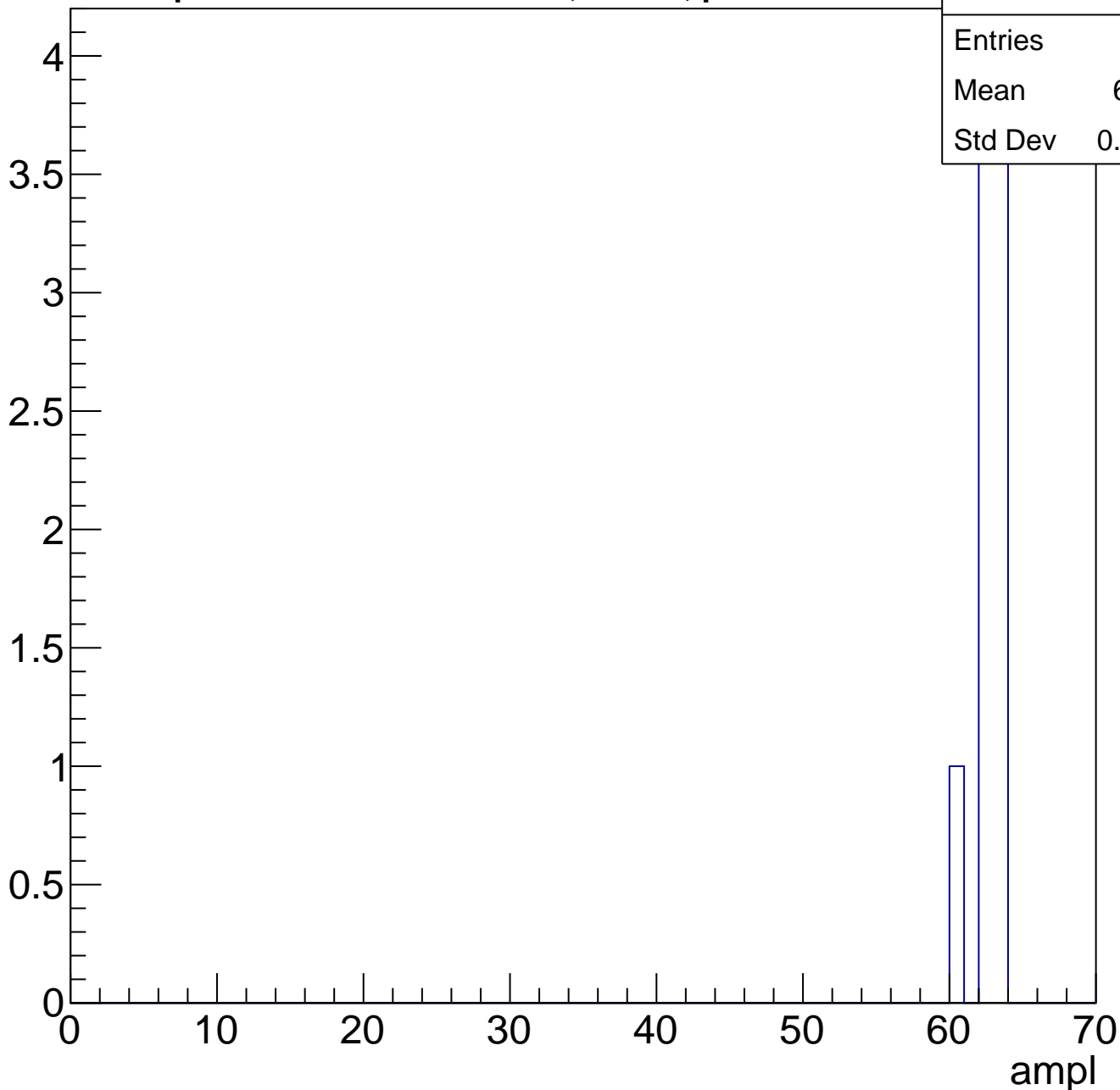
Entries	54
Mean	58.17
Std Dev	8.388



B1L102S, U11-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch86, adc0

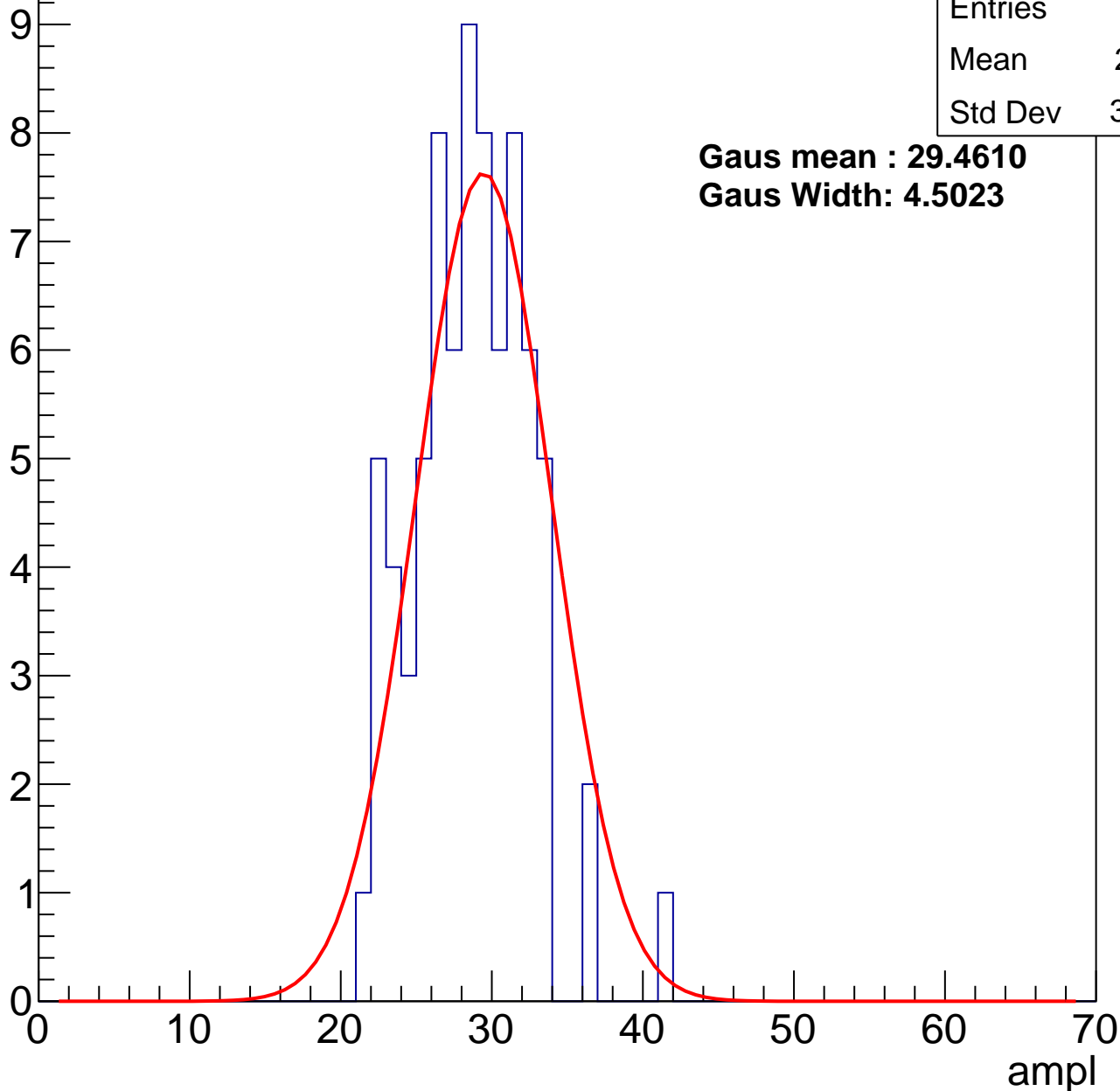
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.21
Std Dev	3.743

Gaus mean : 29.4610

Gaus Width: 4.5023



B1L102S, U11-ch86, adc1

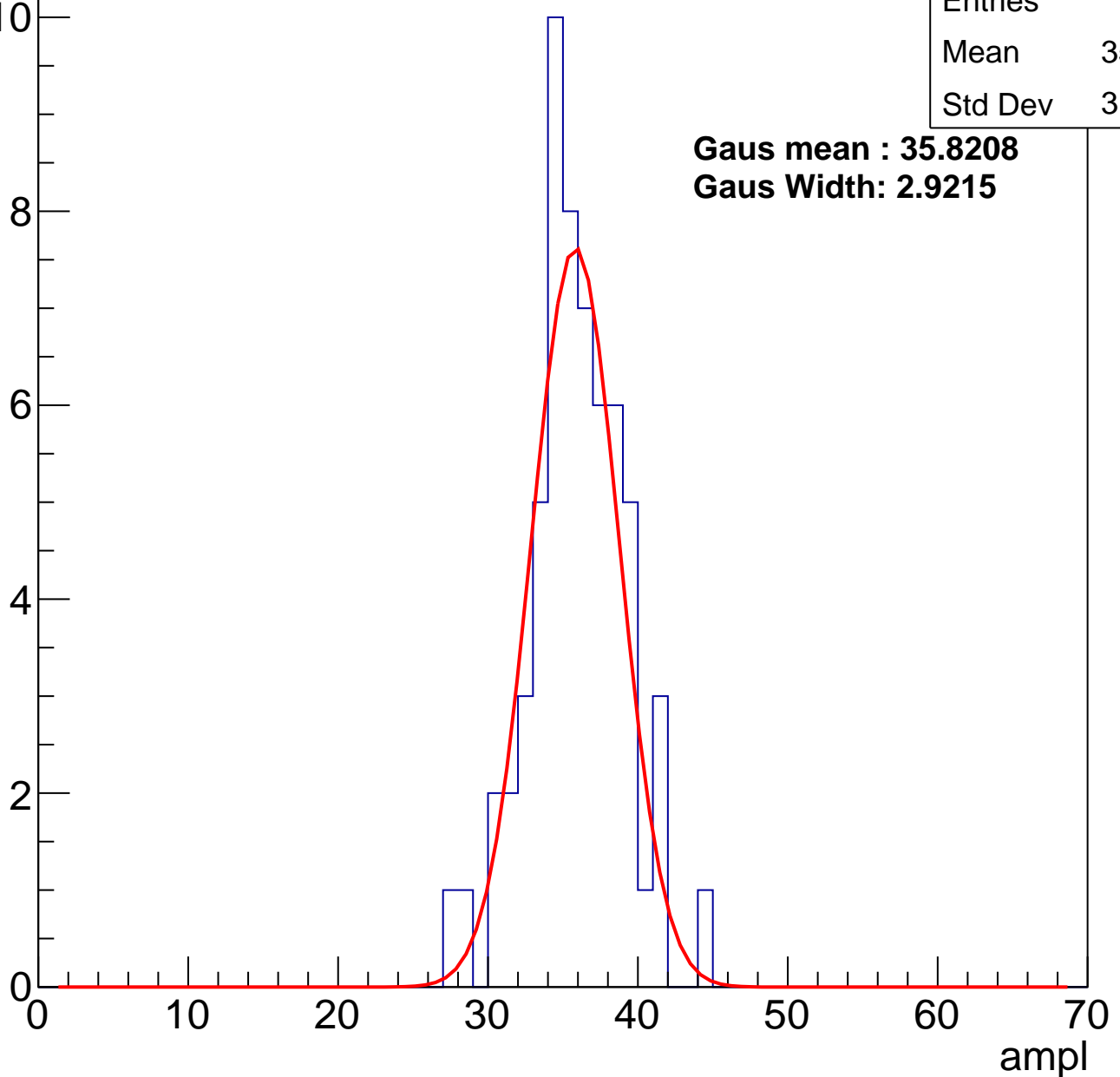
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.44
Std Dev	3.186

Gaus mean : 35.8208

Gaus Width: 2.9215



B1L102S, U11-ch86, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.77
Std Dev	3.498

Gaus mean : 42.3822

Gaus Width: 3.4404

10

8

6

4

2

0

0

10

20

30

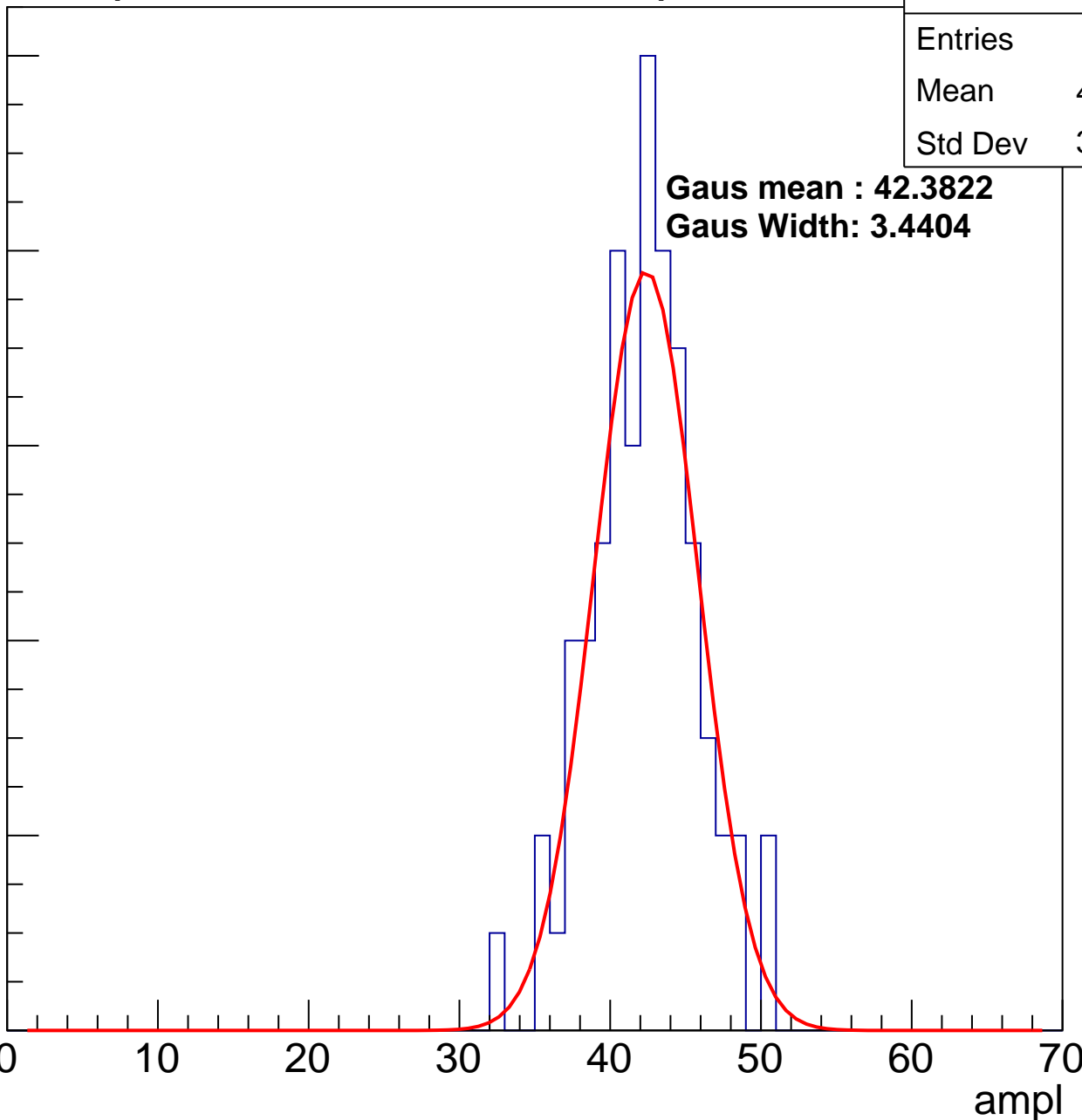
40

50

60

70

ampl

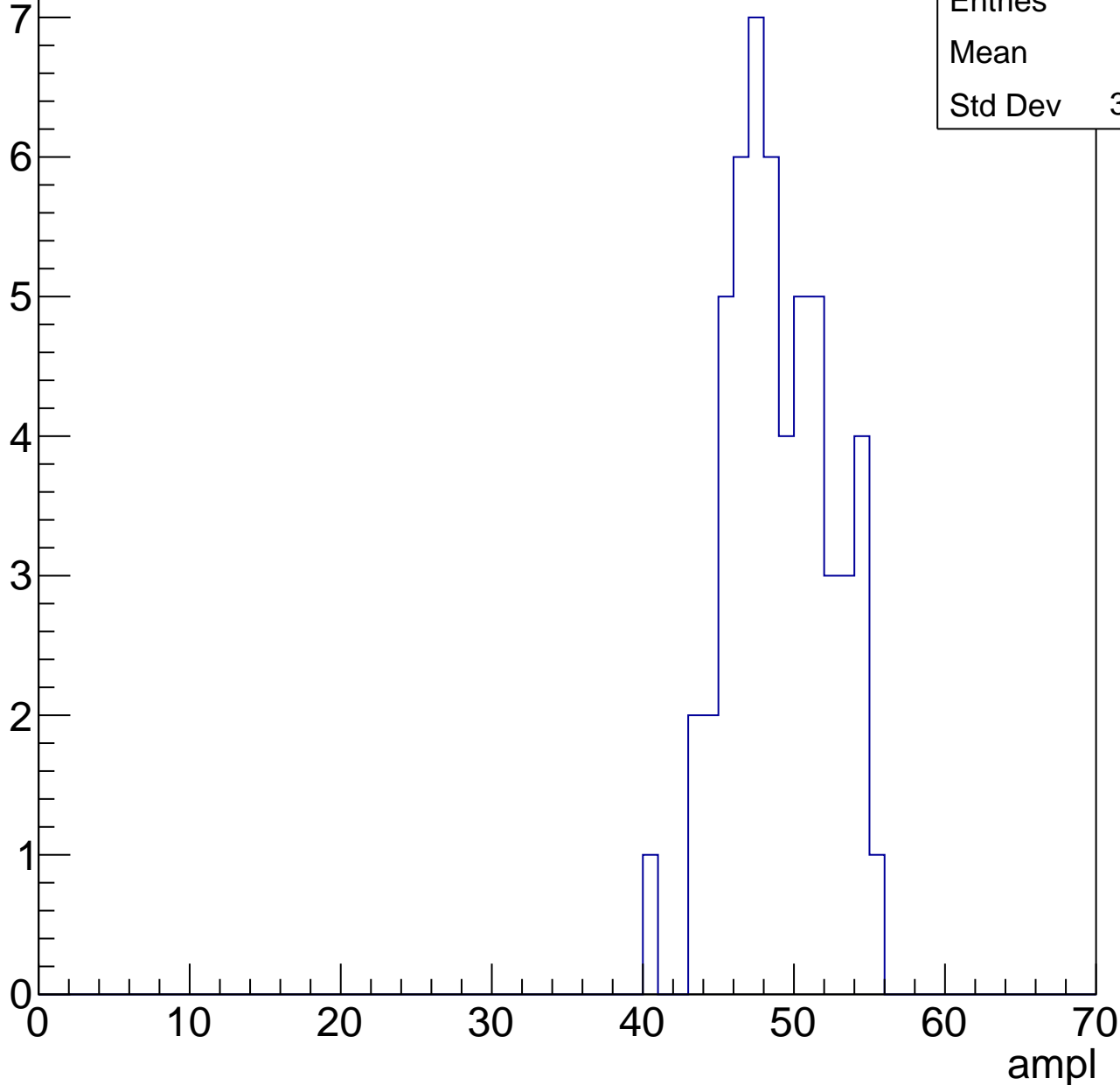


B1L102S, U11-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.5
Std Dev	3.315

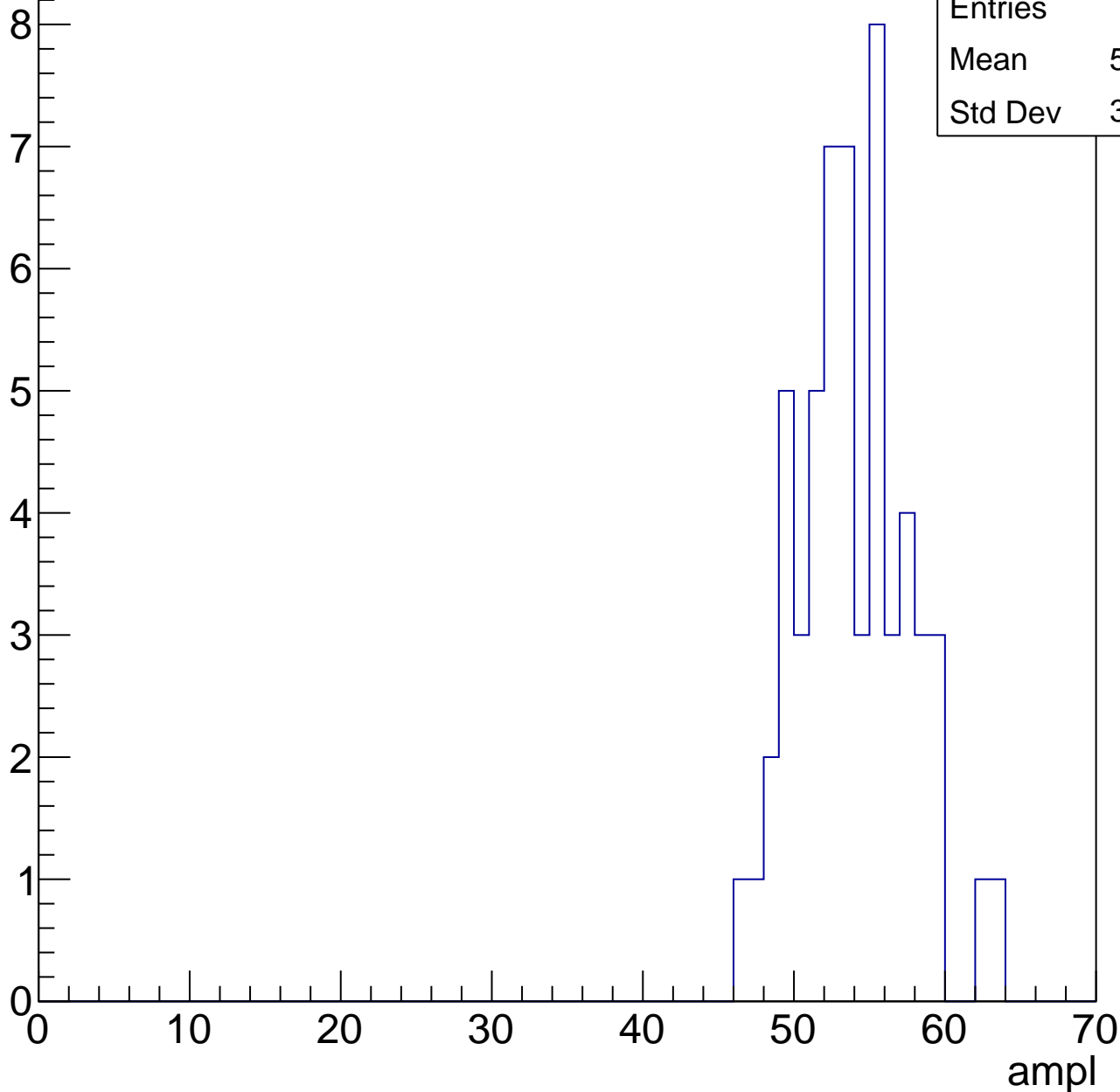


B1L102S, U11-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.47
Std Dev	3.618

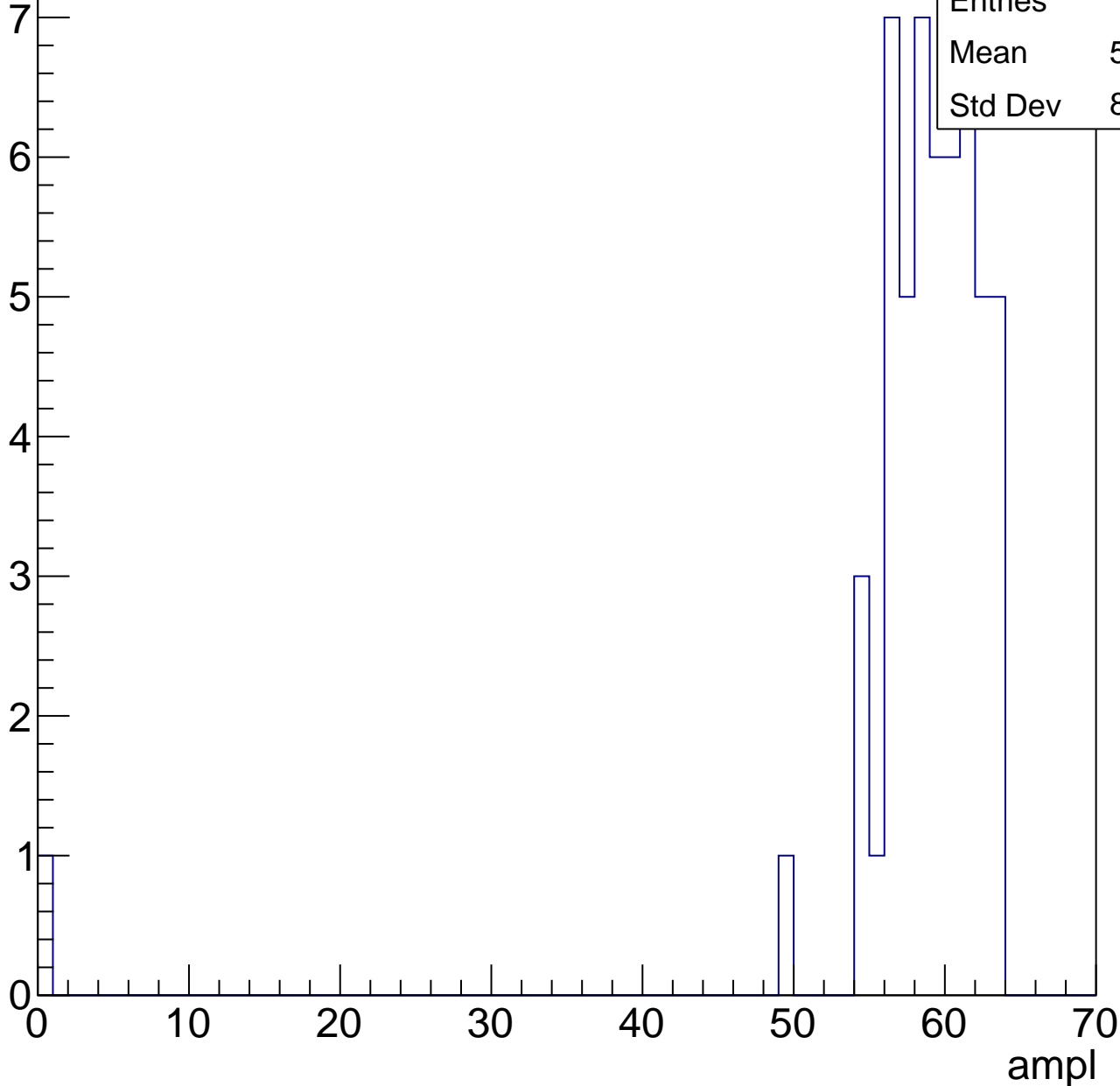


B1L102S, U11-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

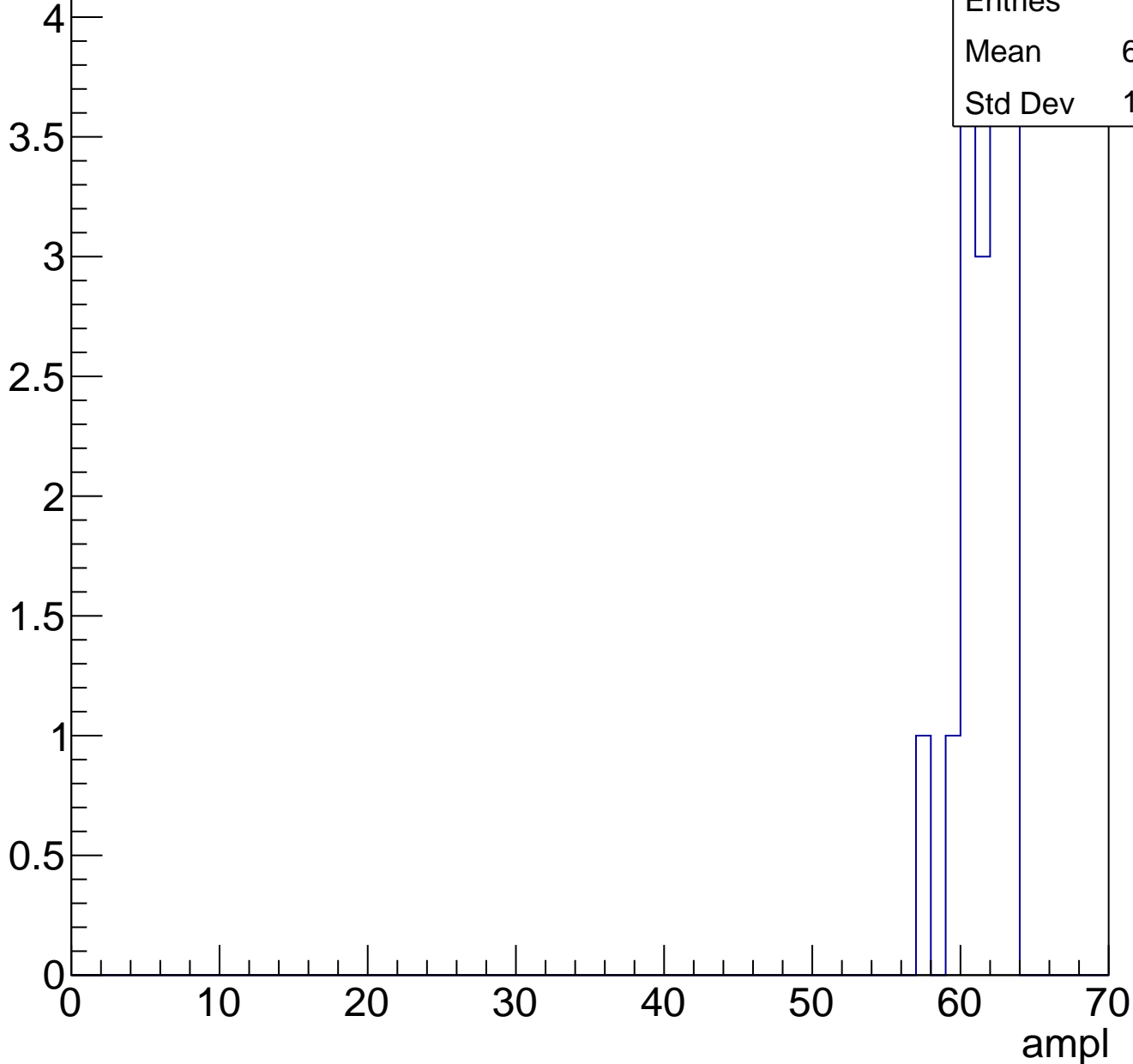
Entries	54
Mean	57.69
Std Dev	8.419



B1L102S, U11-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch87, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.9
Std Dev	6.325

Gaus mean : 28.6547

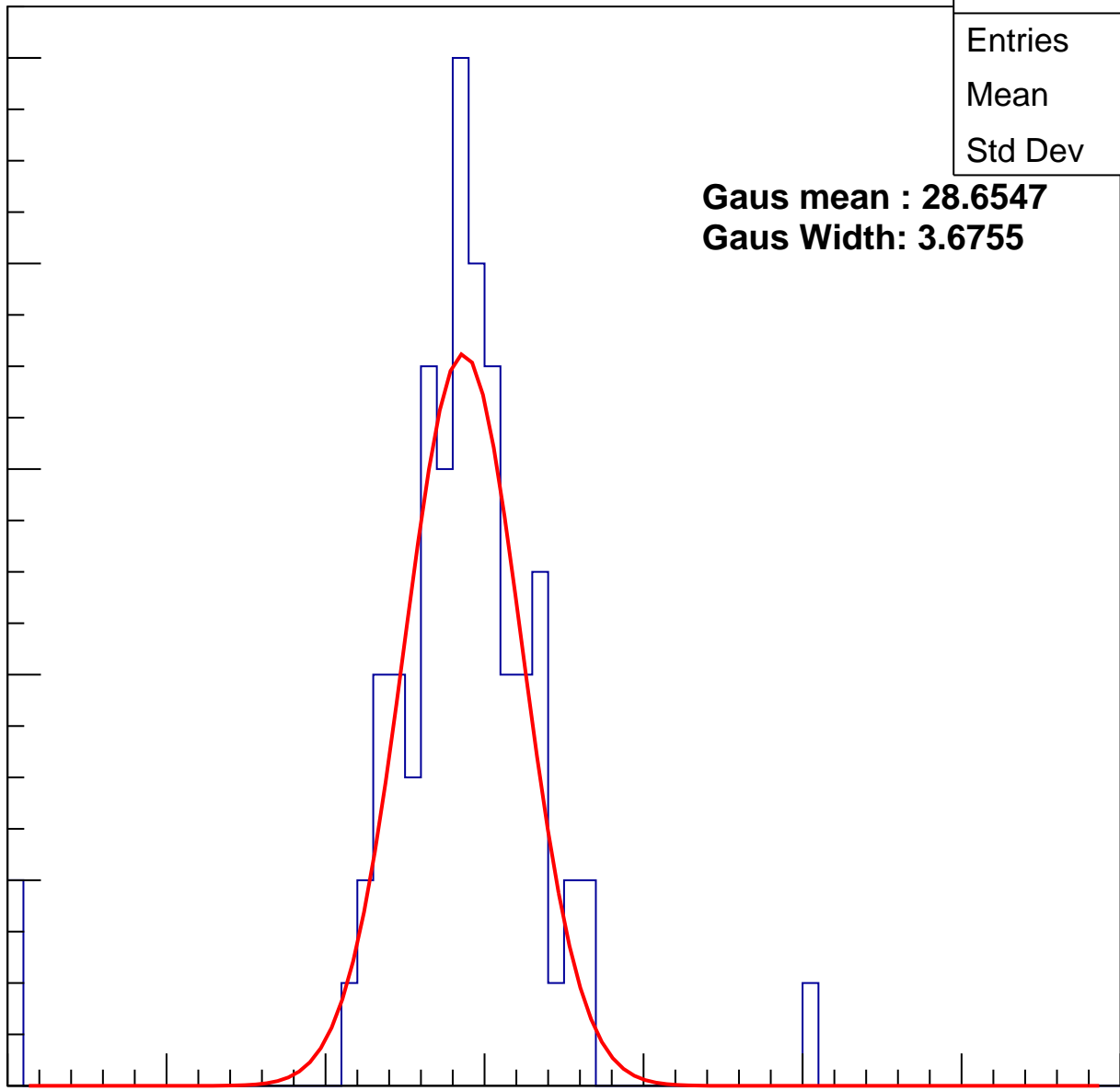
Gaus Width: 3.6755

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch87, adc1

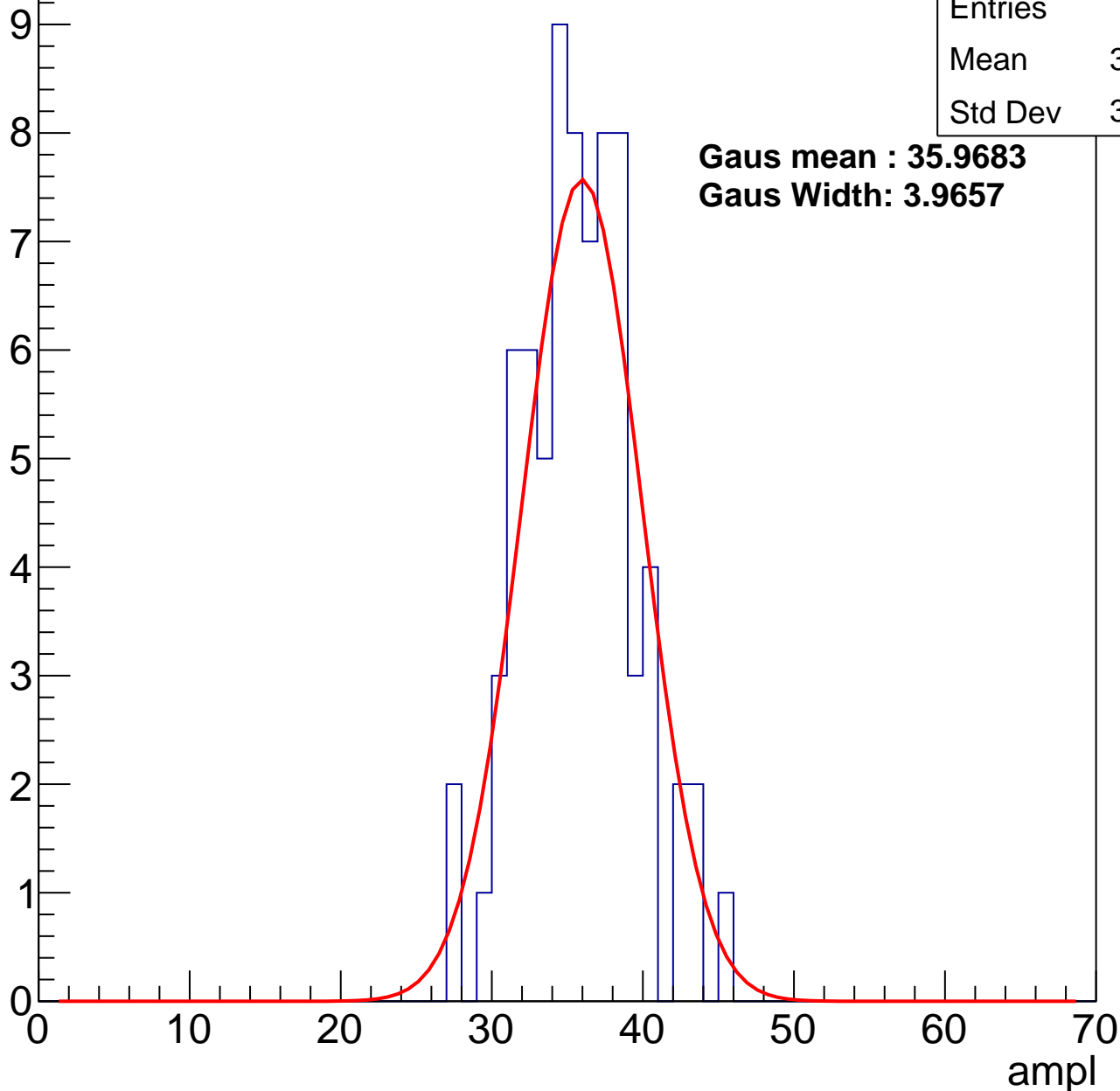
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.28
Std Dev	3.646

Gaus mean : 35.9683

Gaus Width: 3.9657



B1L102S, U11-ch87, adc2

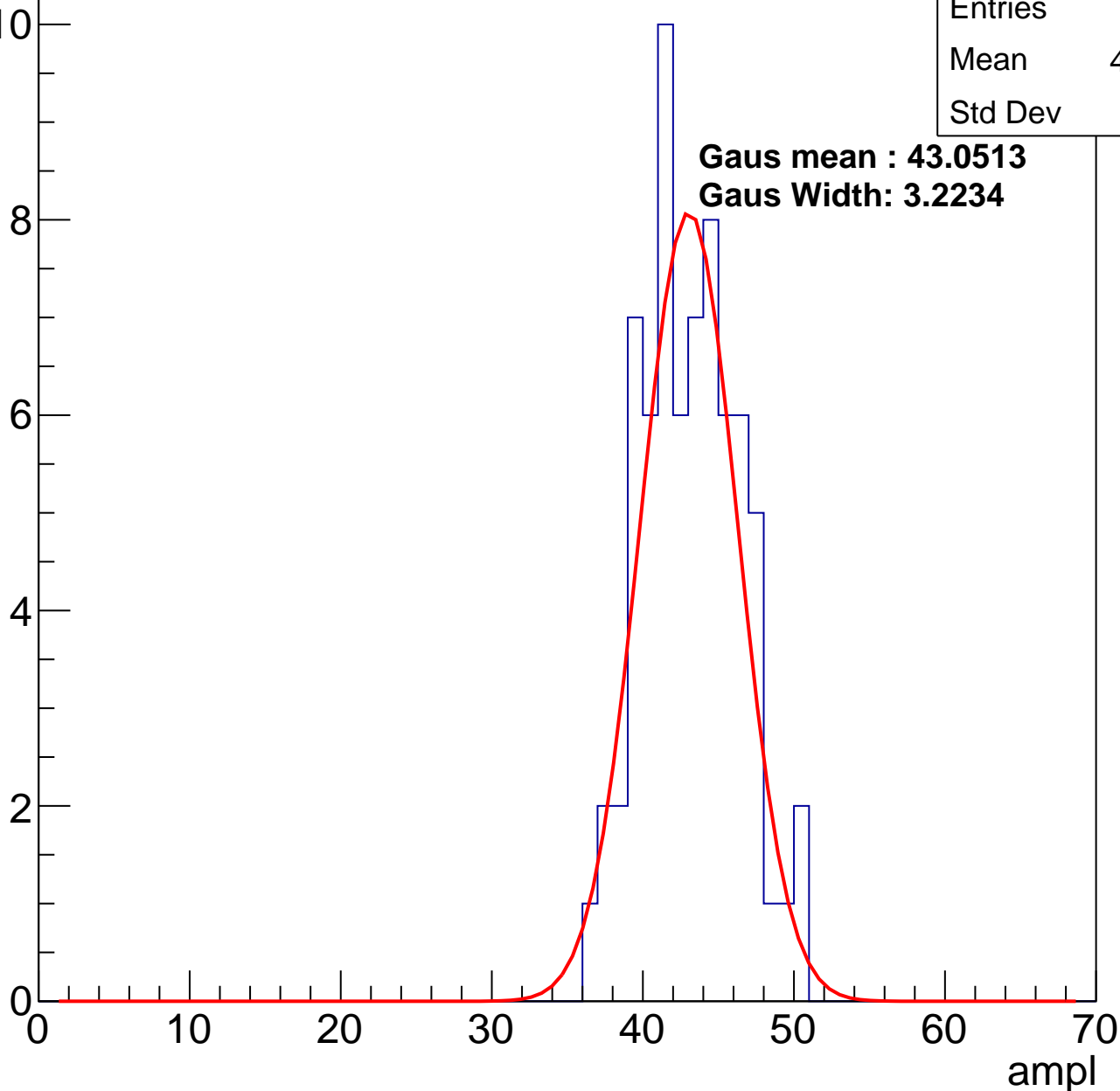
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.74
Std Dev	3.17

Gaus mean : 43.0513

Gaus Width: 3.2234

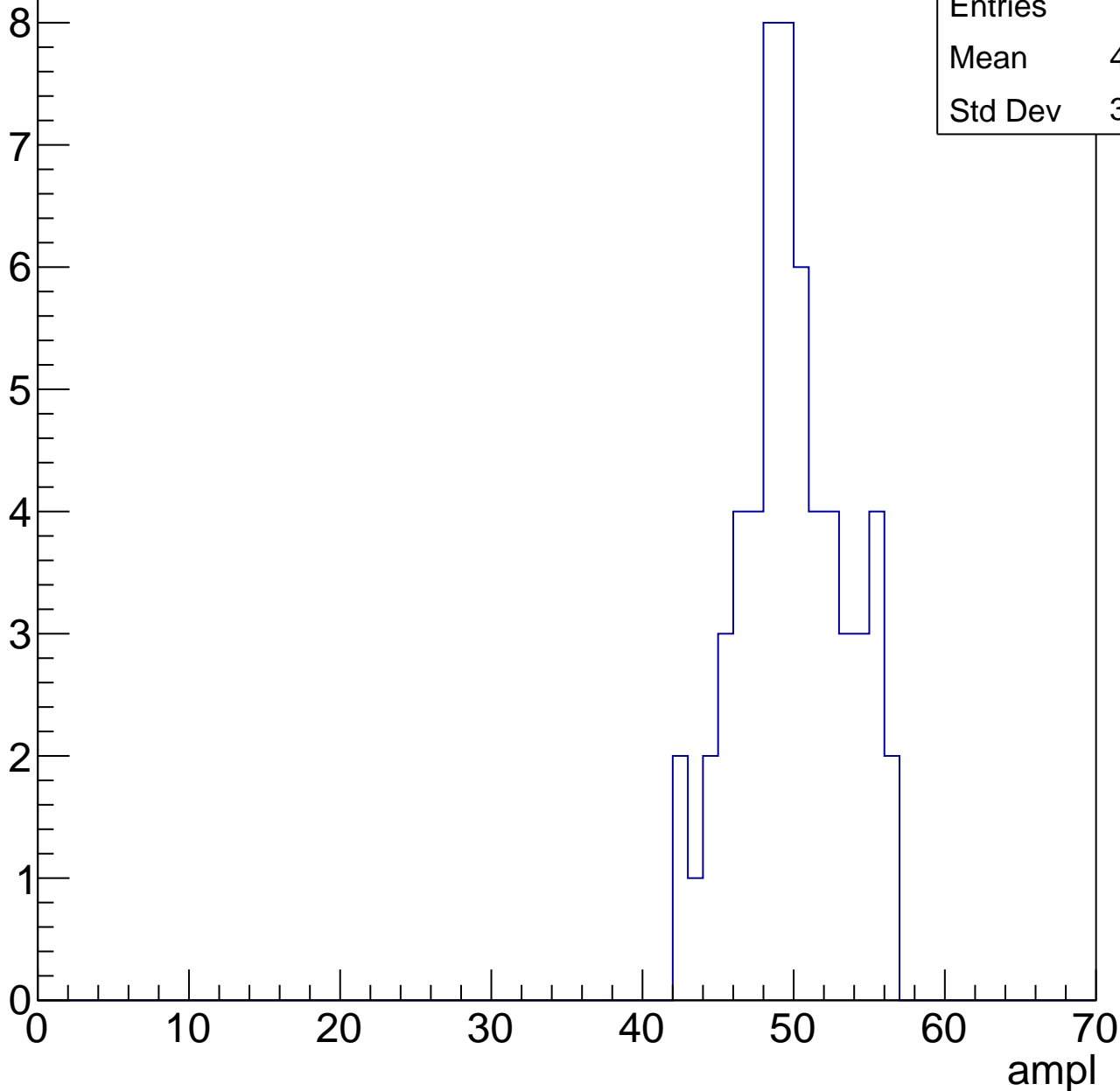


B1L102S, U11-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

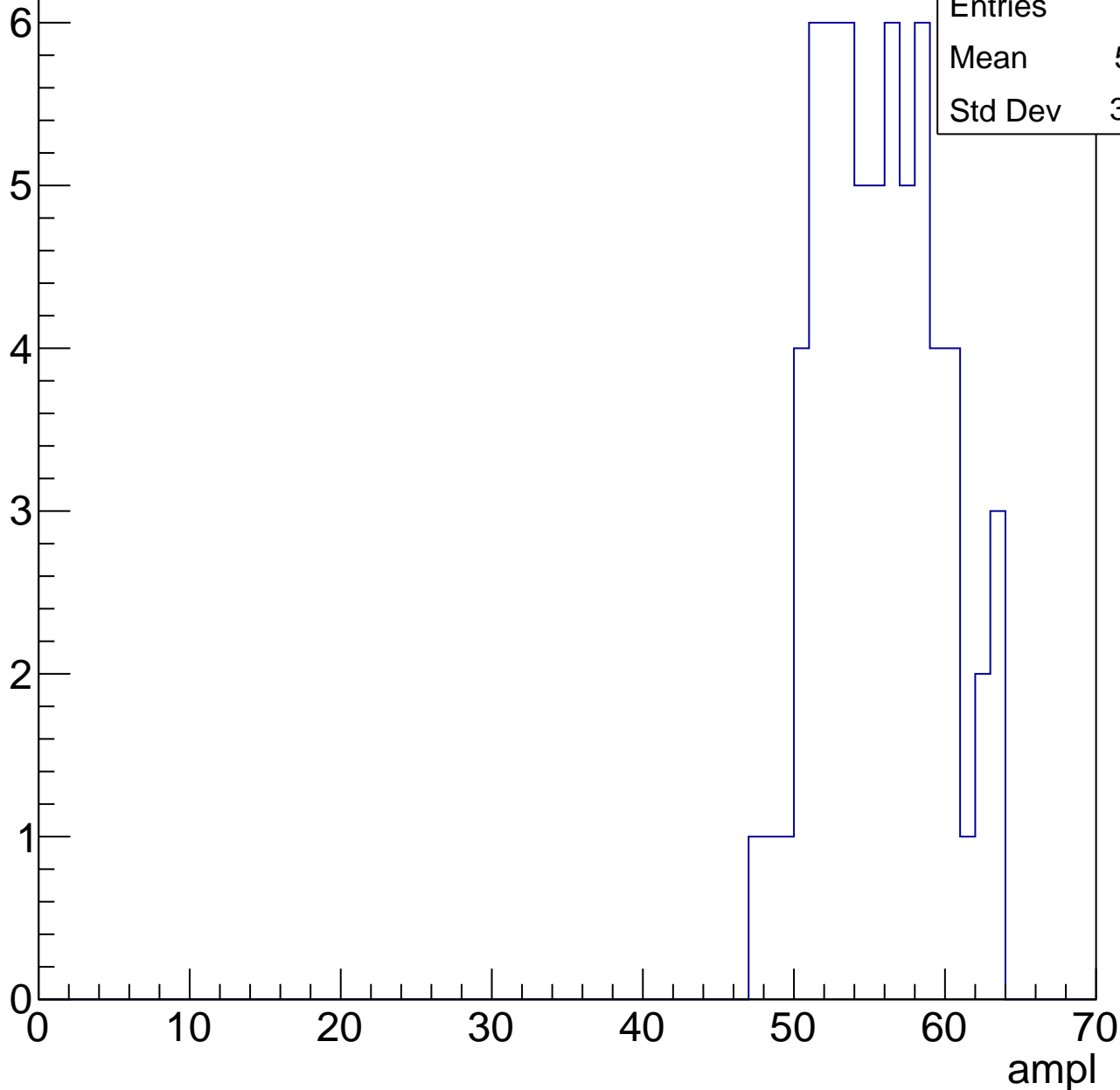
Entries	58
Mean	49.36
Std Dev	3.492



B1L102S, U11-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



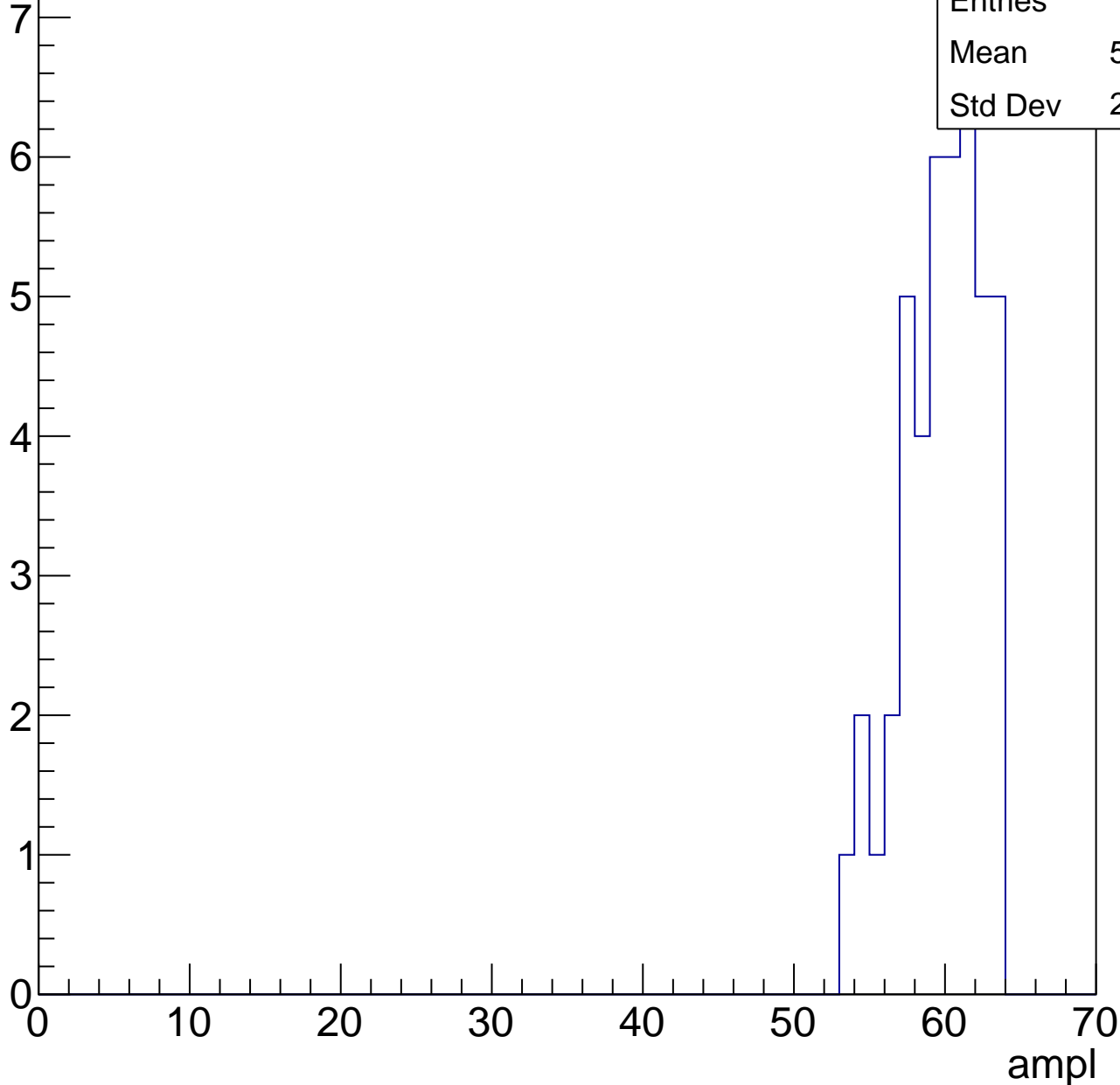
Entries	66
Mean	55.21
Std Dev	3.883

B1L102S, U11-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

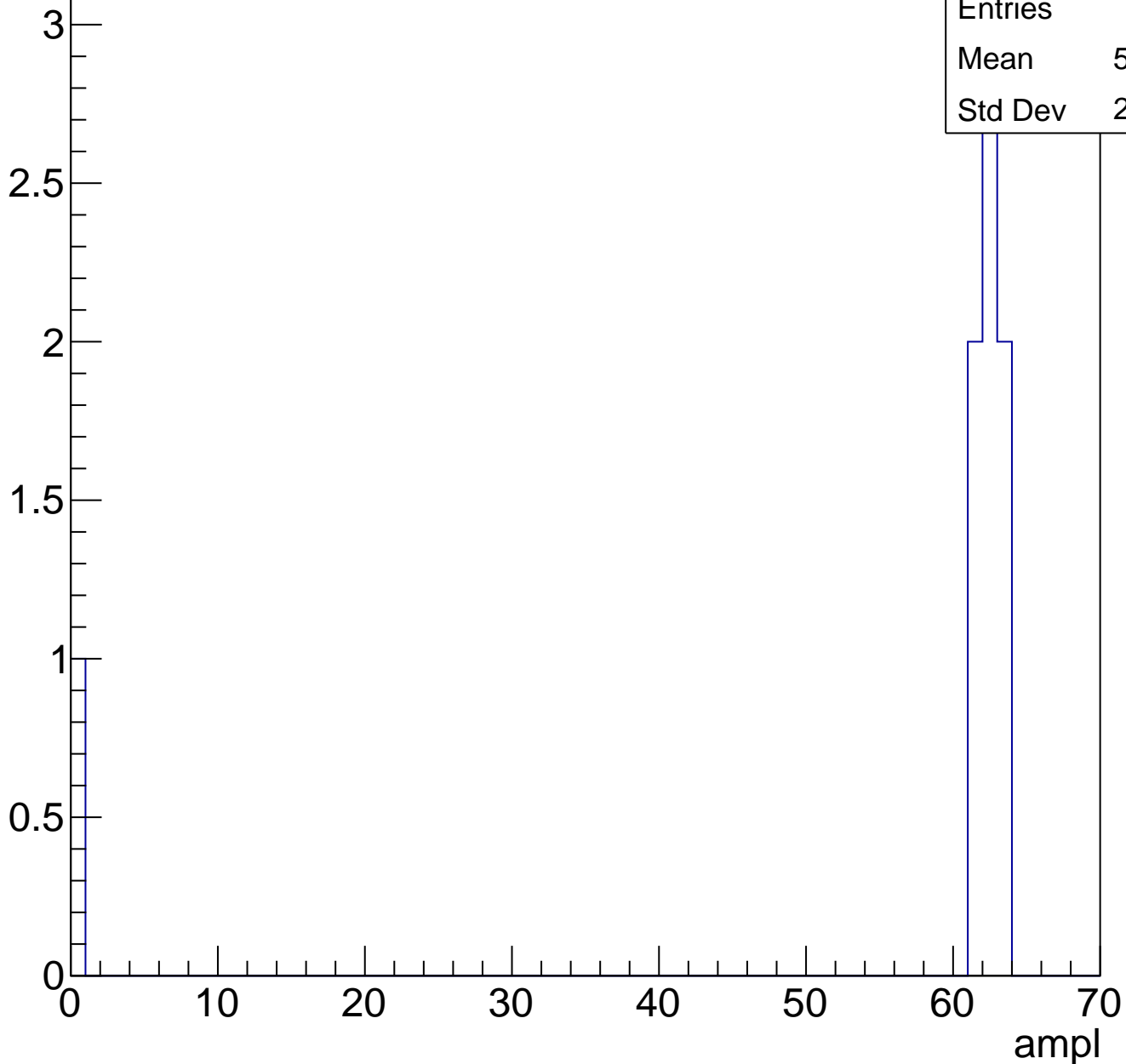
Entries	44
Mean	59.34
Std Dev	2.602



B1L102S, U11-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	8
Mean	54.25
Std Dev	20.52

B1L102S, U11-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch88, adc0

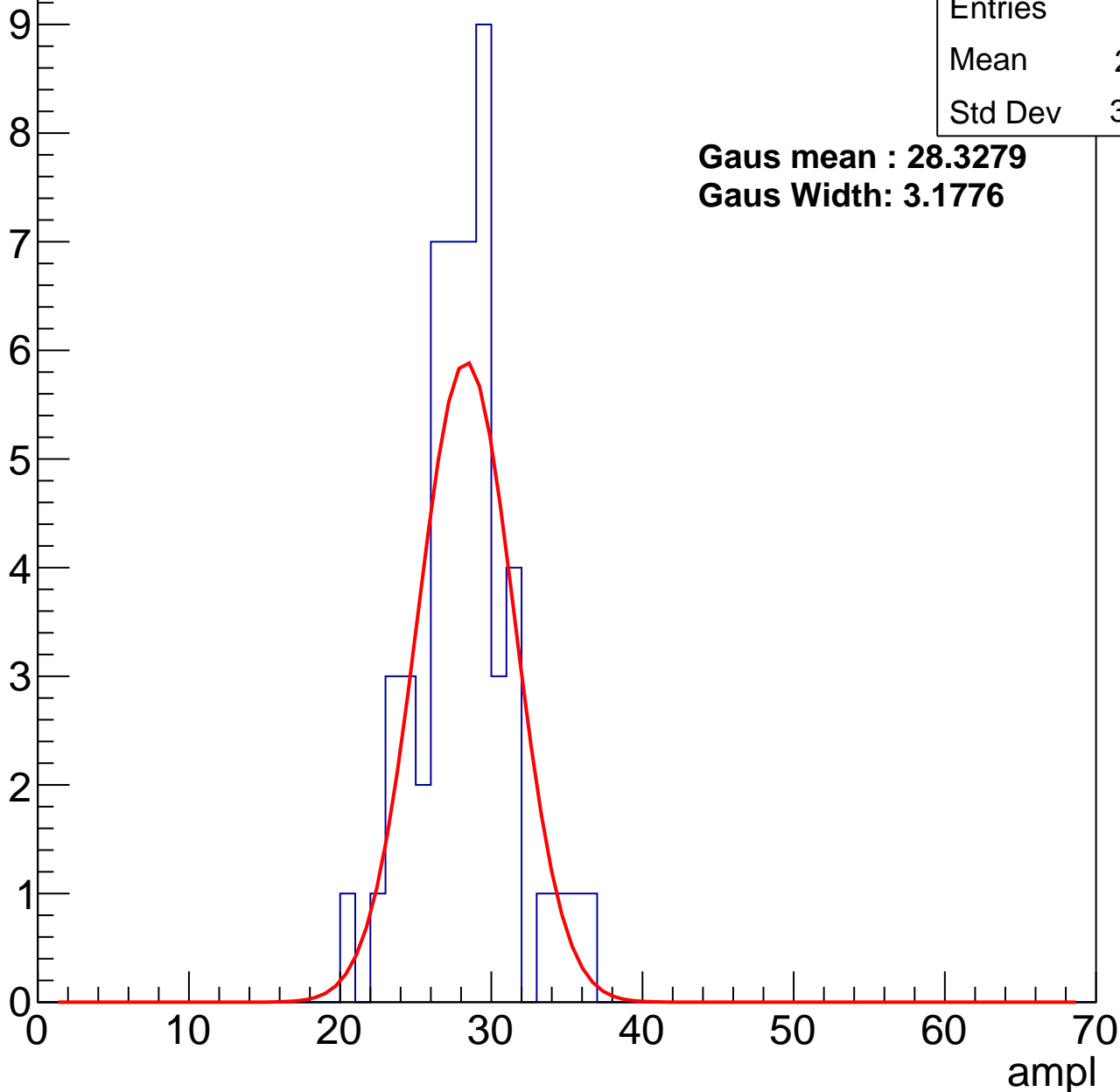
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	27.71
Std Dev	3.139

Gaus mean : 28.3279

Gaus Width: 3.1776



B1L102S, U11-ch88, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	34.36
Std Dev	3.143

Gaus mean : 34.8800

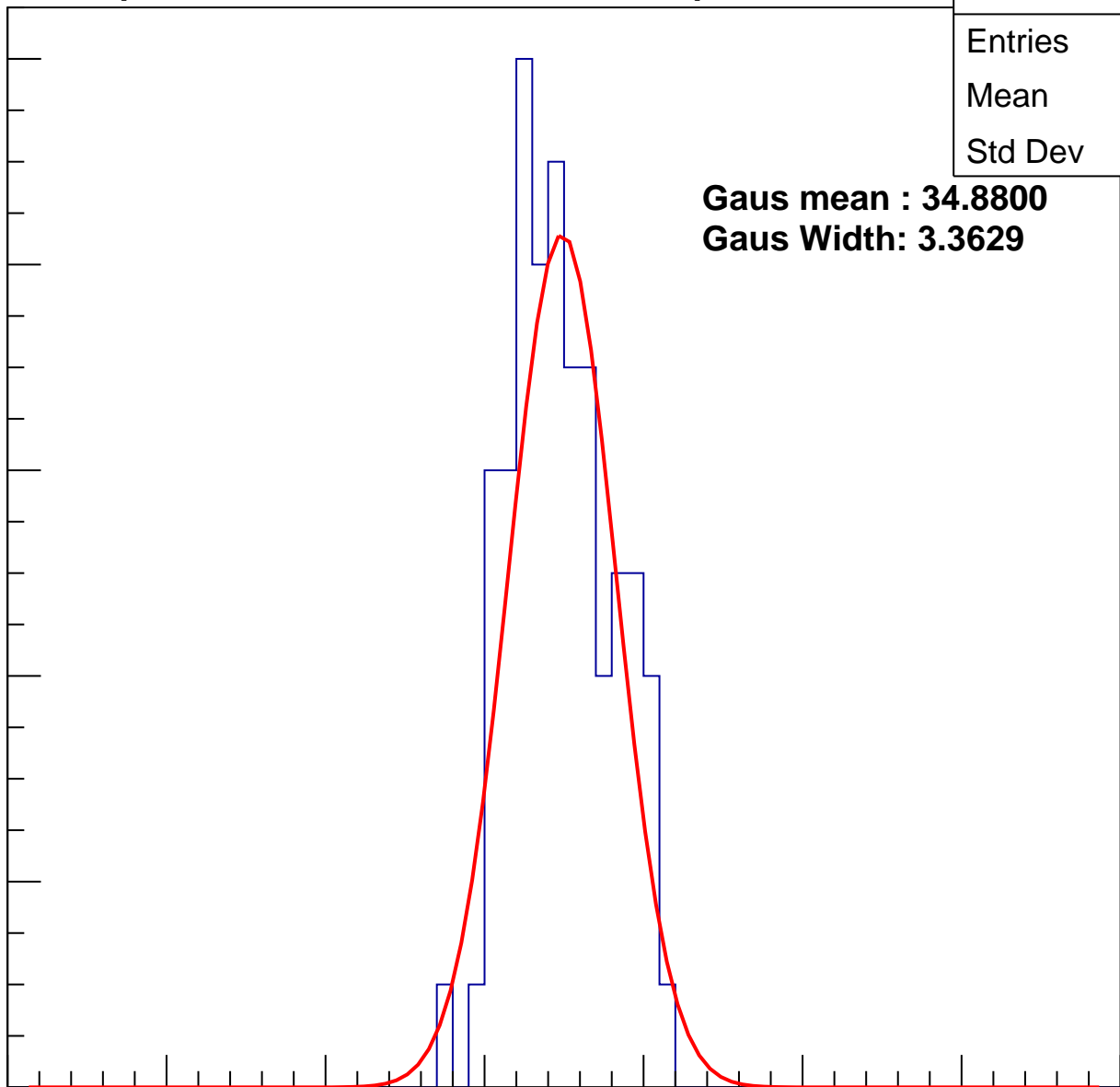
Gaus Width: 3.3629

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch88, adc2

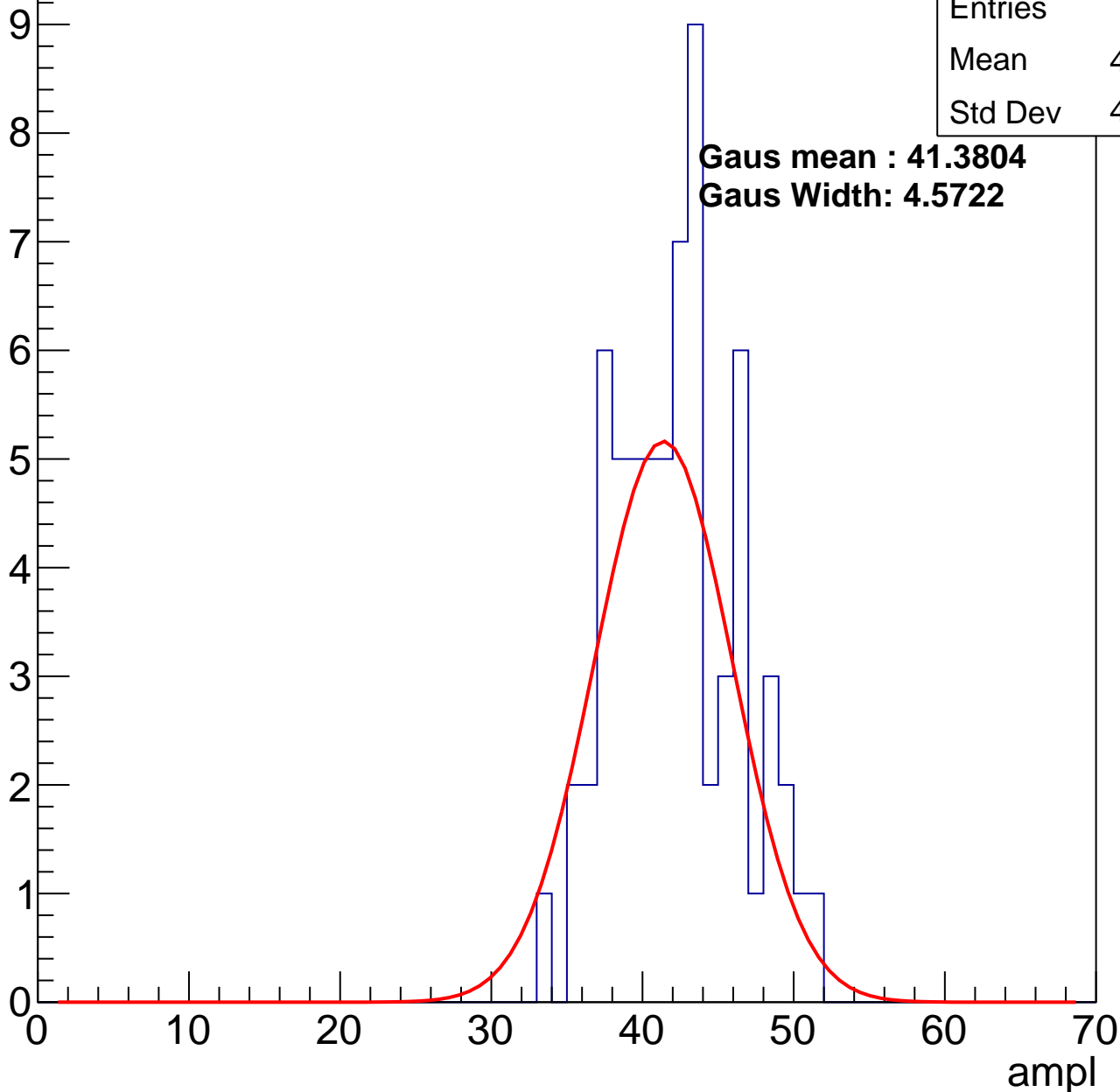
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.77
Std Dev	4.014

Gaus mean : 41.3804

Gaus Width: 4.5722

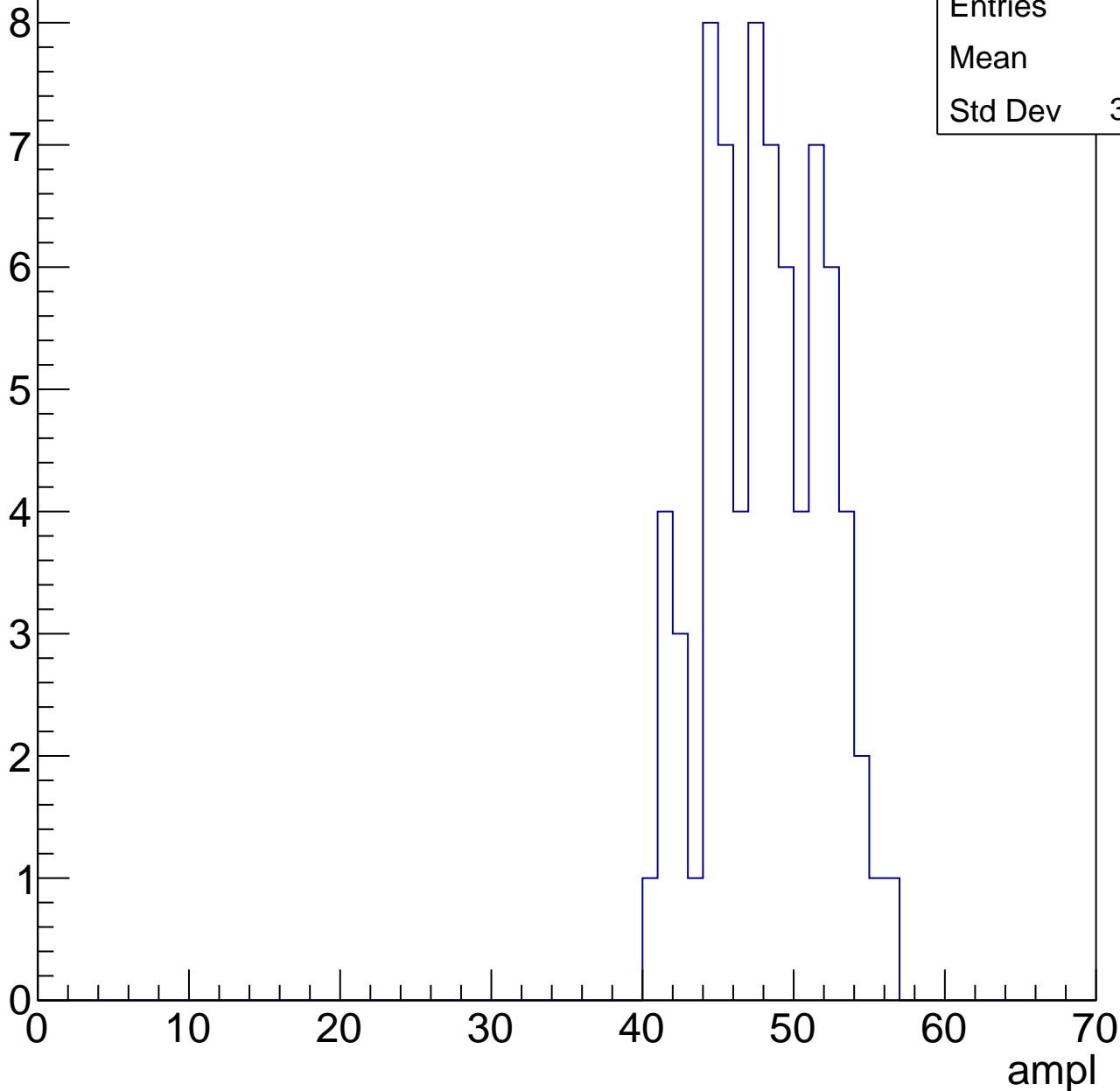


B1L102S, U11-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.7
Std Dev	3.798

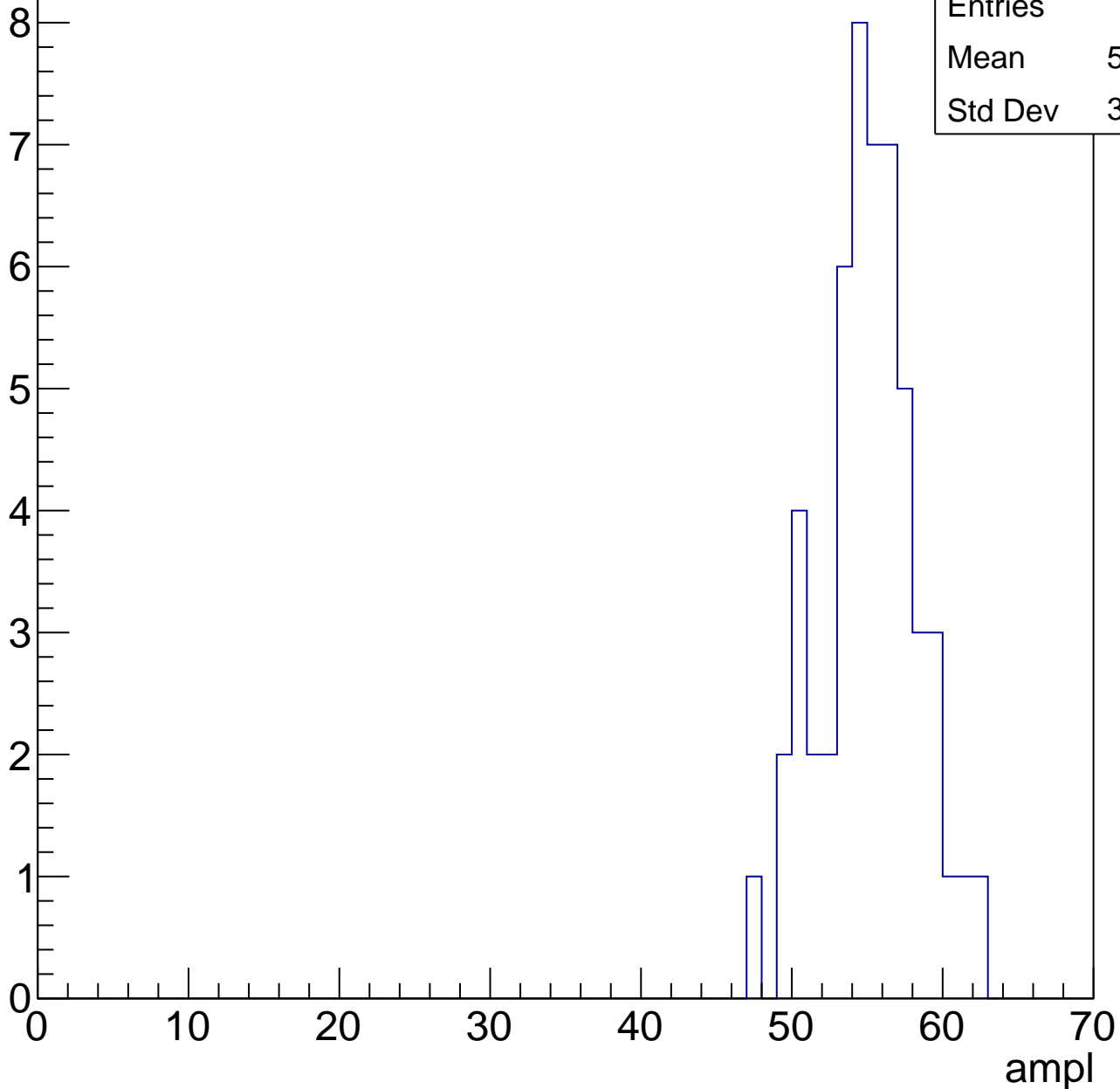


B1L102S, U11-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.66
Std Dev	3.144

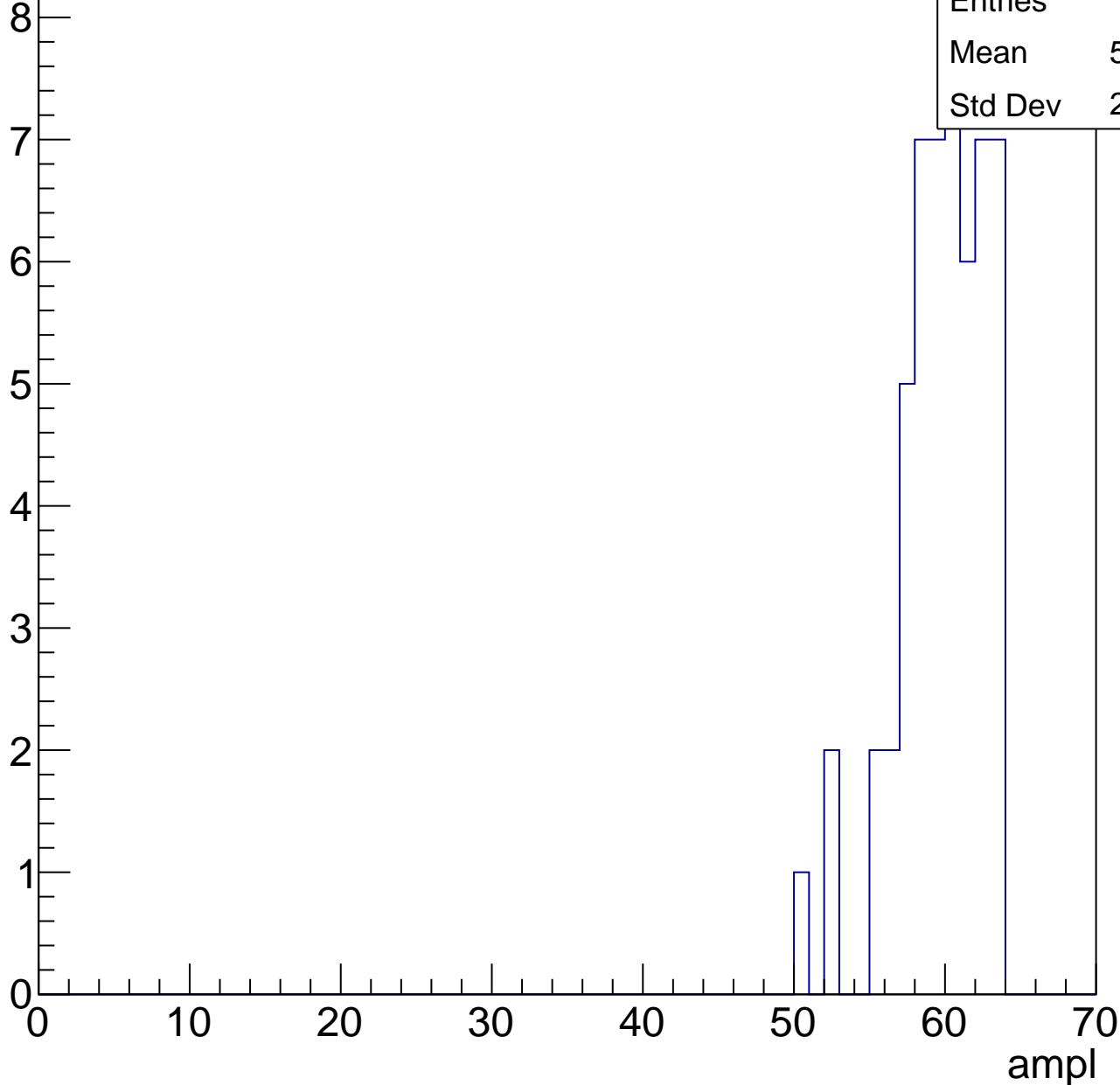


B1L102S, U11-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

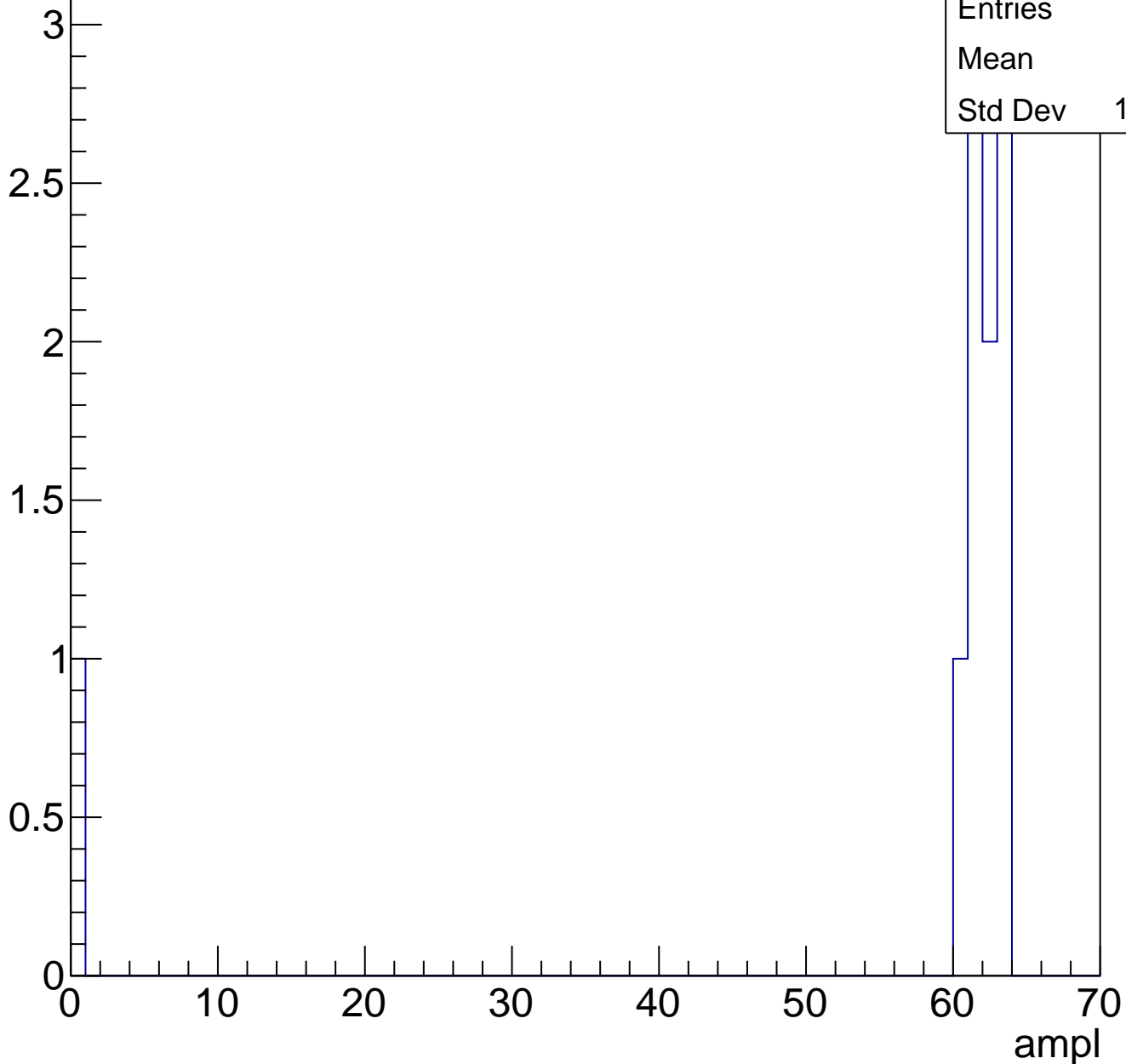
Entries	54
Mean	59.28
Std Dev	2.915



B1L102S, U11-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch89, adc0

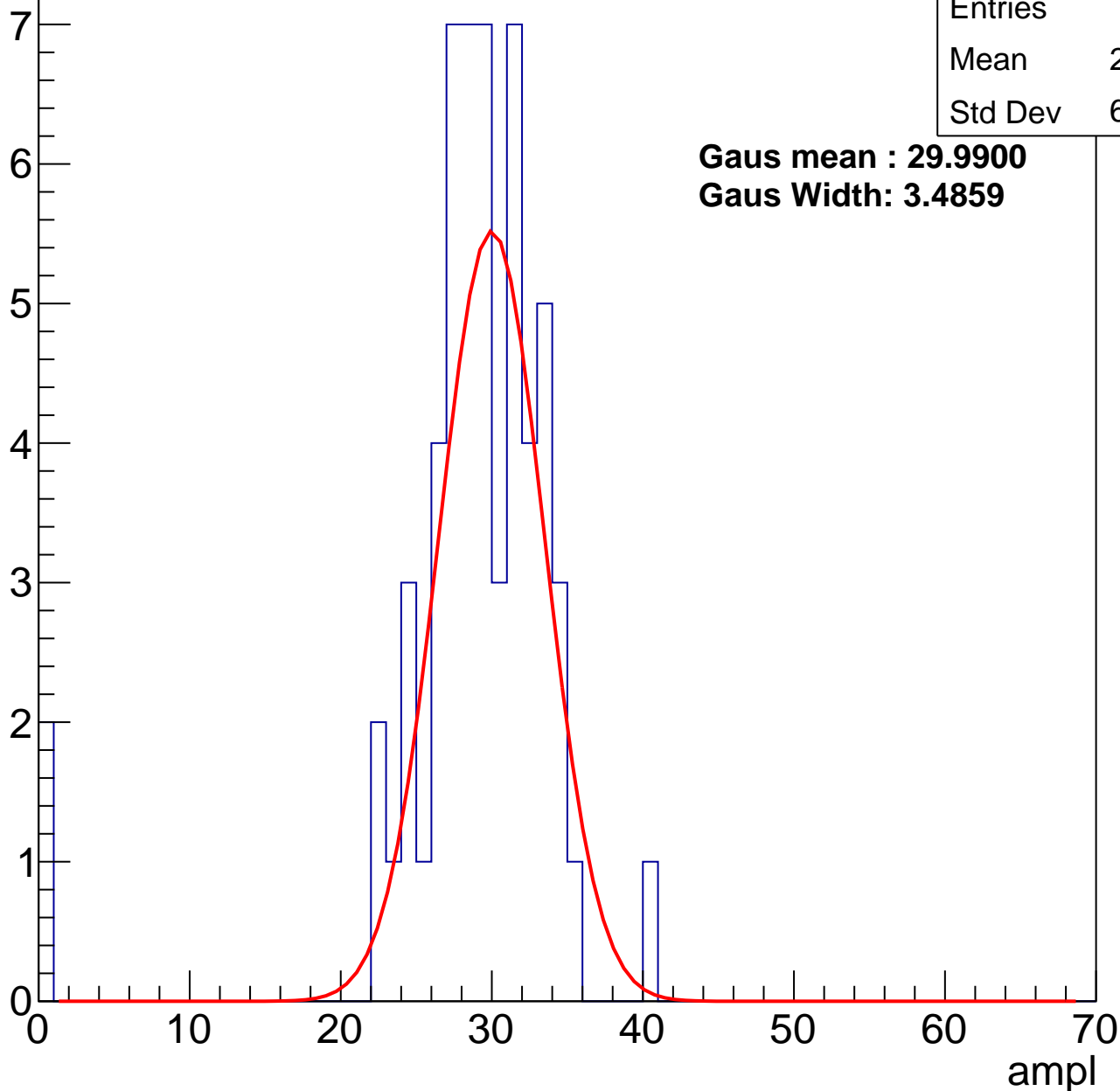
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.16
Std Dev	6.319

Gaus mean : 29.9900

Gaus Width: 3.4859



B1L102S, U11-ch89, adc1

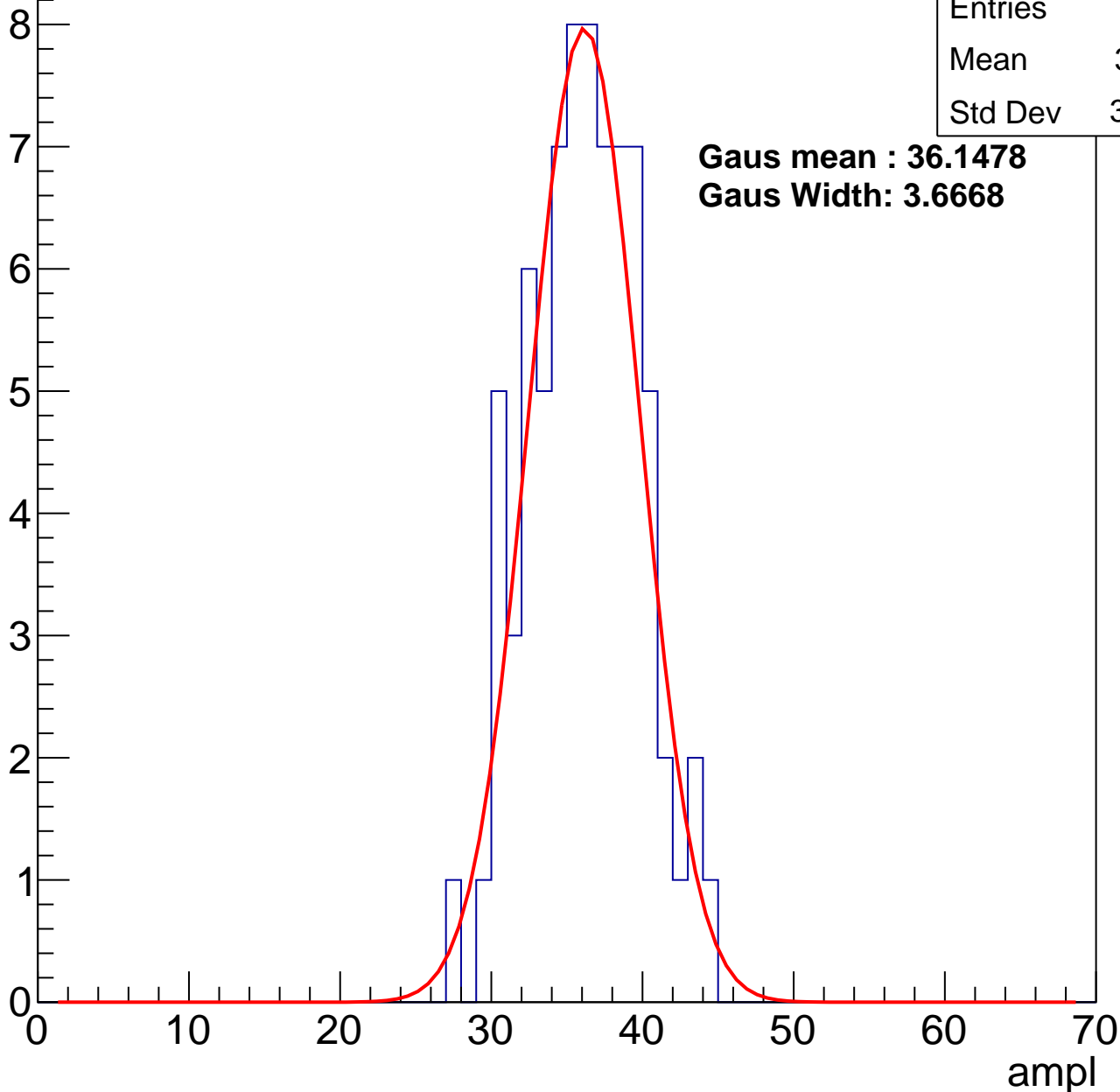
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.71
Std Dev	3.594

Gaus mean : 36.1478

Gaus Width: 3.6668



B1L102S, U11-ch89, adc2

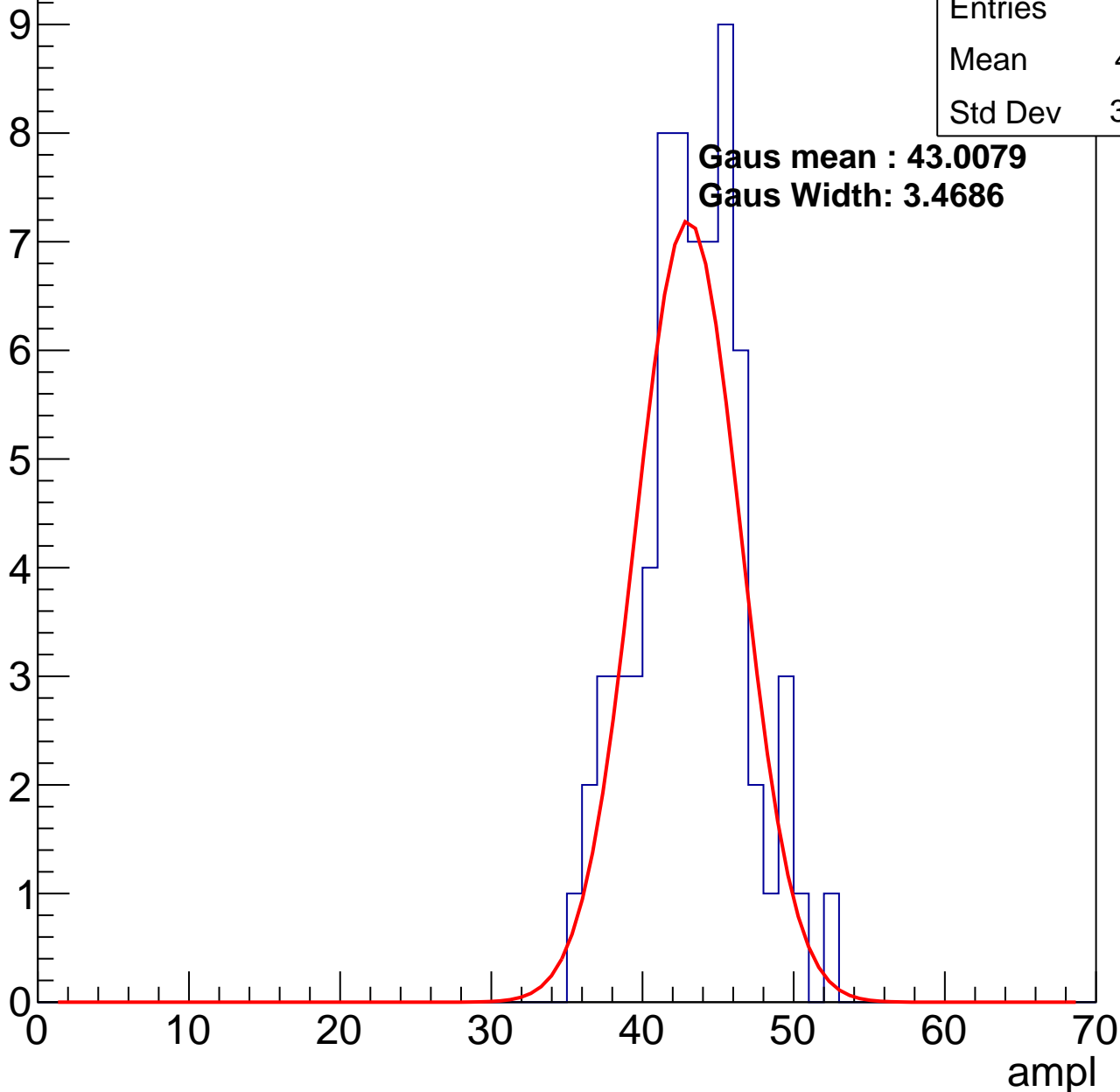
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.81
Std Dev	3.527

Gaus mean : 43.0079

Gaus Width: 3.4686

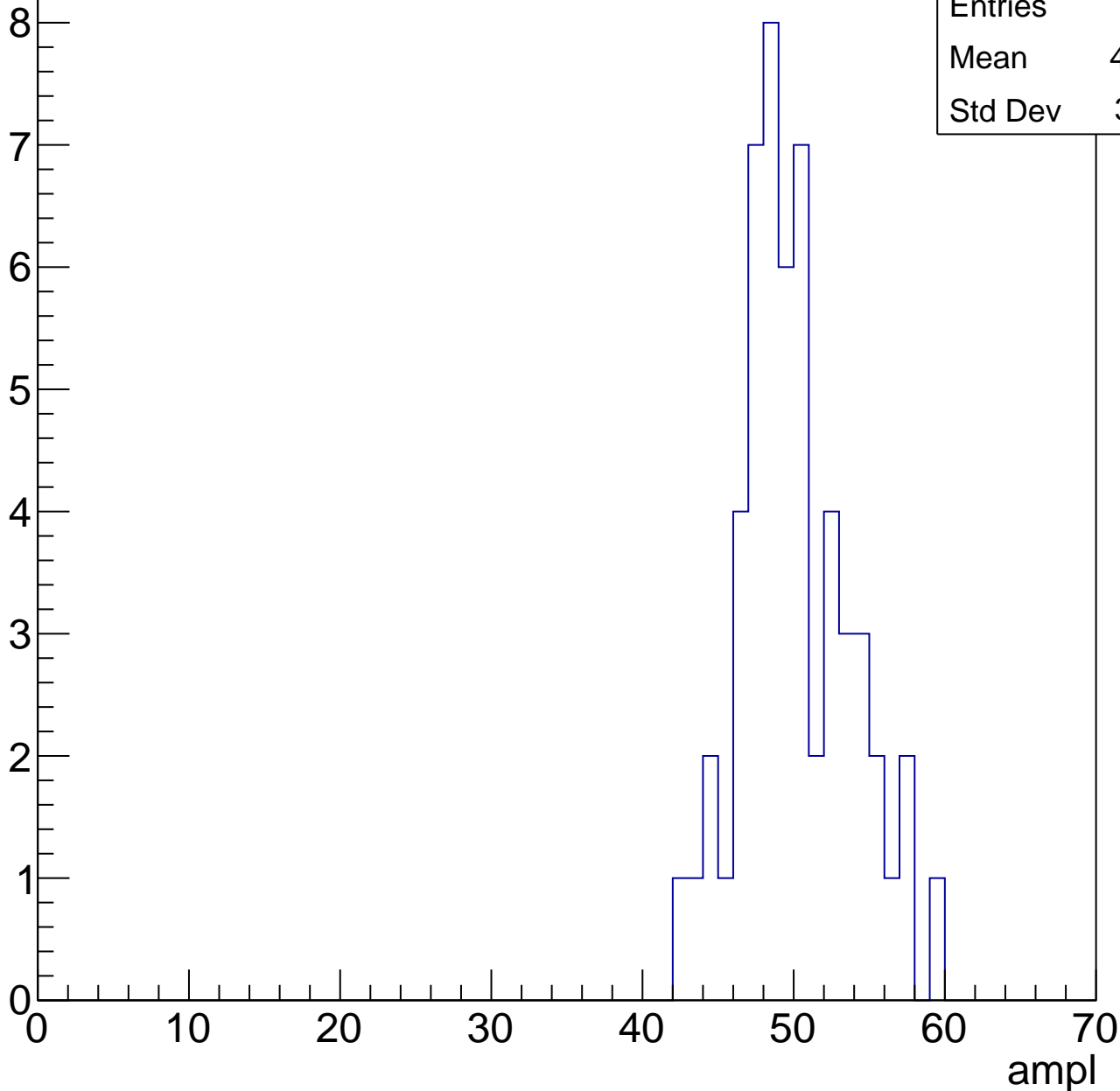


B1L102S, U11-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.62
Std Dev	3.631

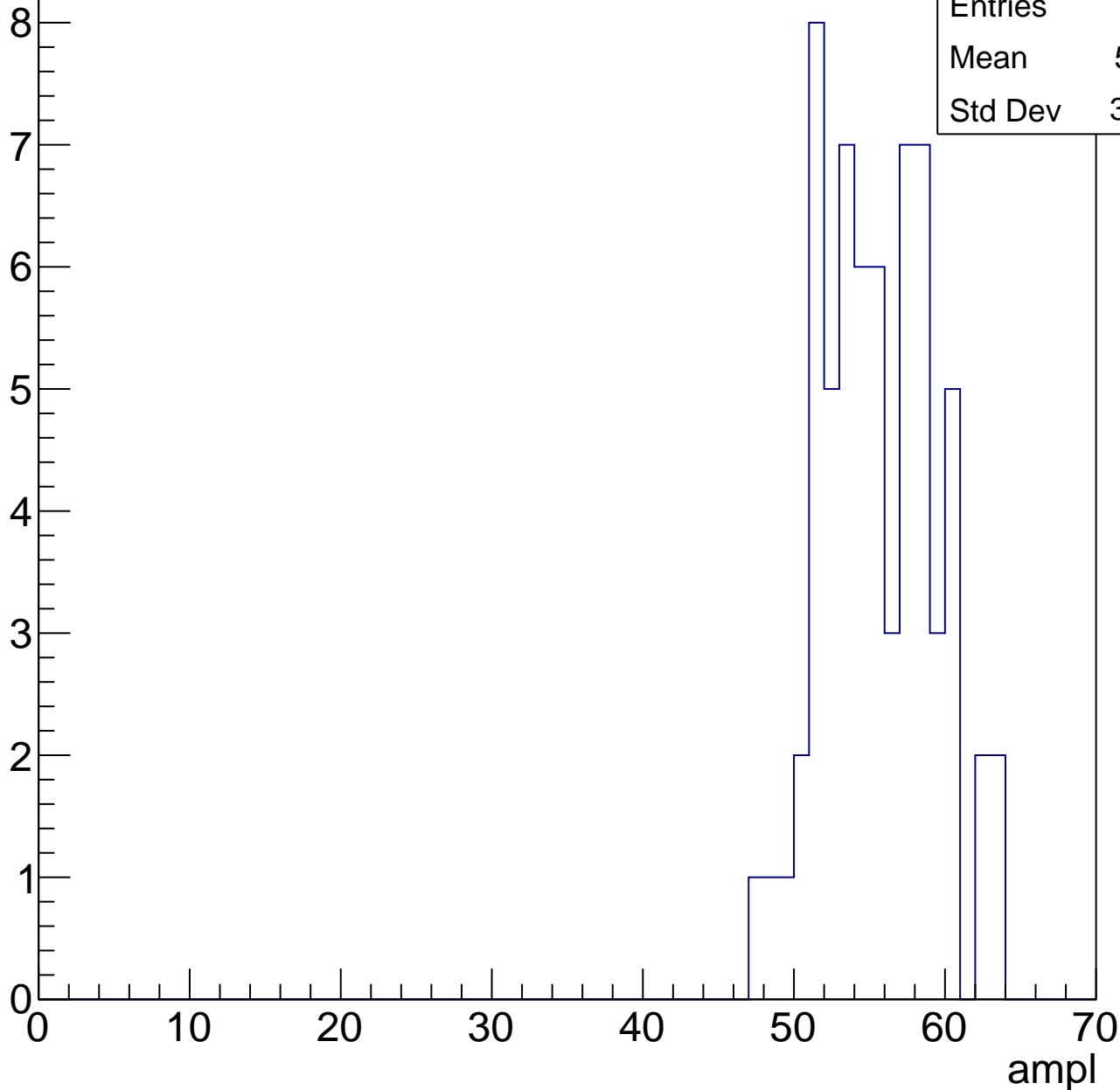


B1L102S, U11-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.11
Std Dev	3.689

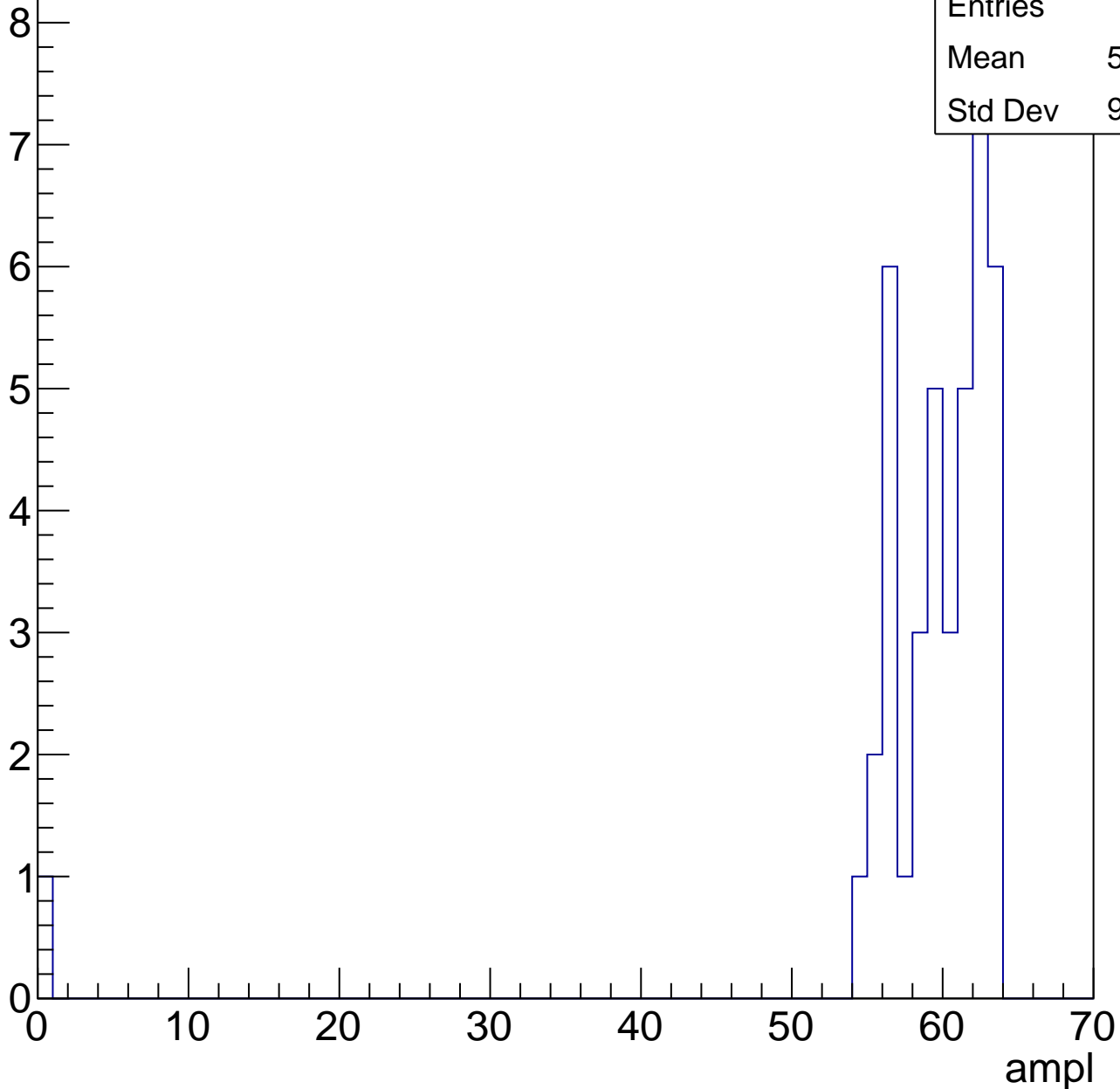


B1L102S, U11-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.17
Std Dev	9.579

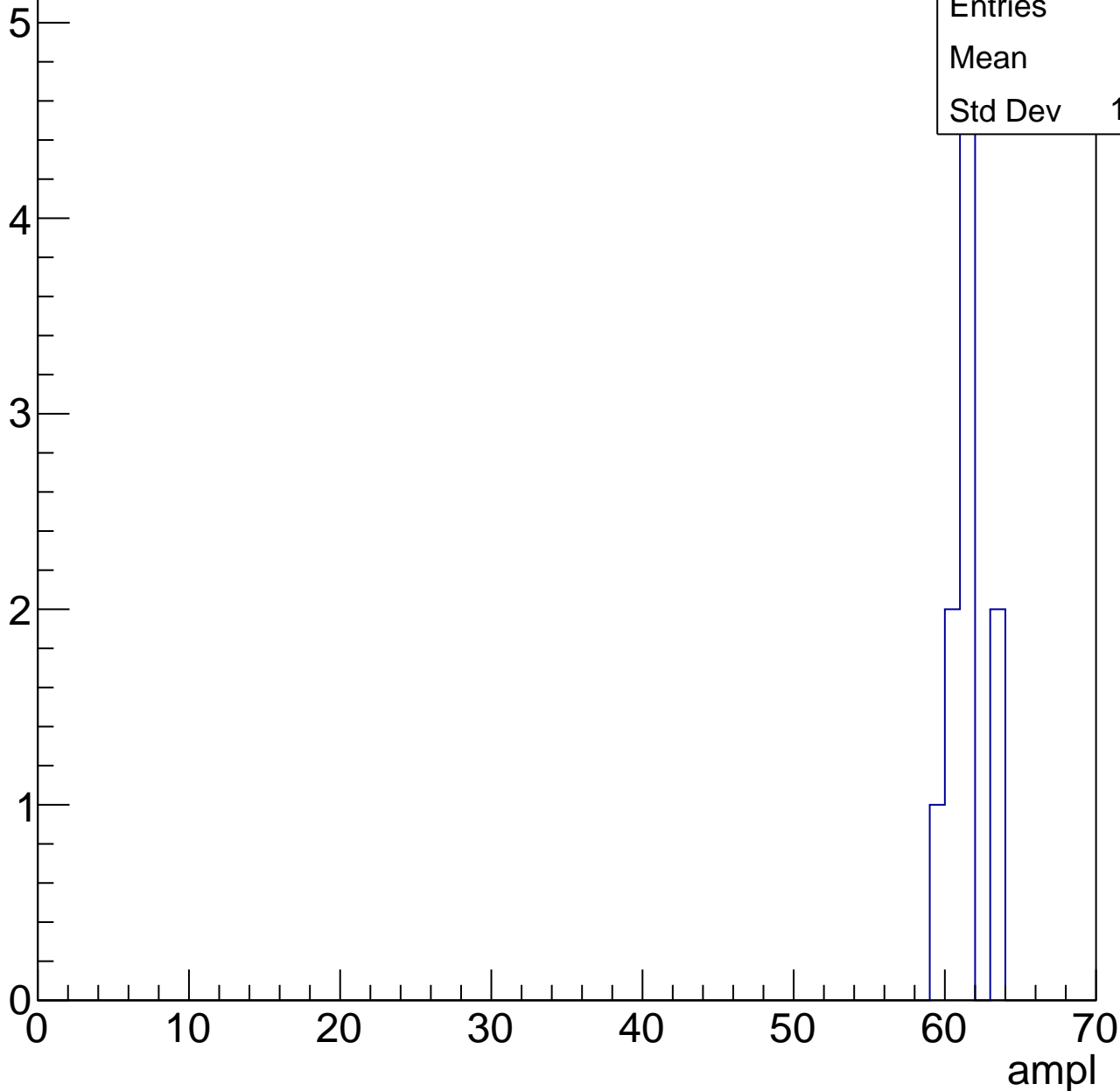


B1L102S, U11-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61
Std Dev	1.183



B1L102S, U11-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch90, adc0

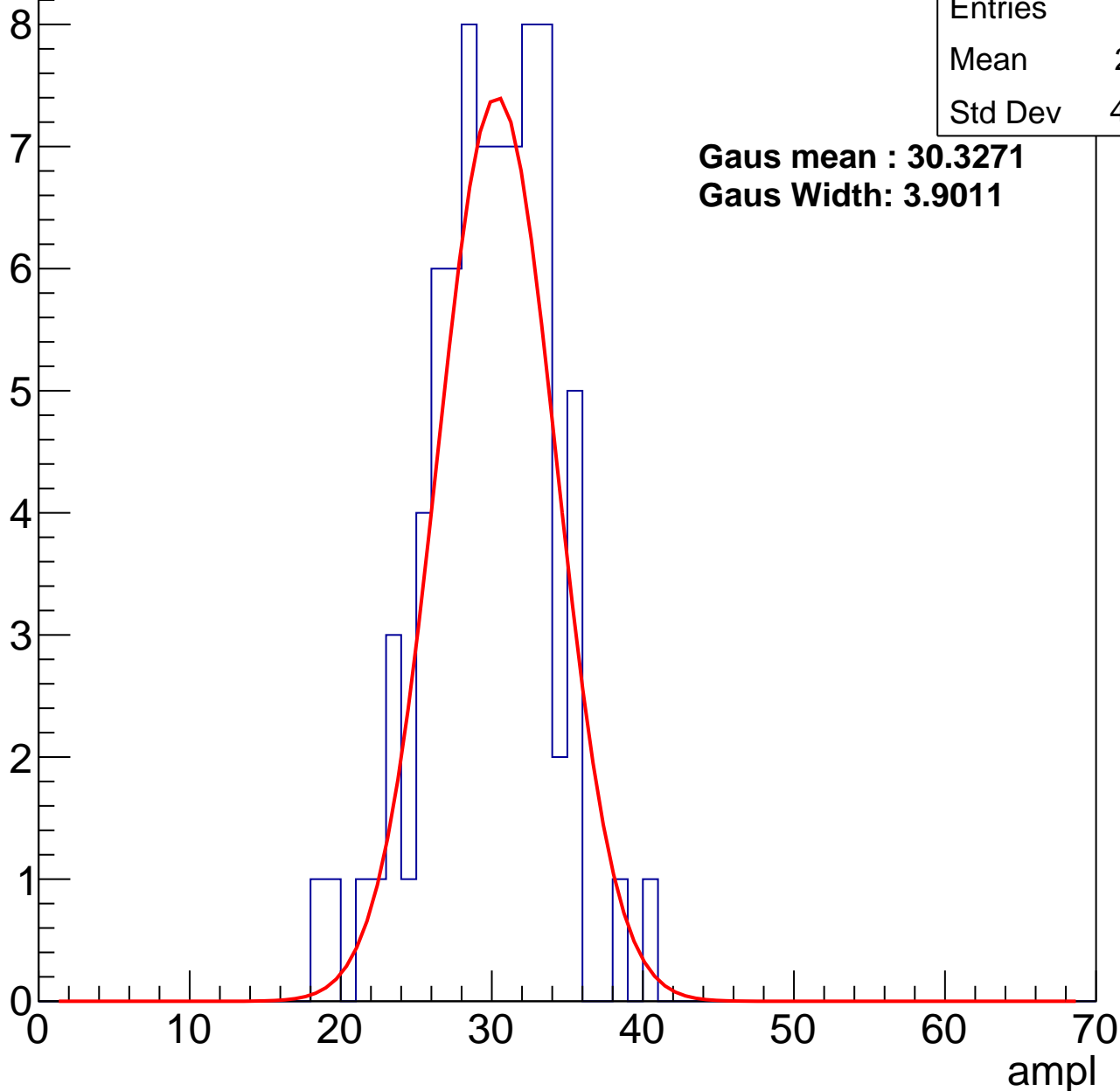
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.31
Std Dev	4.046

Gaus mean : 30.3271

Gaus Width: 3.9011



B1L102S, U11-ch90, adc1

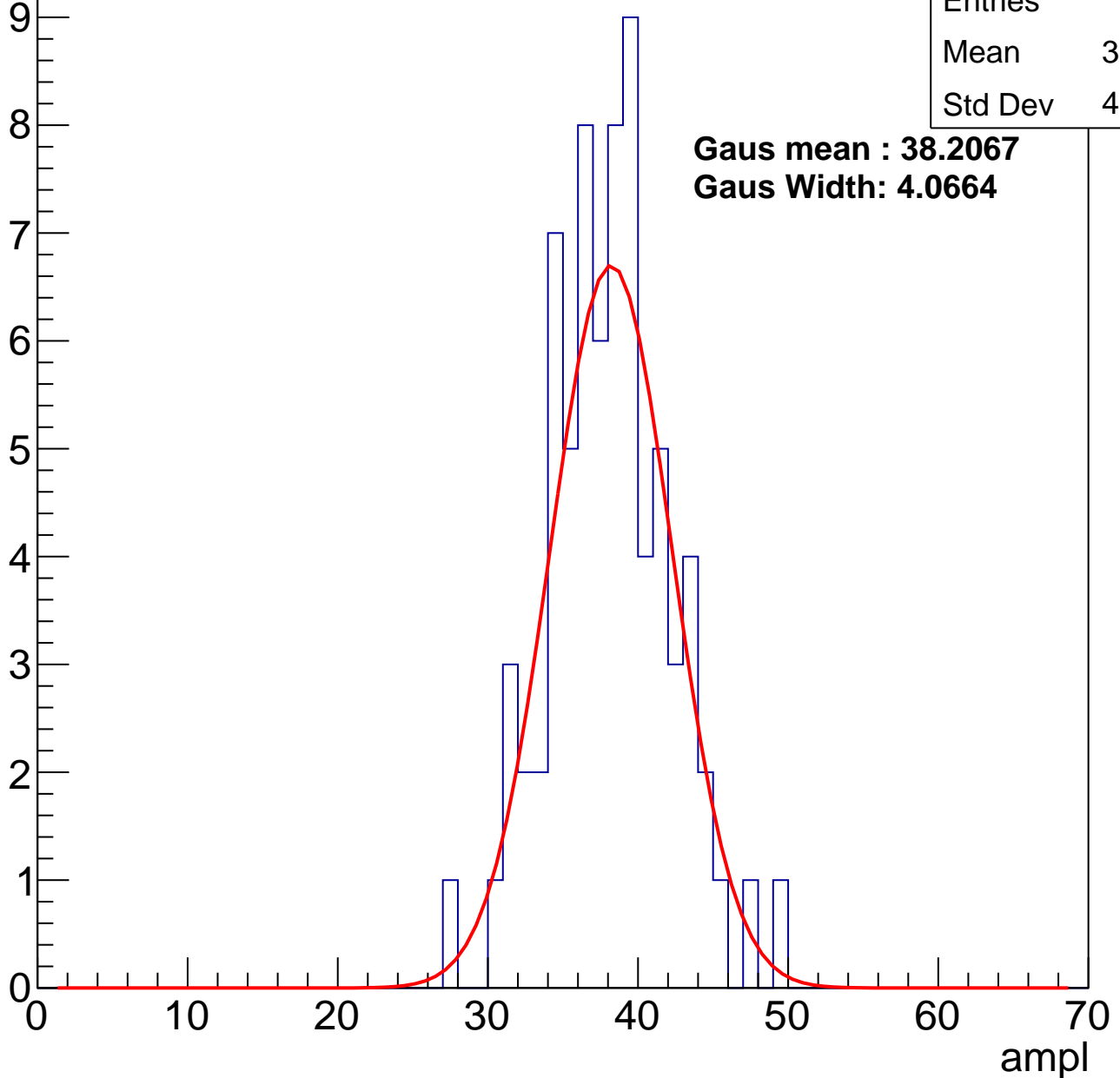
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.67
Std Dev	4.024

Gaus mean : 38.2067

Gaus Width: 4.0664



B1L102S, U11-ch90, adc2

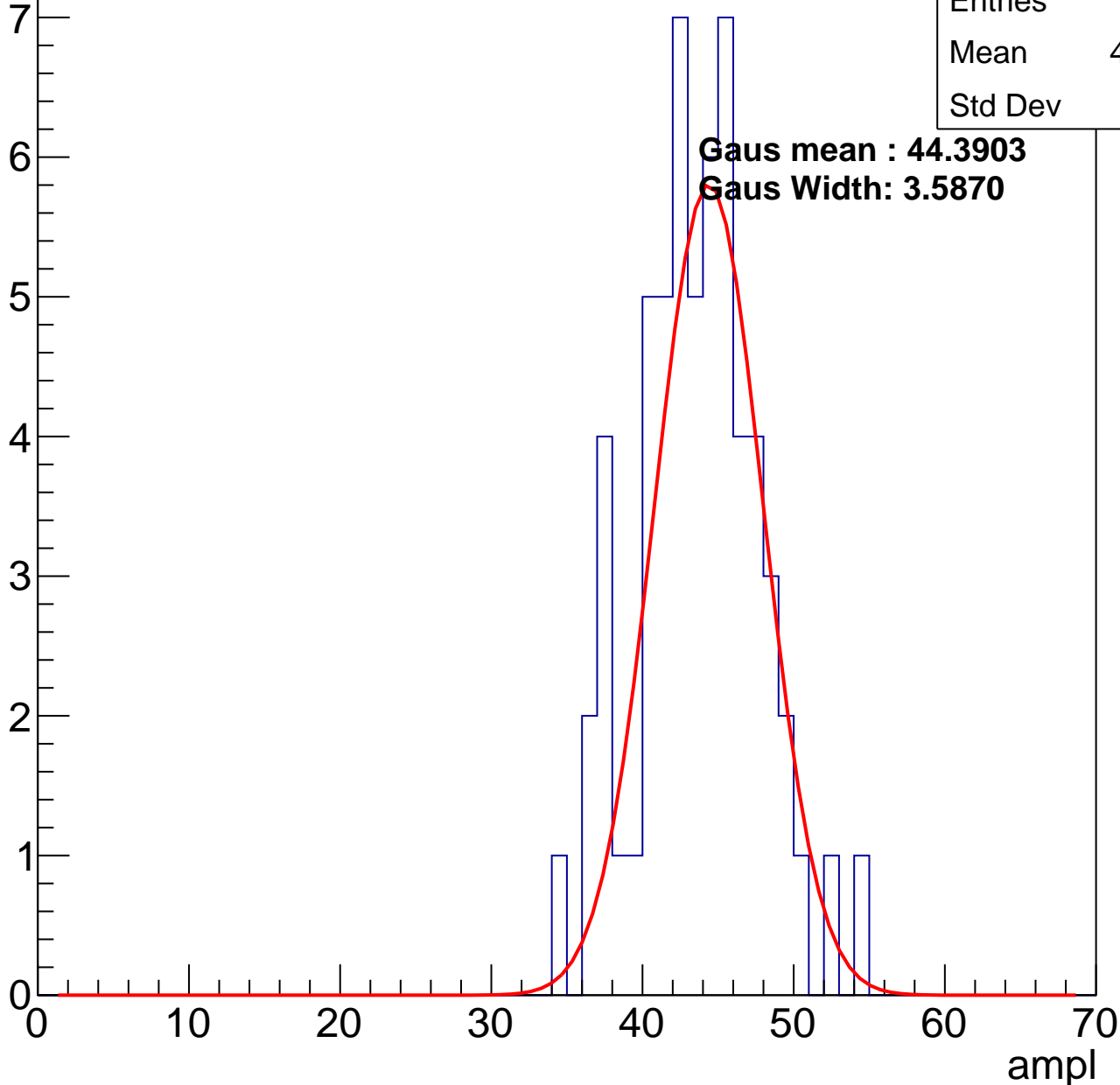
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.23
Std Dev	4.01

Gaus mean : 44.3903

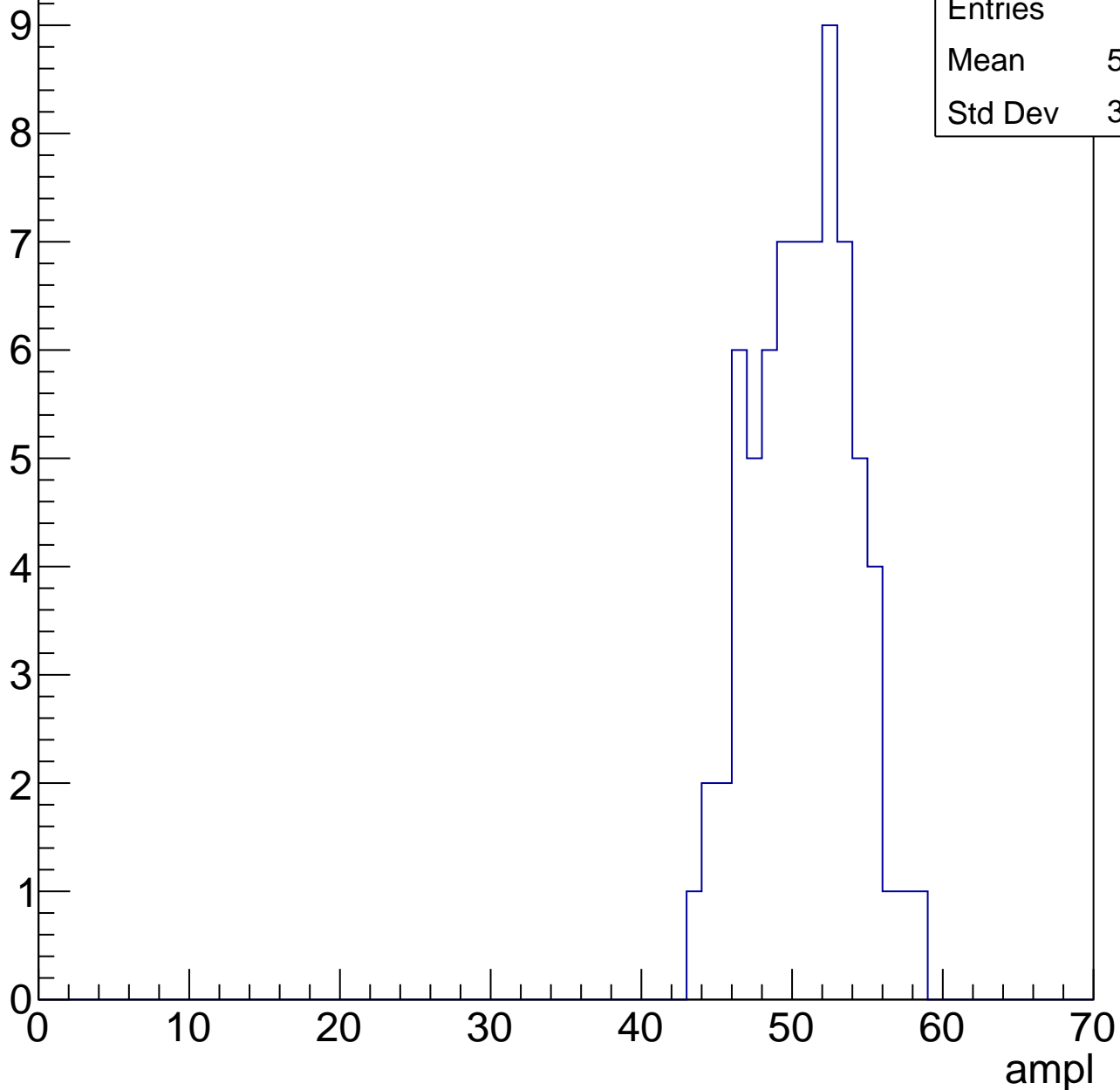
Gaus Width: 3.5870



B1L102S, U11-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

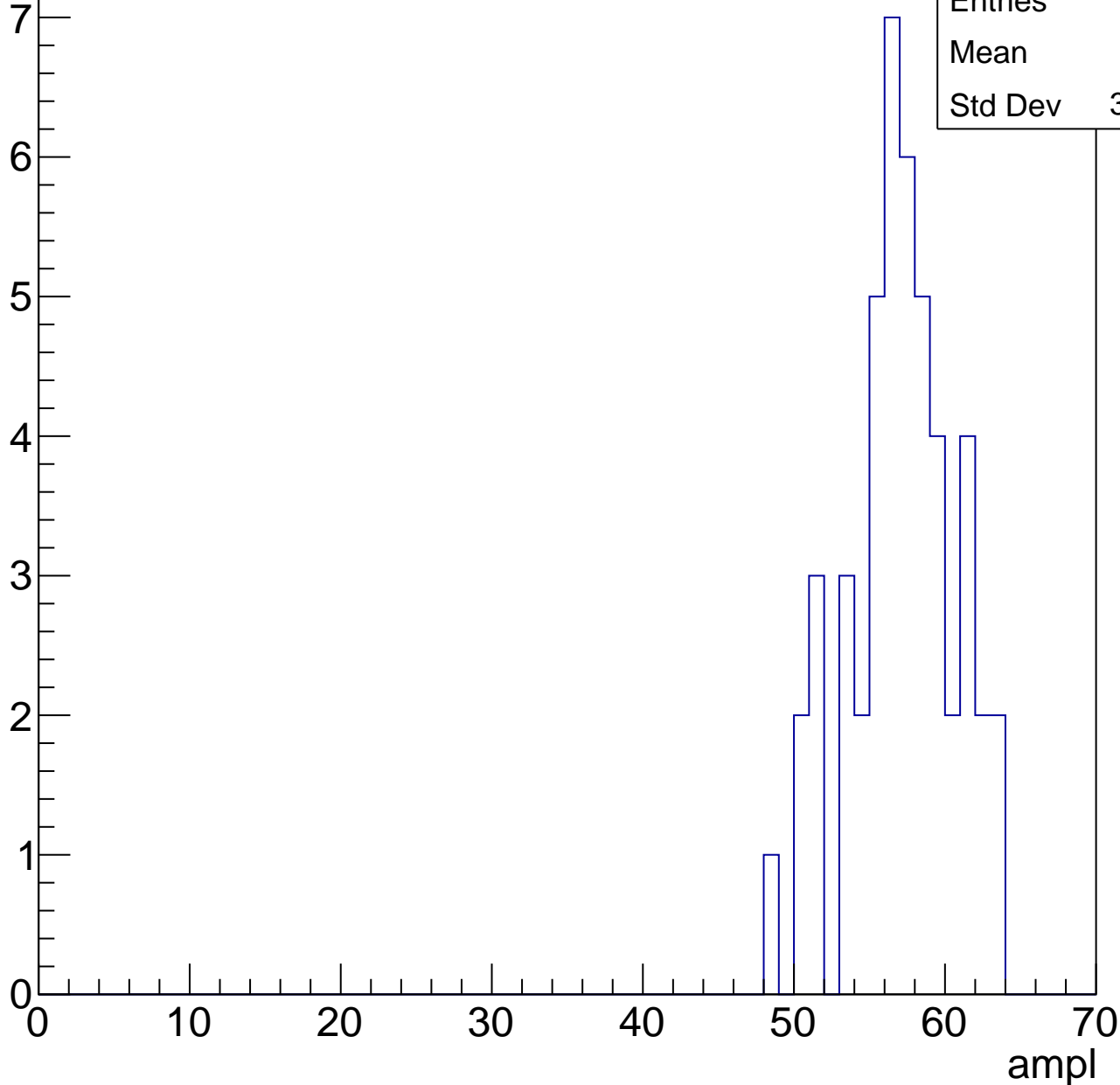


B1L102S, U11-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.6
Std Dev	3.528

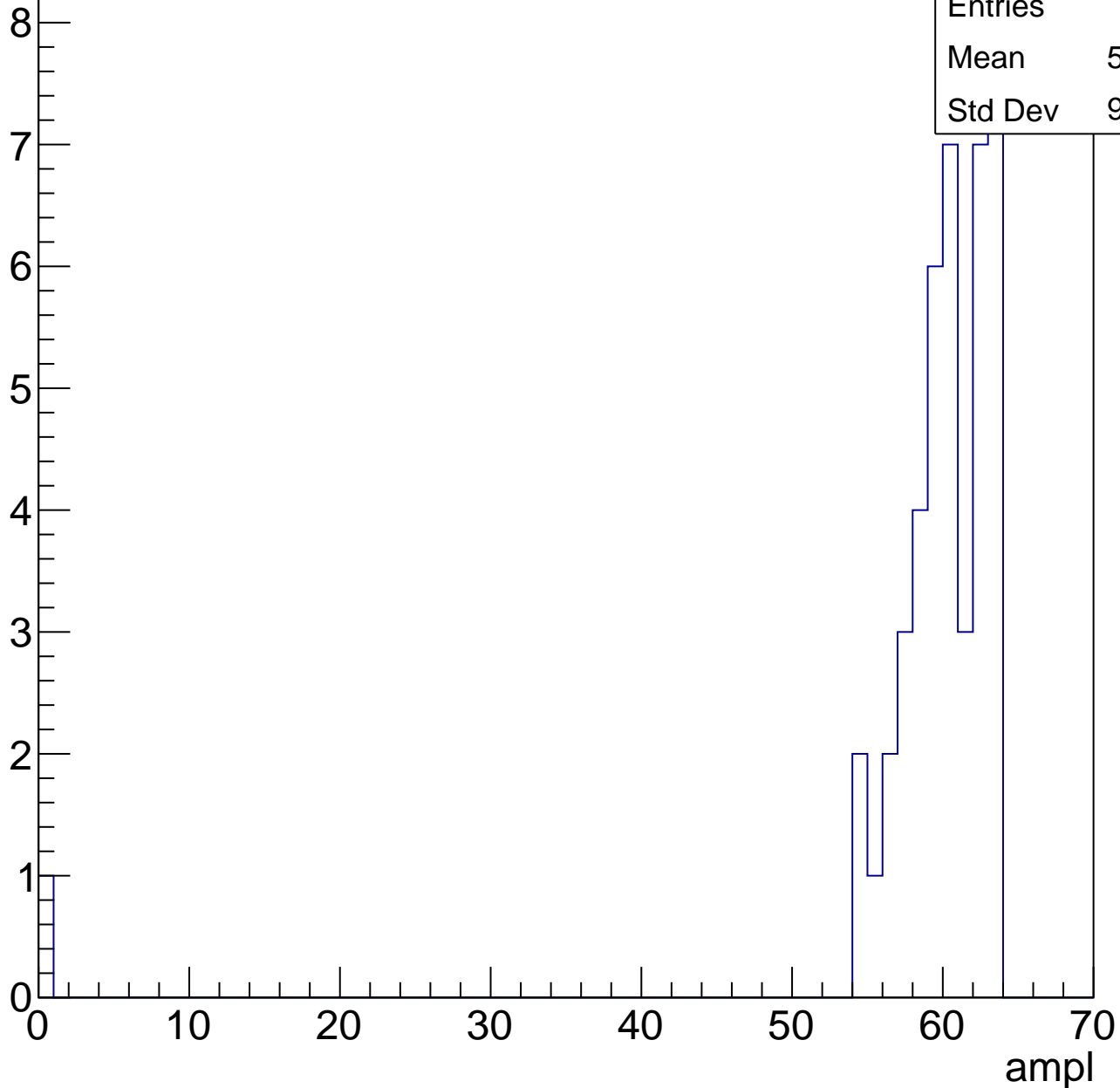


B1L102S, U11-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

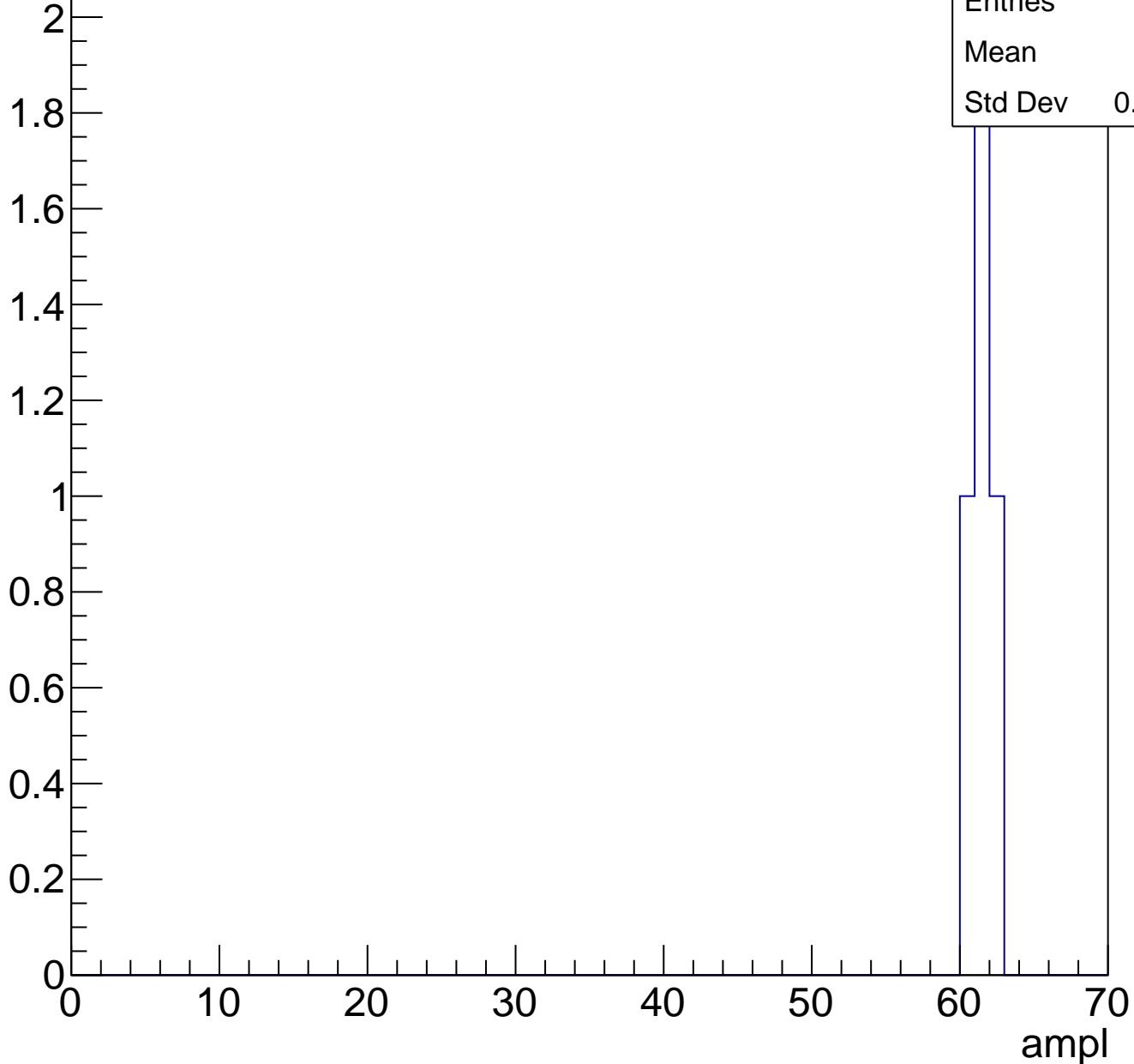
Entries	44
Mean	58.48
Std Dev	9.267



B1L102S, U11-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	61
Std Dev	0

B1L102S, U11-ch91, adc0

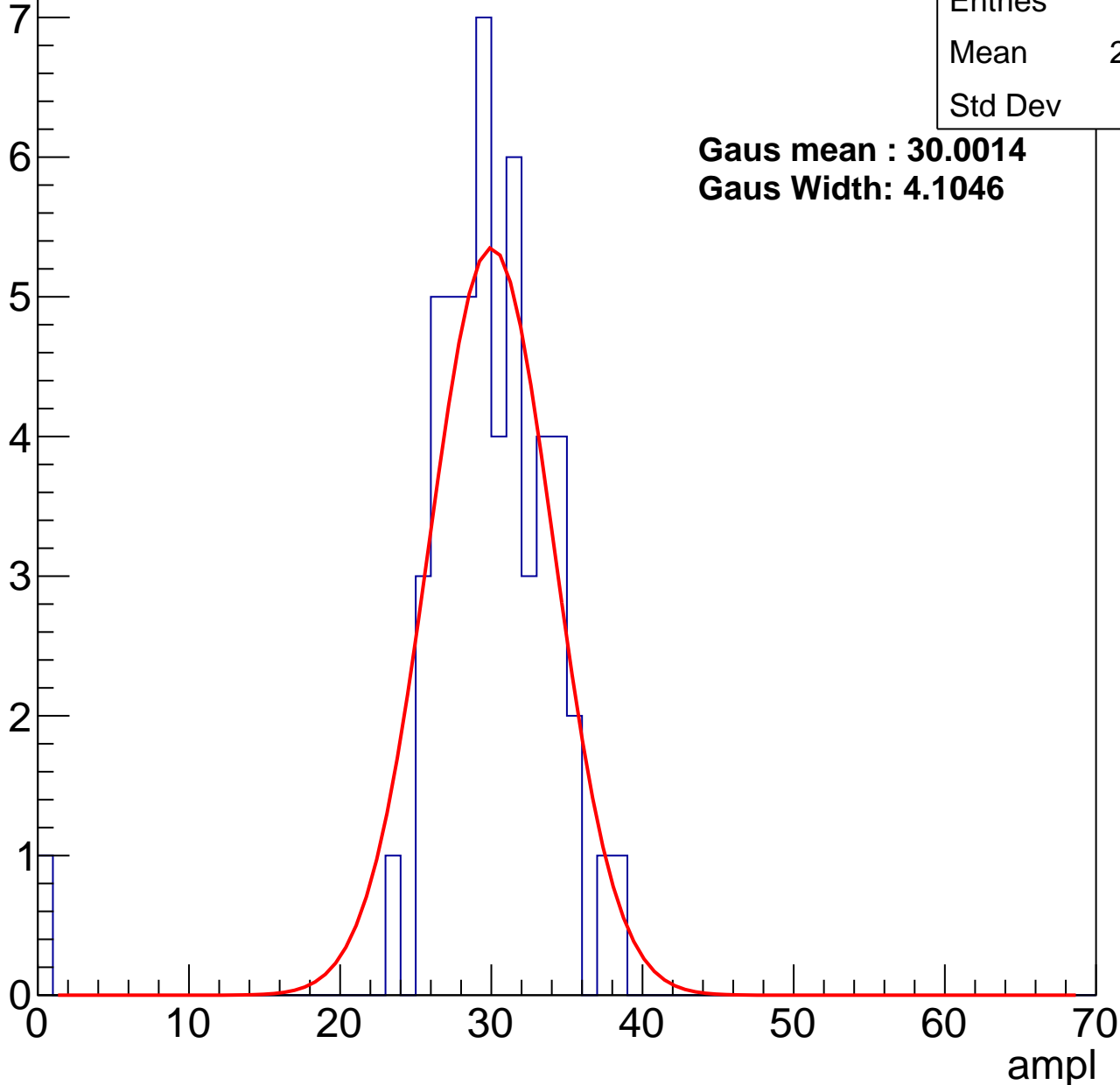
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	29.25
Std Dev	5.24

Gaus mean : 30.0014

Gaus Width: 4.1046



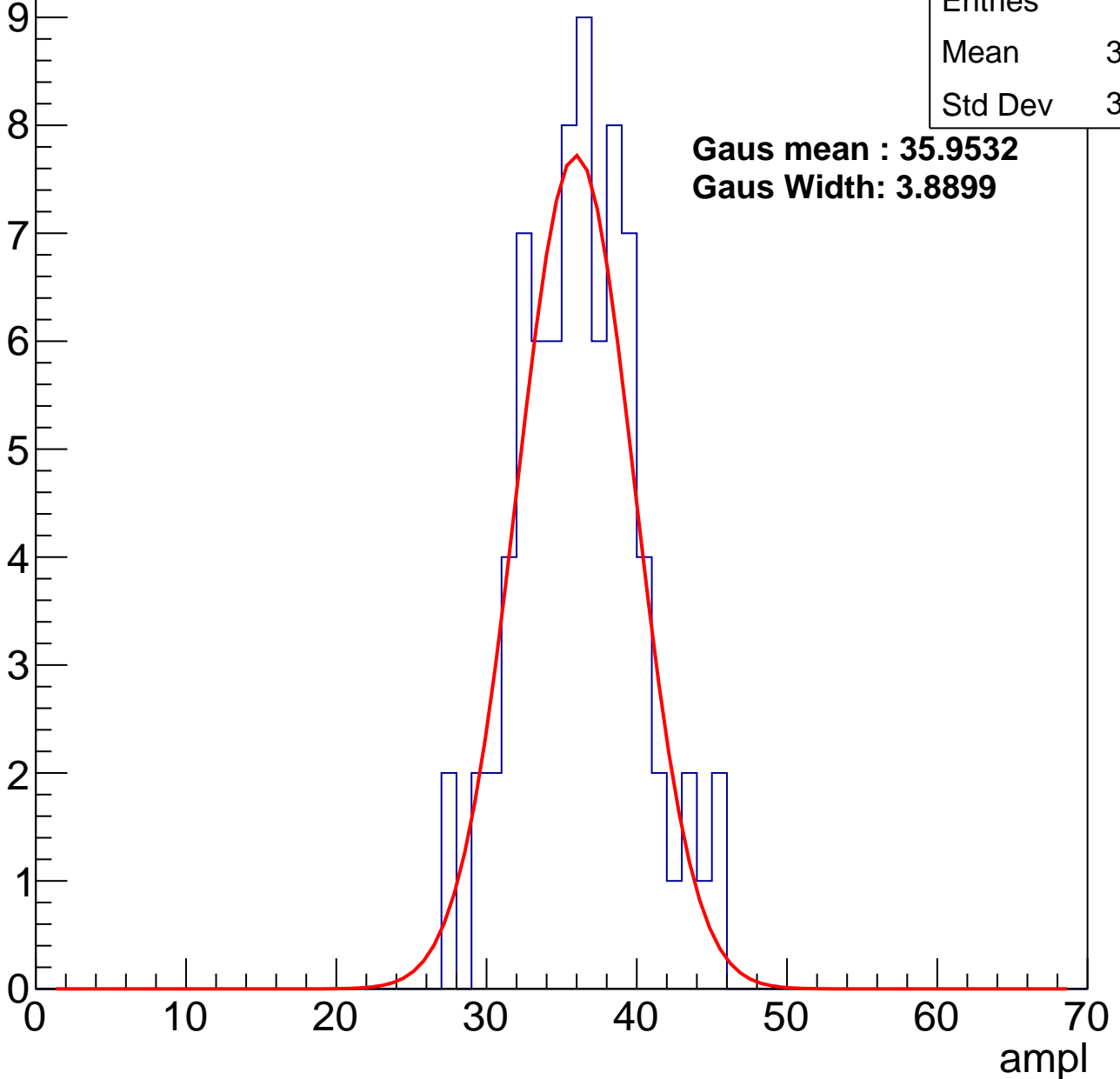
B1L102S, U11-ch91, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.81
Std Dev	3.901

Gaus mean : 35.9532
Gaus Width: 3.8899



B1L102S, U11-ch91, adc2

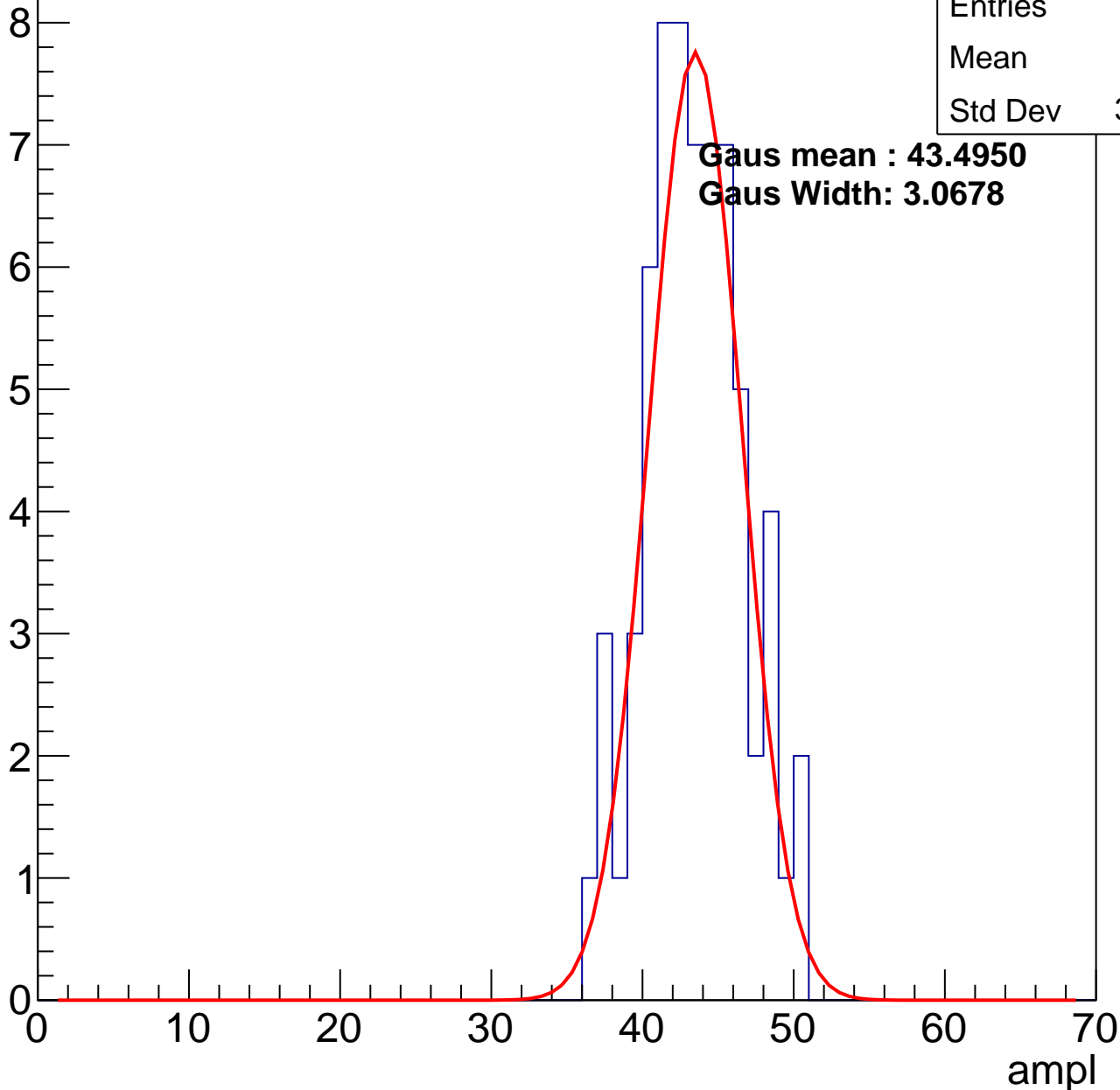
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43
Std Dev	3.211

Gaus mean : 43.4950

Gaus Width: 3.0678

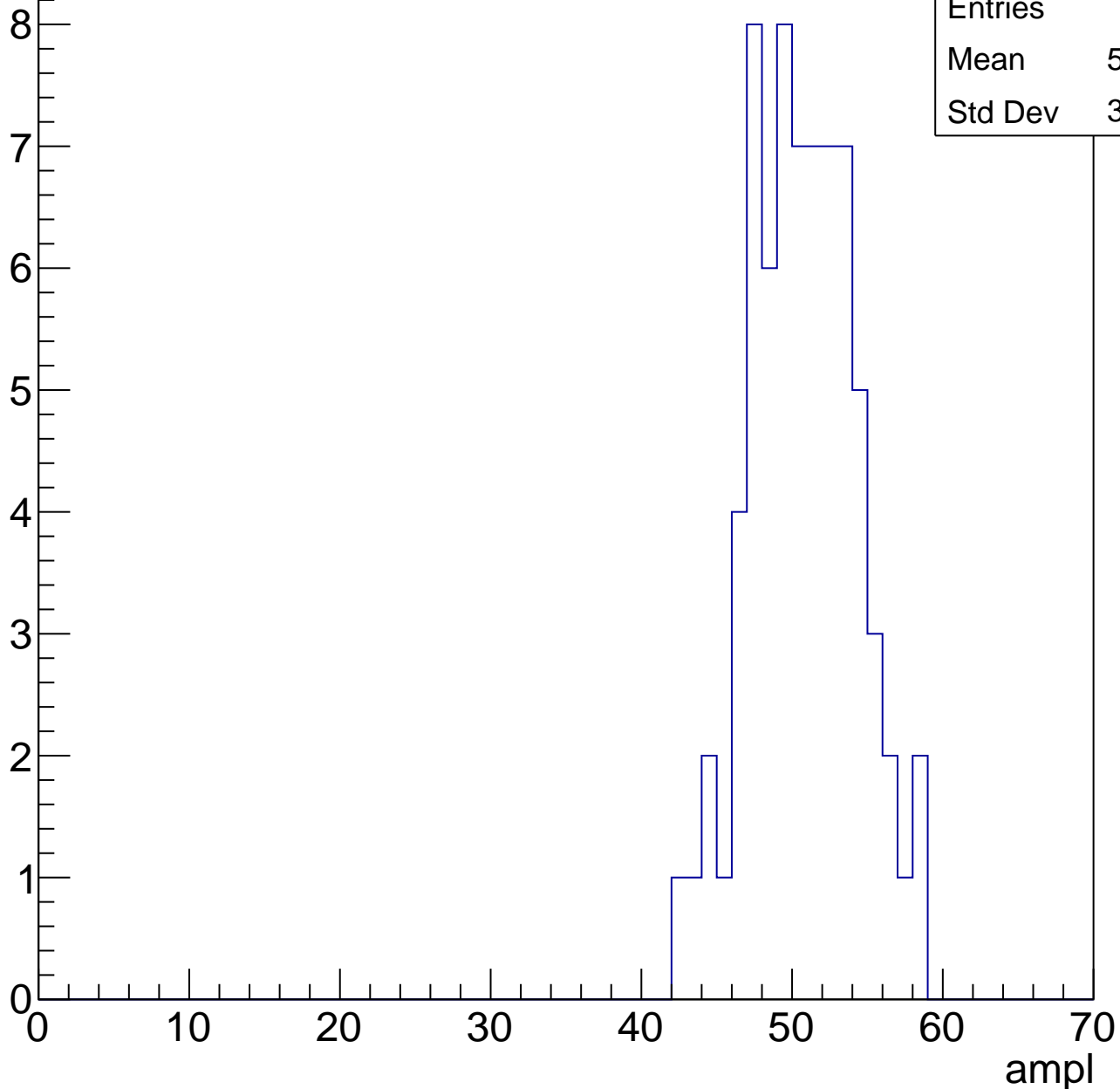


B1L102S, U11-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

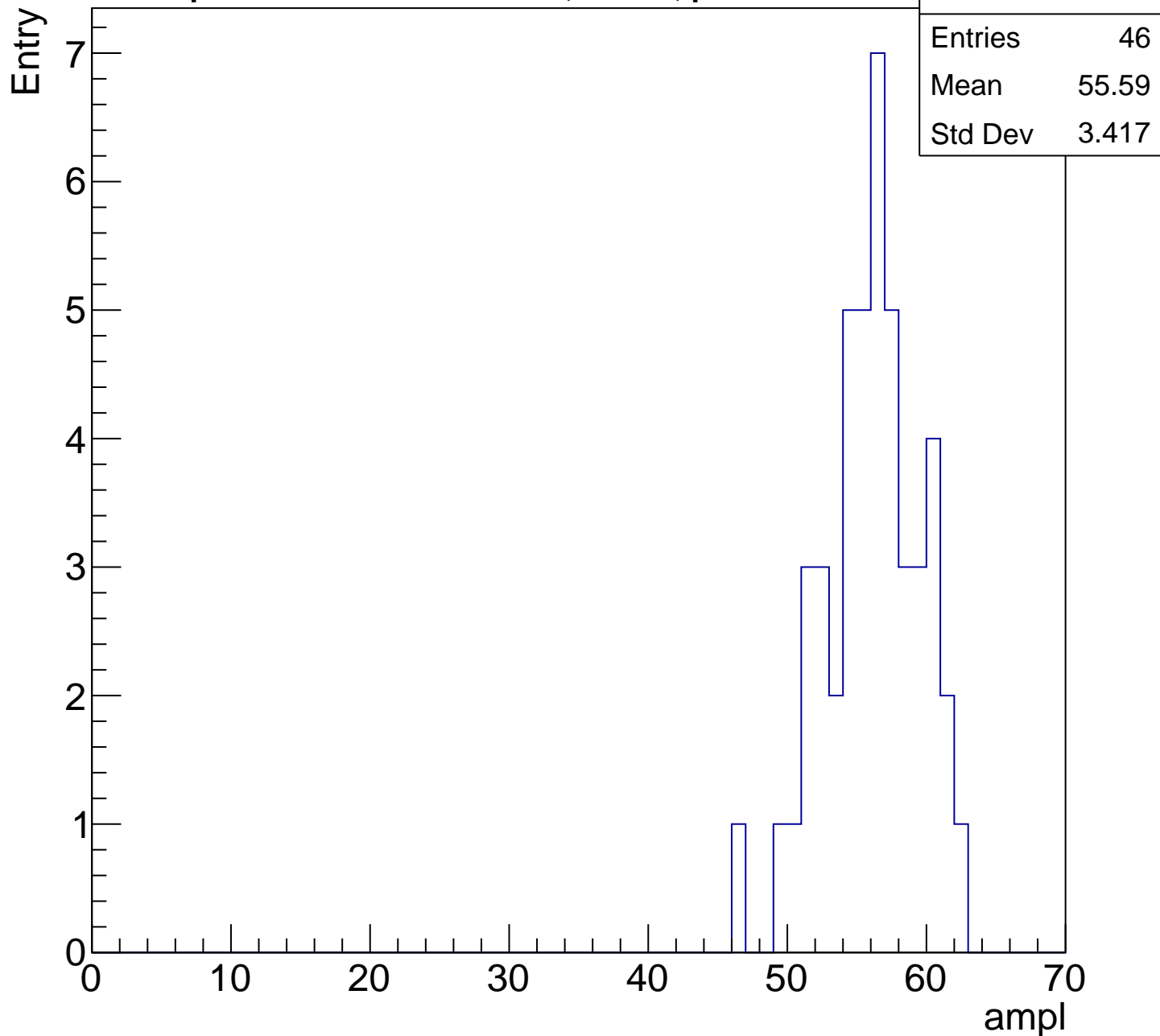
Entry

Entries	72
Mean	50.28
Std Dev	3.485



B1L102S, U11-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

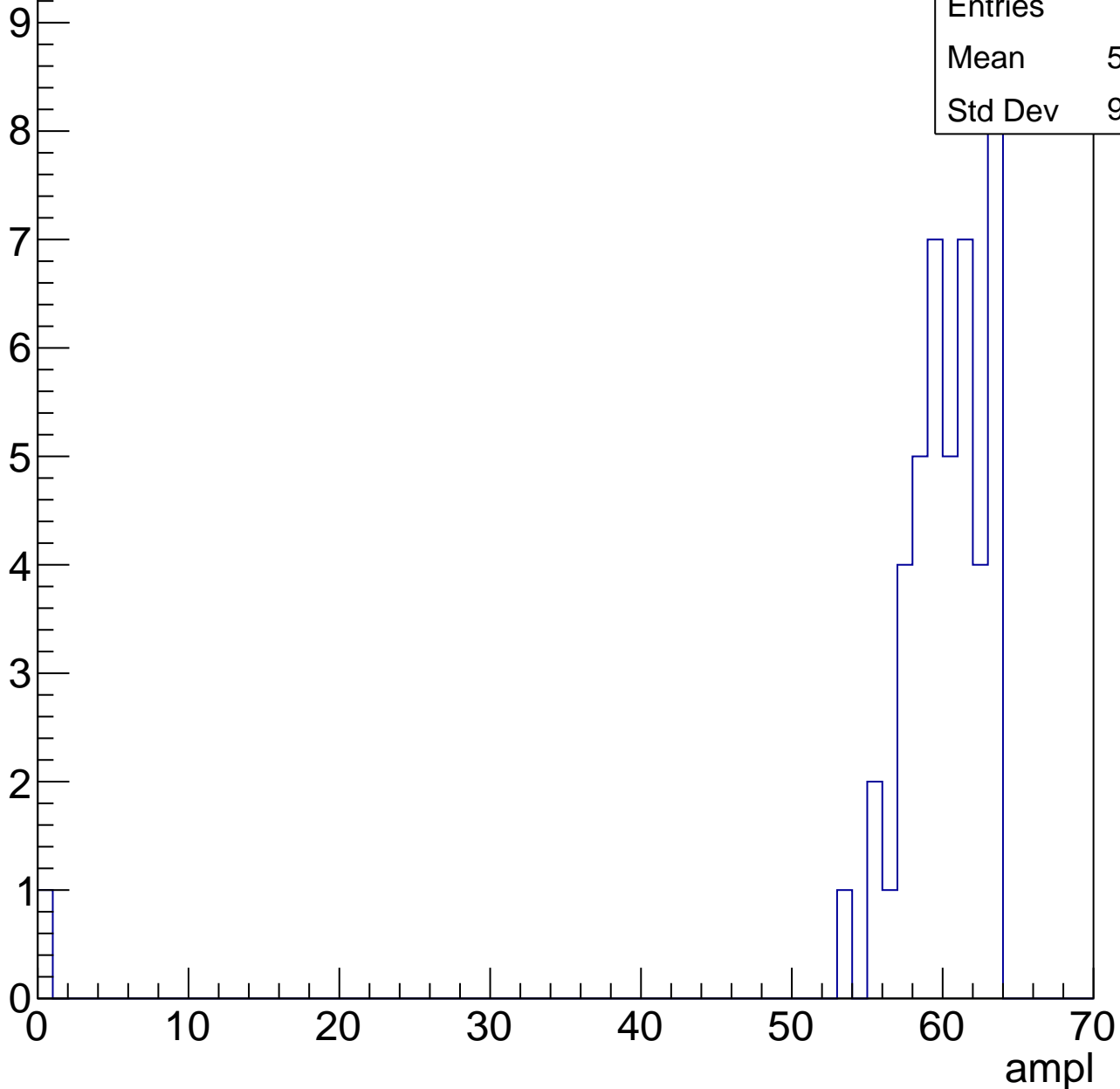


B1L102S, U11-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.52
Std Dev	9.067

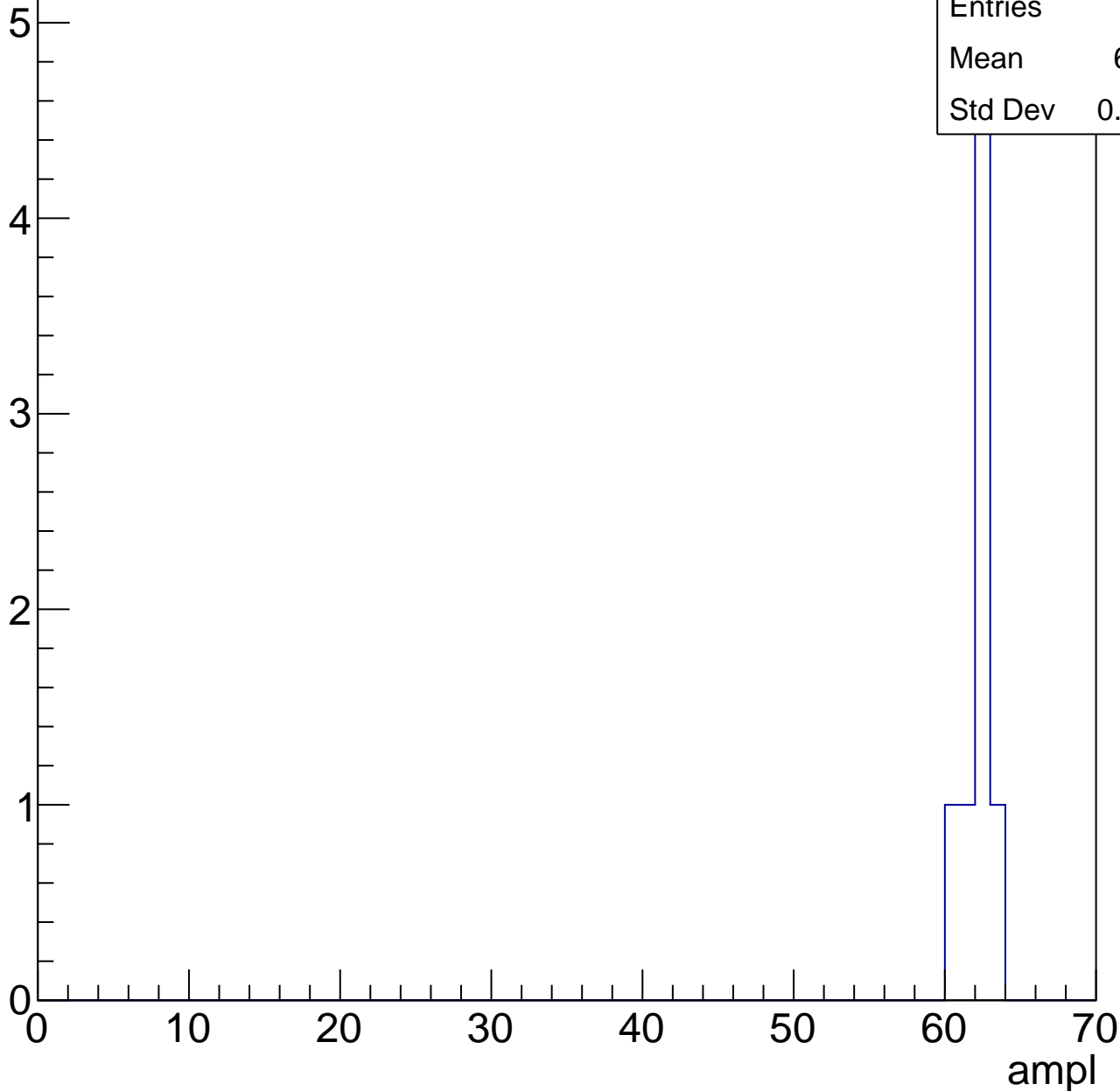


B1L102S, U11-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	61.75
Std Dev	0.8292



B1L102S, U11-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L102S, U11-ch92, adc0

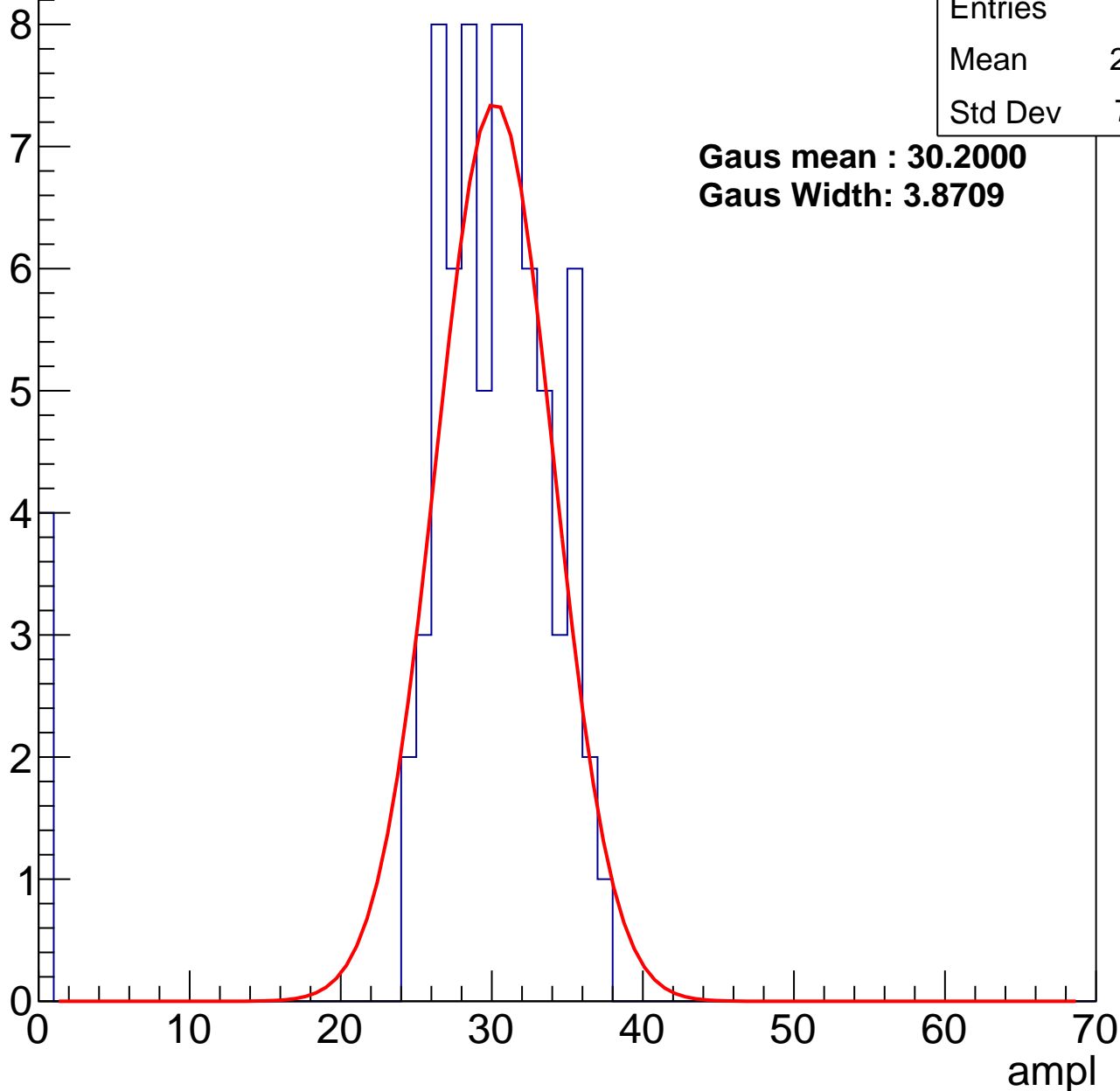
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.37
Std Dev	7.451

Gaus mean : 30.2000

Gaus Width: 3.8709



B1L102S, U11-ch92, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	37.47
Std Dev	3.573

Gaus mean : 37.4975

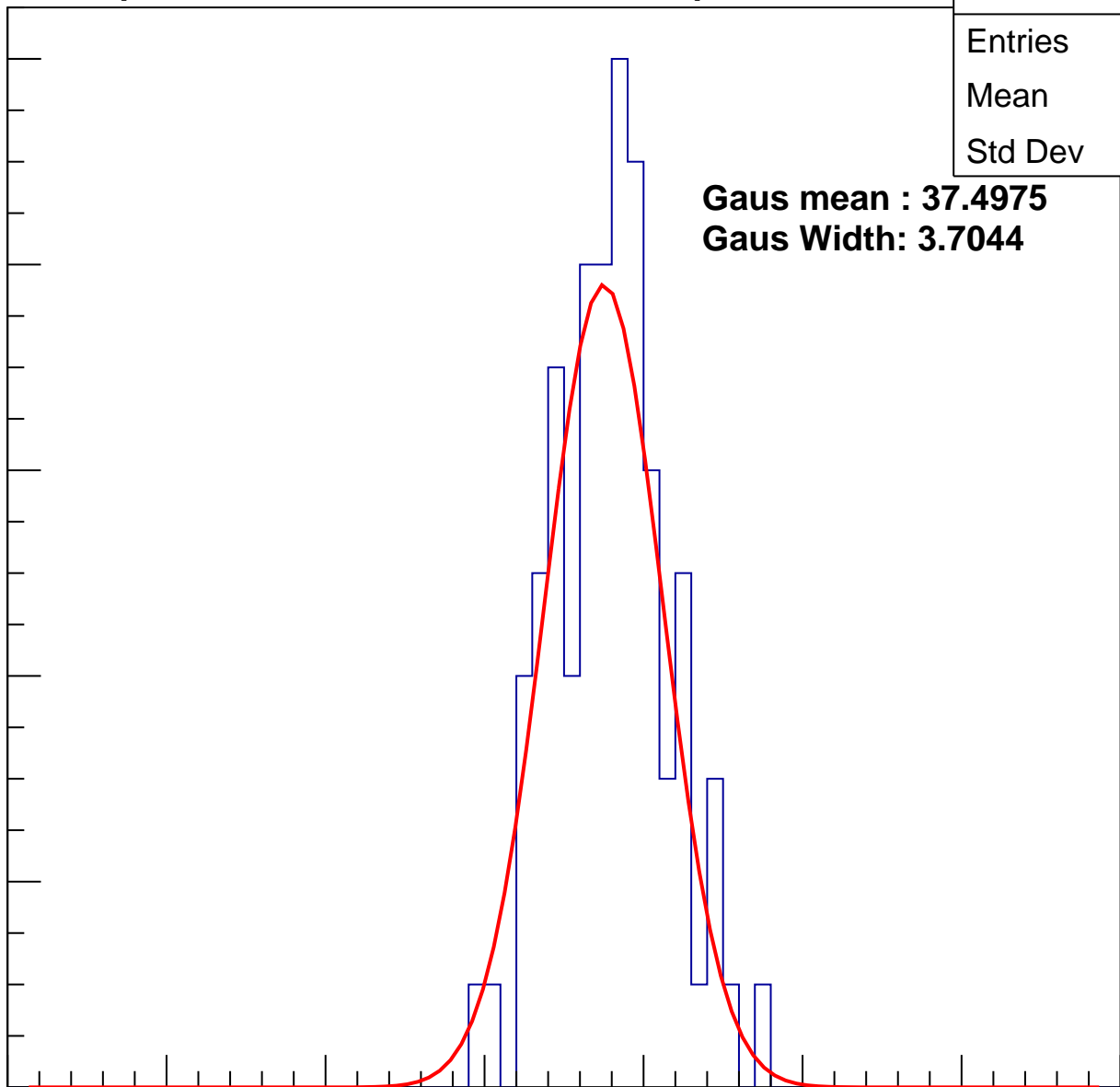
Gaus Width: 3.7044

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch92, adc2

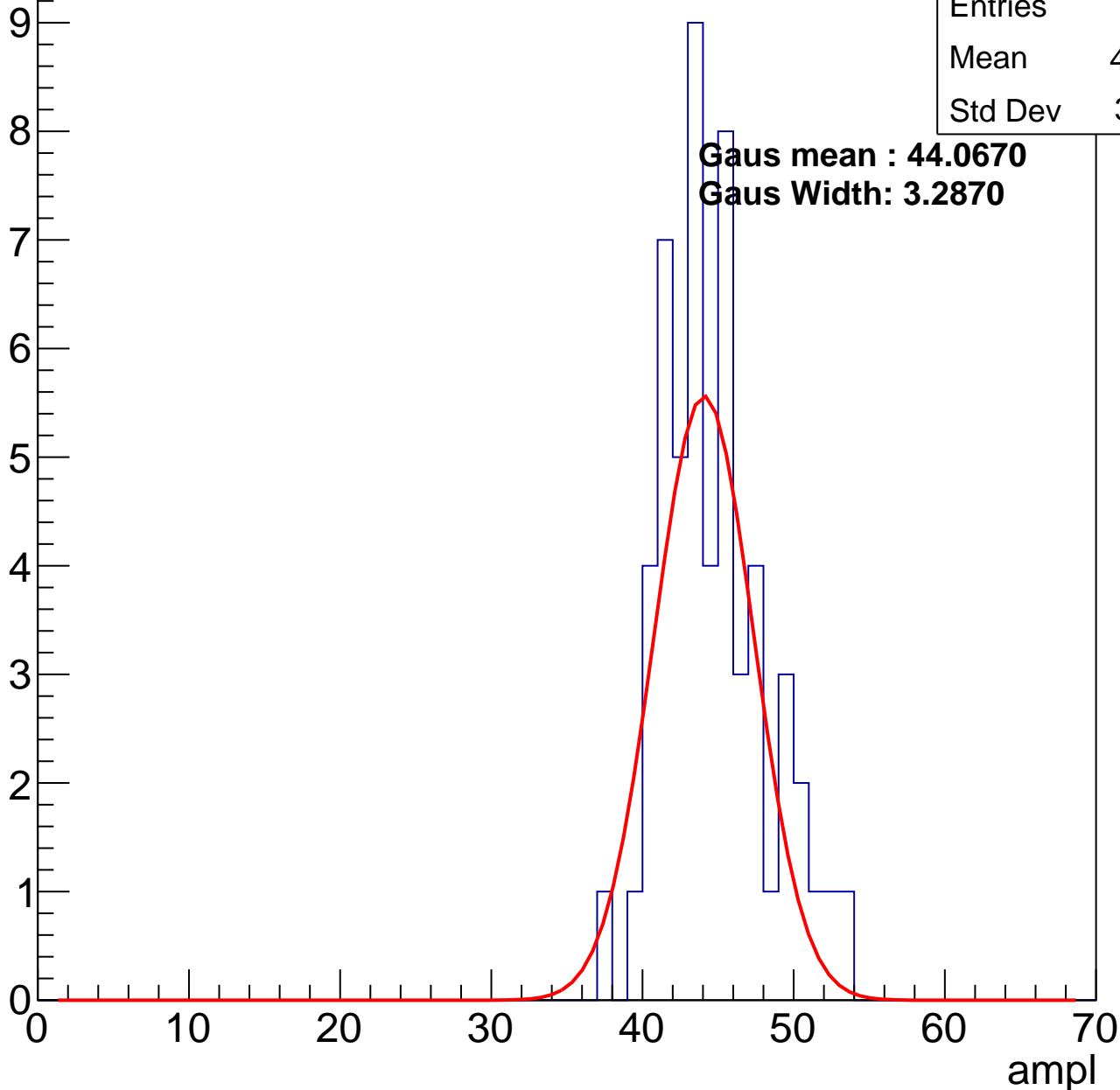
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	44.24
Std Dev	3.411

Gaus mean : 44.0670

Gaus Width: 3.2870

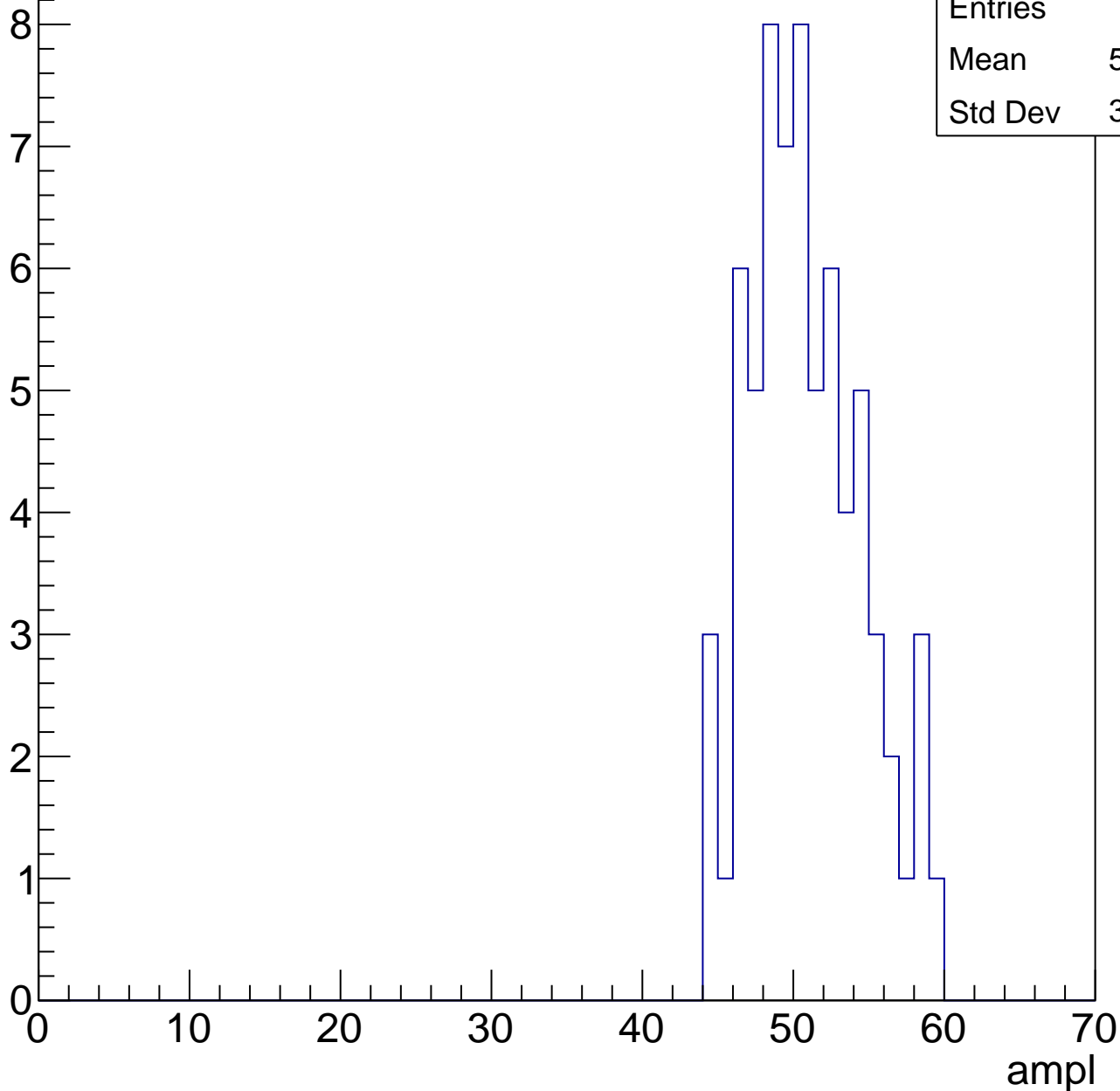


B1L102S, U11-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.46
Std Dev	3.664

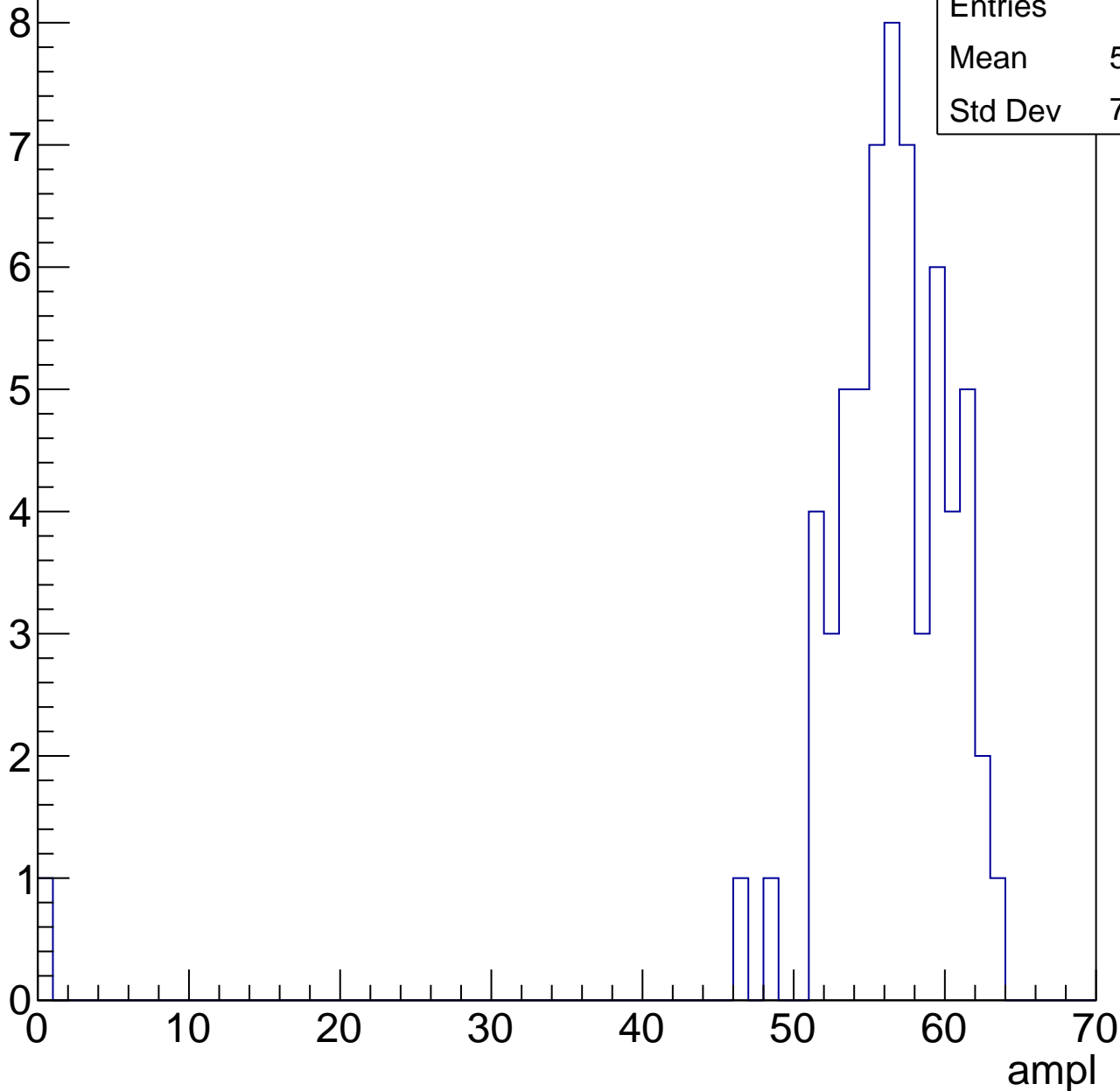


B1L102S, U11-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

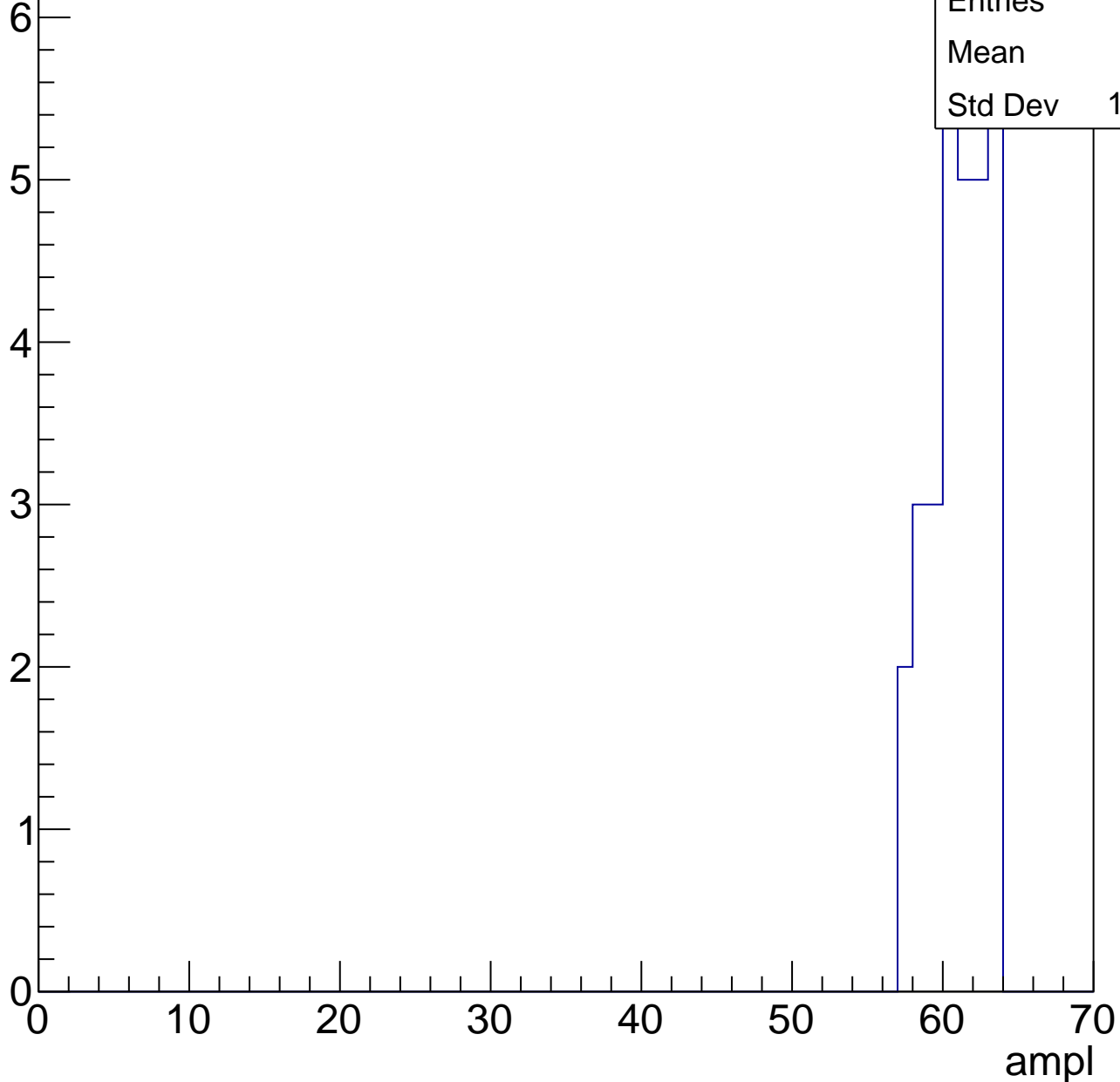
Entries	63
Mean	55.25
Std Dev	7.836



B1L102S, U11-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

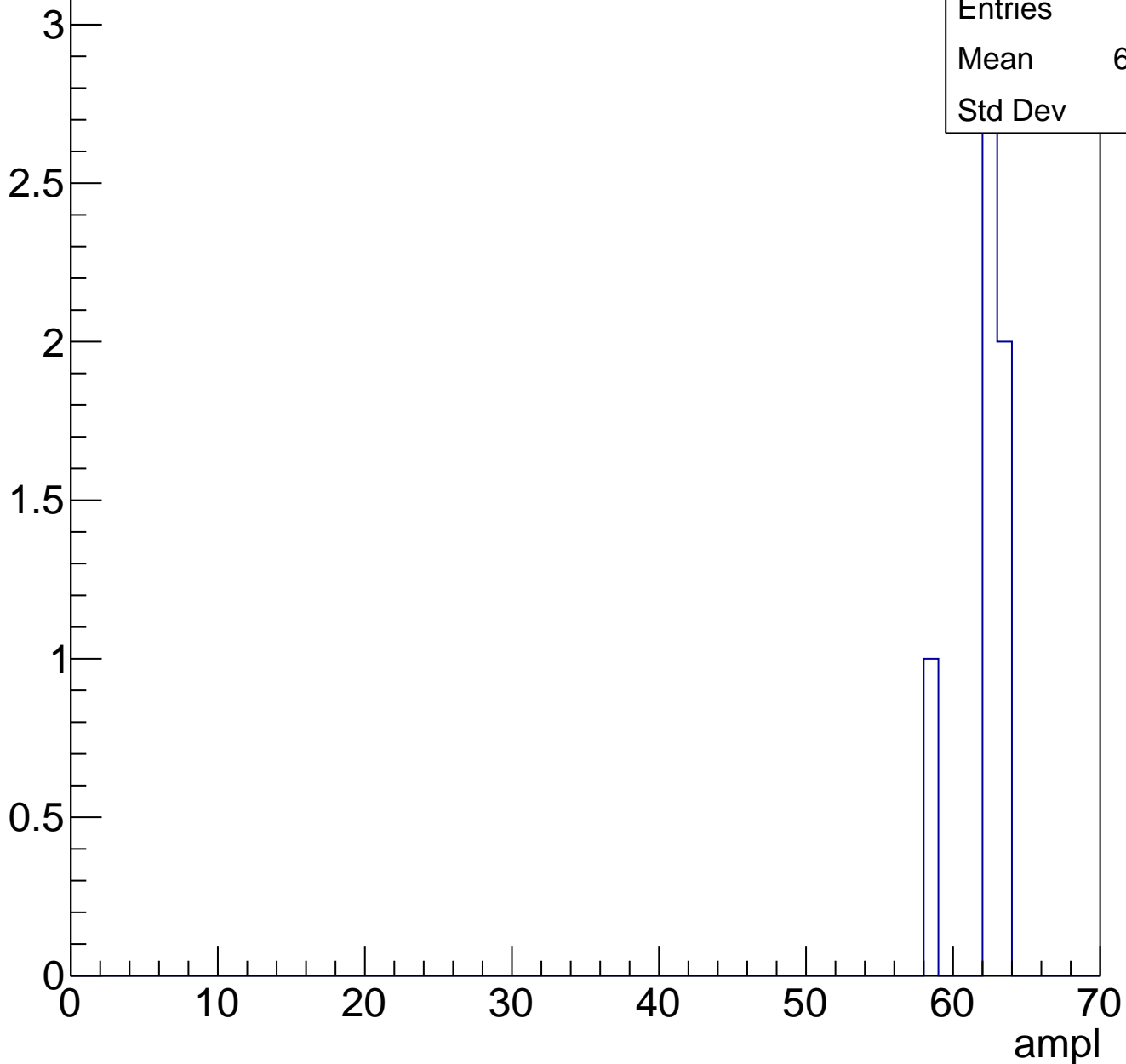


Entries	30
Mean	60.6
Std Dev	1.837

B1L102S, U11-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

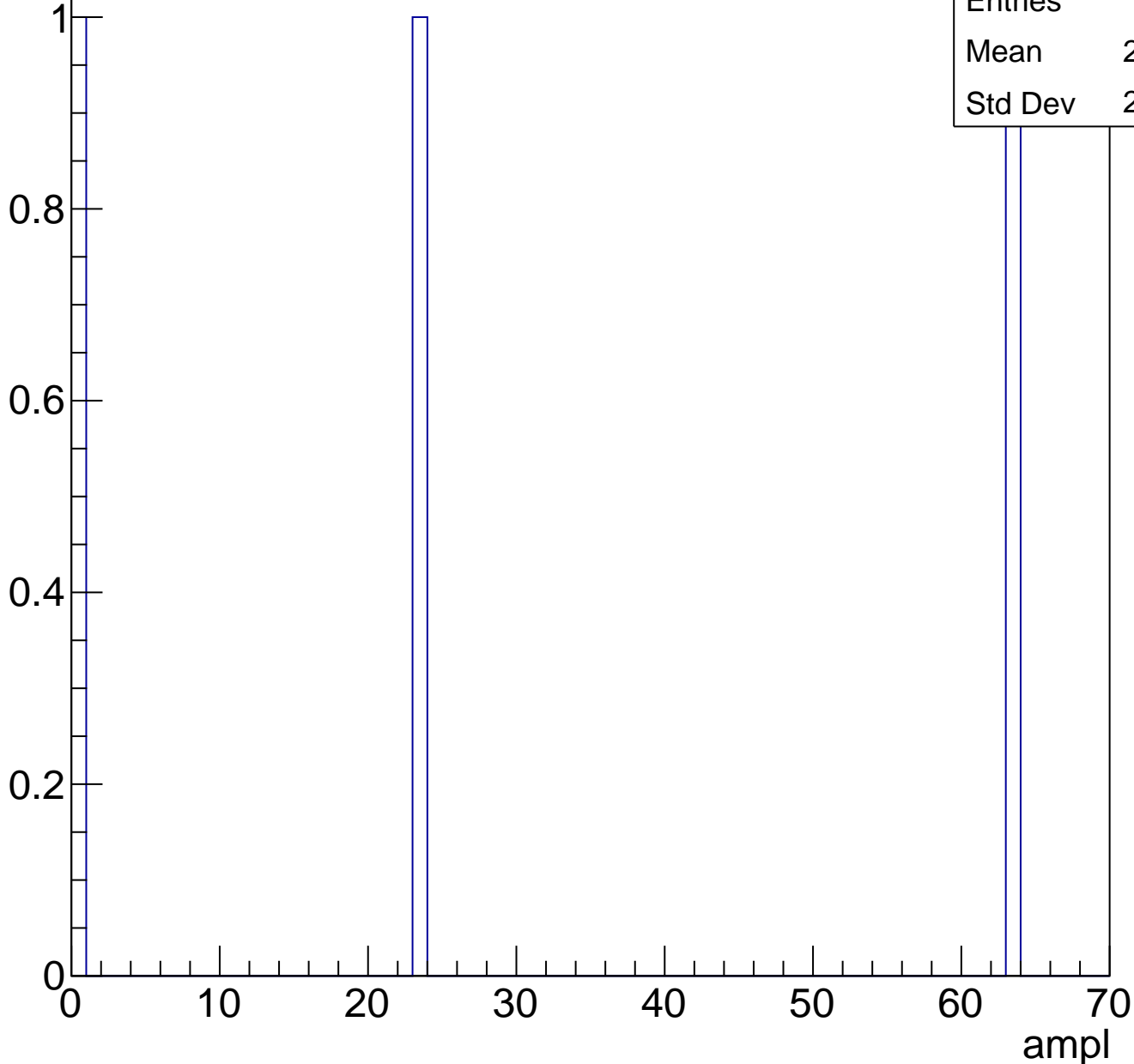
Entry



B1L102S, U11-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B1L102S, U11-ch93, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	28.54
Std Dev	5.663

Gaus mean : 29.8092

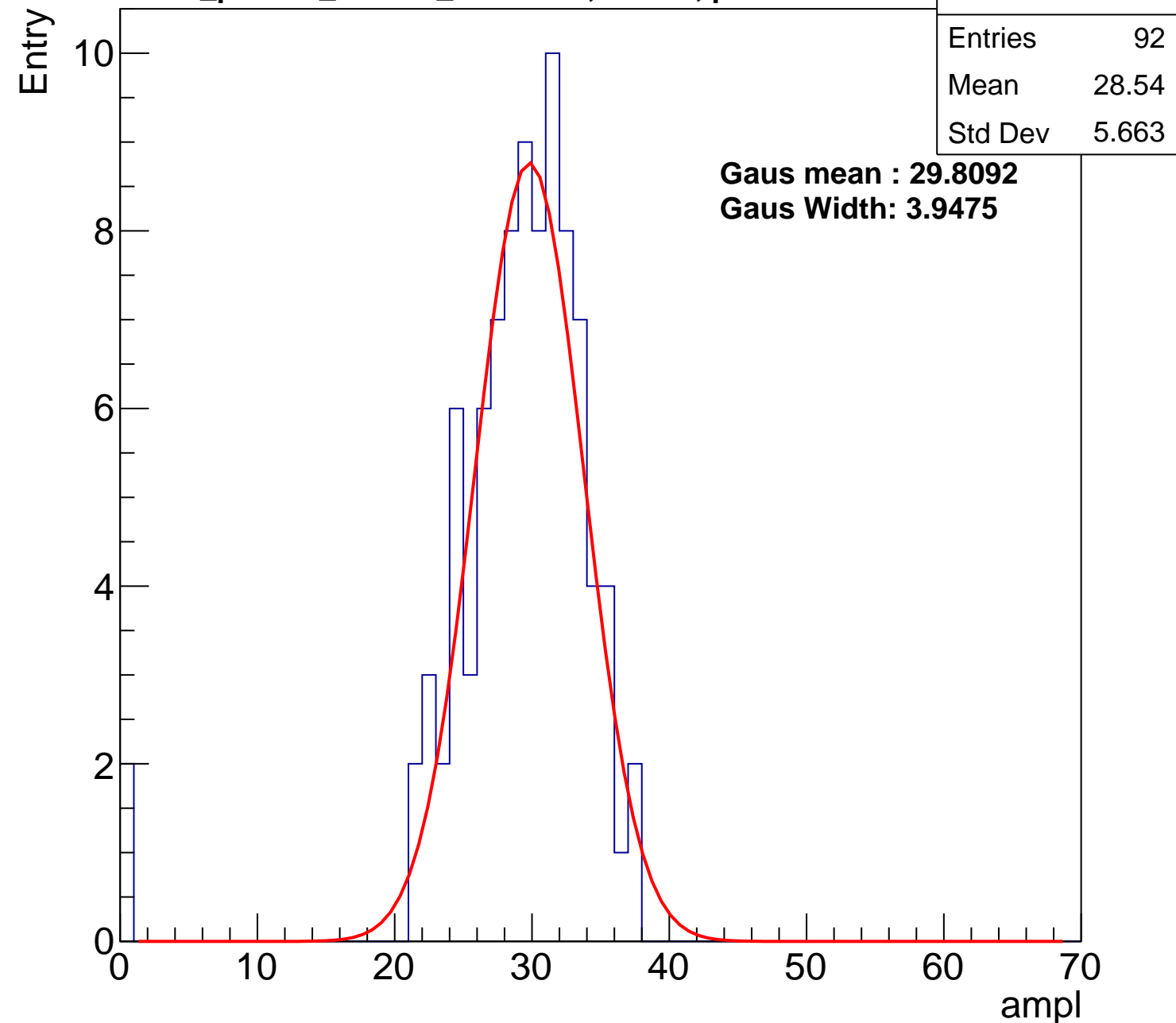
Gaus Width: 3.9475

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch93, adc1

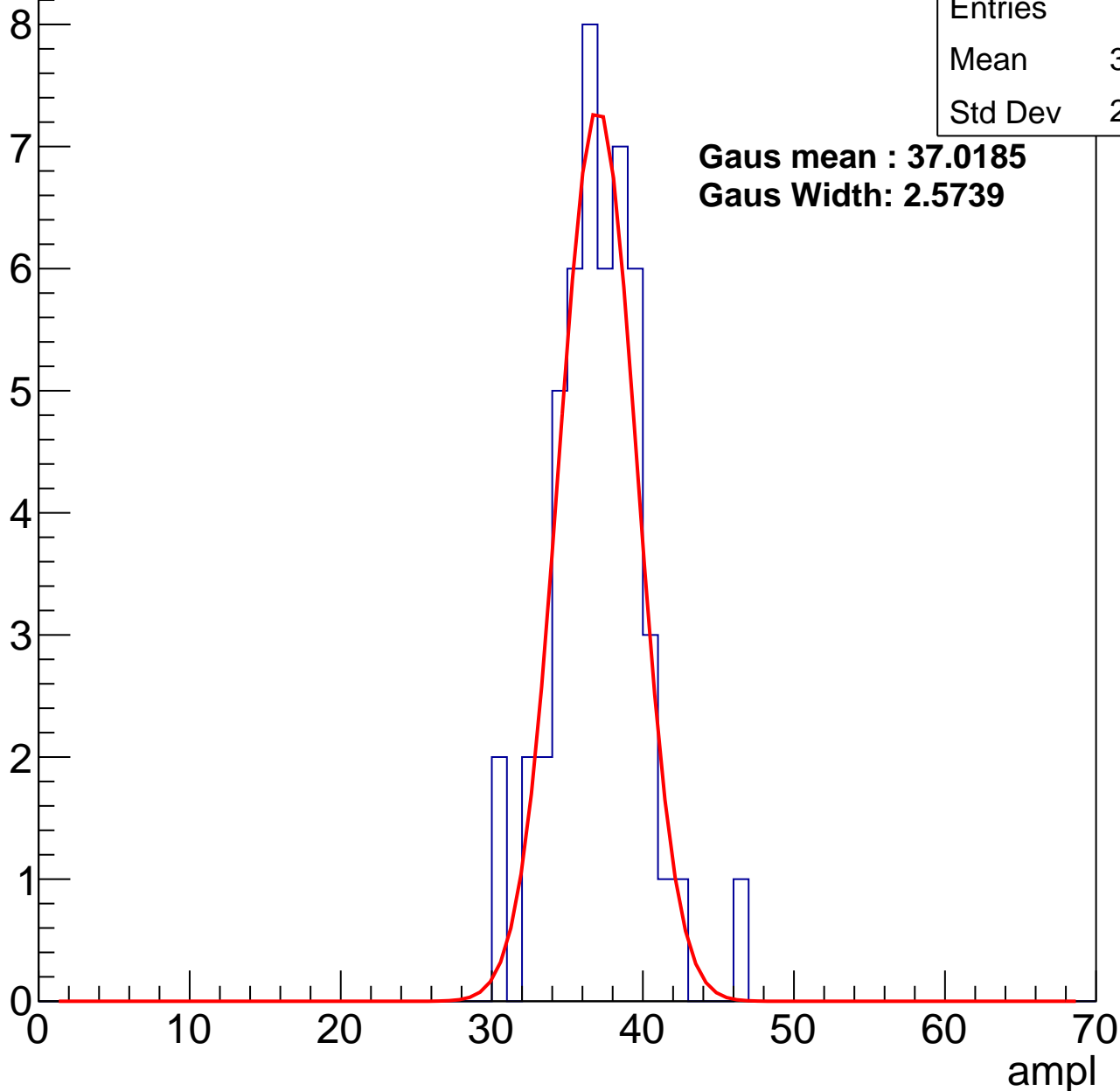
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	36.58
Std Dev	2.926

Gaus mean : 37.0185

Gaus Width: 2.5739



B1L102S, U11-ch93, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	42.61
Std Dev	3.573

Gaus mean : 42.9709

Gaus Width: 2.9901

Entry

10

8

6

4

2

0

0

10

20

30

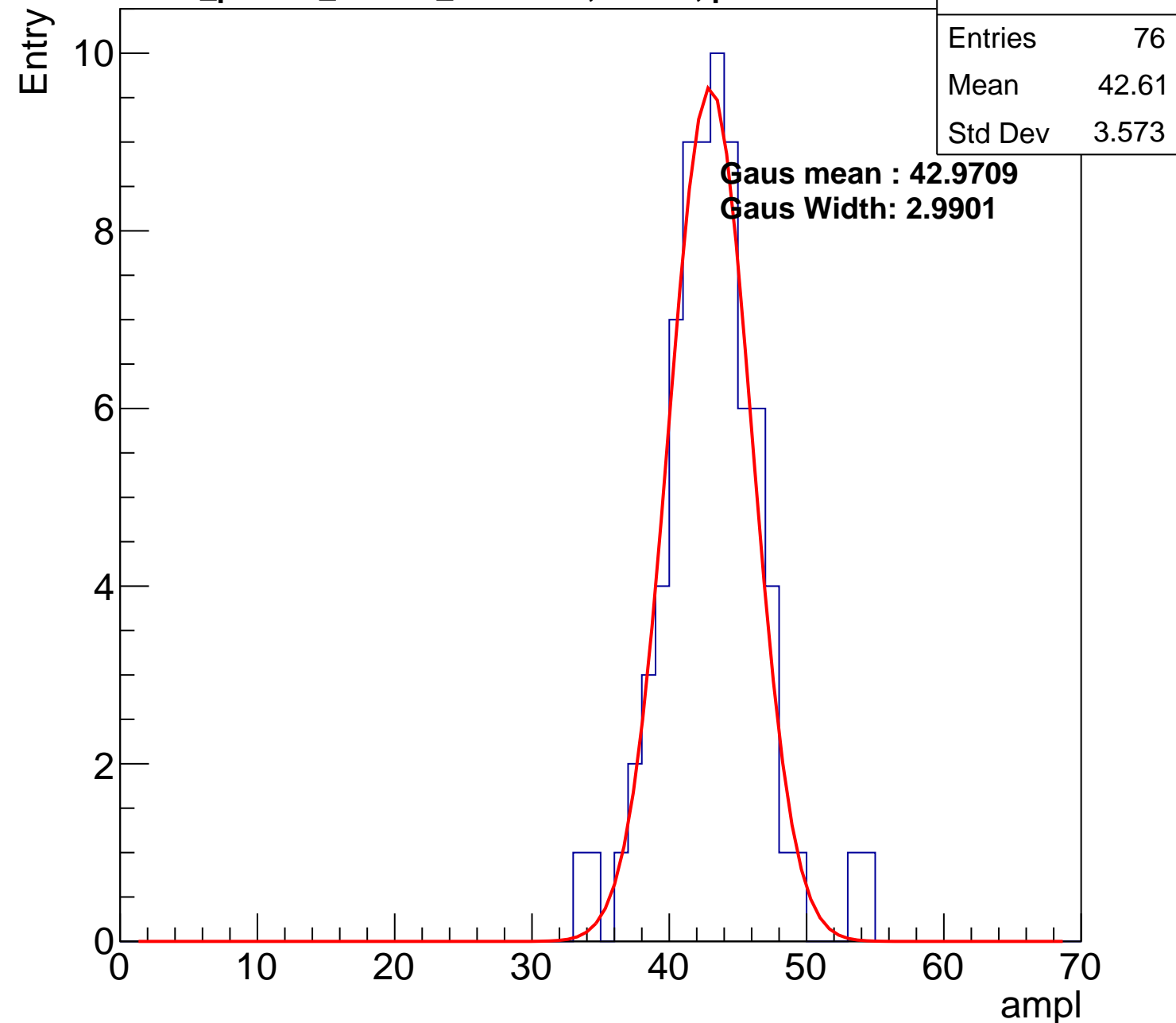
40

50

60

70

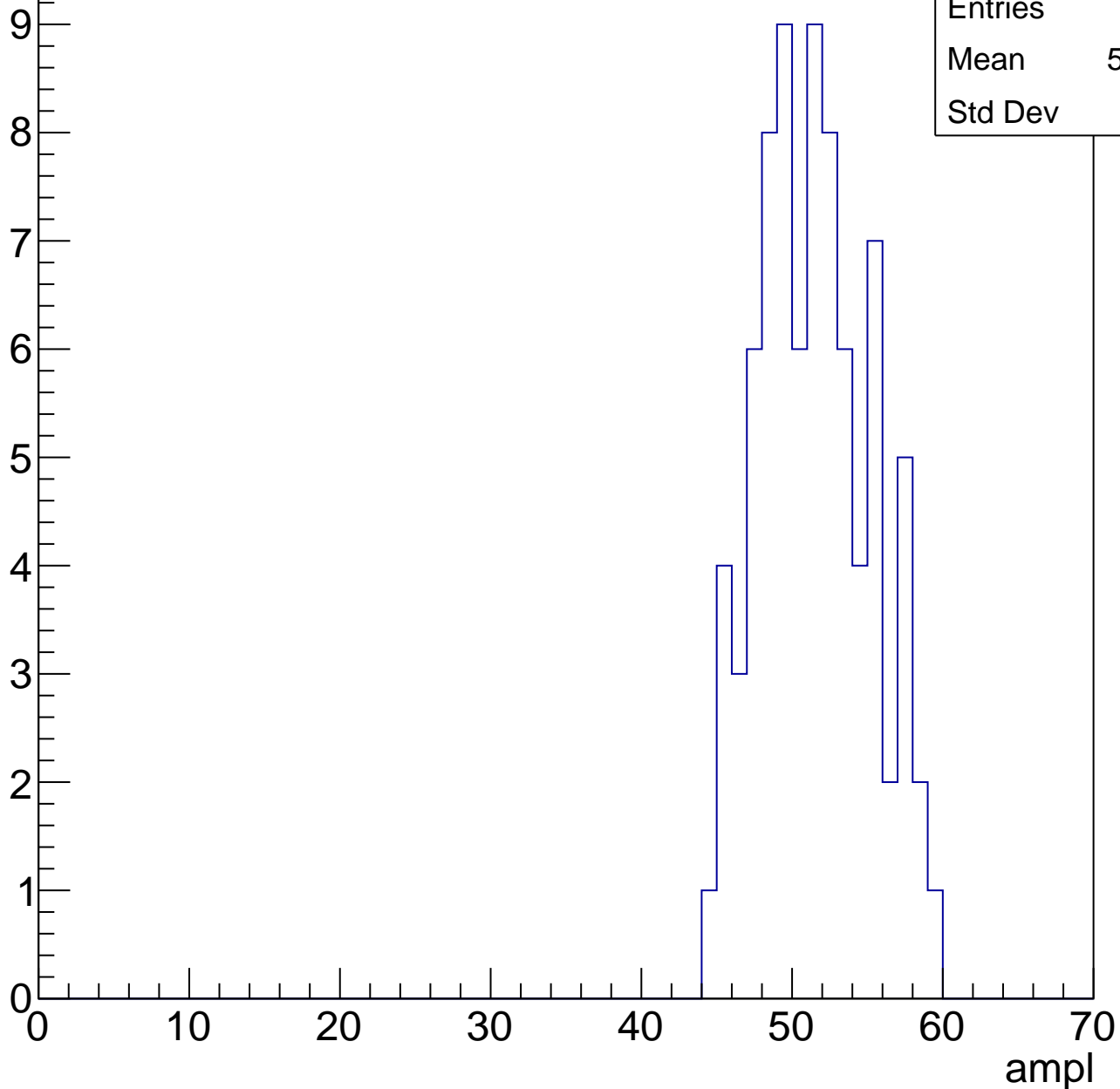
ampl



B1L102S, U11-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

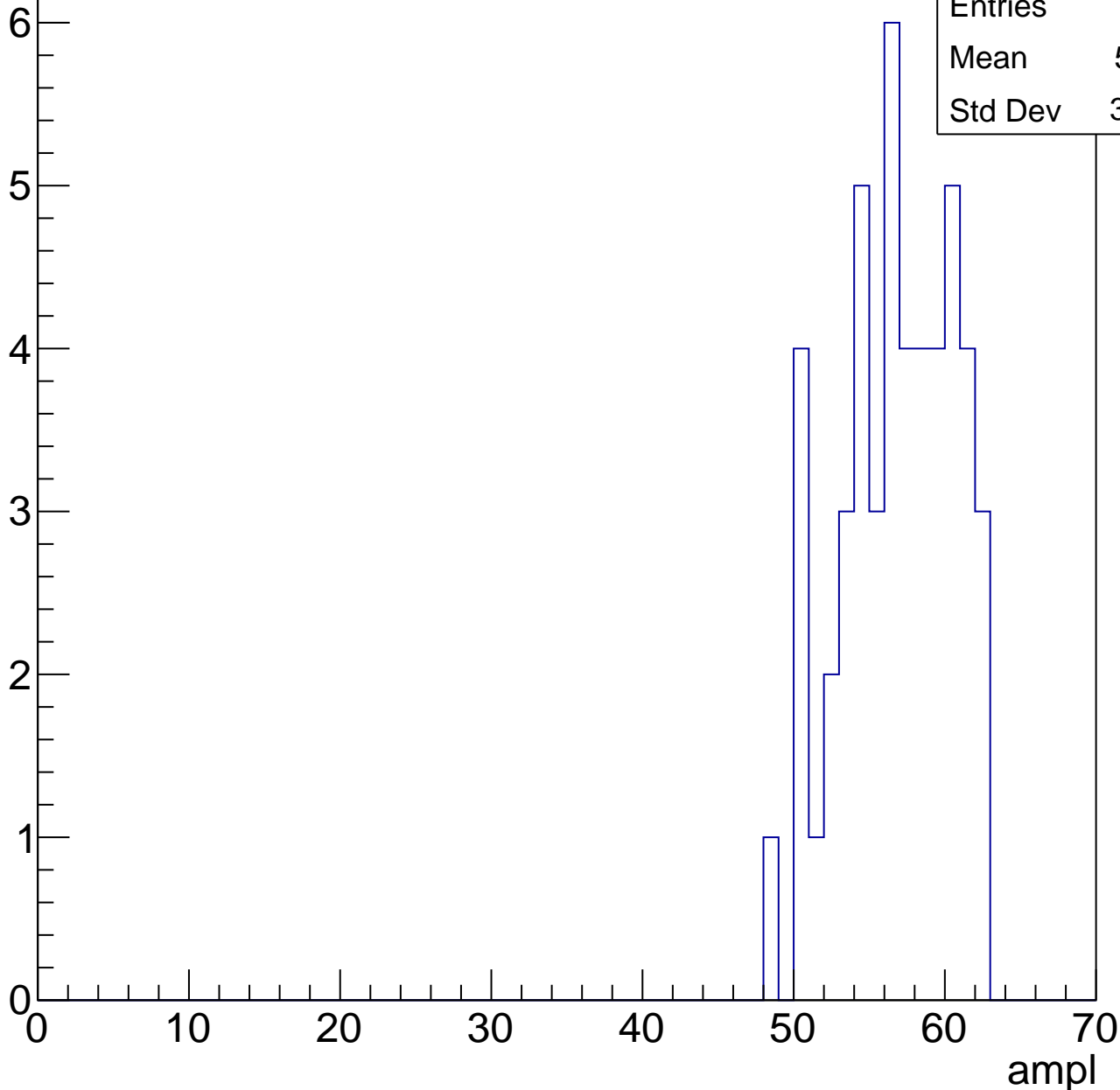


B1L102S, U11-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.31
Std Dev	3.654

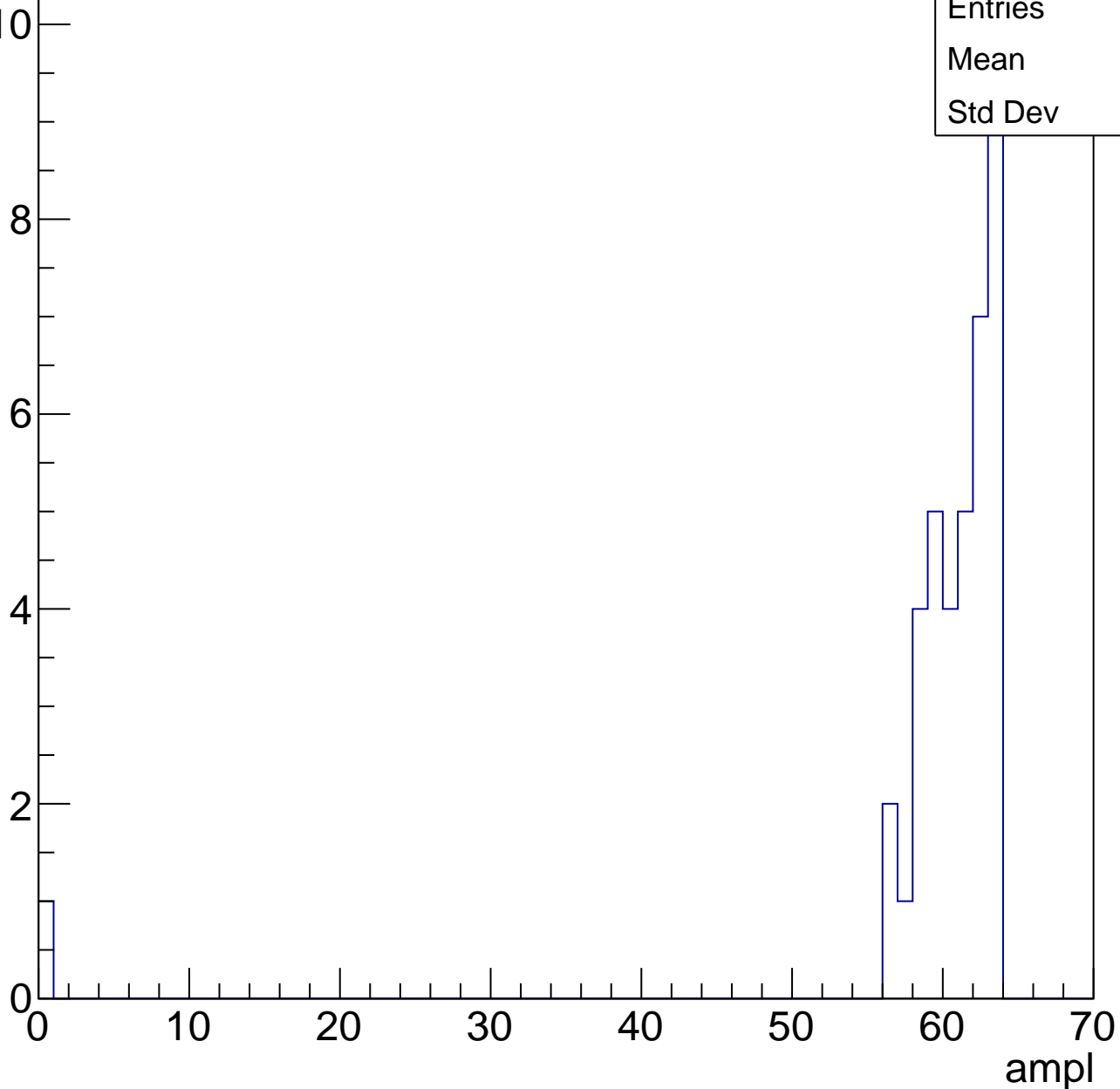


B1L102S, U11-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.1
Std Dev	9.81



B1L102S, U11-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

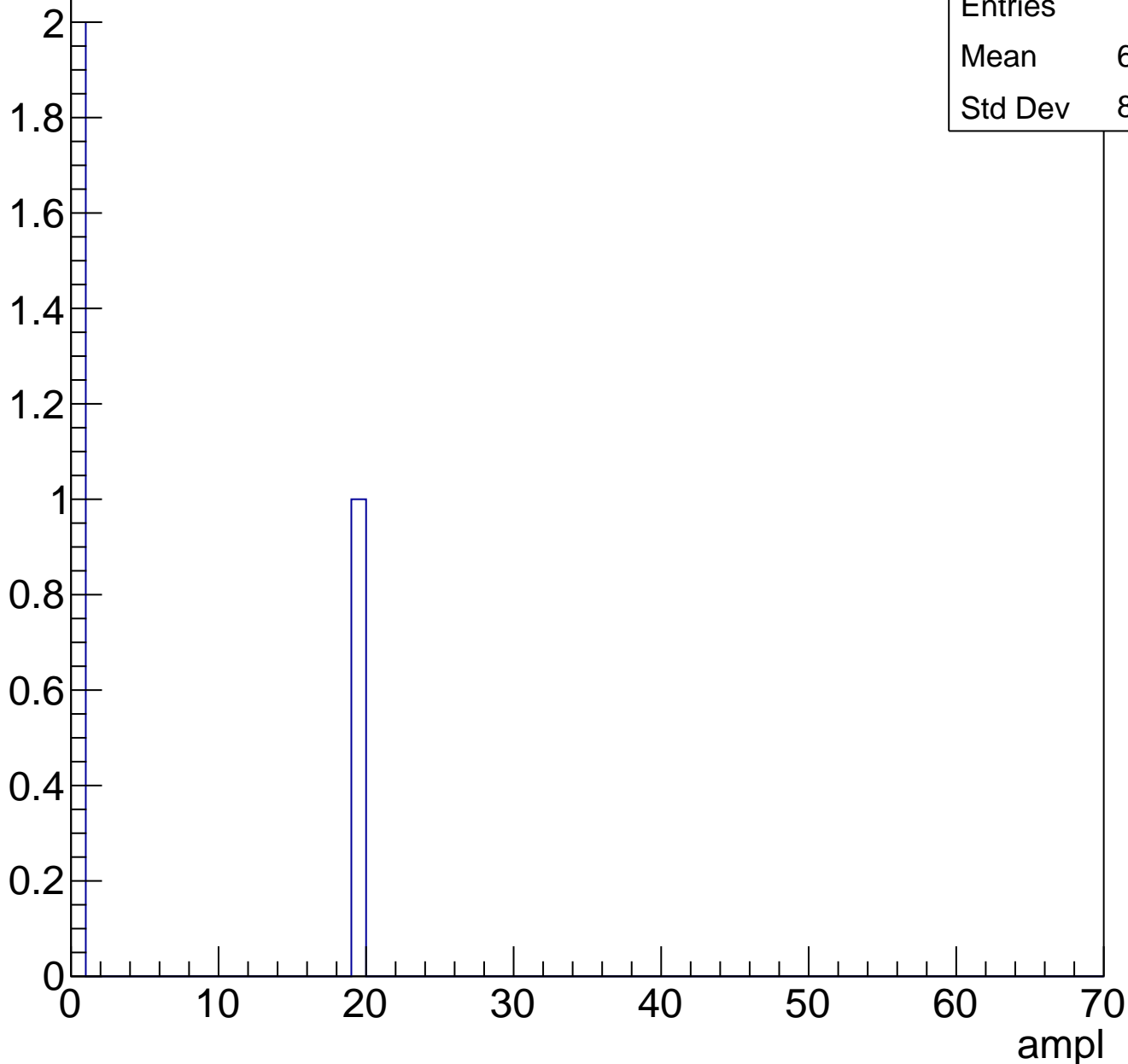
Entry



B1L102S, U11-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L102S, U11-ch94, adc0

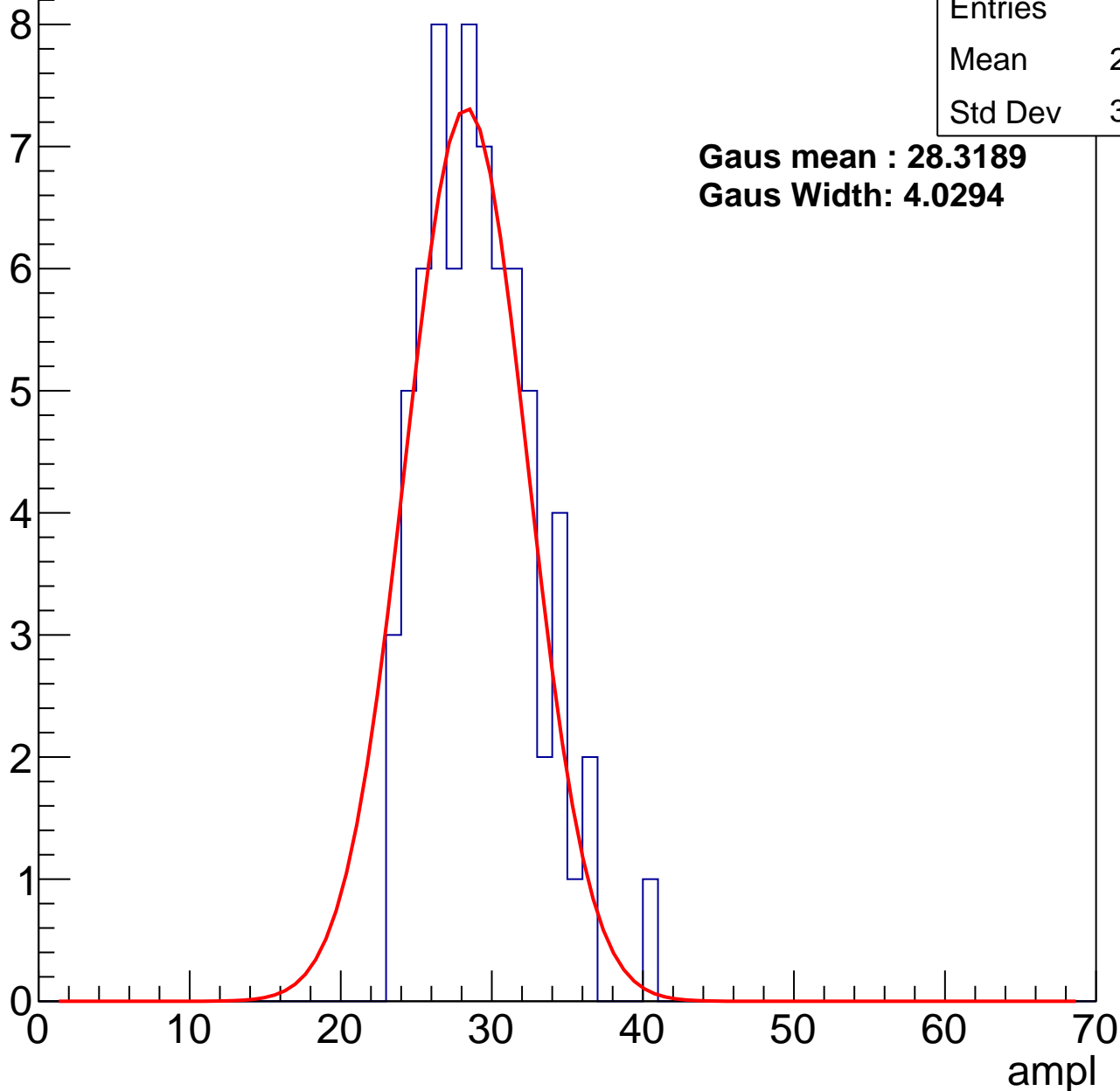
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.73
Std Dev	3.565

Gaus mean : 28.3189

Gaus Width: 4.0294



B1L102S, U11-ch94, adc1

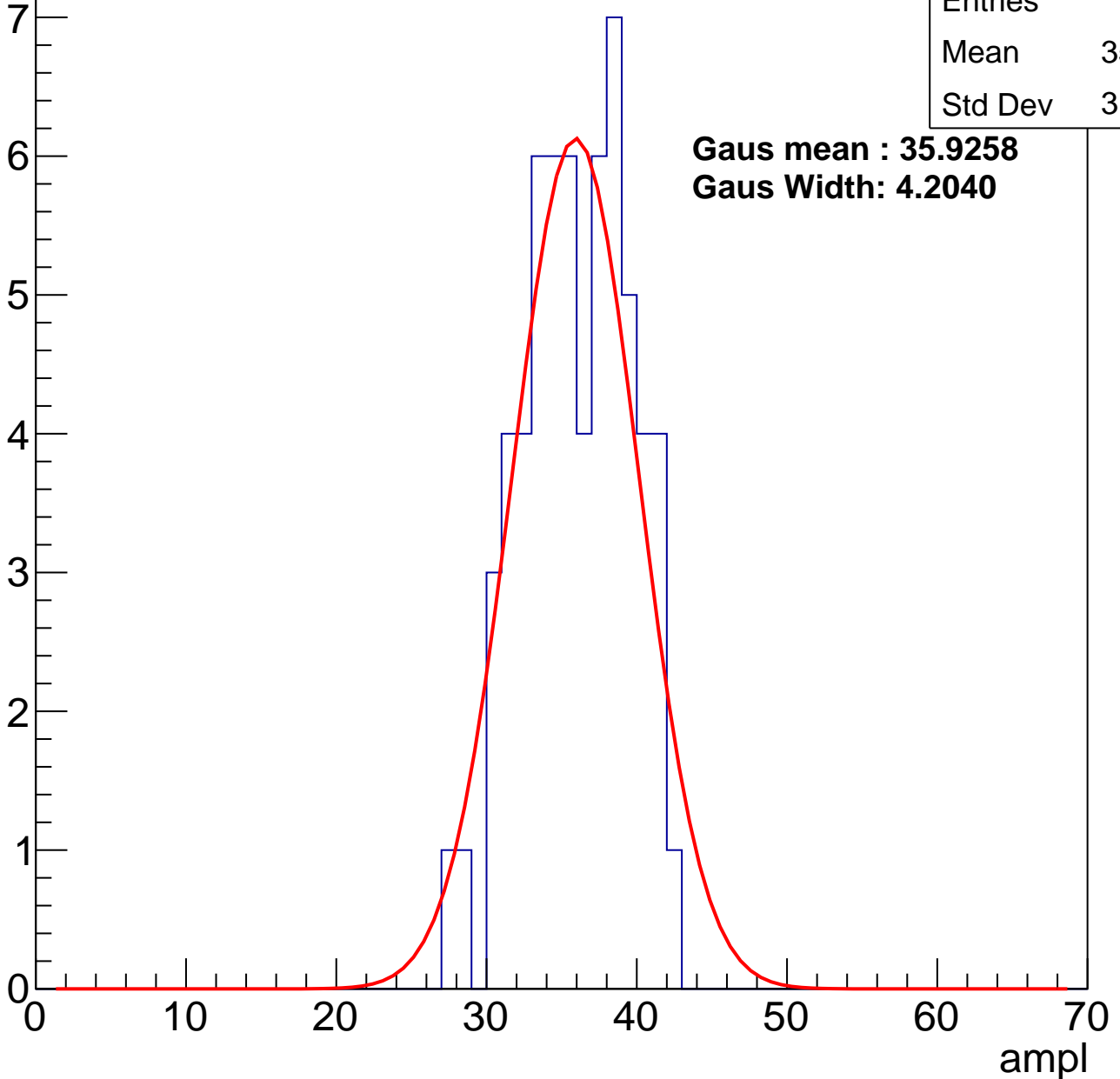
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.52
Std Dev	3.514

Gaus mean : 35.9258

Gaus Width: 4.2040



B1L102S, U11-ch94, adc2

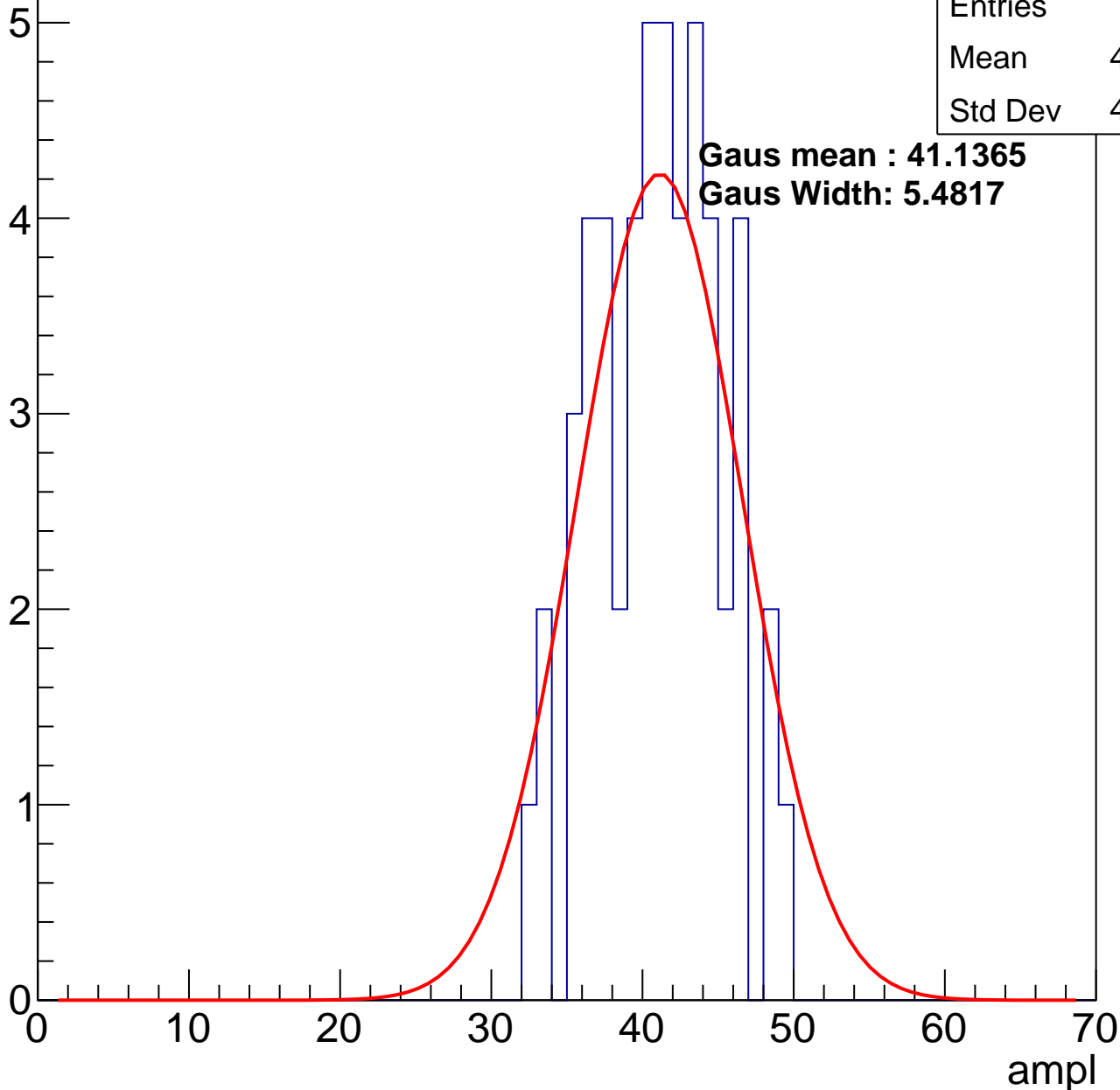
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	40.58
Std Dev	4.078

Gaus mean : 41.1365

Gaus Width: 5.4817

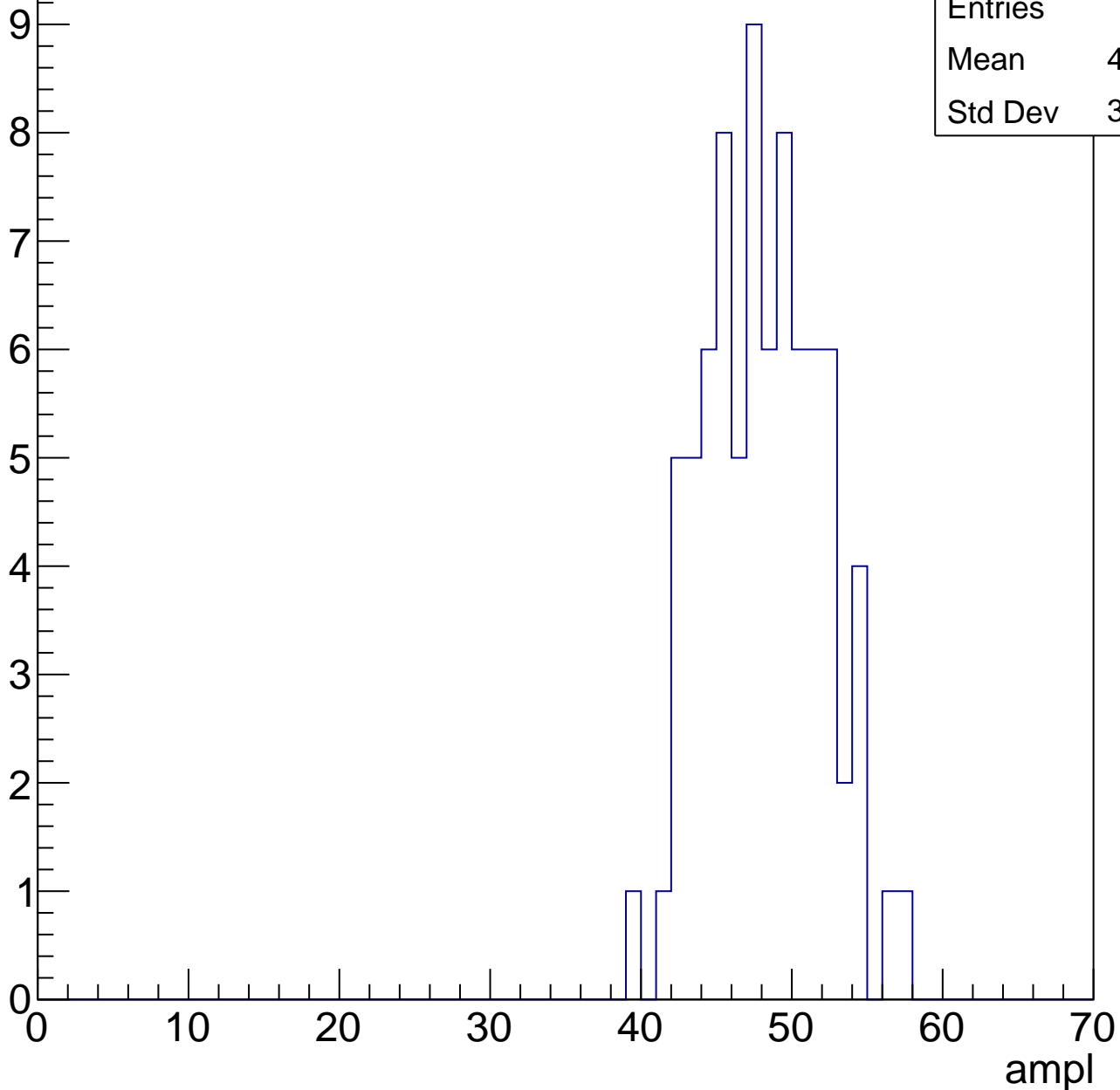


B1L102S, U11-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	47.69
Std Dev	3.783

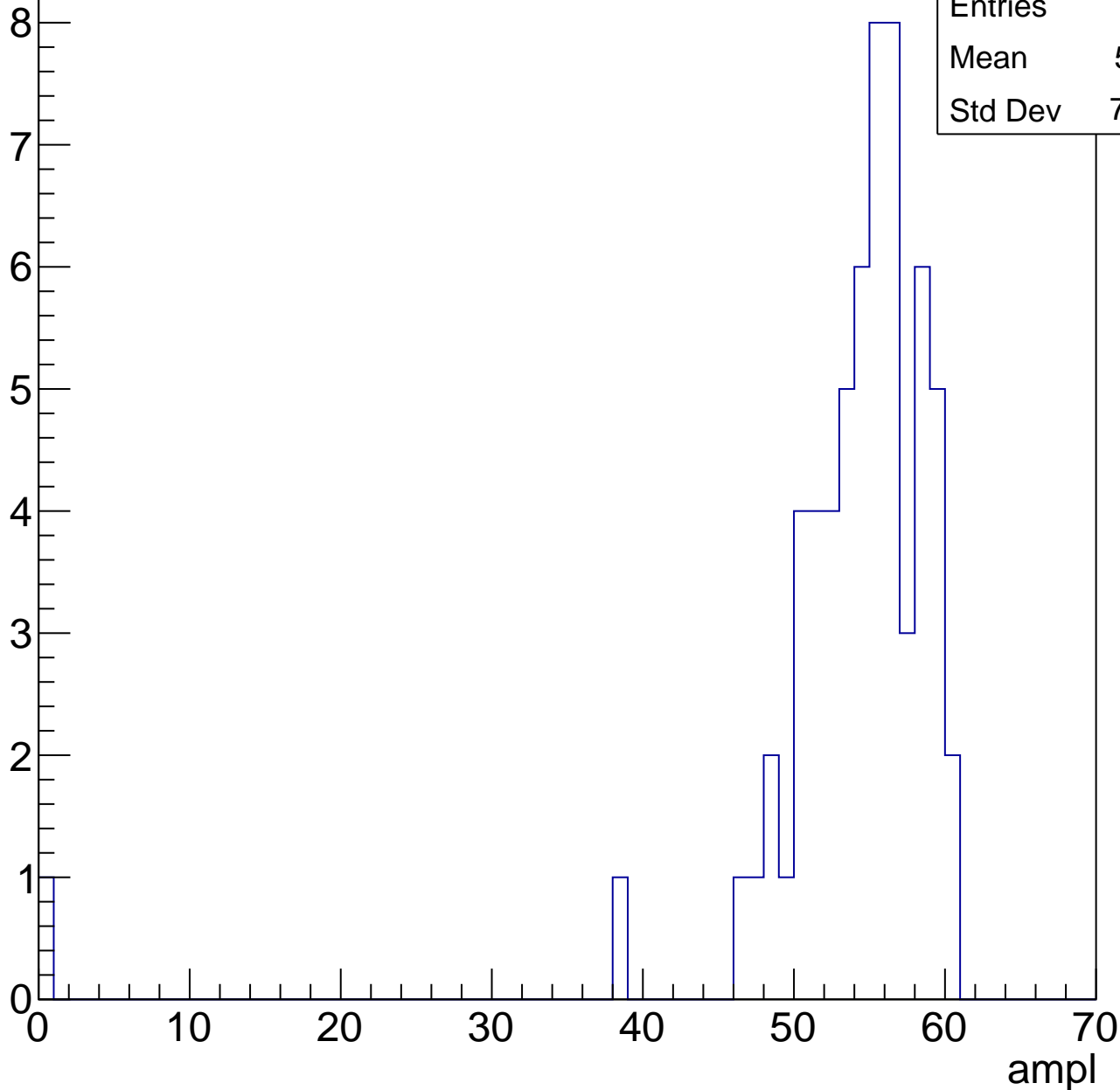


B1L102S, U11-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

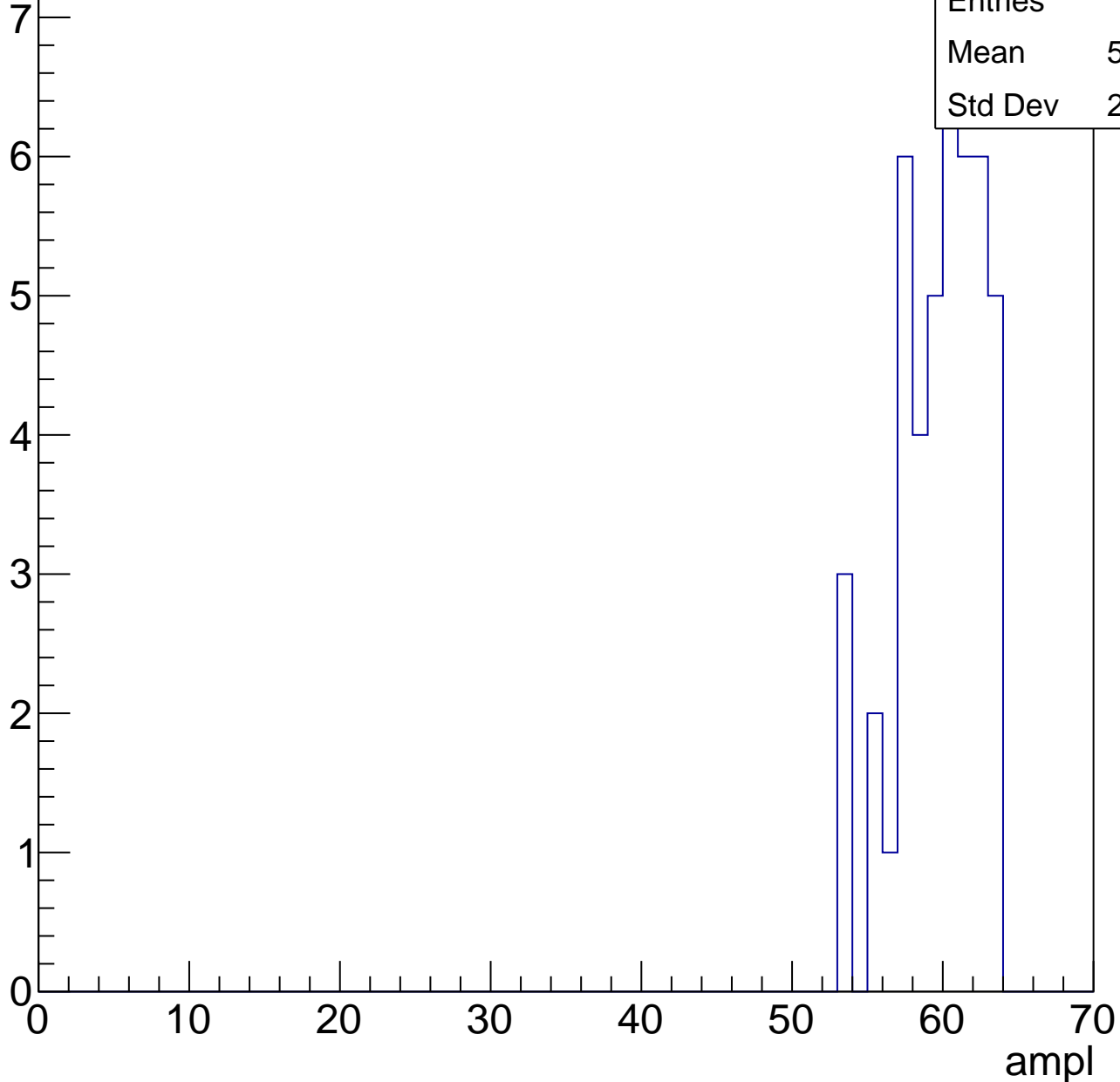
Entries	62
Mean	53.21
Std Dev	7.854



B1L102S, U11-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

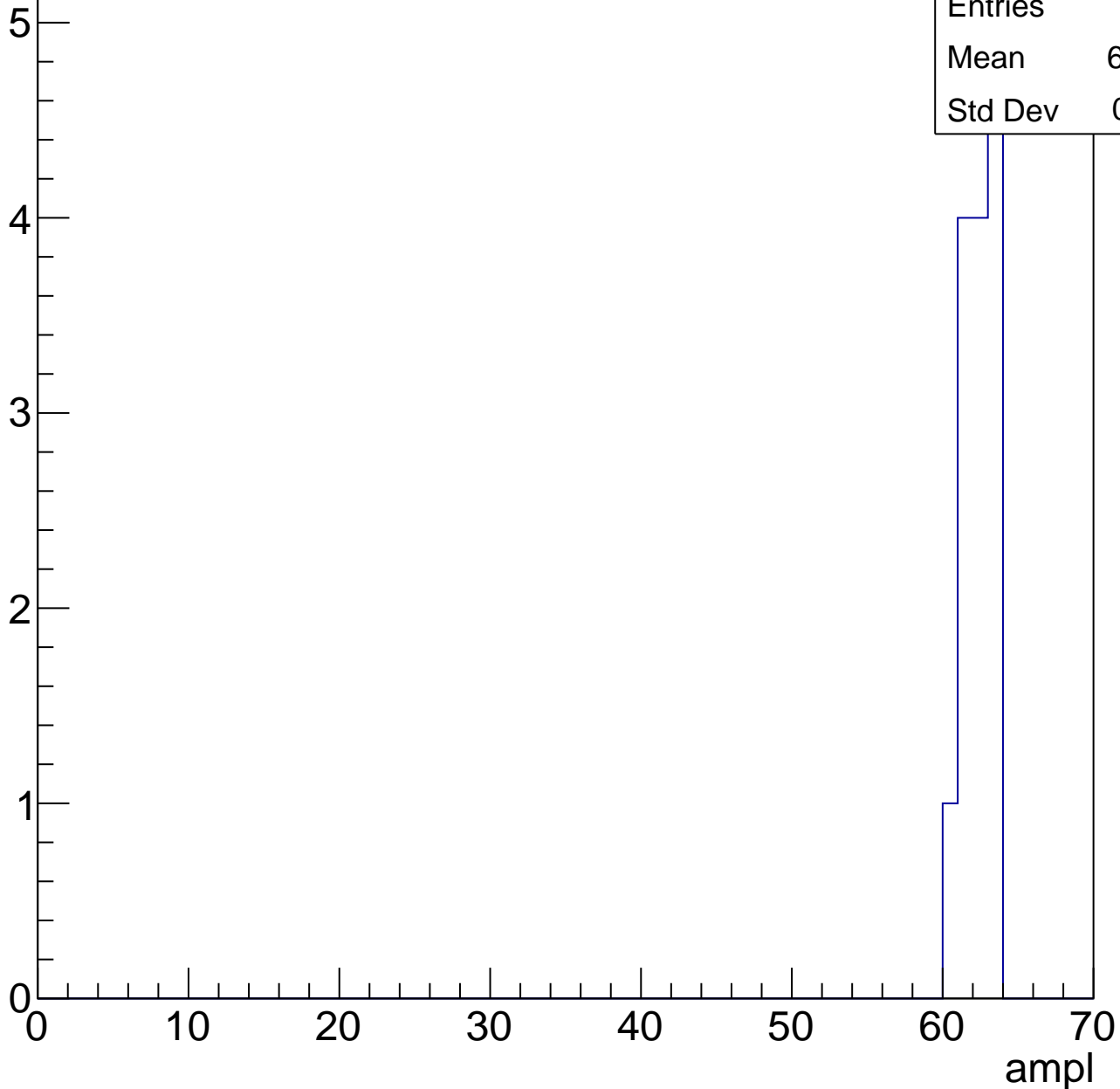


B1L102S, U11-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.93
Std Dev	0.961



B1L102S, U11-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch95, adc0

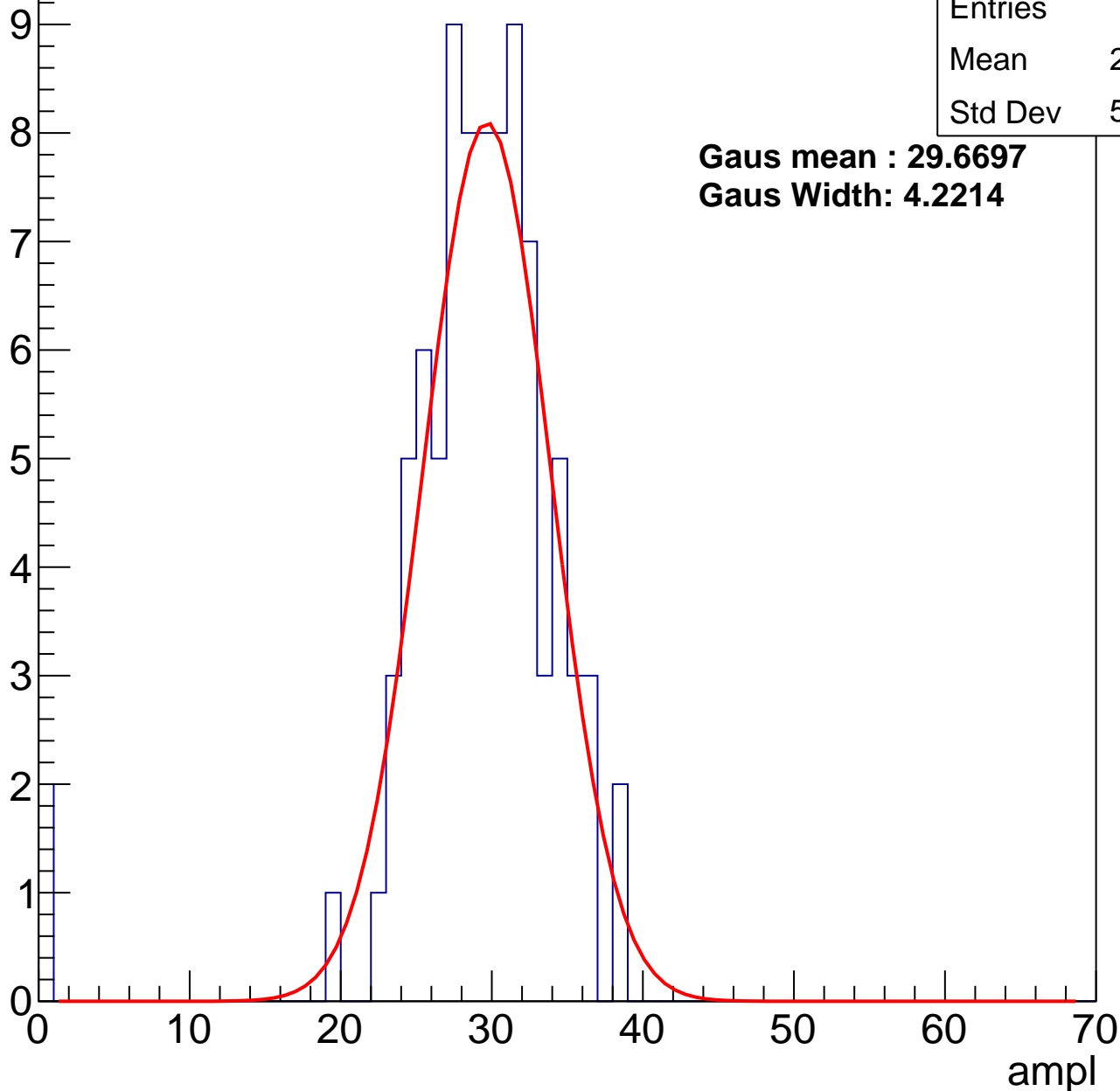
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.52
Std Dev	5.766

Gaus mean : 29.6697

Gaus Width: 4.2214



B1L102S, U11-ch95, adc1

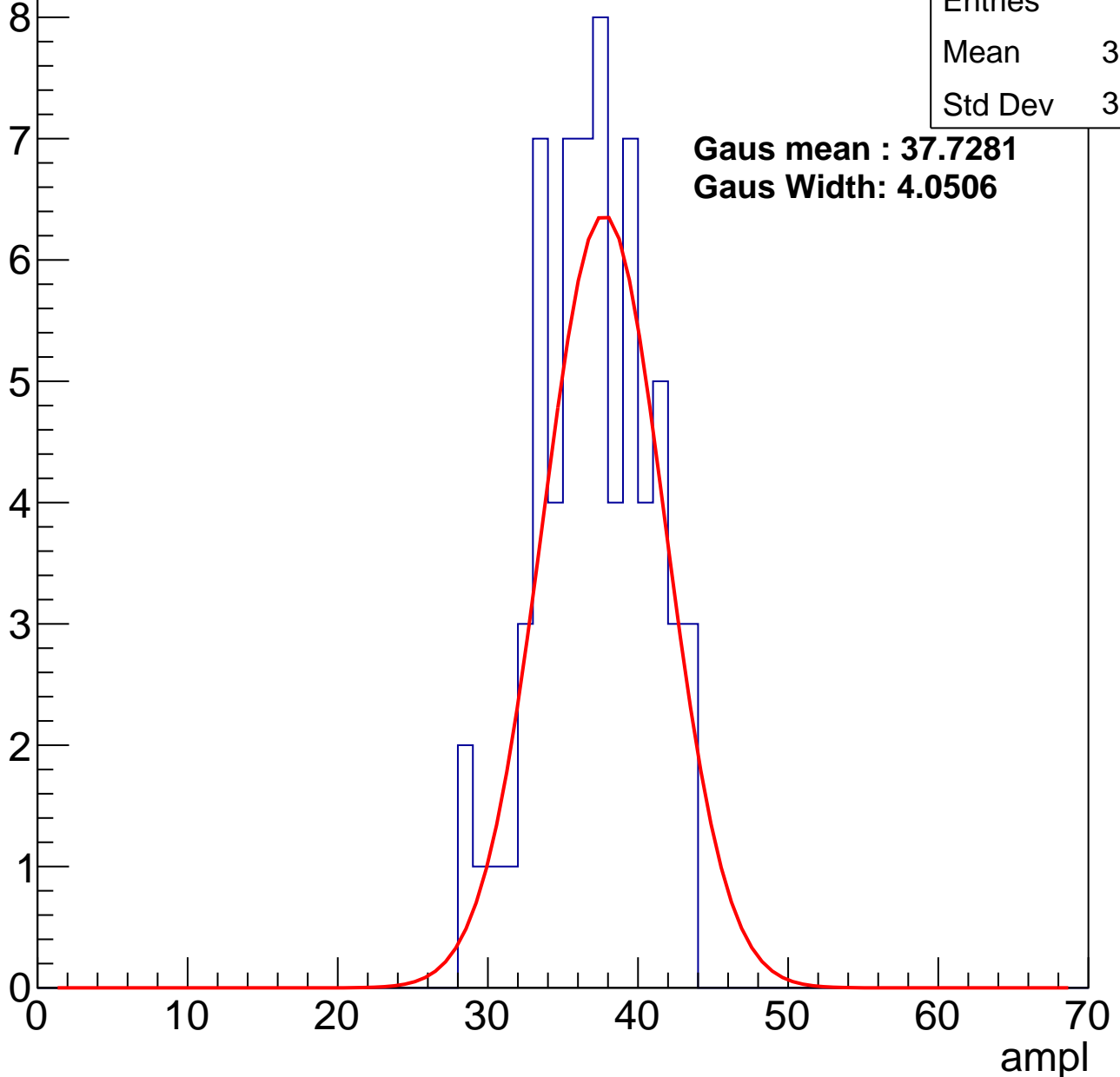
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.52
Std Dev	3.634

Gaus mean : 37.7281

Gaus Width: 4.0506



B1L102S, U11-ch95, adc2

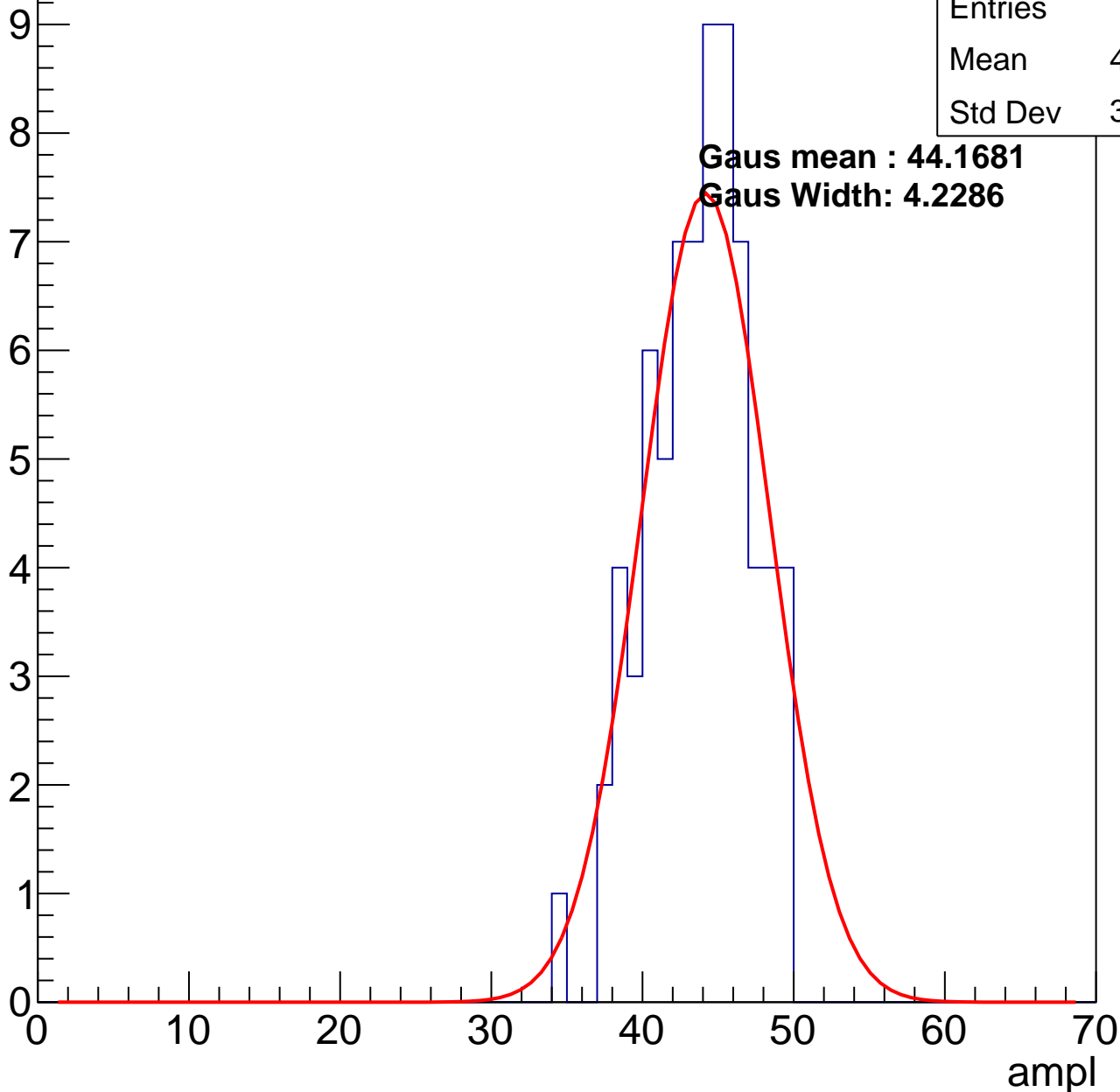
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.28
Std Dev	3.318

Gaus mean : 44.1681

Gaus Width: 4.2286

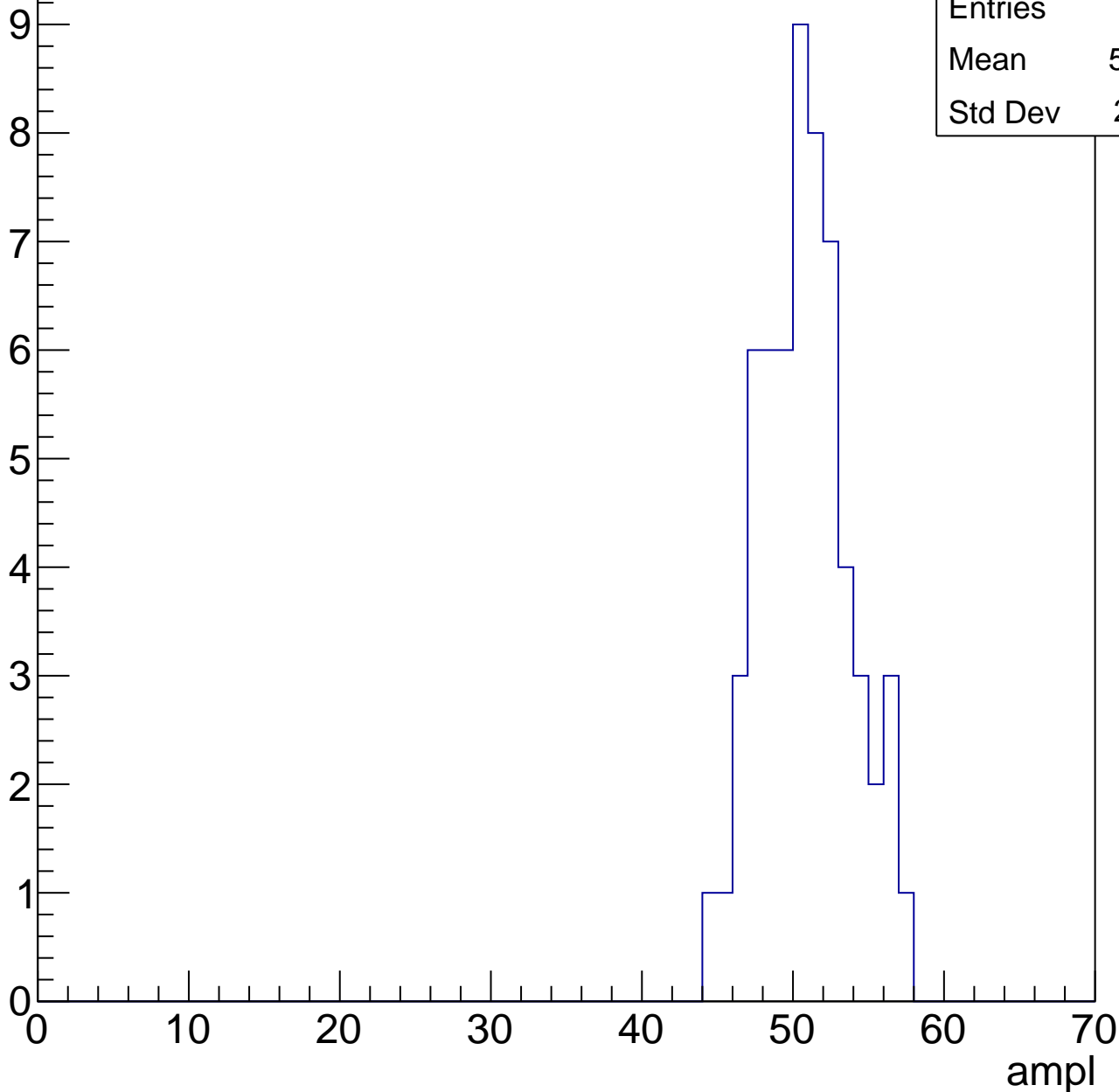


B1L102S, U11-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.37
Std Dev	2.921

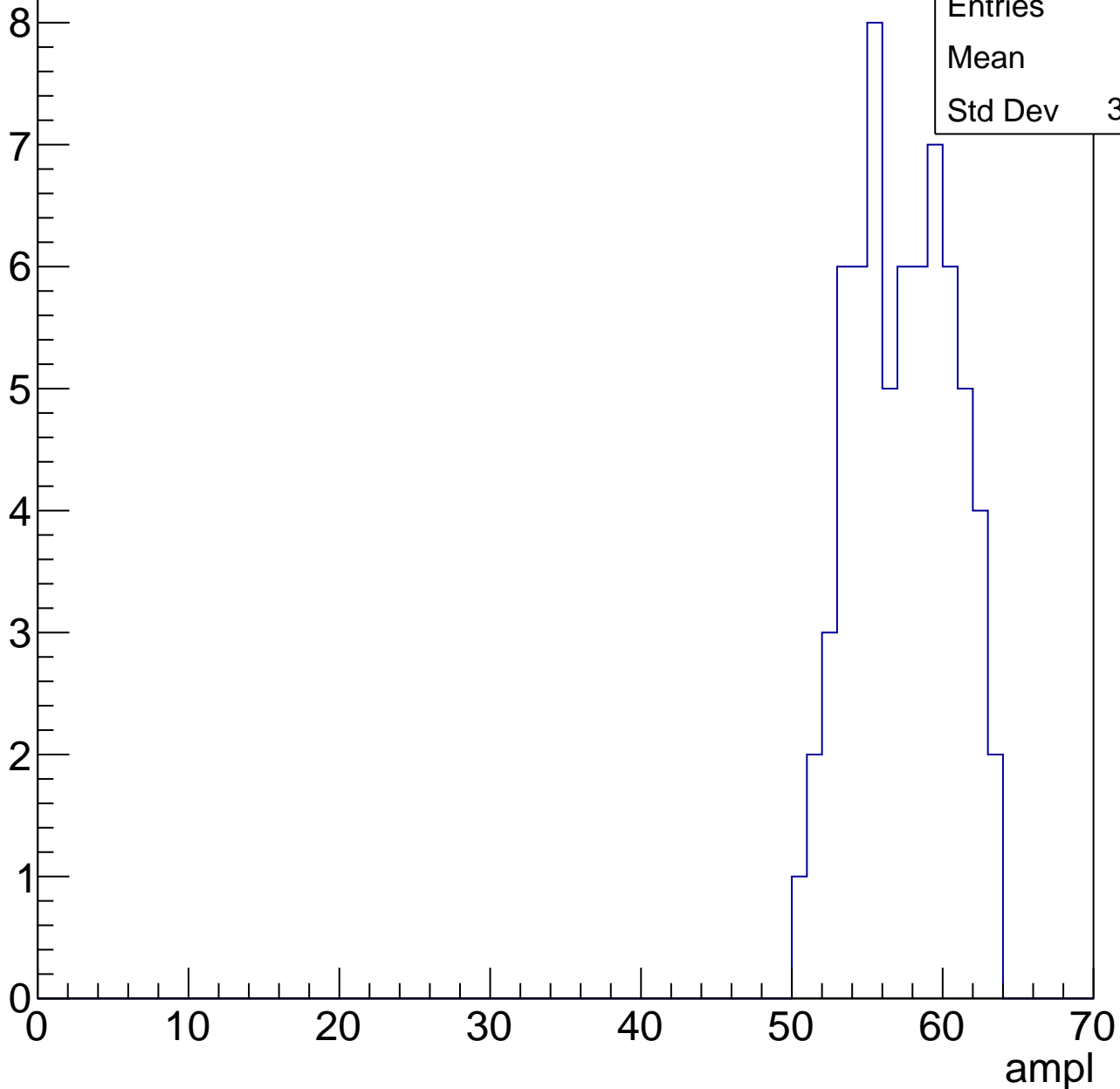


B1L102S, U11-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	56.9
Std Dev	3.283

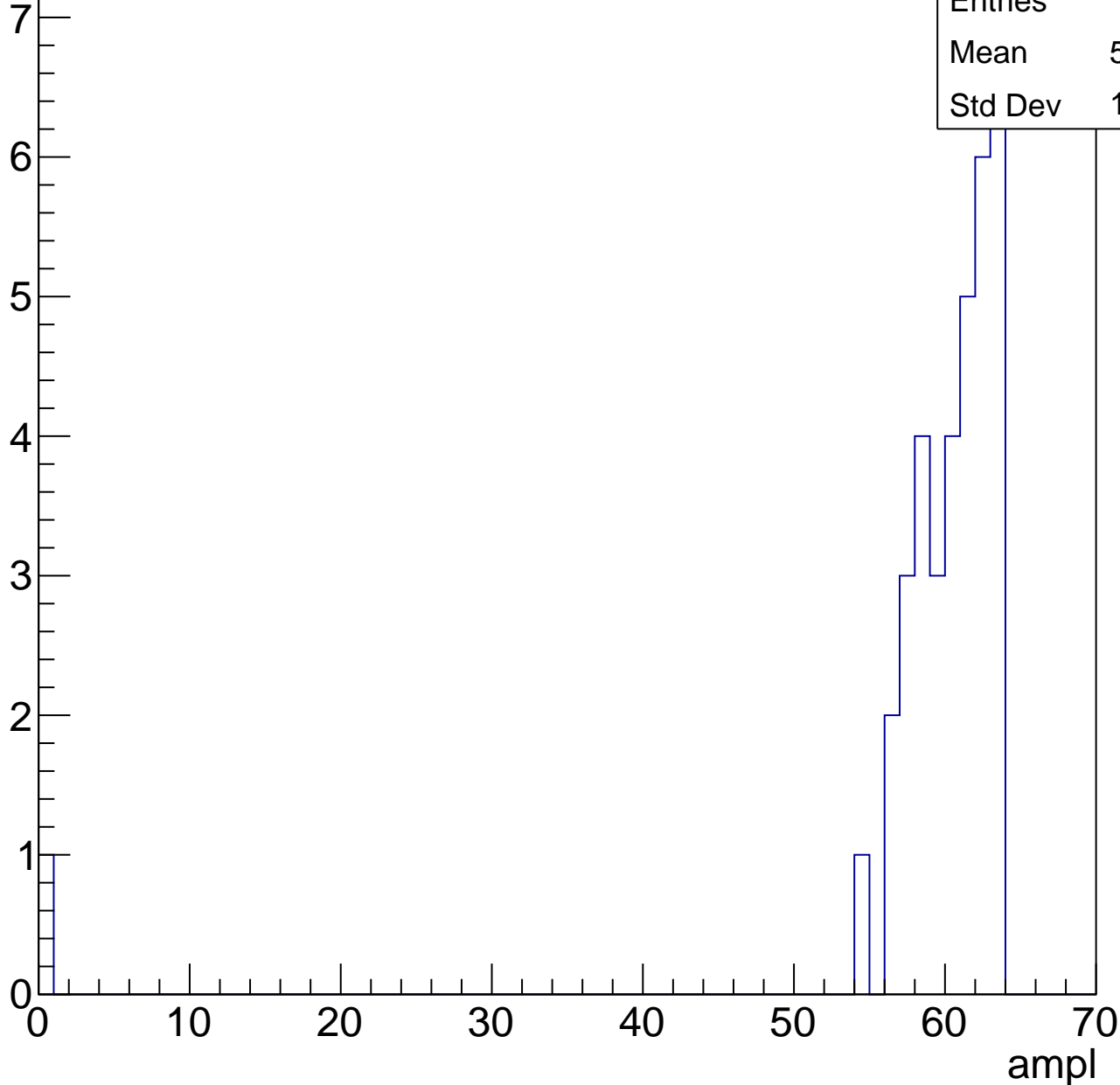


B1L102S, U11-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.44
Std Dev	10.16



B1L102S, U11-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch96, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	53
Mean	27.19
Std Dev	3.354

Gaus mean : 27.5320

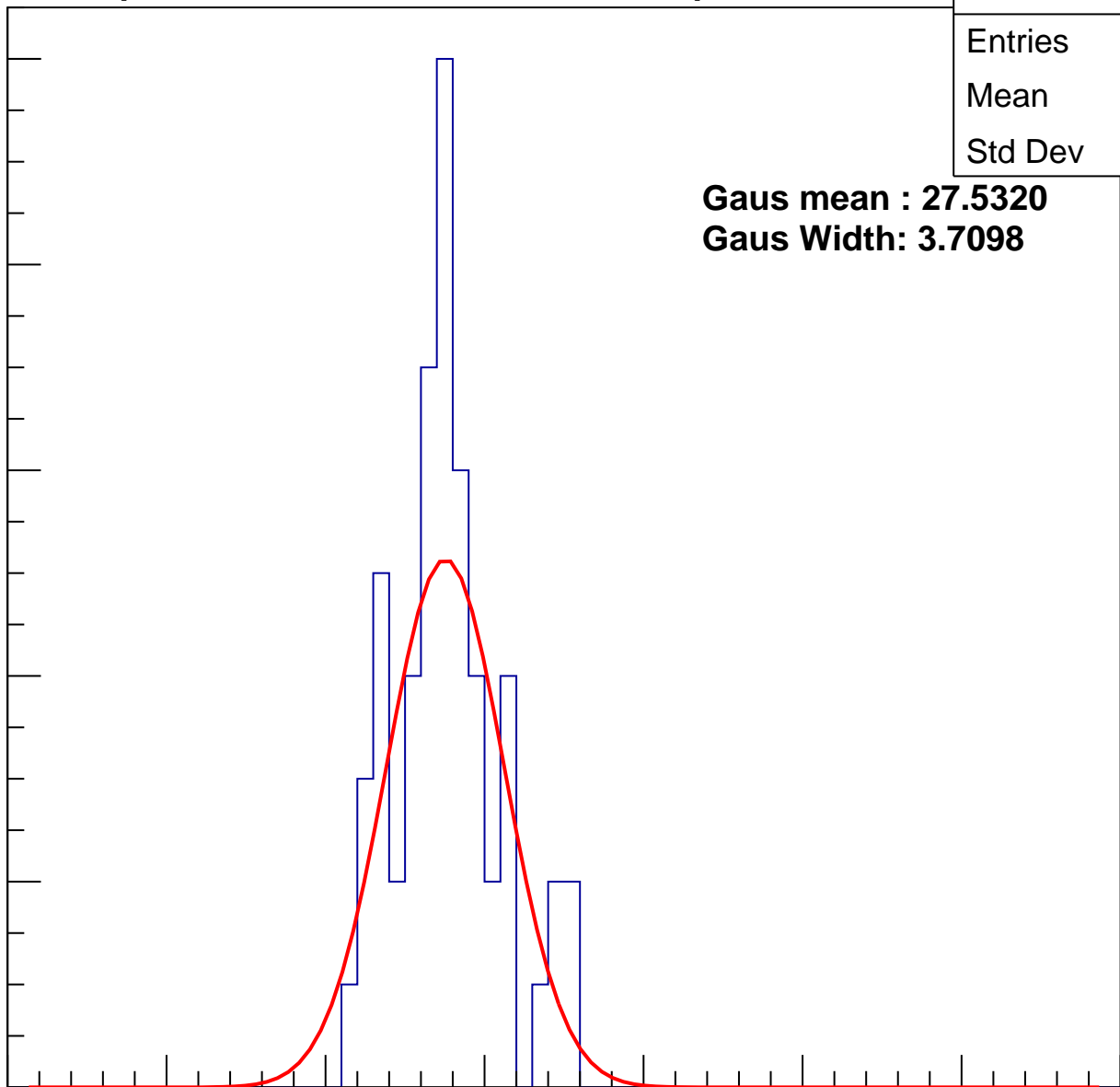
Gaus Width: 3.7098

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch96, adc1

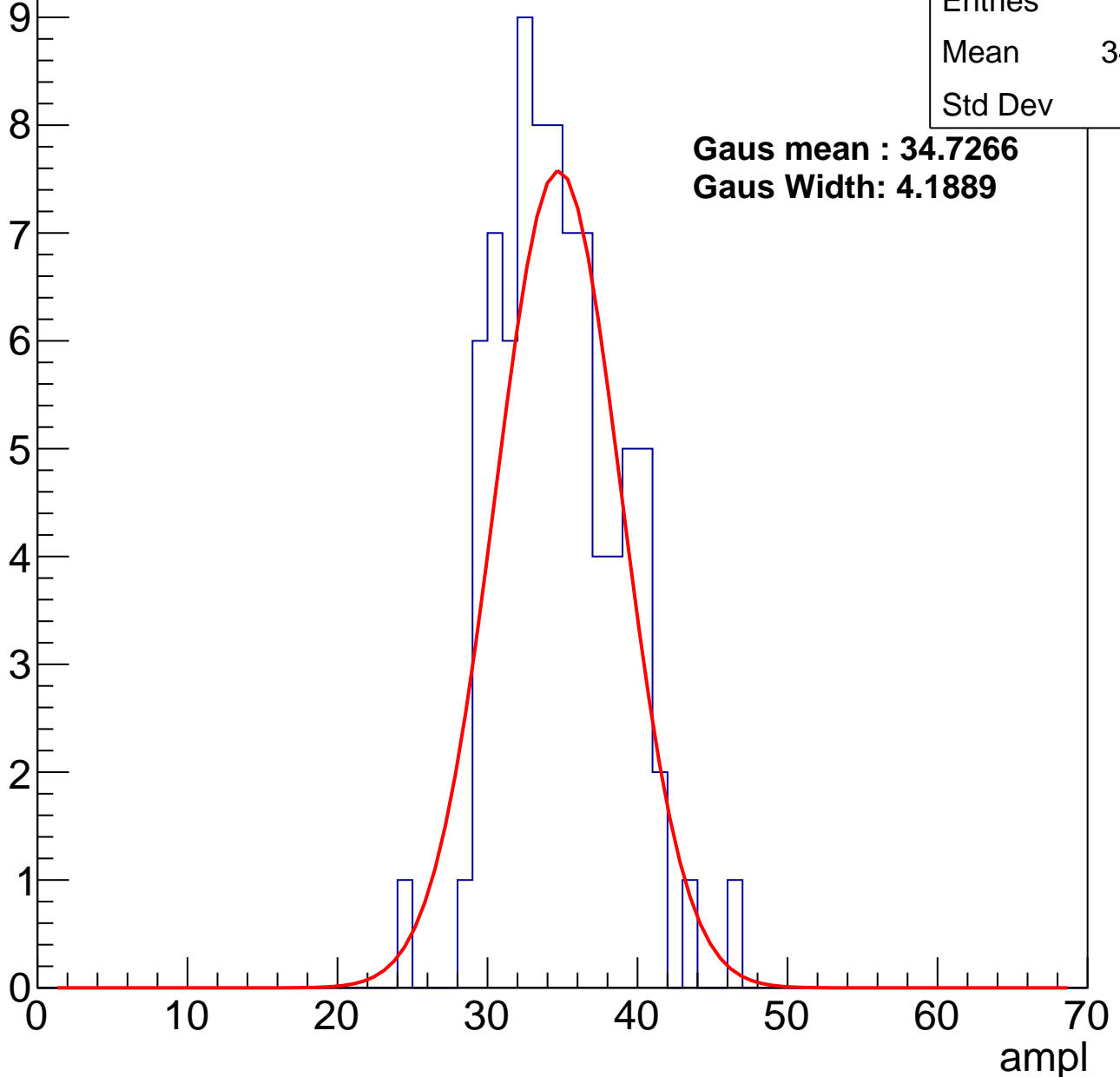
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.26
Std Dev	3.91

Gaus mean : 34.7266

Gaus Width: 4.1889



B1L102S, U11-ch96, adc2

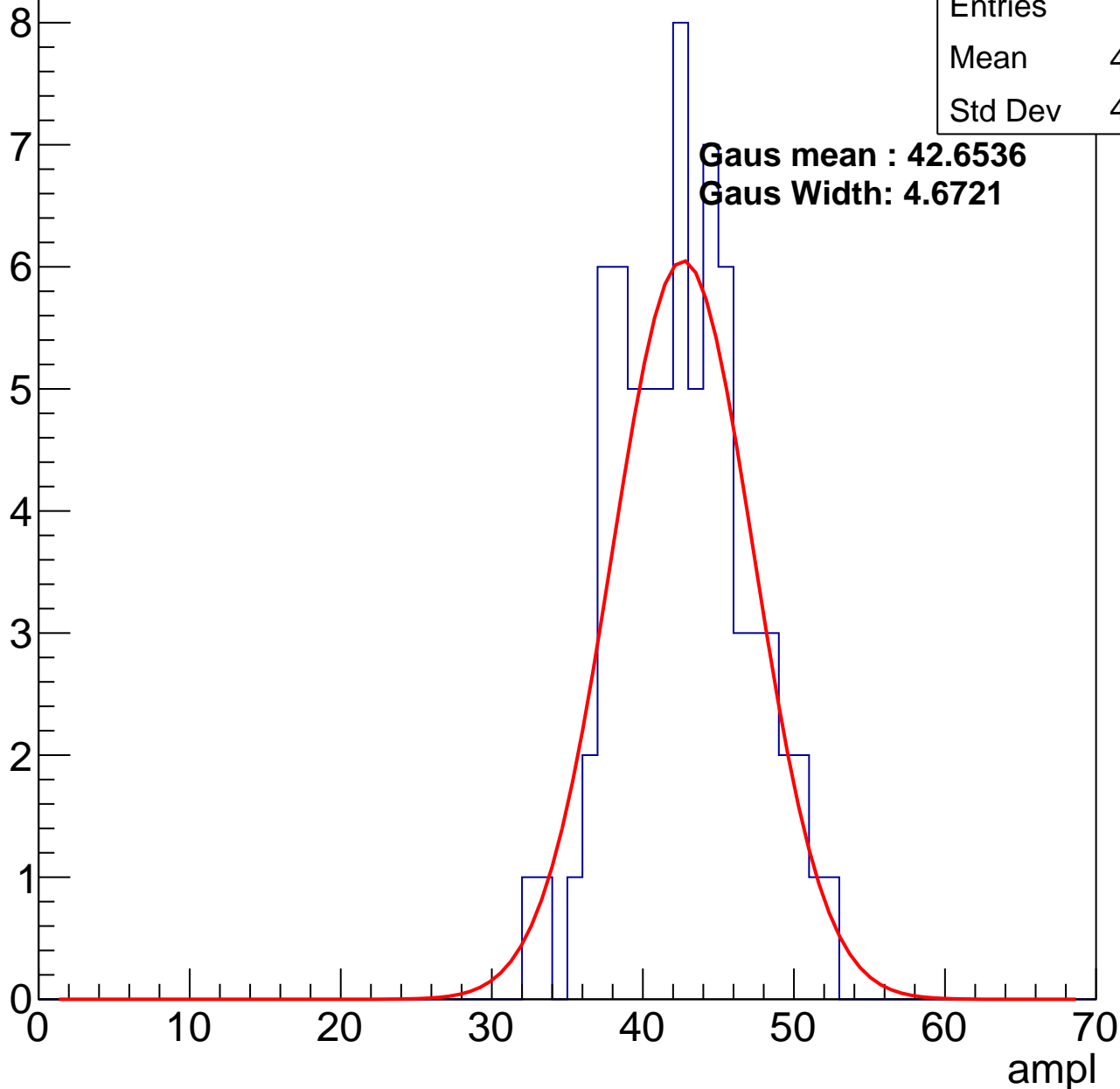
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.12
Std Dev	4.287

Gaus mean : 42.6536

Gaus Width: 4.6721

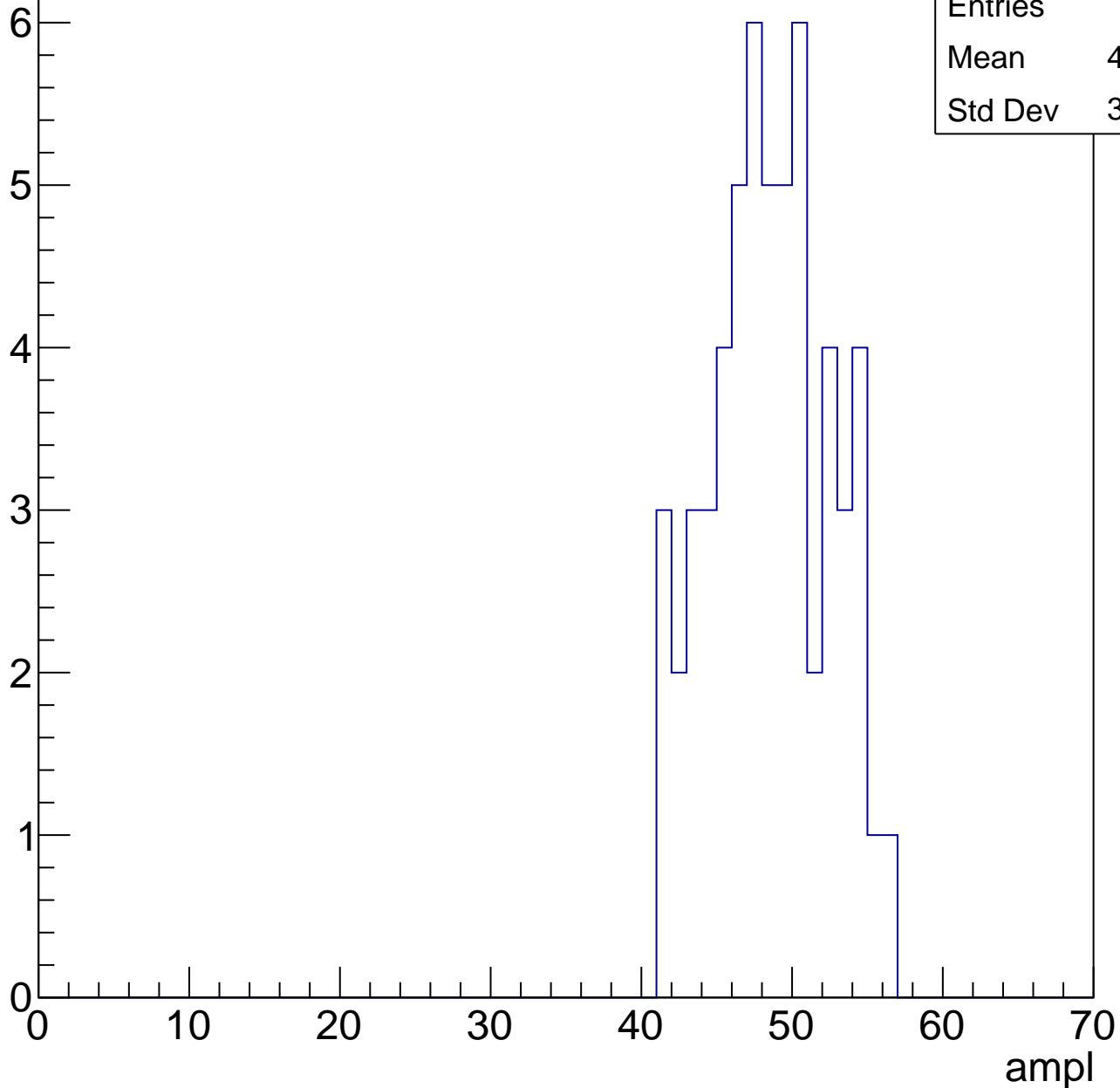


B1L102S, U11-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.09
Std Dev	3.854

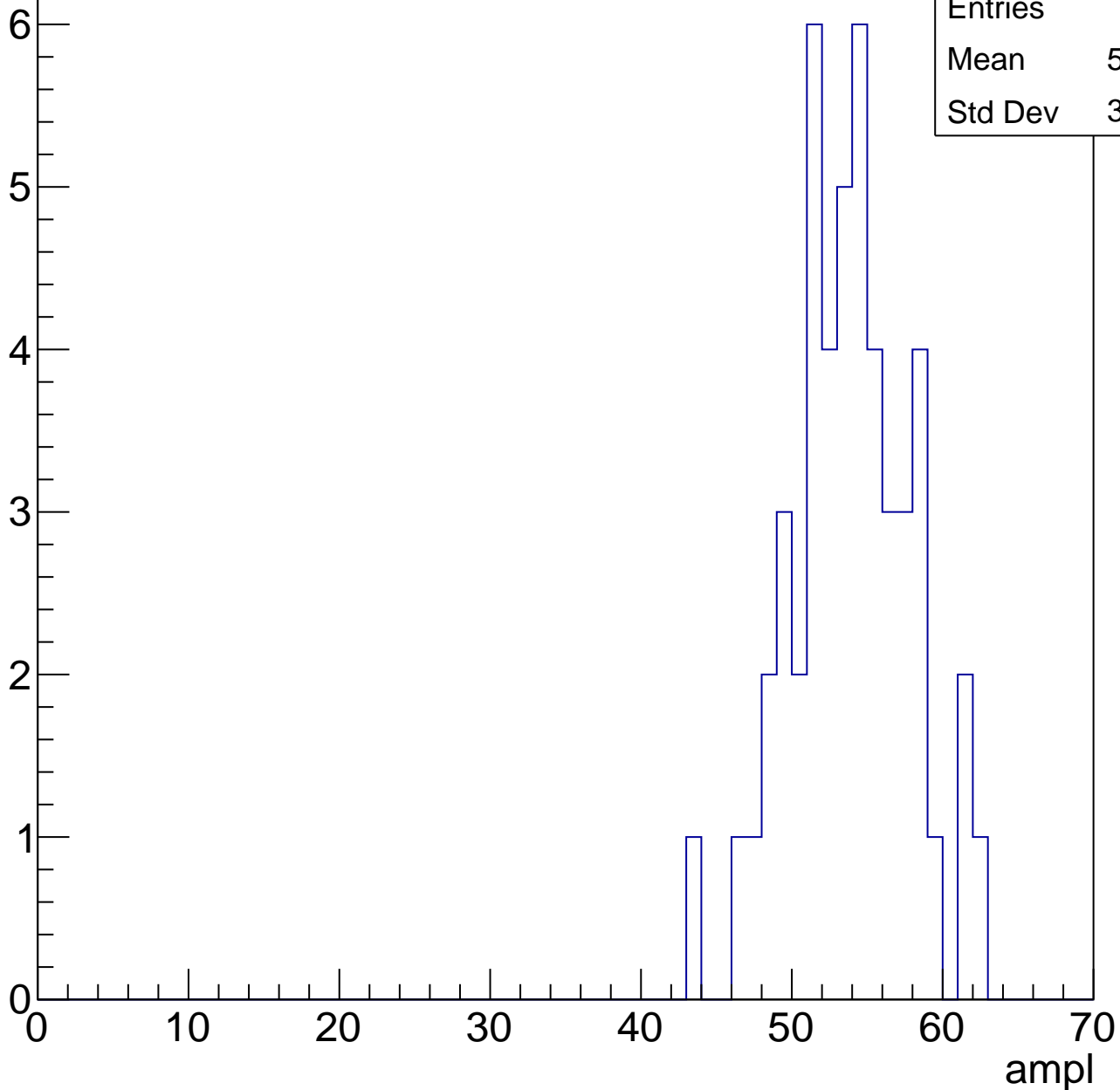


B1L102S, U11-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.39
Std Dev	3.948



B1L102S, U11-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	57.91
Std Dev	7.691

Entry

10

8

6

4

2

0

0

10

20

30

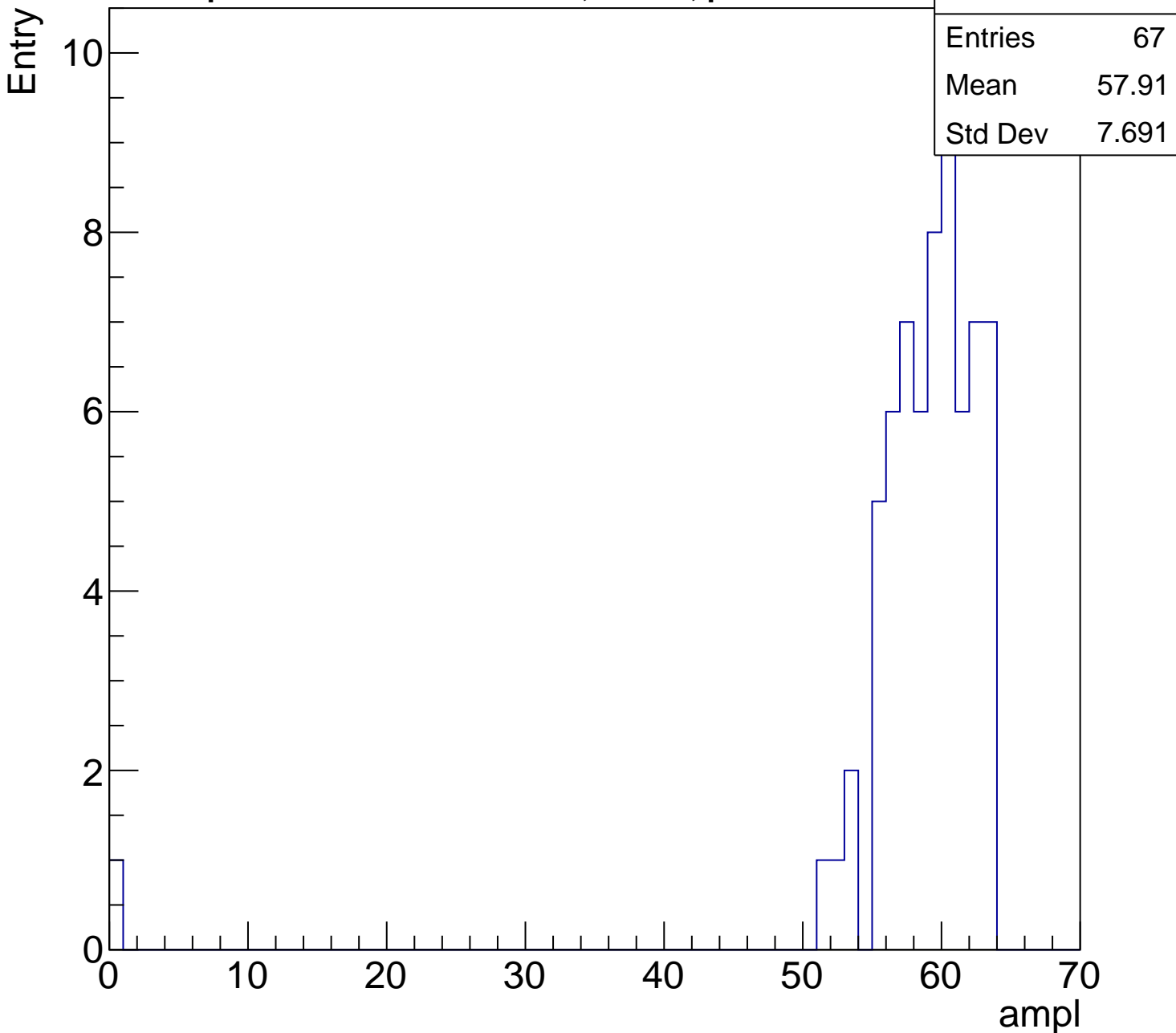
40

50

60

ampl

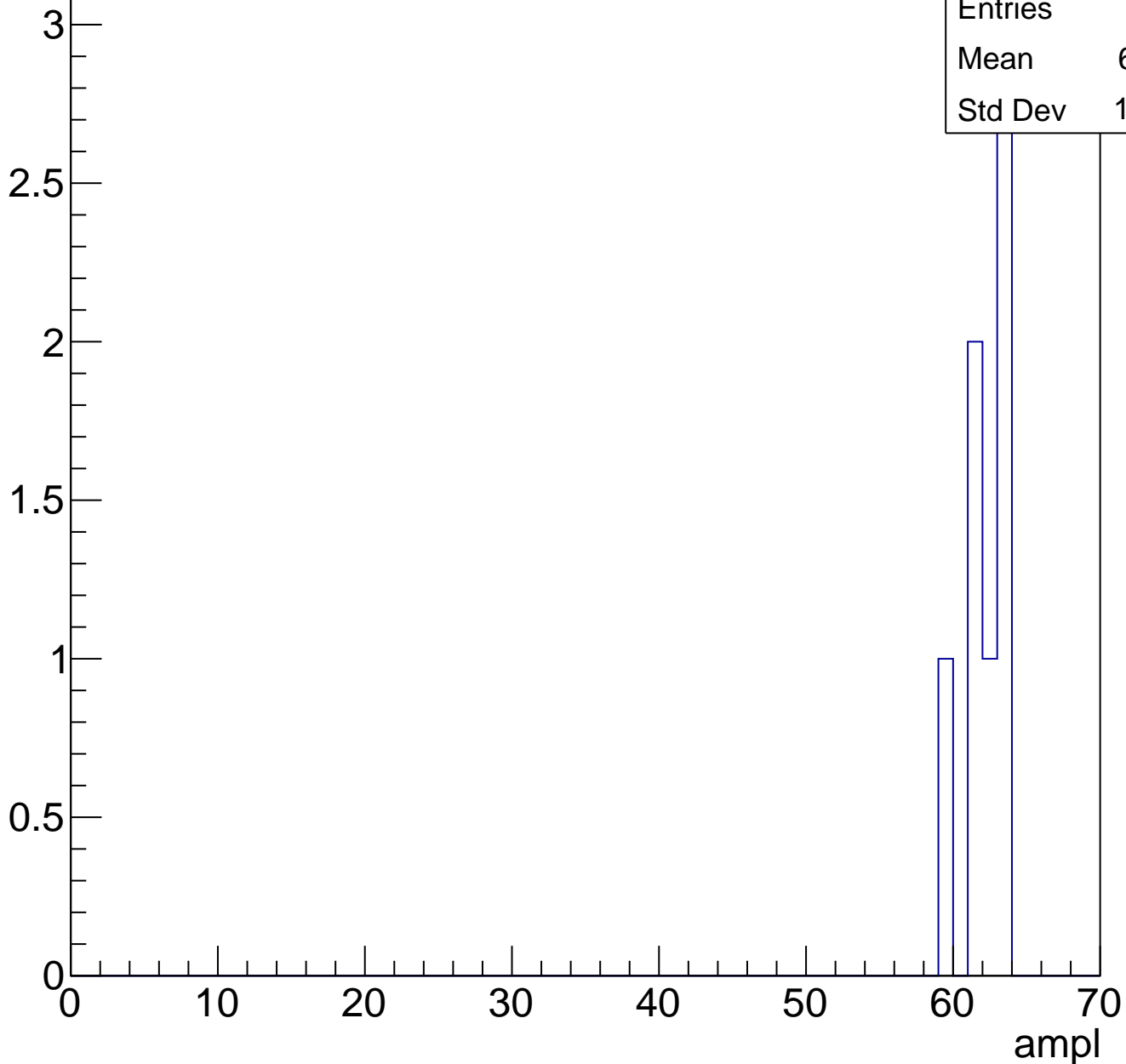
70



B1L102S, U11-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

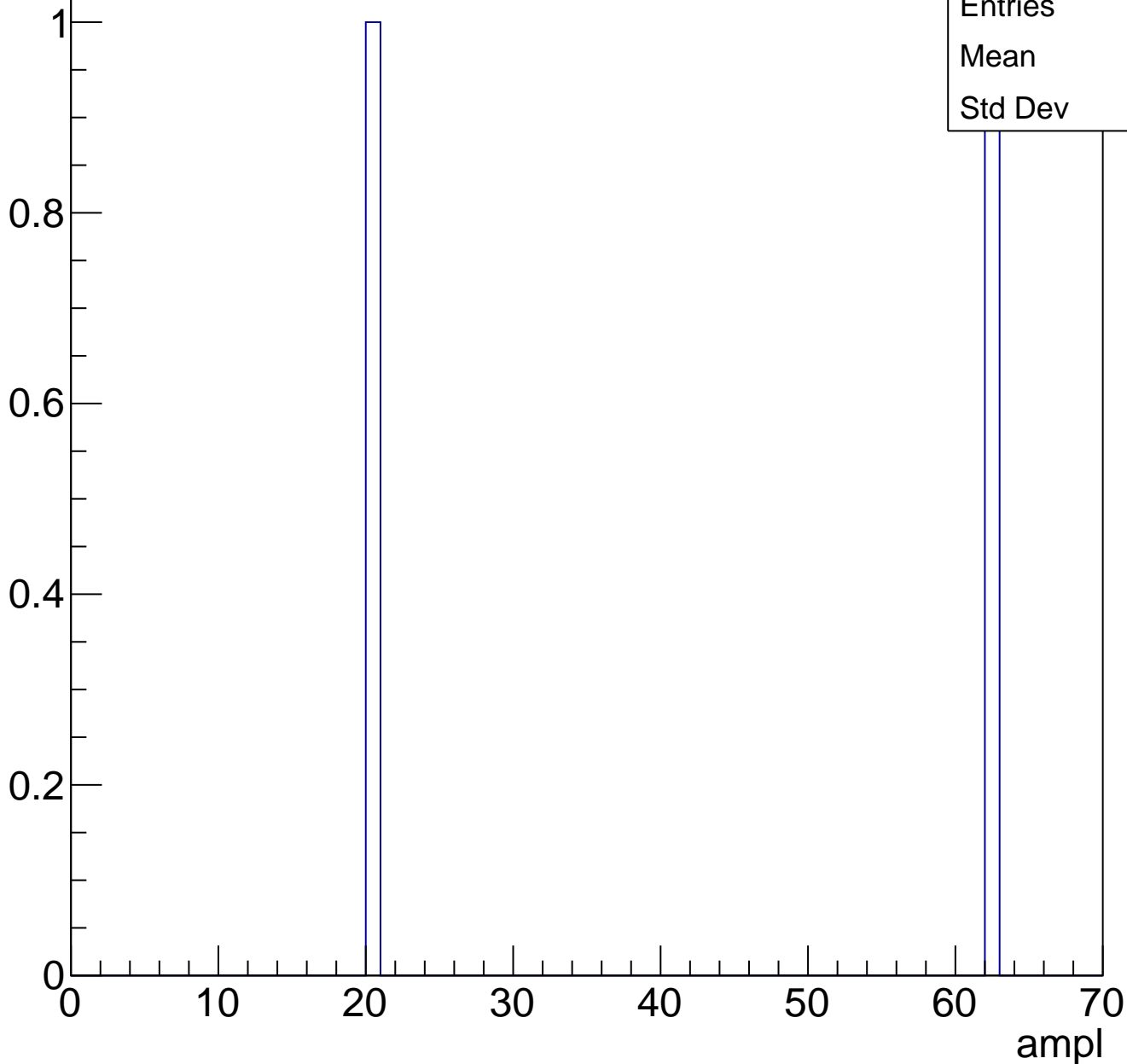
Entry



B1L102S, U11-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch97, adc0

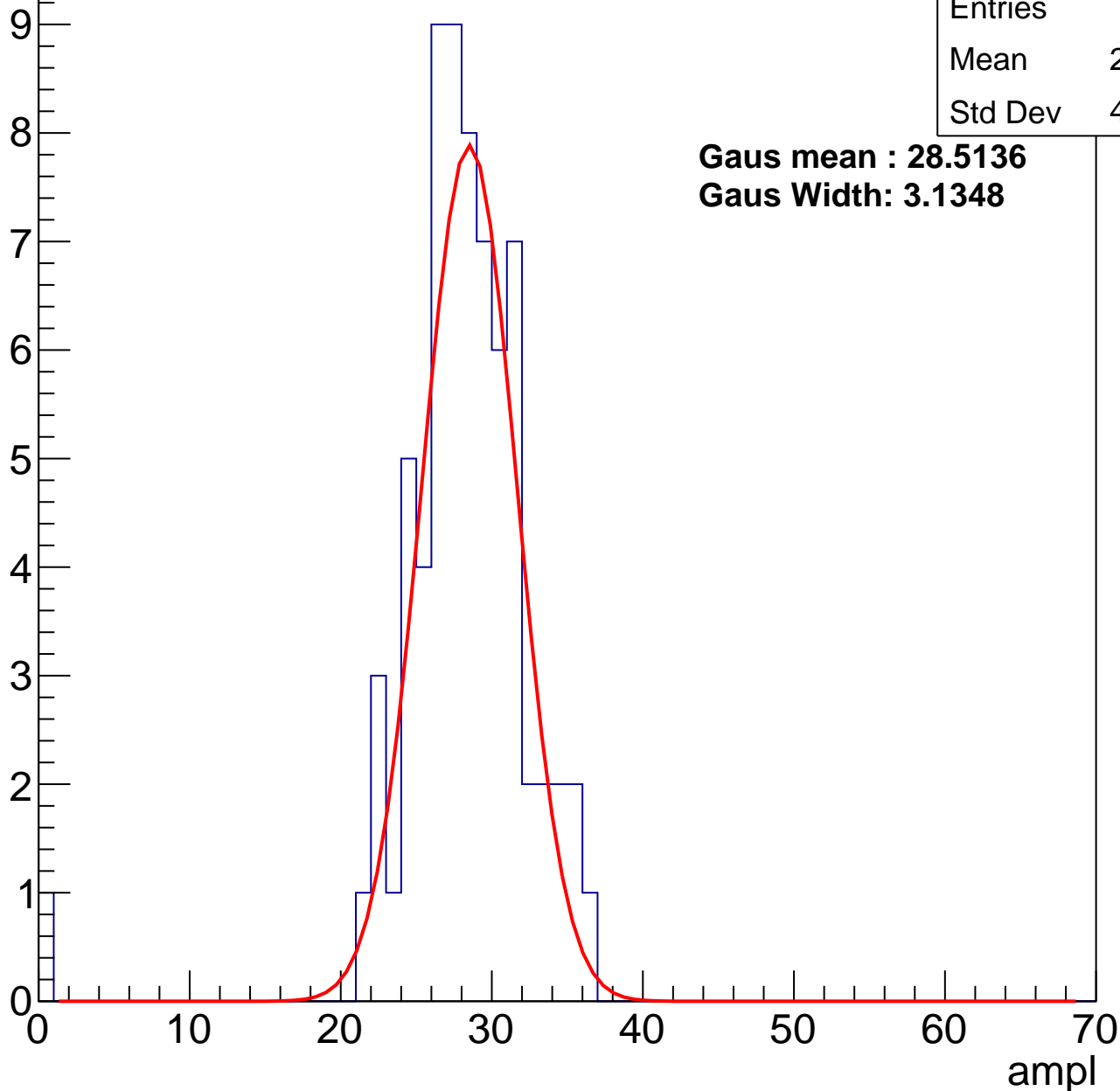
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.64
Std Dev	4.672

Gaus mean : 28.5136

Gaus Width: 3.1348



B1L102S, U11-ch97, adc1

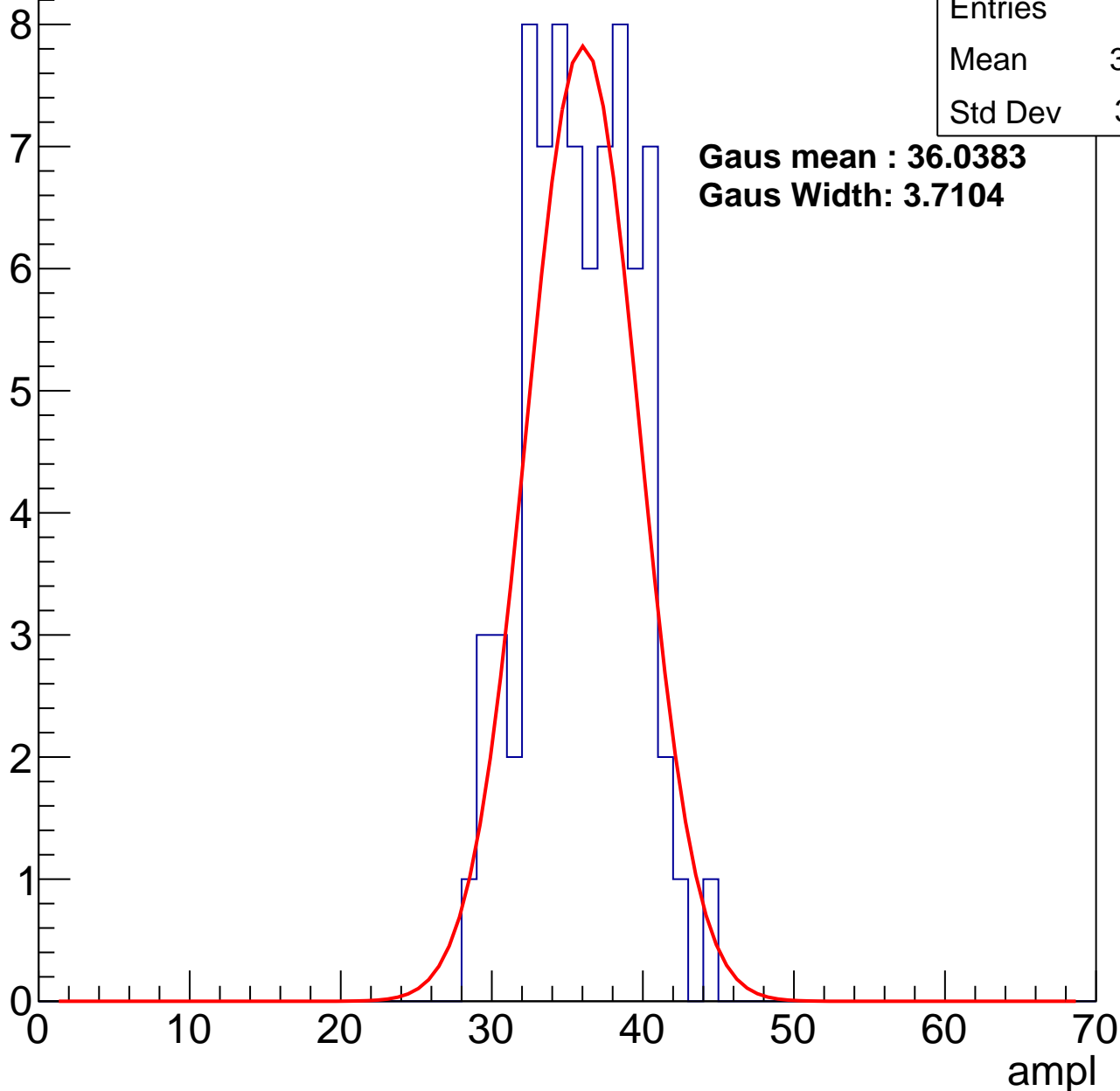
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.48
Std Dev	3.481

Gaus mean : 36.0383

Gaus Width: 3.7104



B1L102S, U11-ch97, adc2

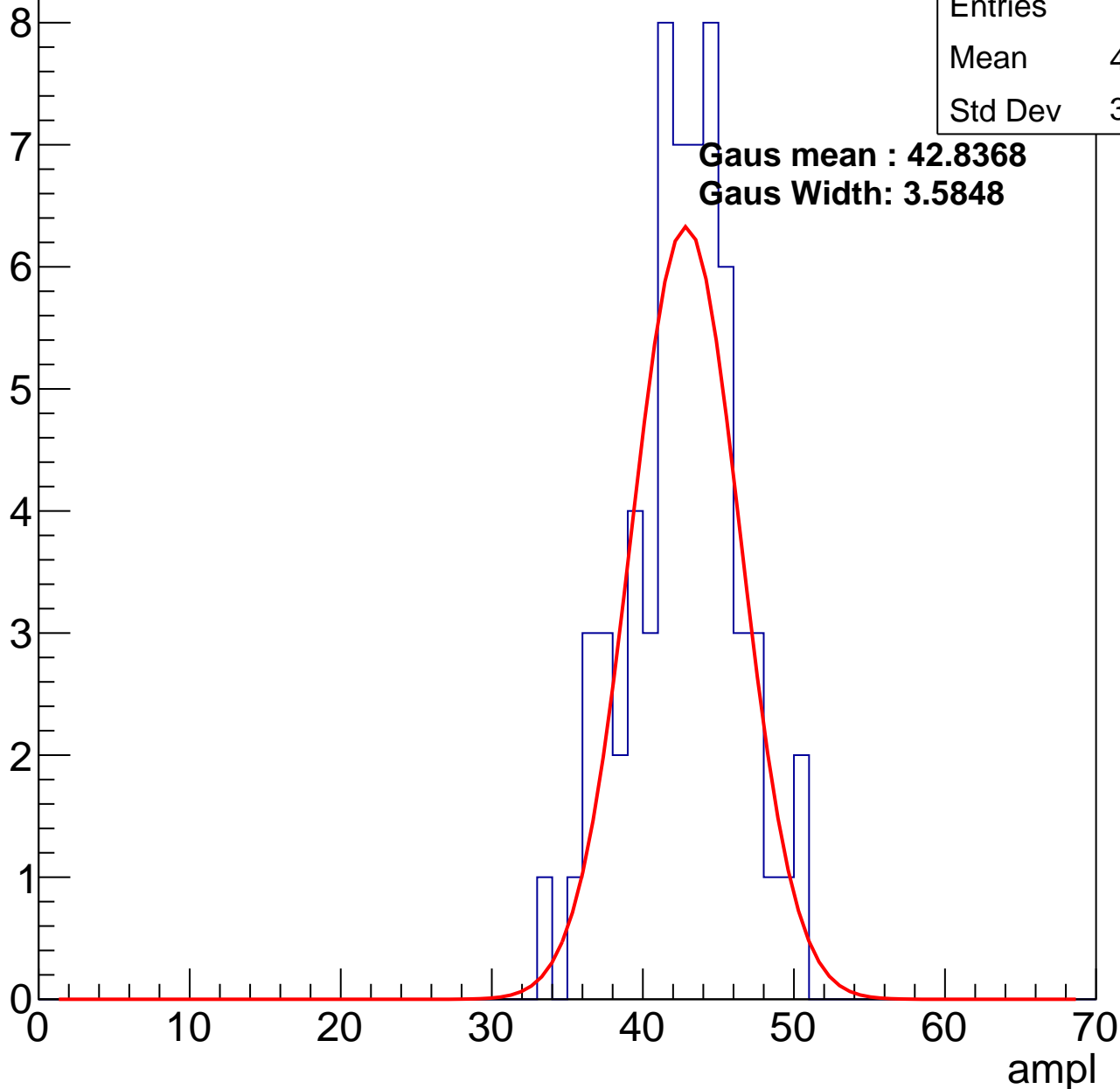
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.22
Std Dev	3.623

Gaus mean : 42.8368

Gaus Width: 3.5848

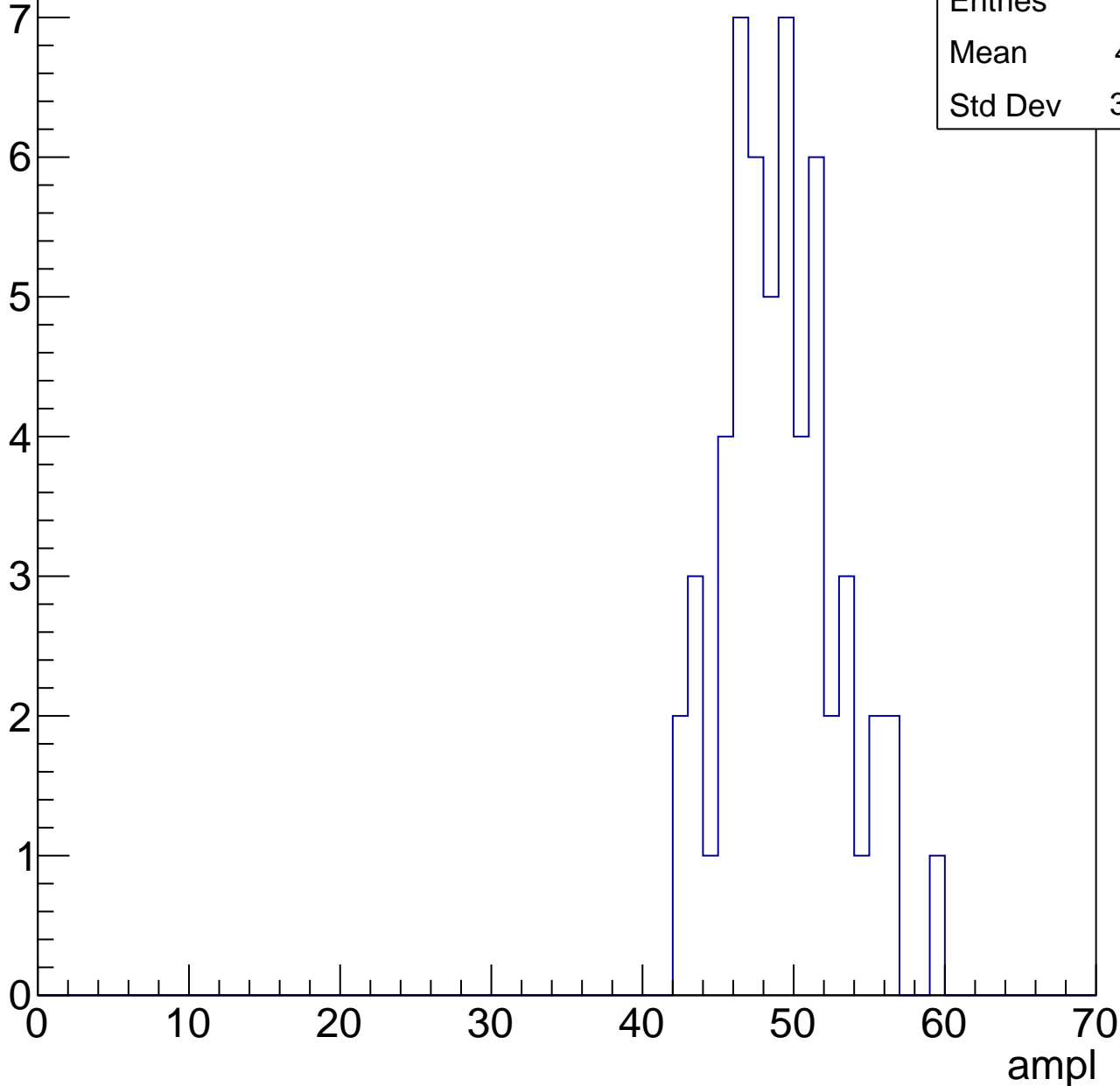


B1L102S, U11-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.71
Std Dev	3.712

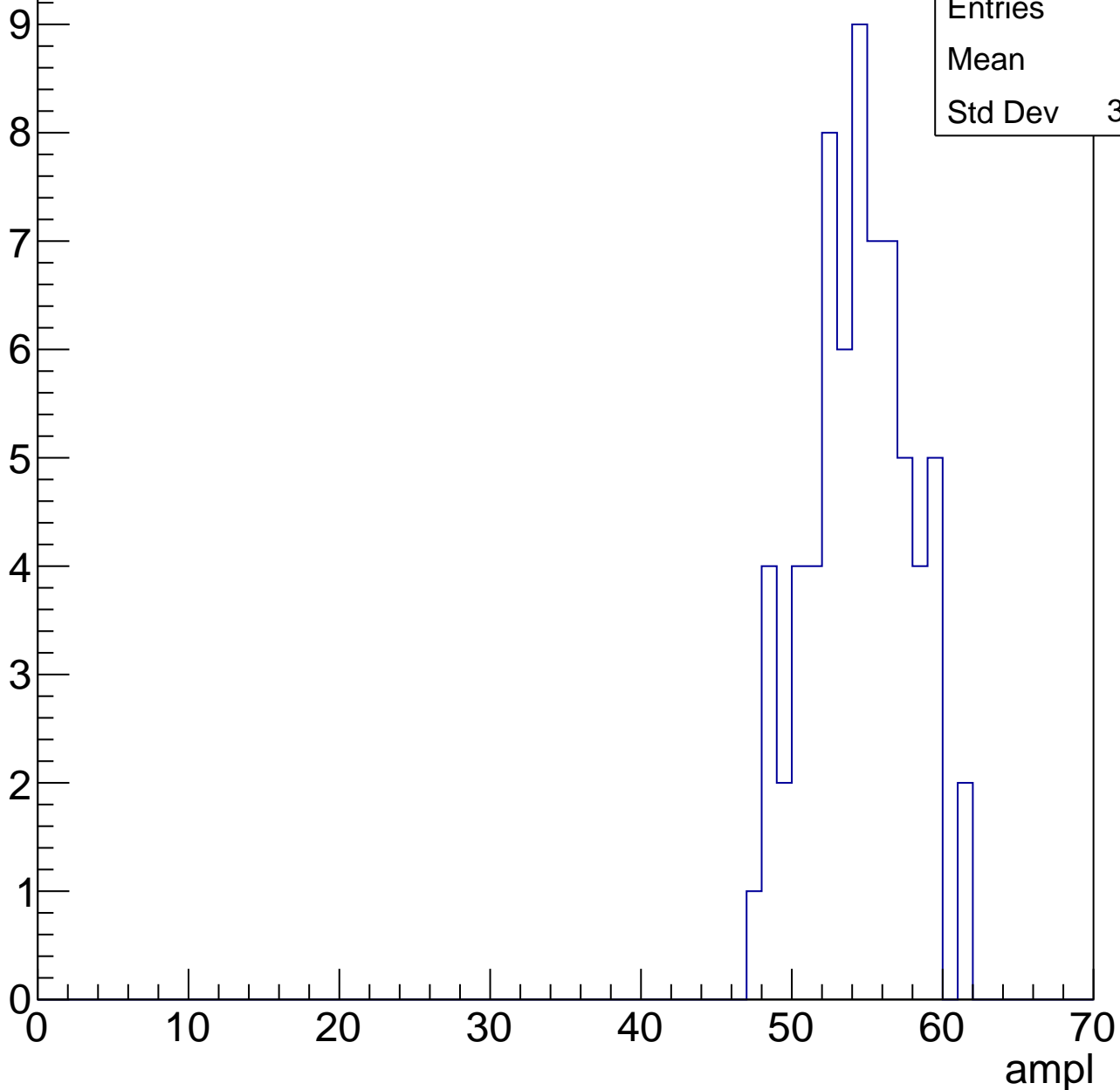


B1L102S, U11-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

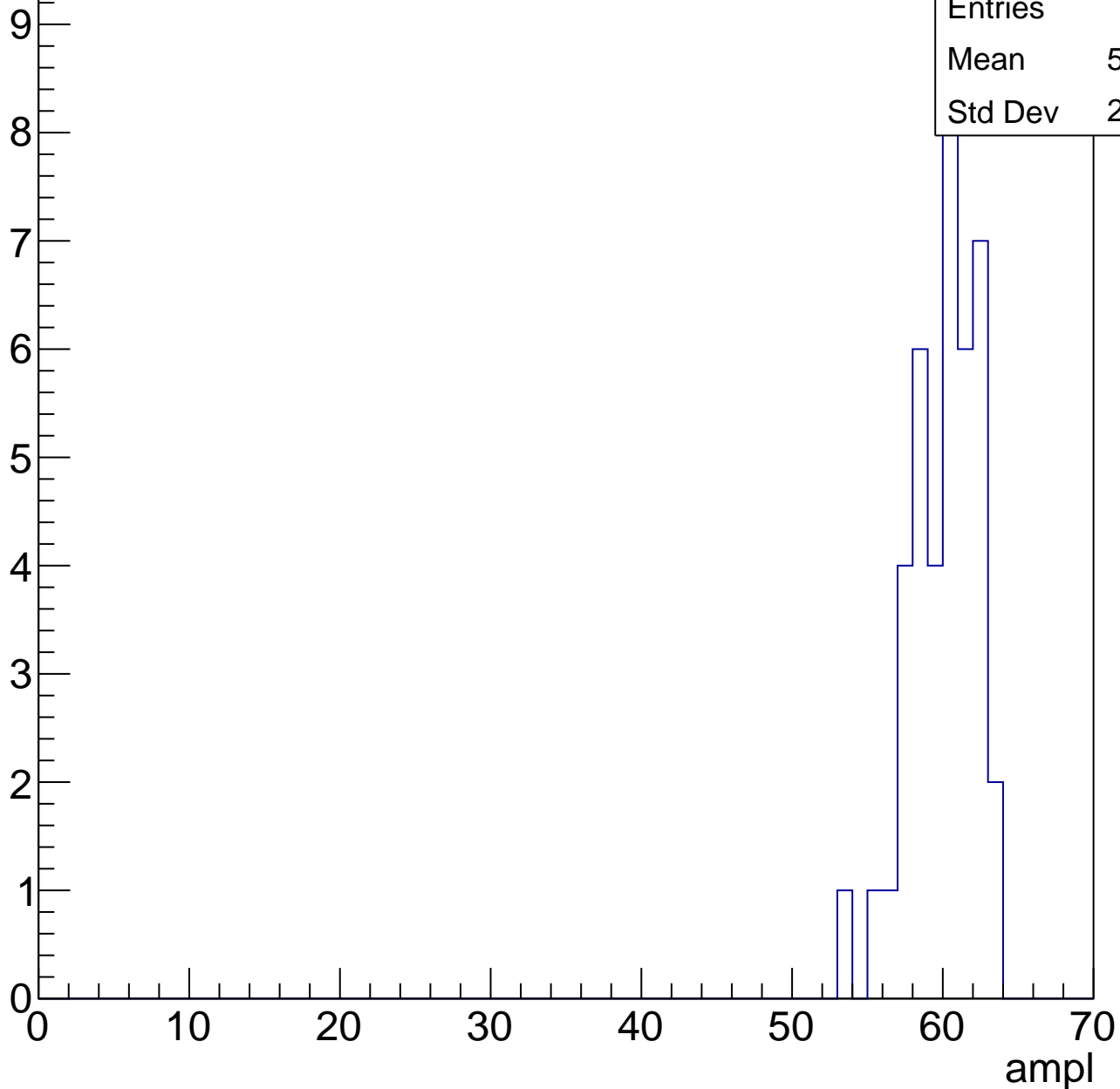
Entries	68
Mean	54
Std Dev	3.317



B1L102S, U11-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

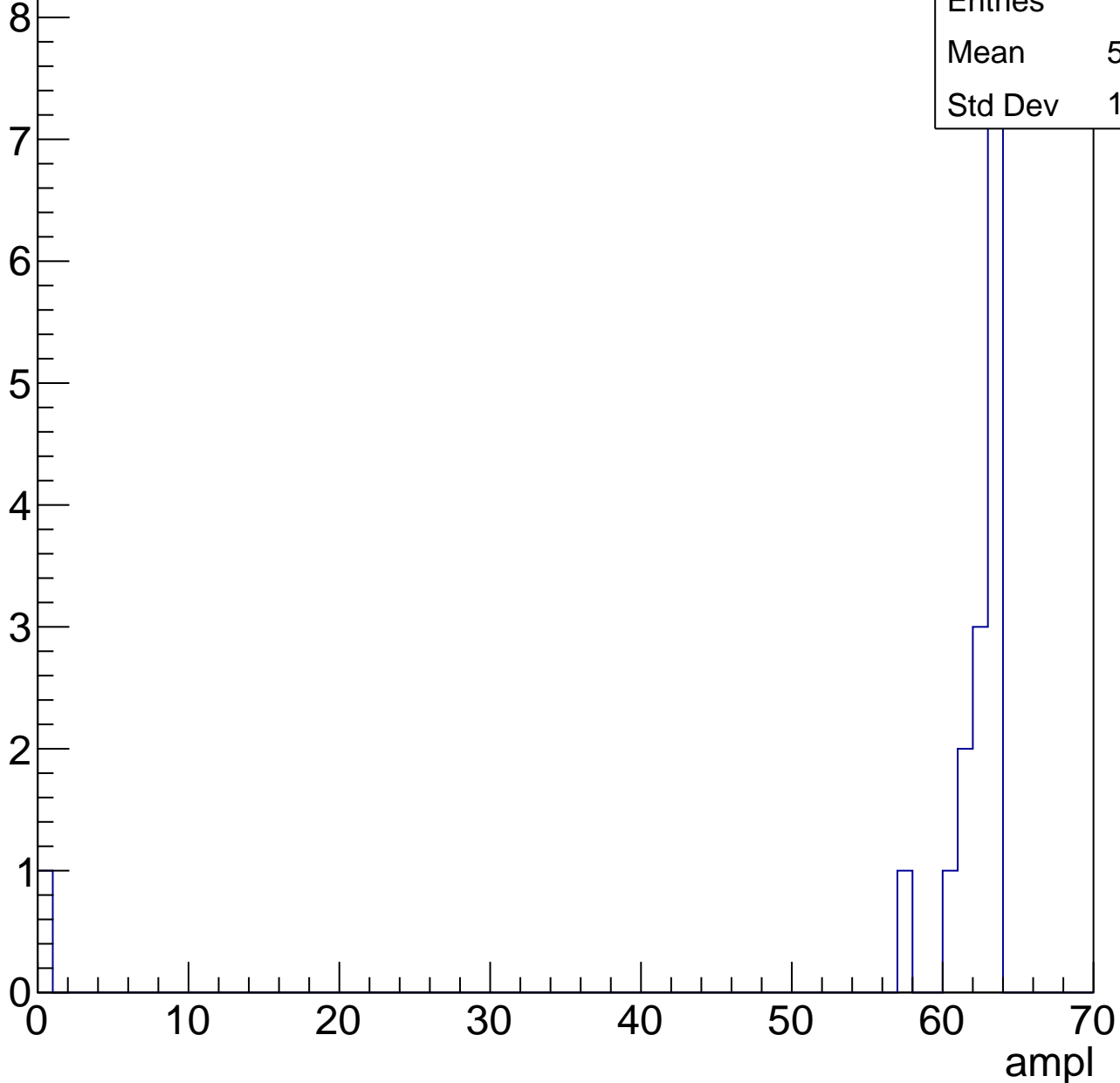


B1L102S, U11-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	58.06
Std Dev	15.07



B1L102S, U11-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch98, adc0

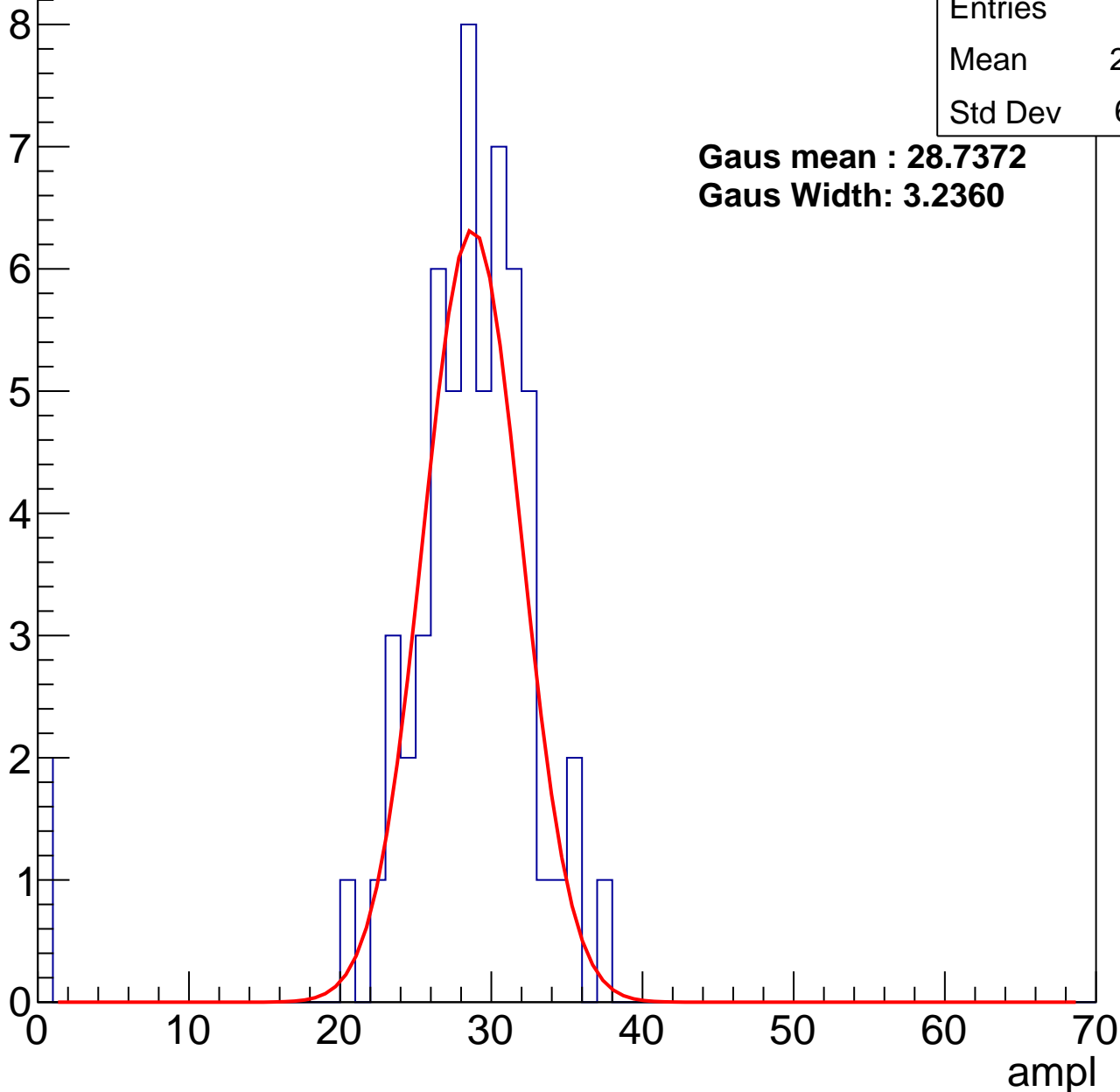
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.53
Std Dev	6.141

Gaus mean : 28.7372

Gaus Width: 3.2360



B1L102S, U11-ch98, adc1

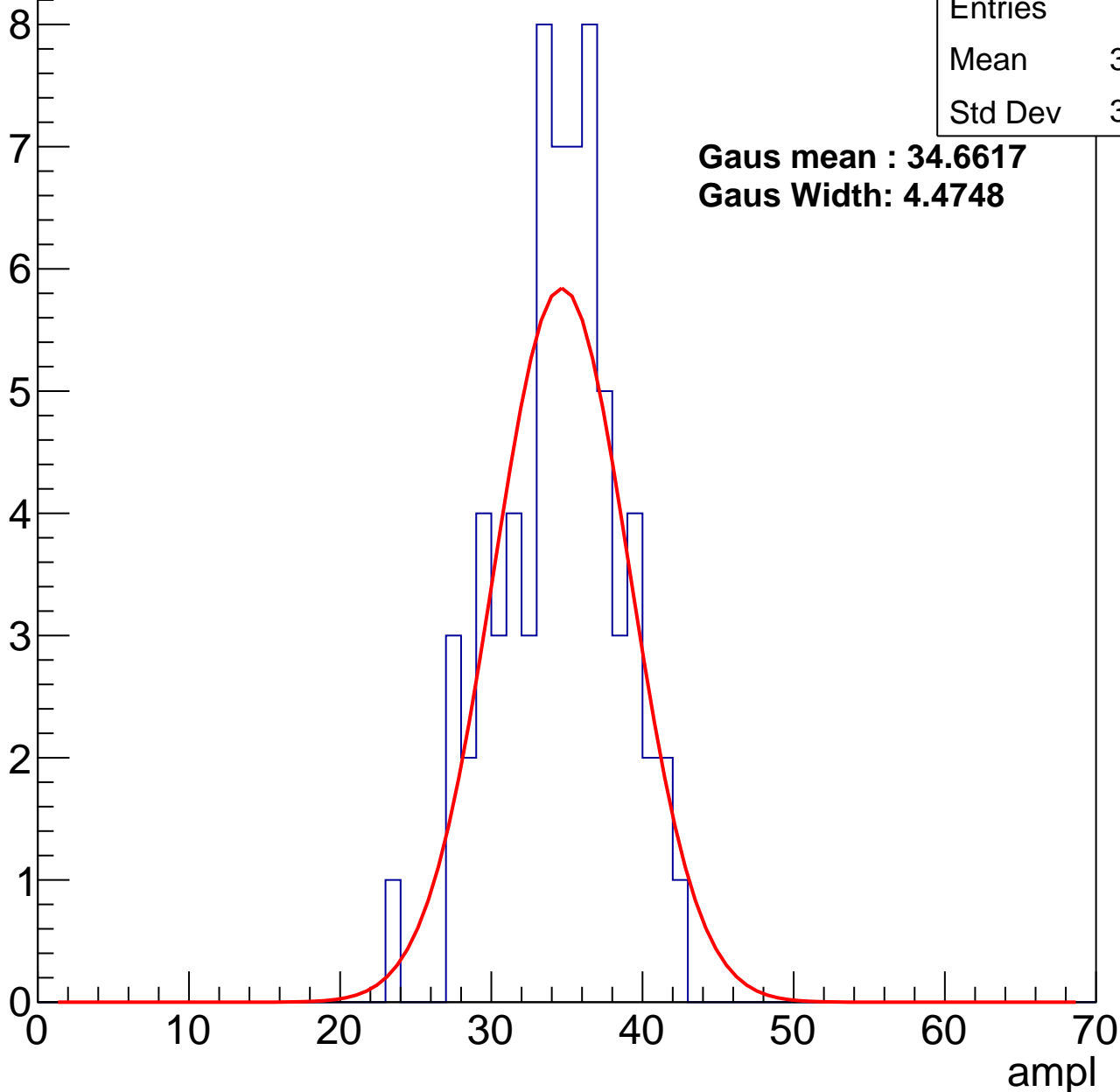
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.03
Std Dev	3.882

Gaus mean : 34.6617

Gaus Width: 4.4748



B1L102S, U11-ch98, adc2

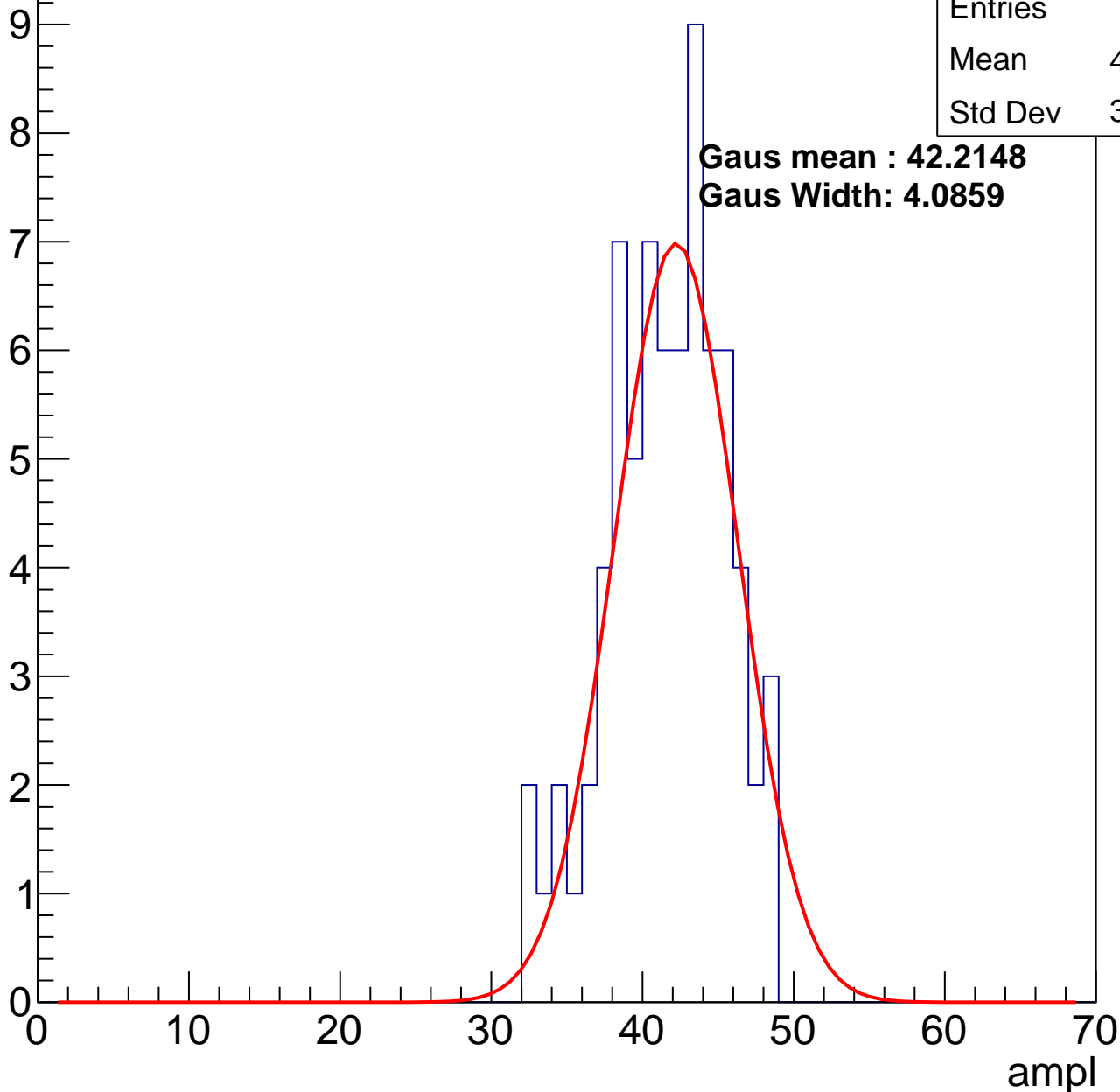
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.12
Std Dev	3.835

Gaus mean : 42.2148

Gaus Width: 4.0859

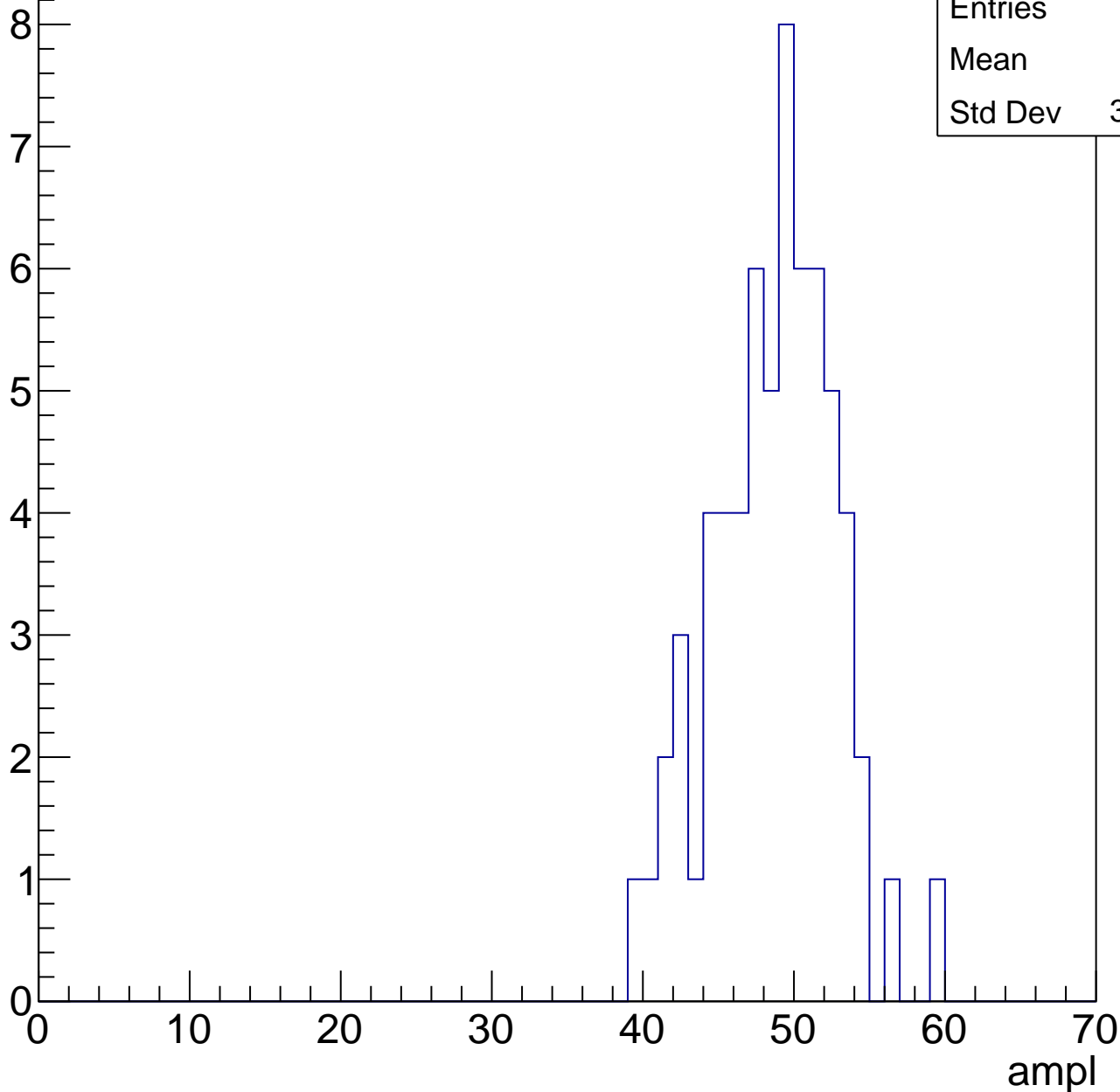


B1L102S, U11-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.2
Std Dev	3.989

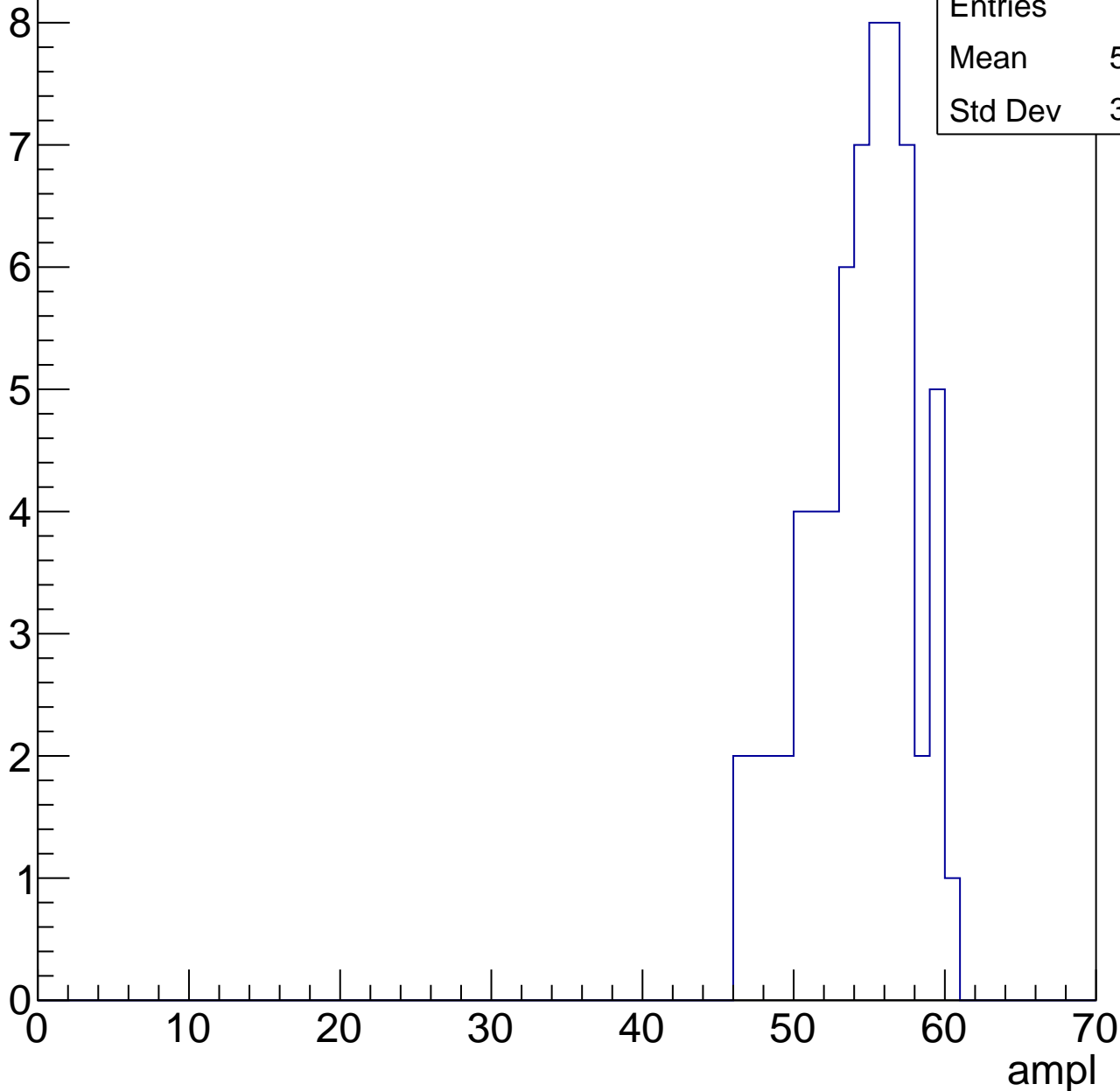


B1L102S, U11-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

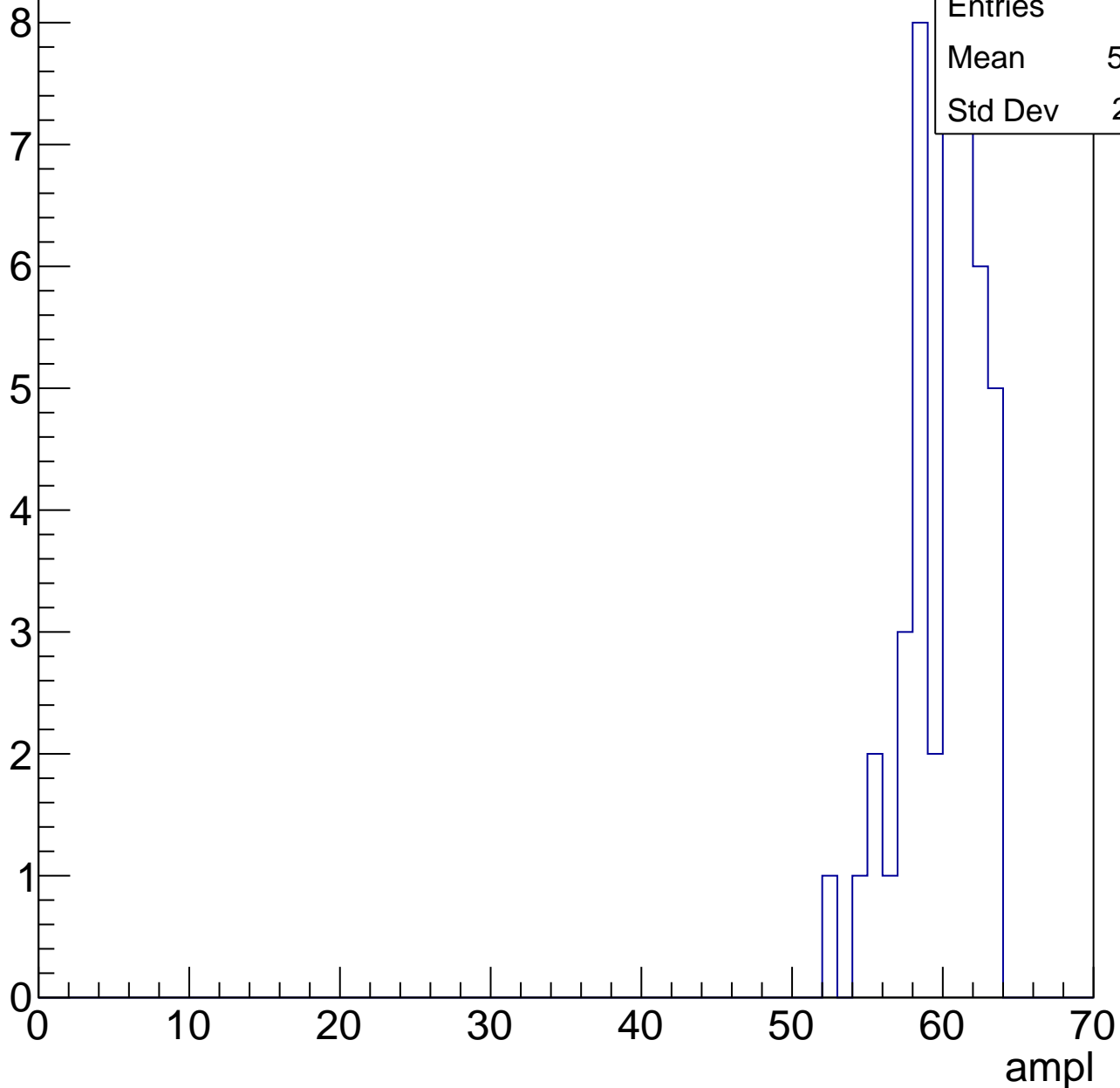
Entries	64
Mean	53.84
Std Dev	3.456



B1L102S, U11-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

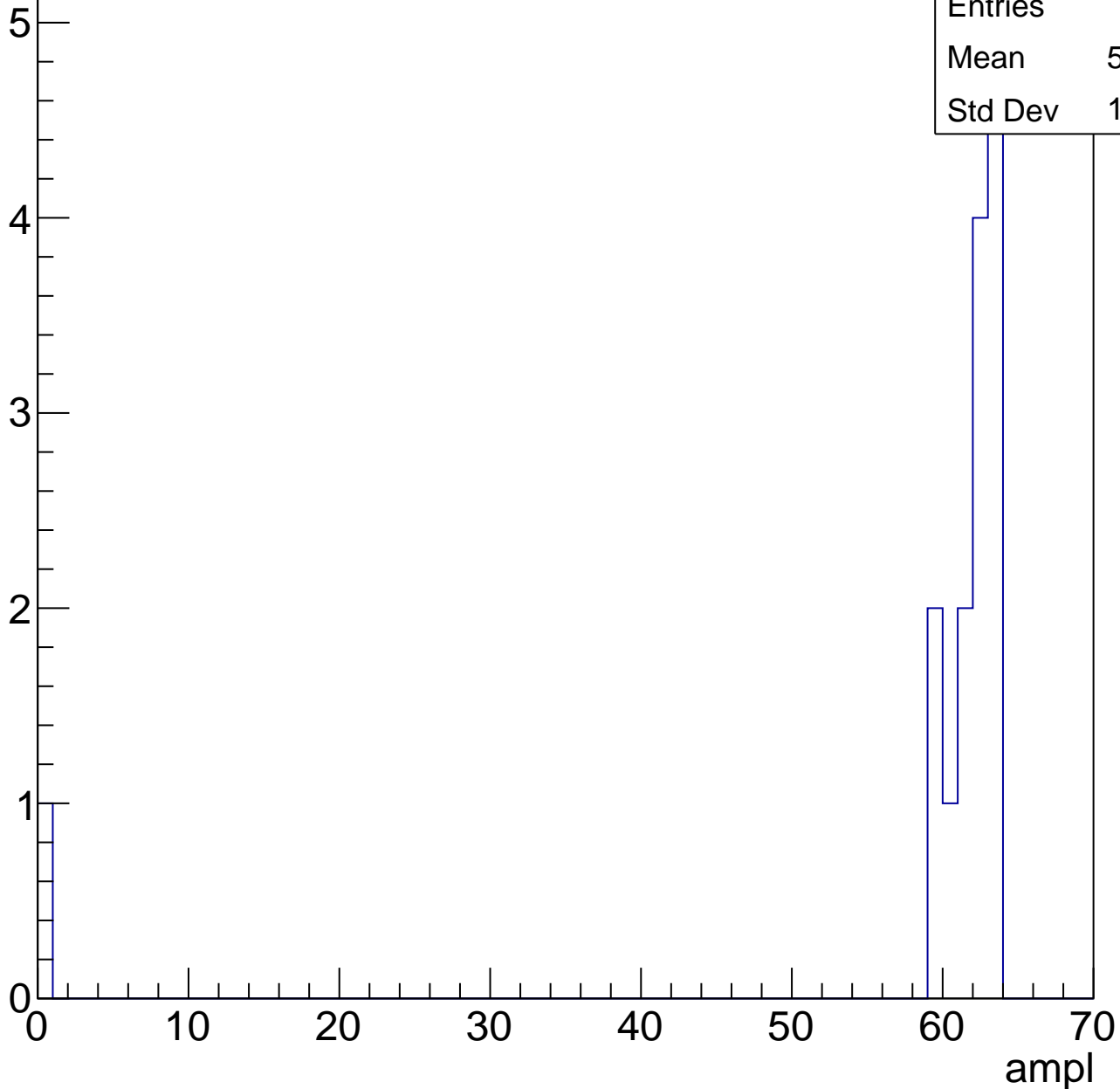


B1L102S, U11-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.53
Std Dev	15.44



B1L102S, U11-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U11-ch99, adc0

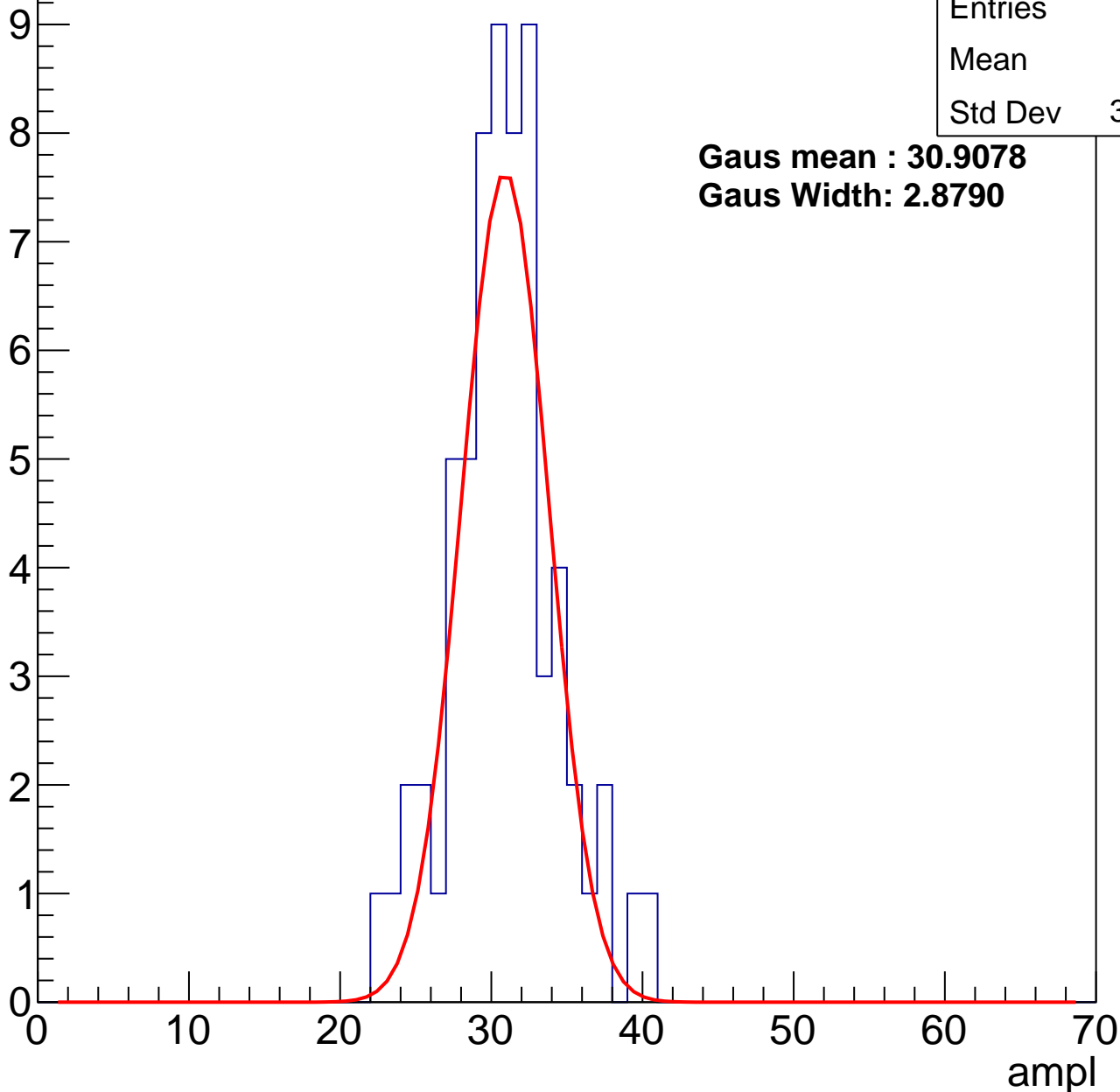
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.4
Std Dev	3.538

Gaus mean : 30.9078

Gaus Width: 2.8790



B1L102S, U11-ch99, adc1

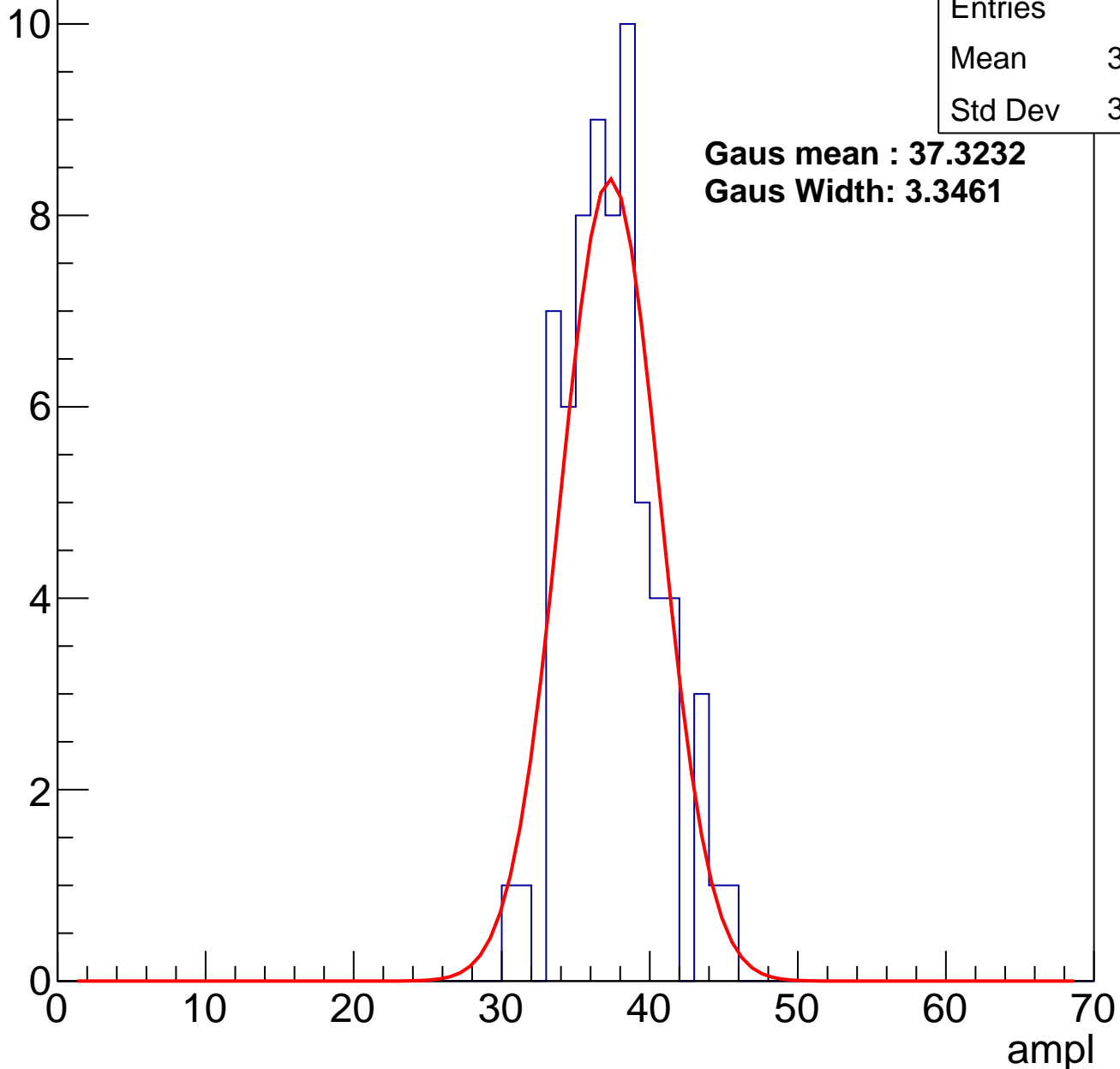
calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	36.96
Std Dev	3.075

Gaus mean : 37.3232

Gaus Width: 3.3461

Entry



B1L102S, U11-ch99, adc2

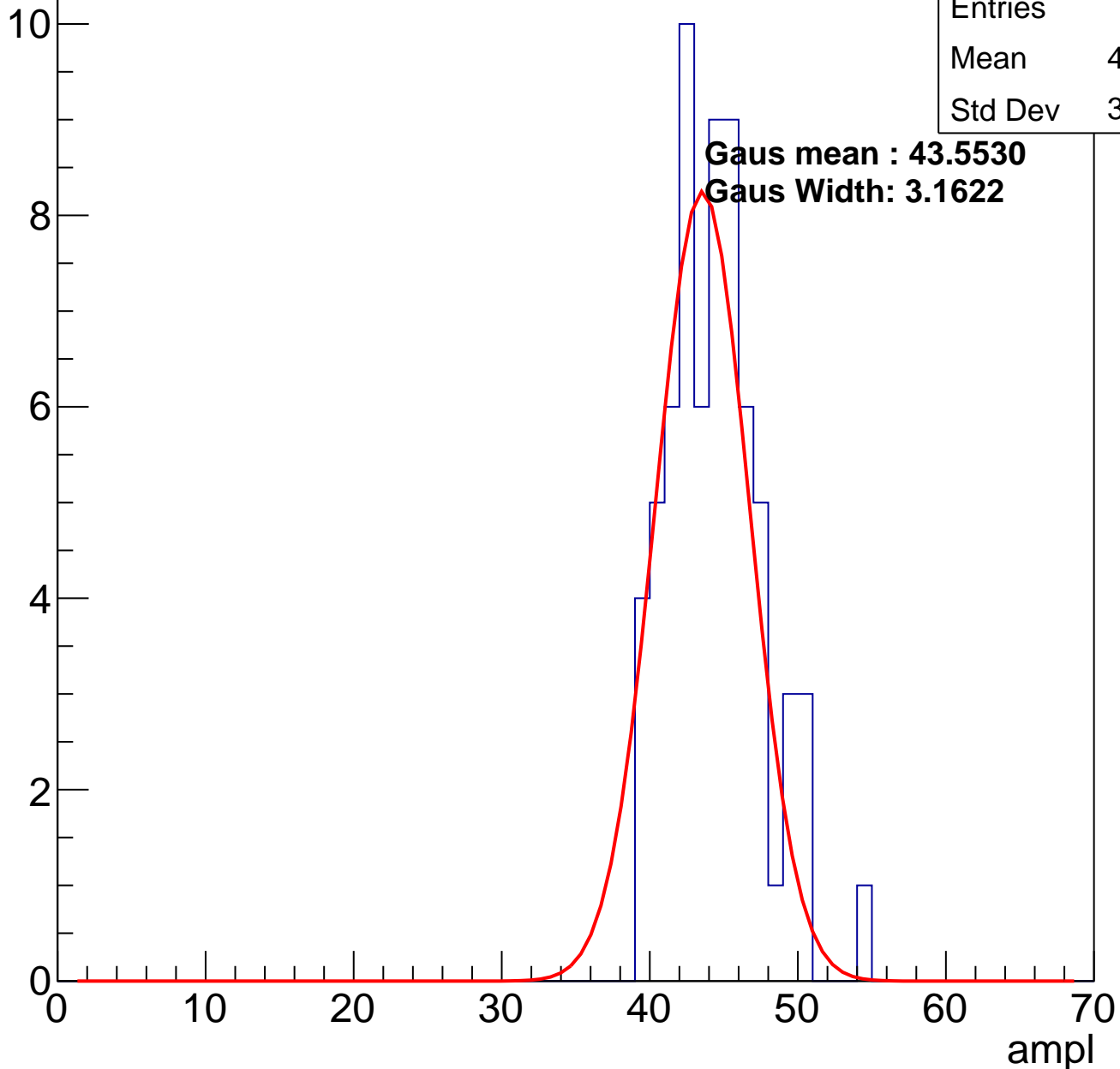
calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	43.99
Std Dev	3.108

Gaus mean : 43.5530

Gaus Width: 3.1622

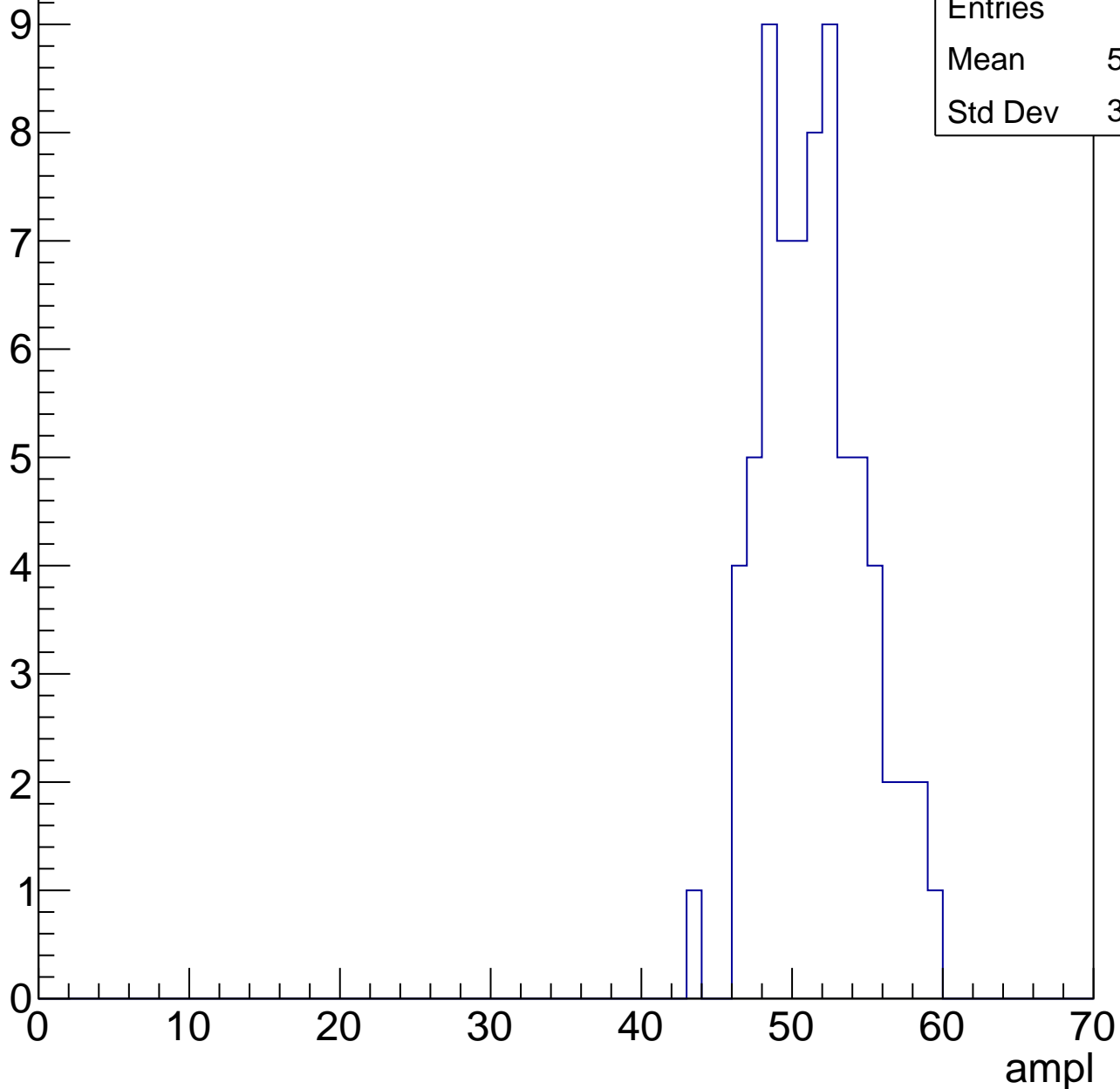
Entry



B1L102S, U11-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

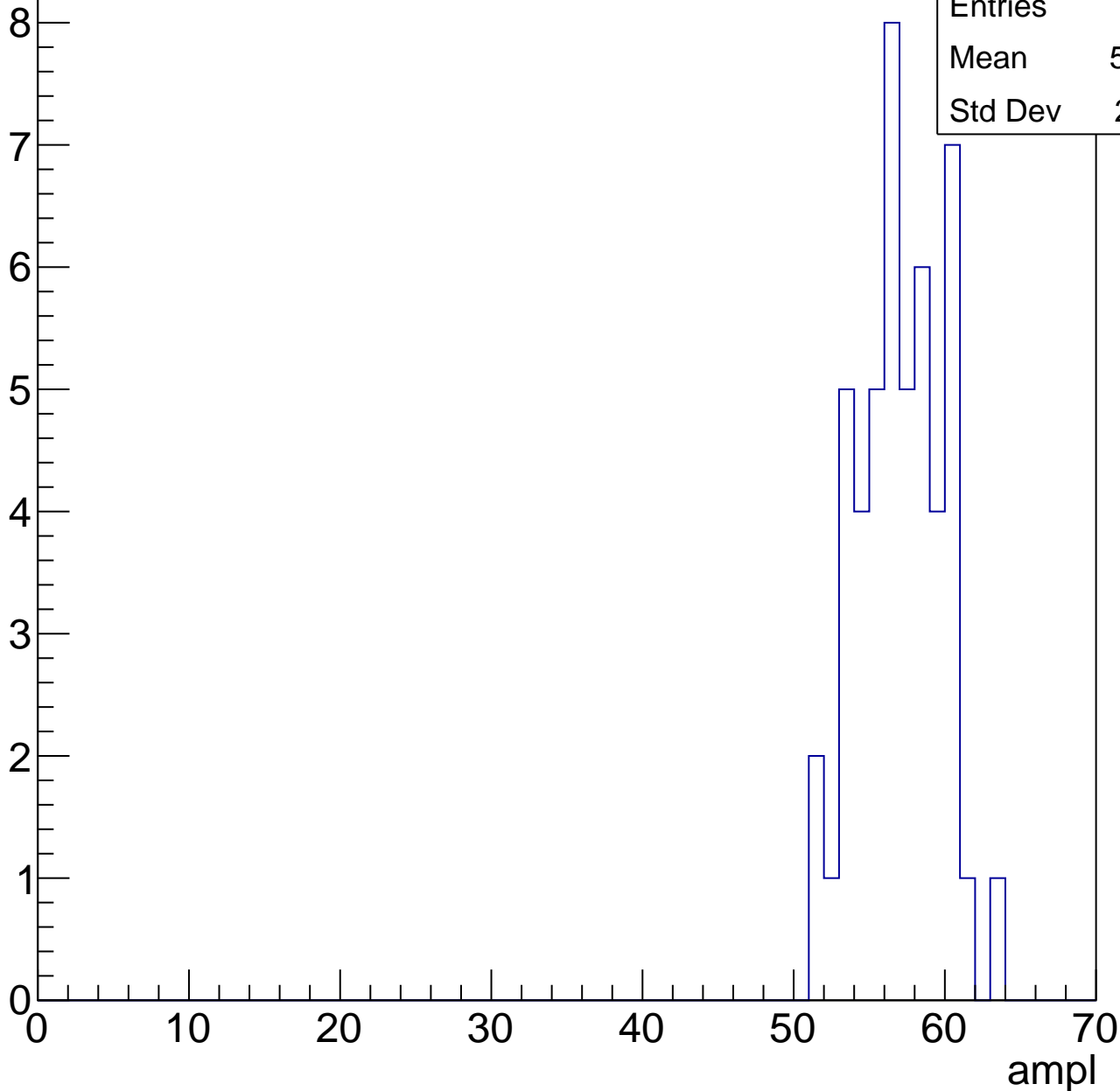


B1L102S, U11-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.55
Std Dev	2.741

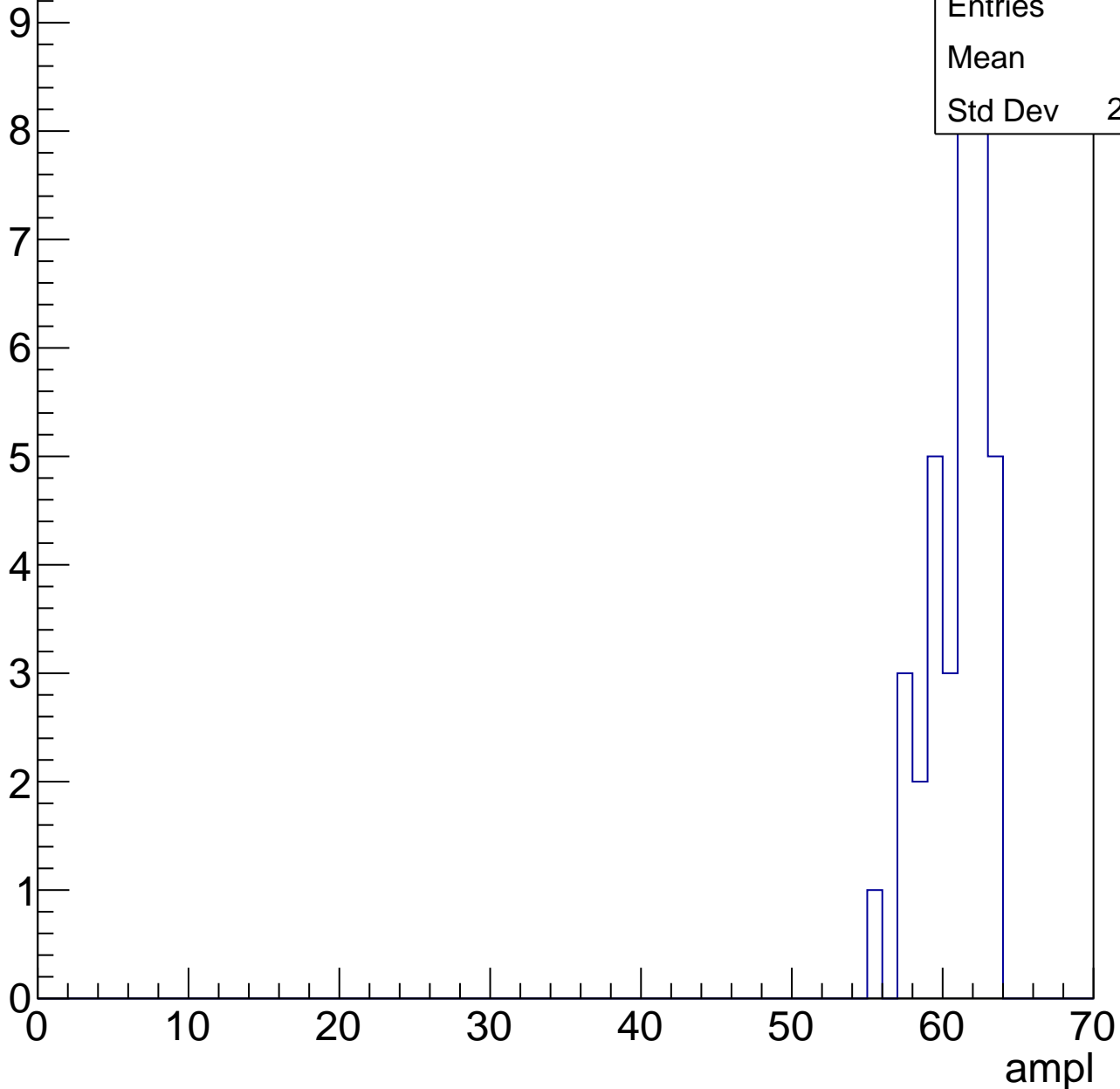


B1L102S, U11-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

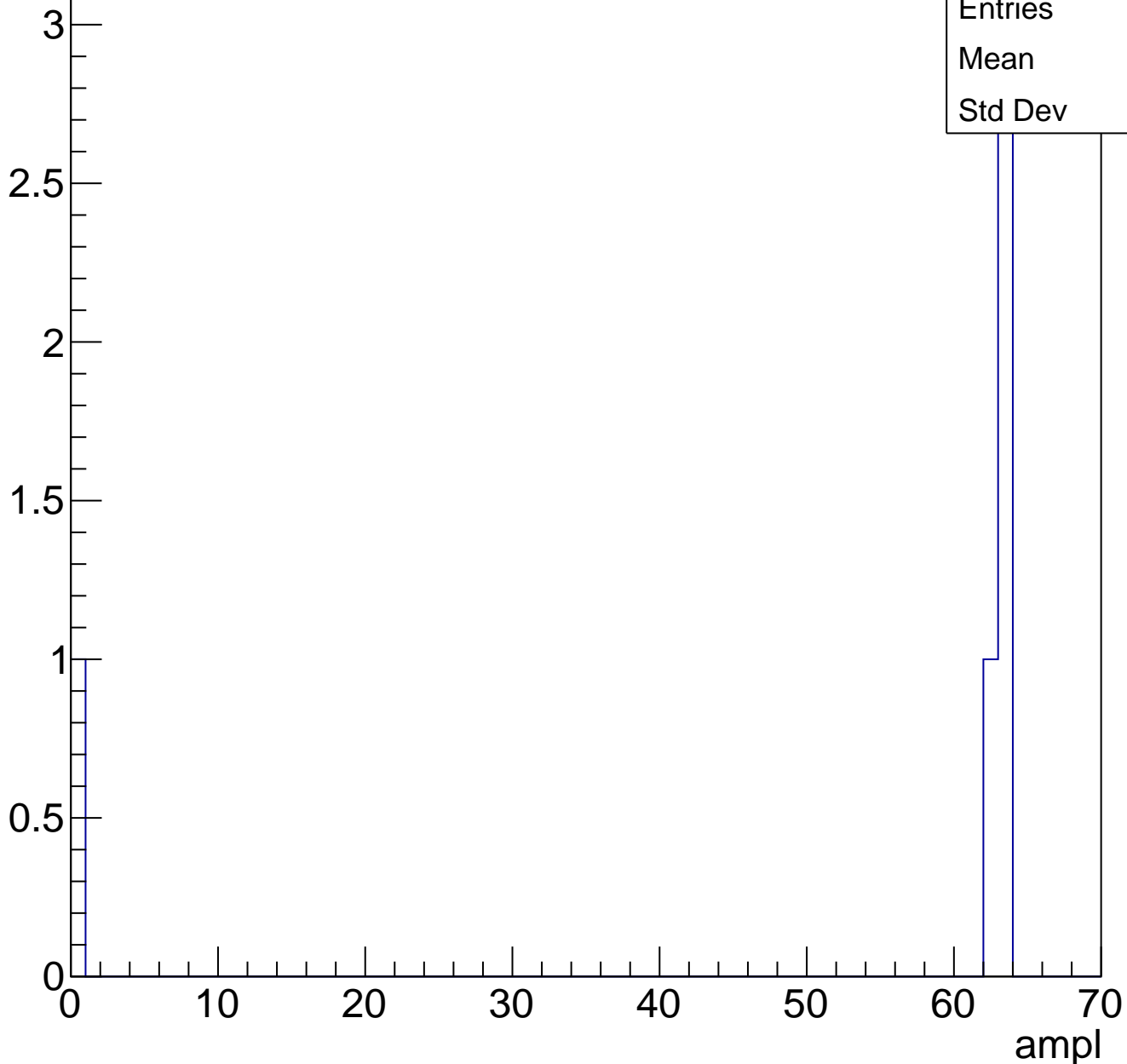
Entries	36
Mean	60.5
Std Dev	2.007



B1L102S, U11-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch100, adc0

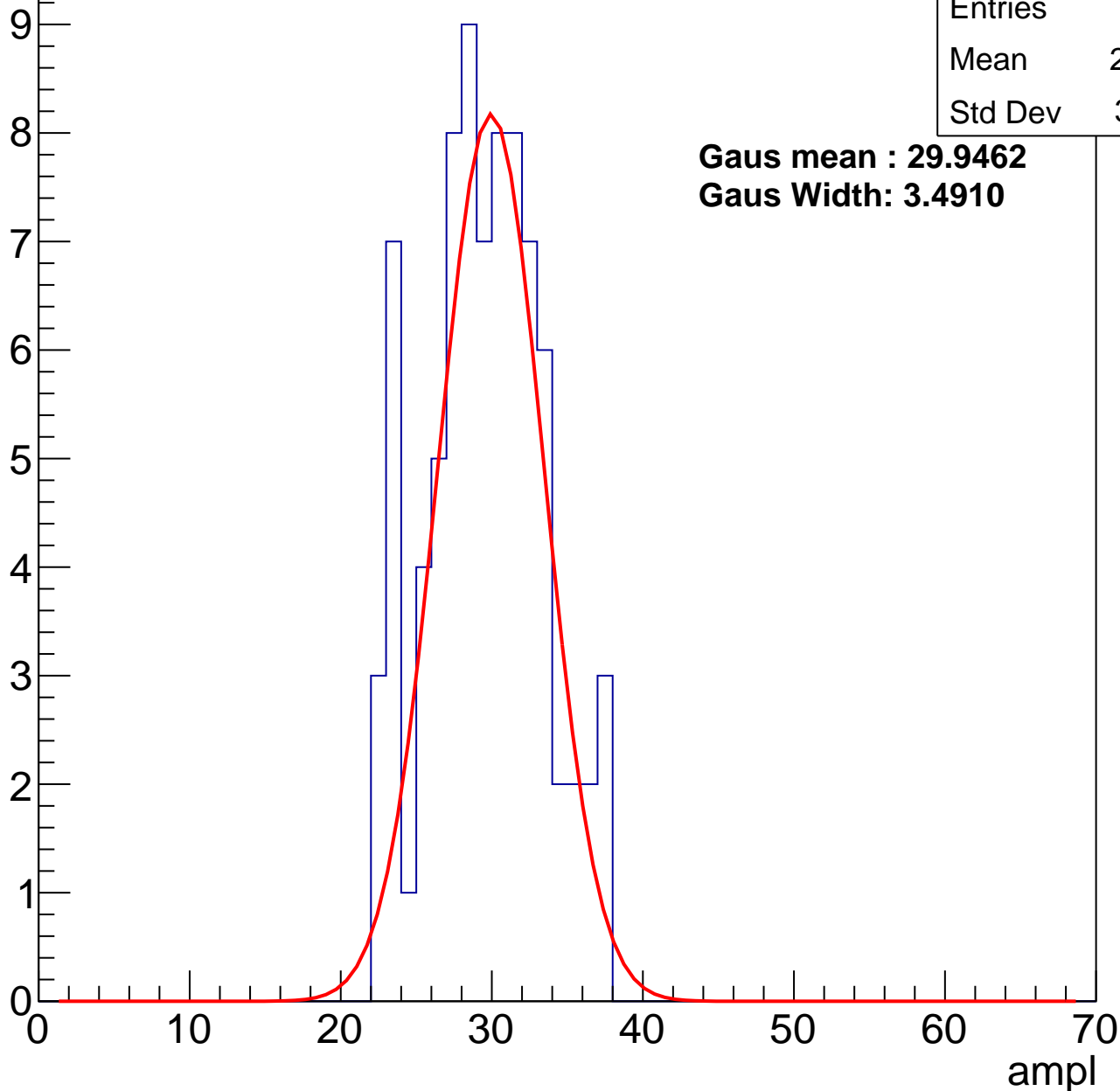
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	29.06
Std Dev	3.801

Gaus mean : 29.9462

Gaus Width: 3.4910



B1L102S, U11-ch100, adc1

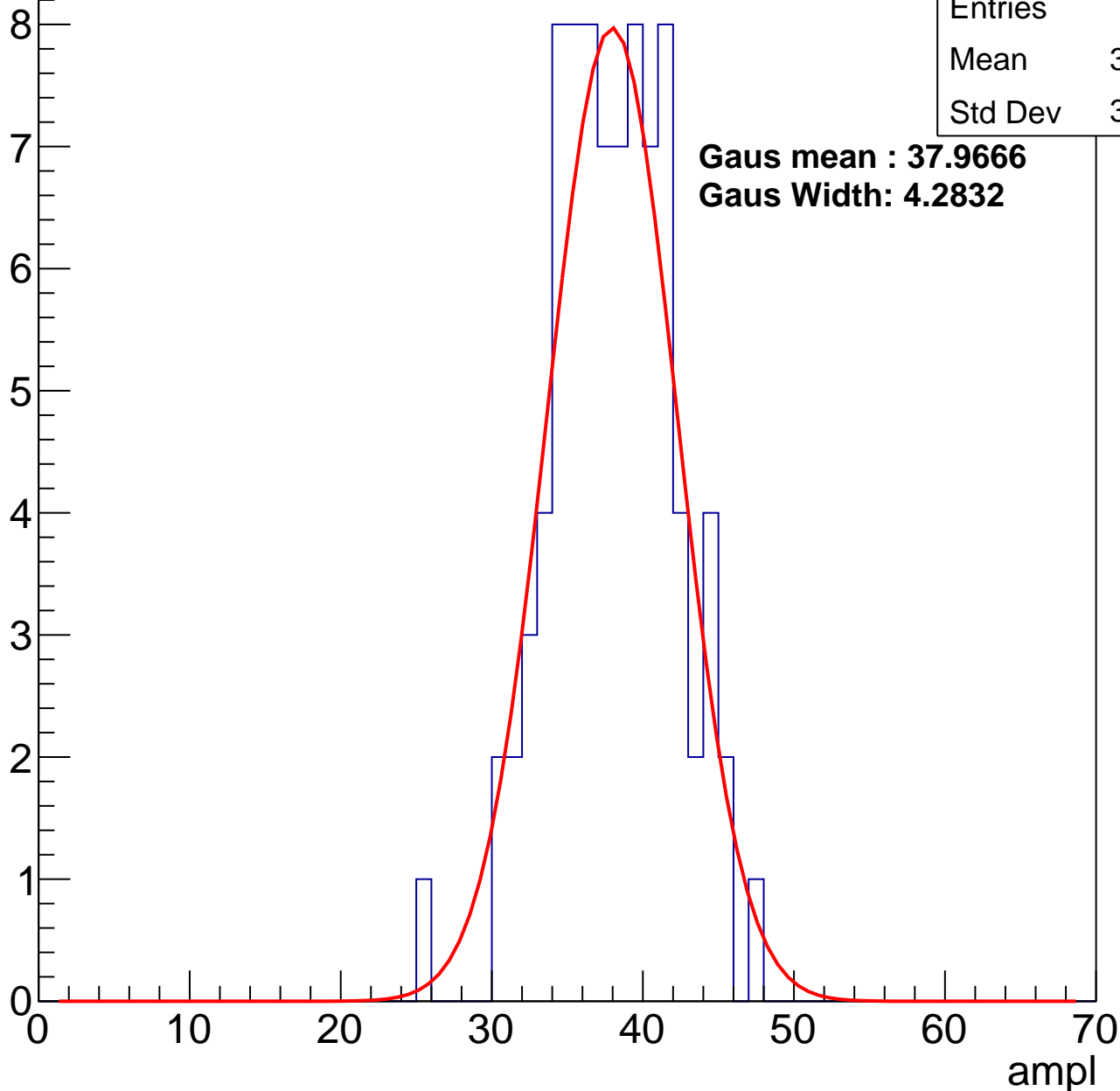
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	37.52
Std Dev	3.988

Gaus mean : 37.9666

Gaus Width: 4.2832



B1L102S, U11-ch100, adc2

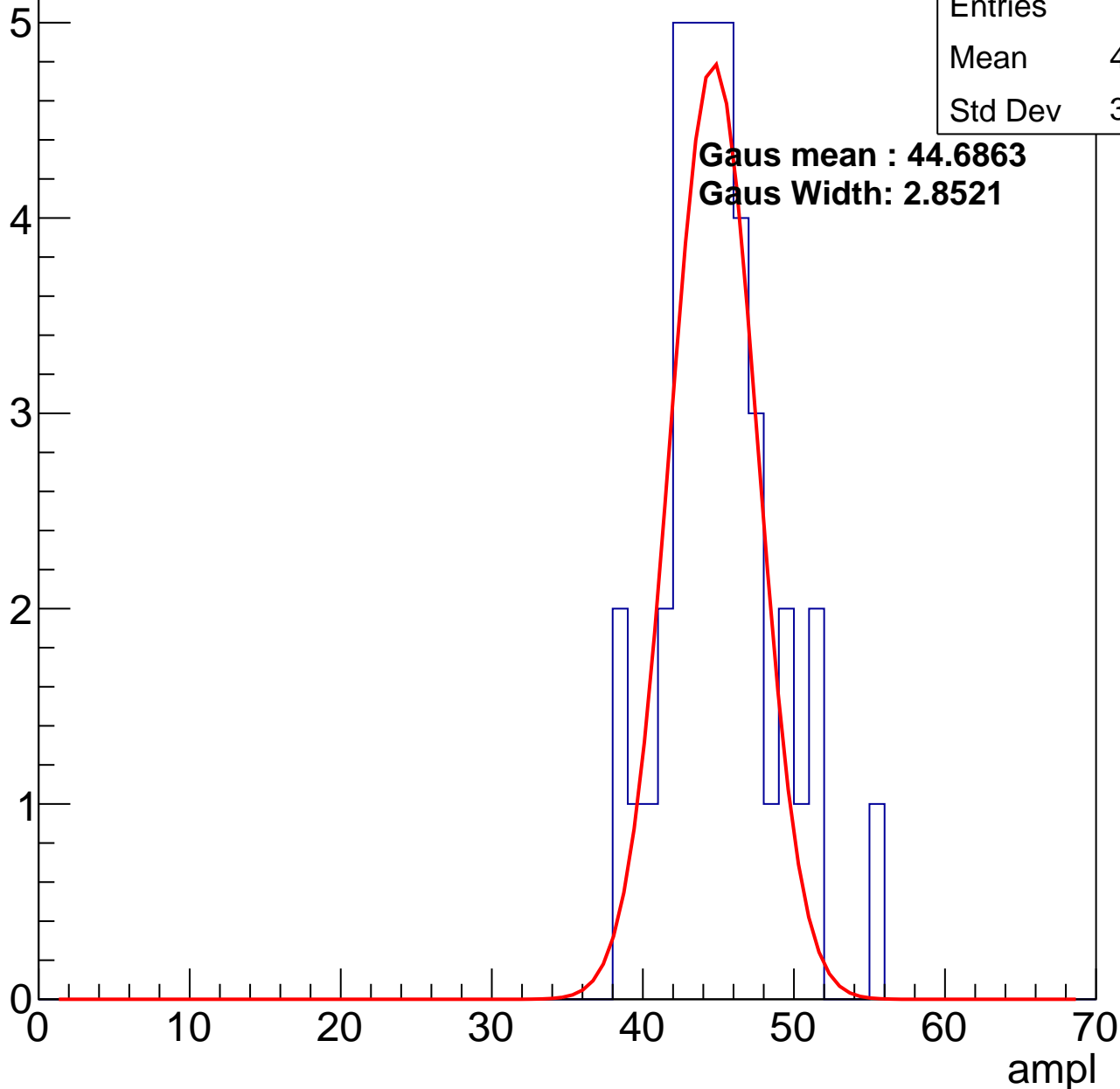
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	44.62
Std Dev	3.569

Gaus mean : 44.6863

Gaus Width: 2.8521

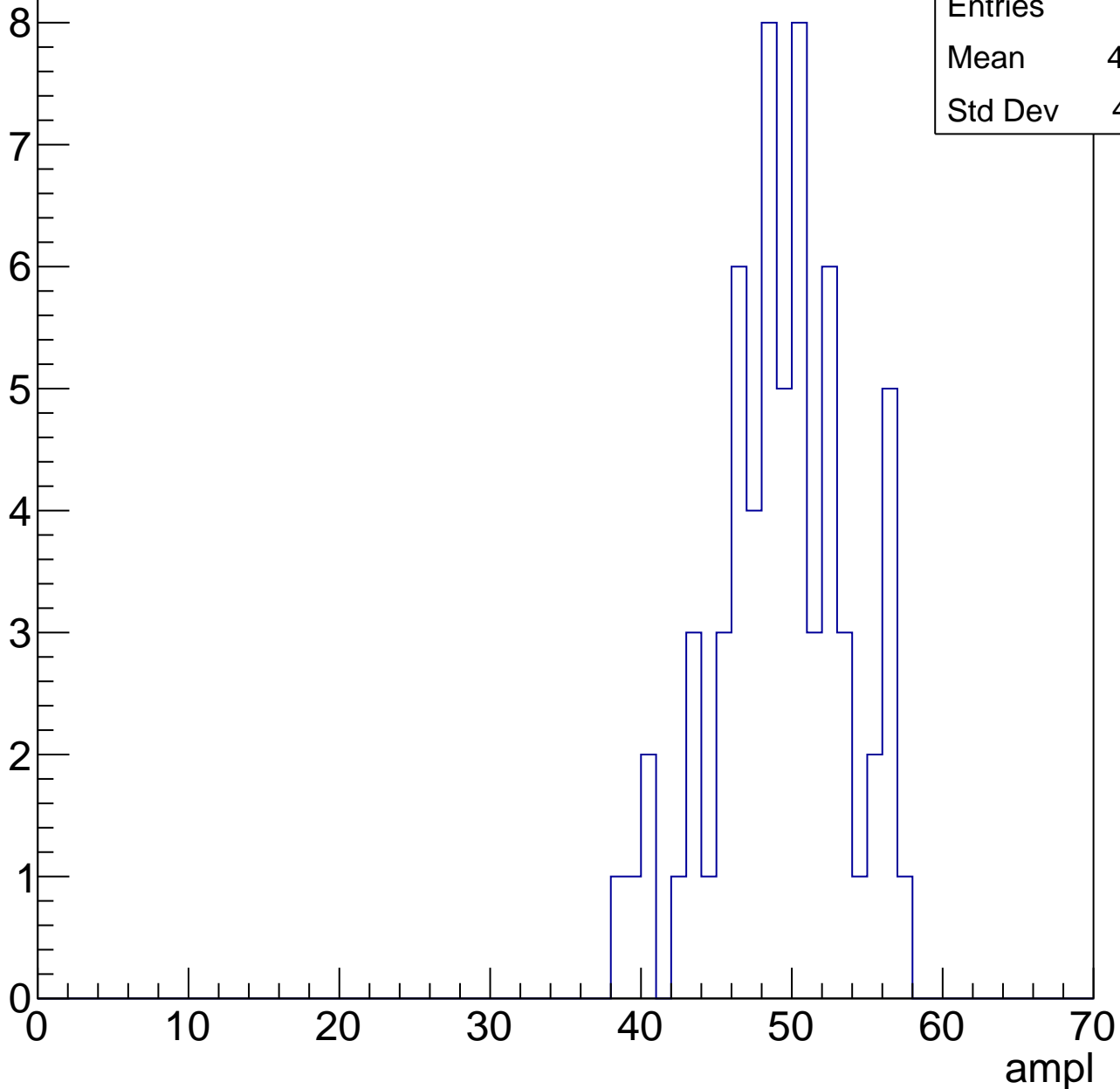


B1L102S, U11-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.83
Std Dev	4.361

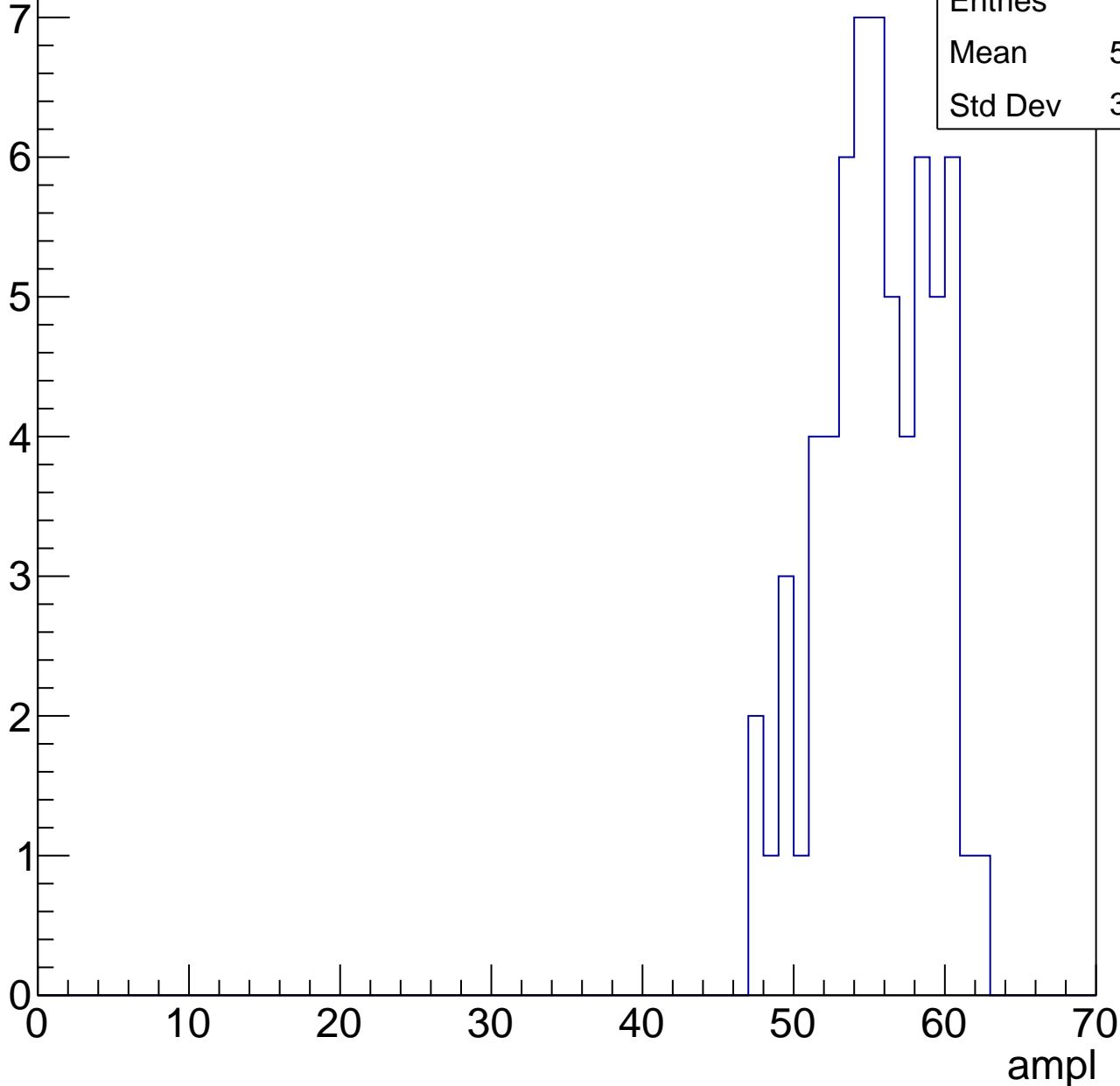


B1L102S, U11-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.02
Std Dev	3.632

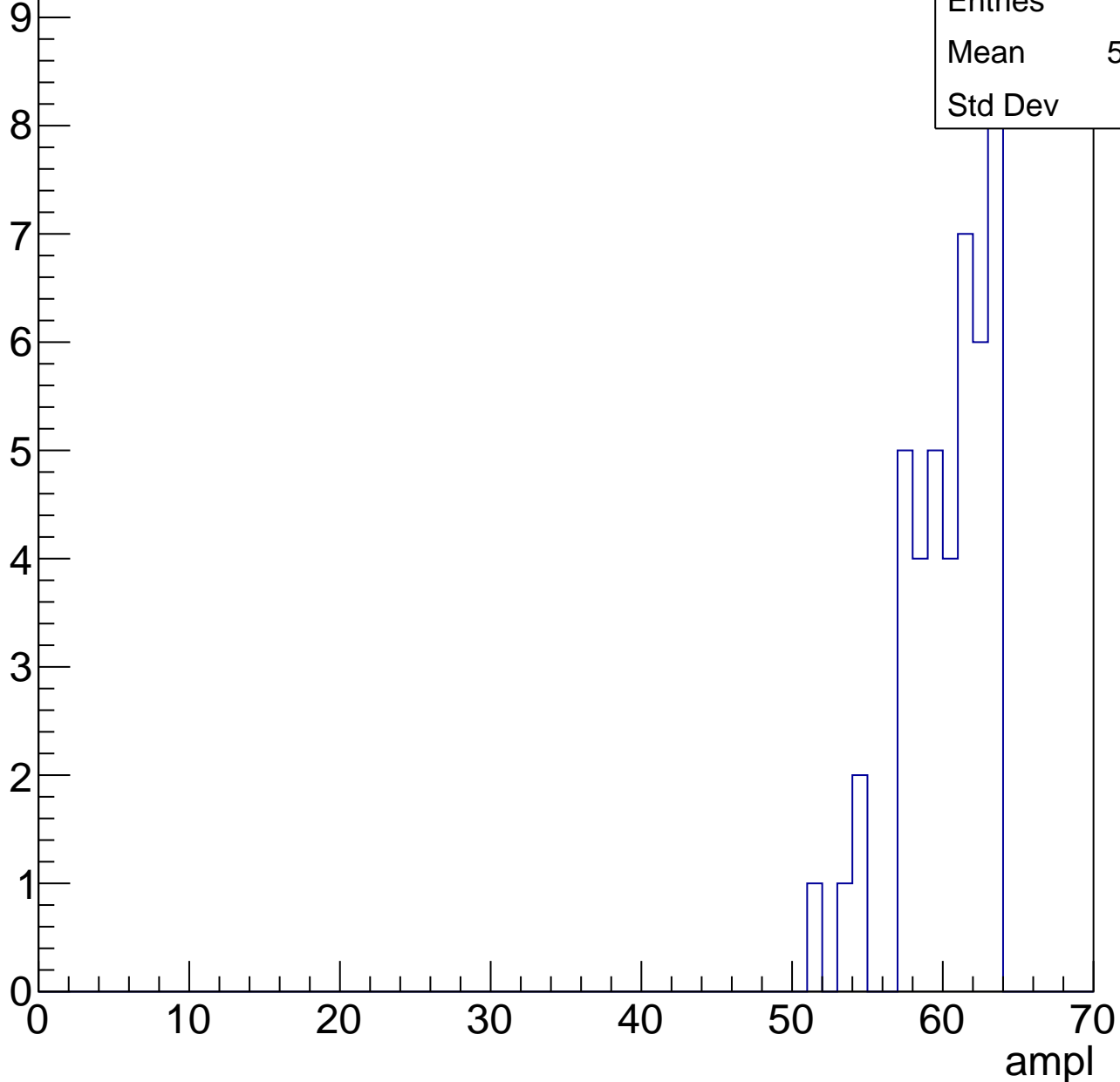


B1L102S, U11-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.77
Std Dev	2.93



B1L102S, U11-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

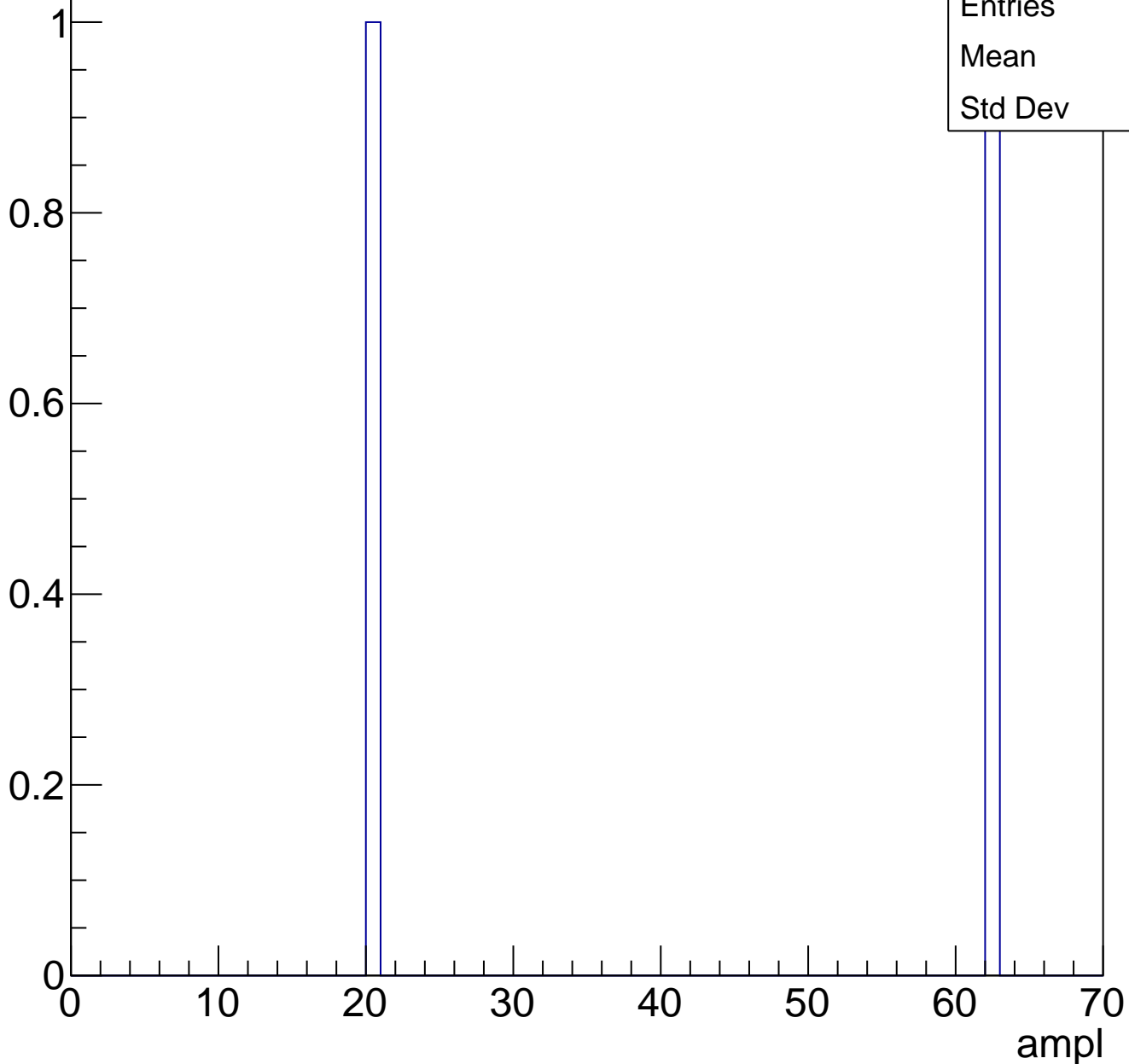
Entries	6
Mean	51.5
Std Dev	23.04

ampl

B1L102S, U11-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch101, adc0

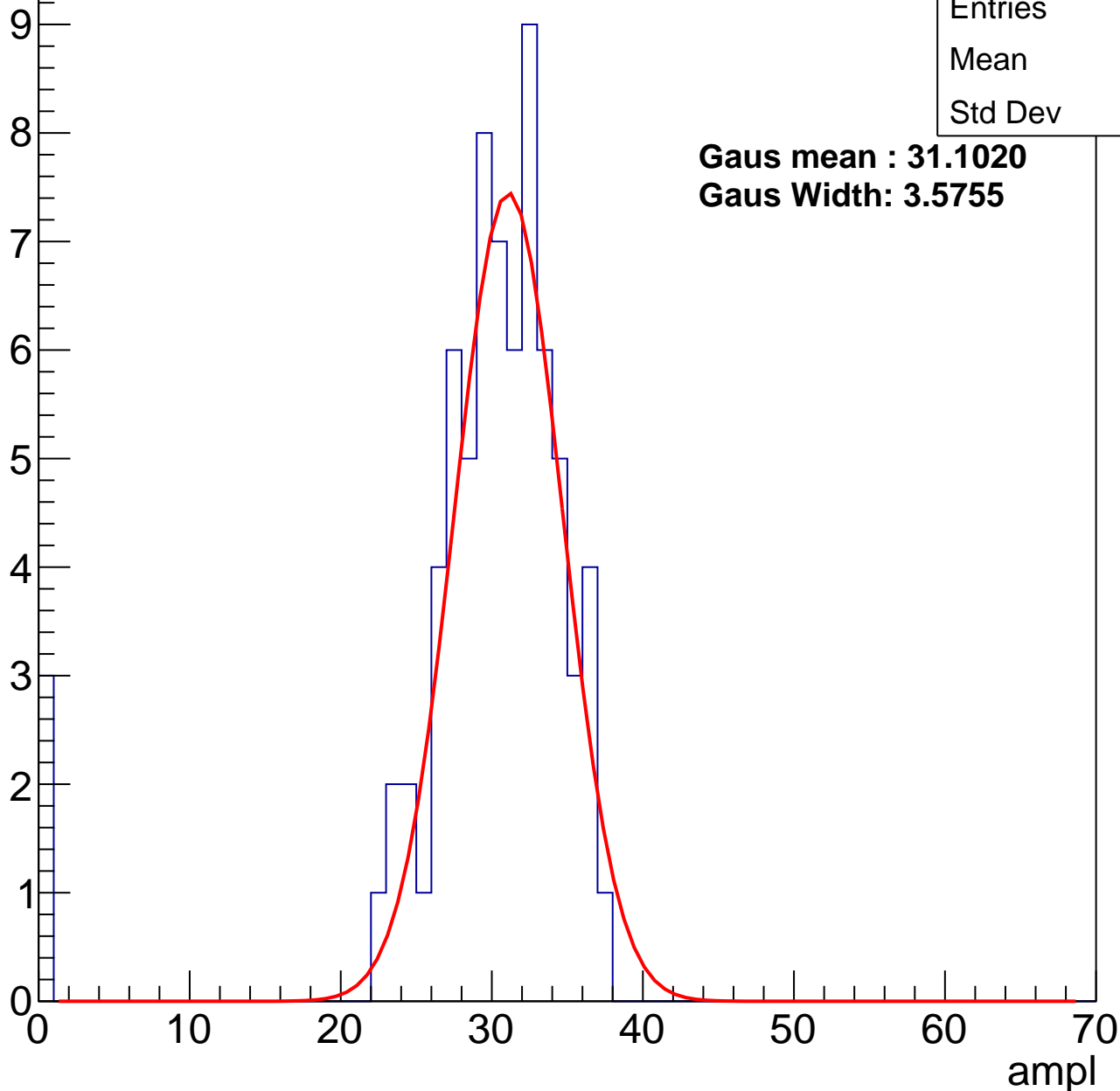
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29
Std Dev	6.9

Gaus mean : 31.1020

Gaus Width: 3.5755



B1L102S, U11-ch101, adc1

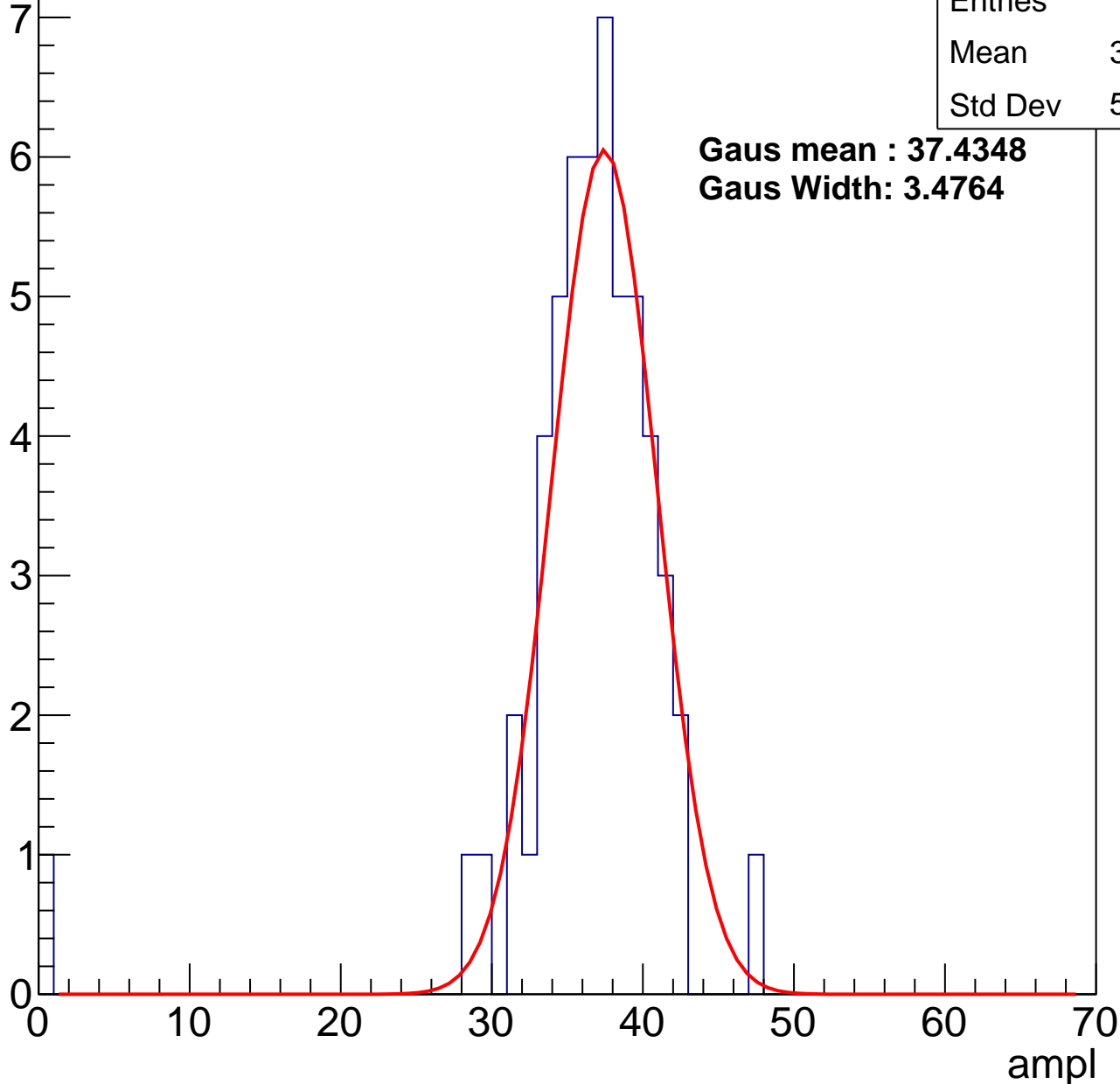
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.87
Std Dev	5.994

Gaus mean : 37.4348

Gaus Width: 3.4764



B1L102S, U11-ch101, adc2

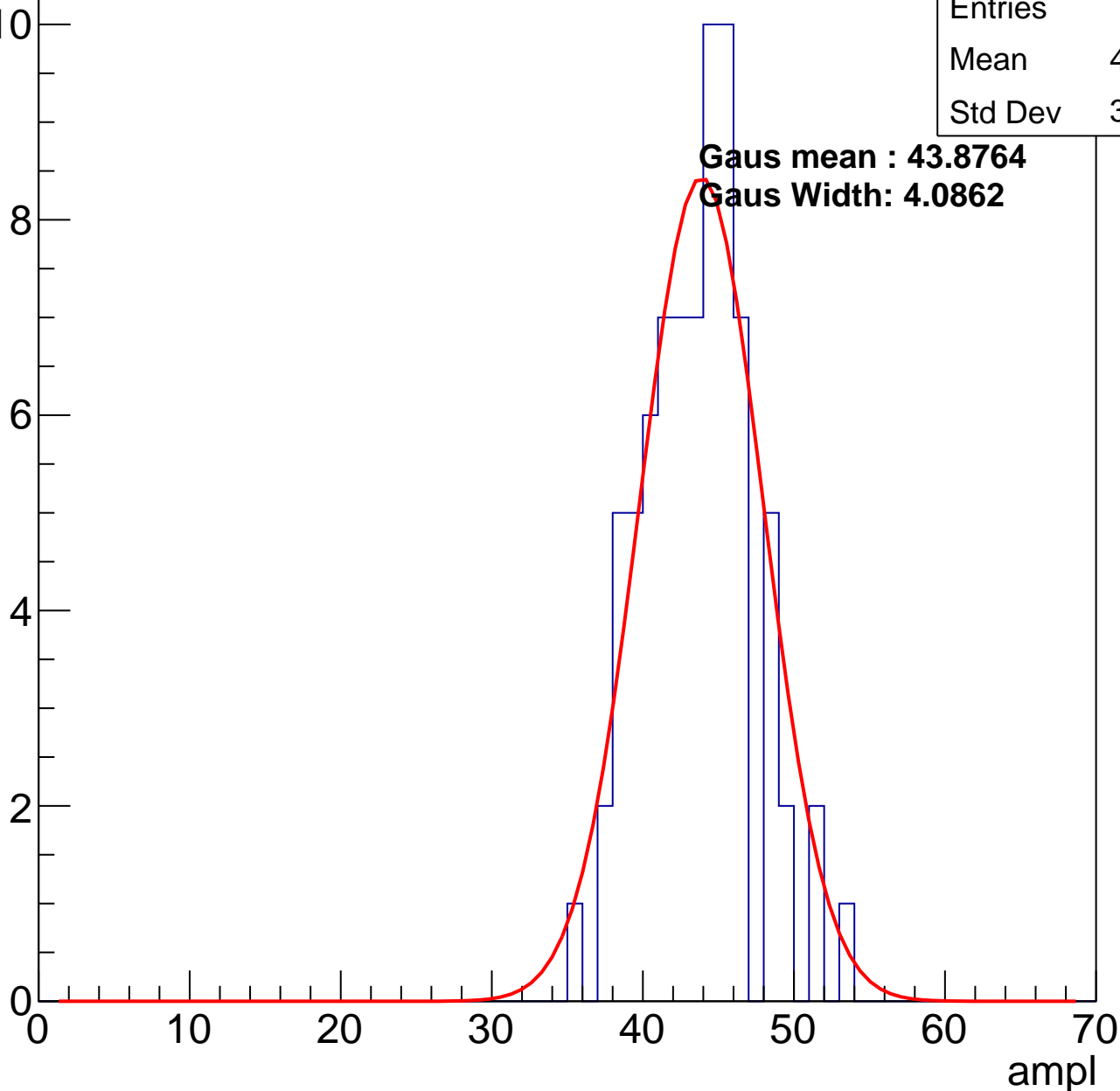
calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	43.13
Std Dev	3.543

Gaus mean : 43.8764

Gaus Width: 4.0862

Entry

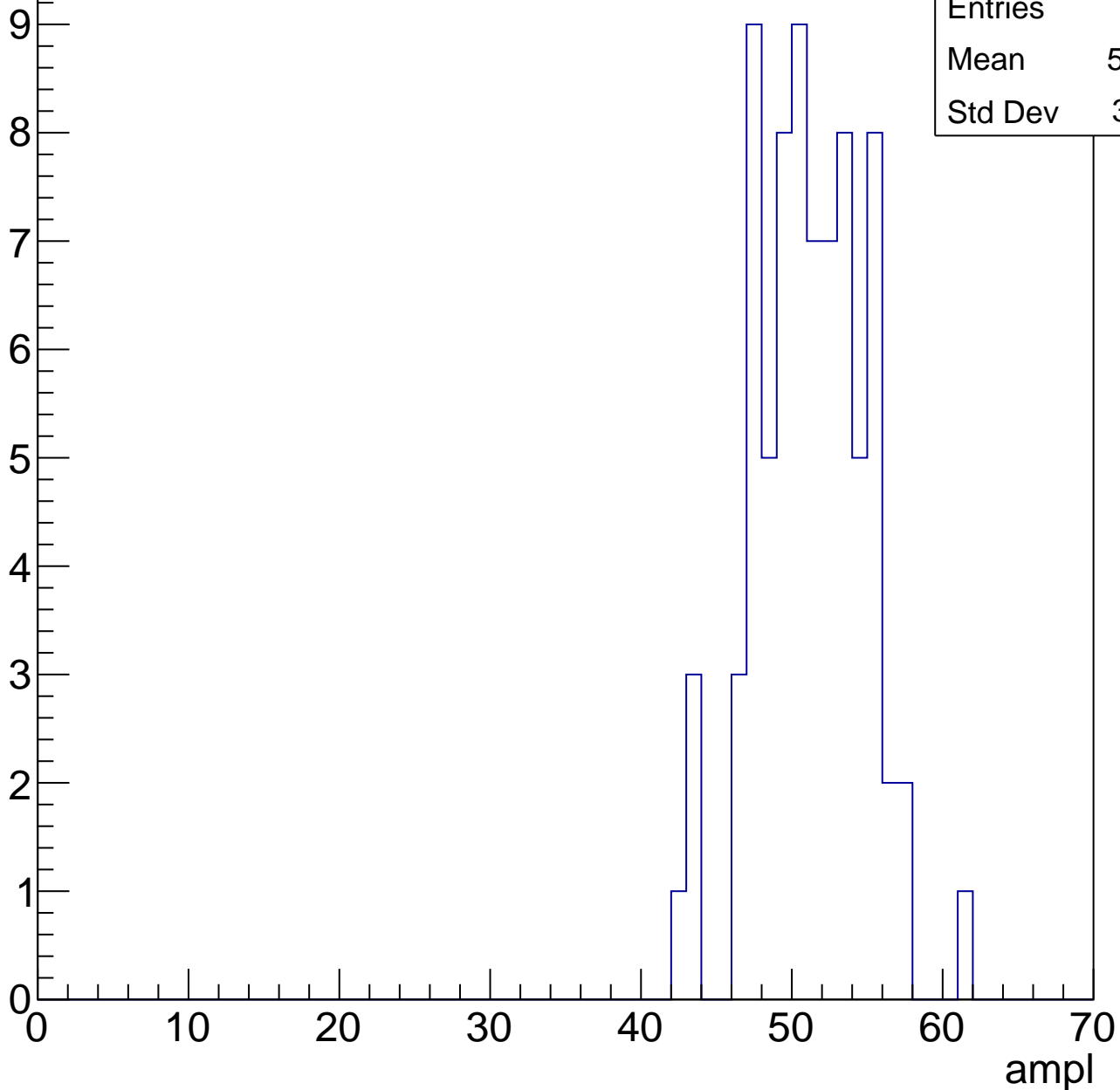


B1L102S, U11-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	50.72
Std Dev	3.591

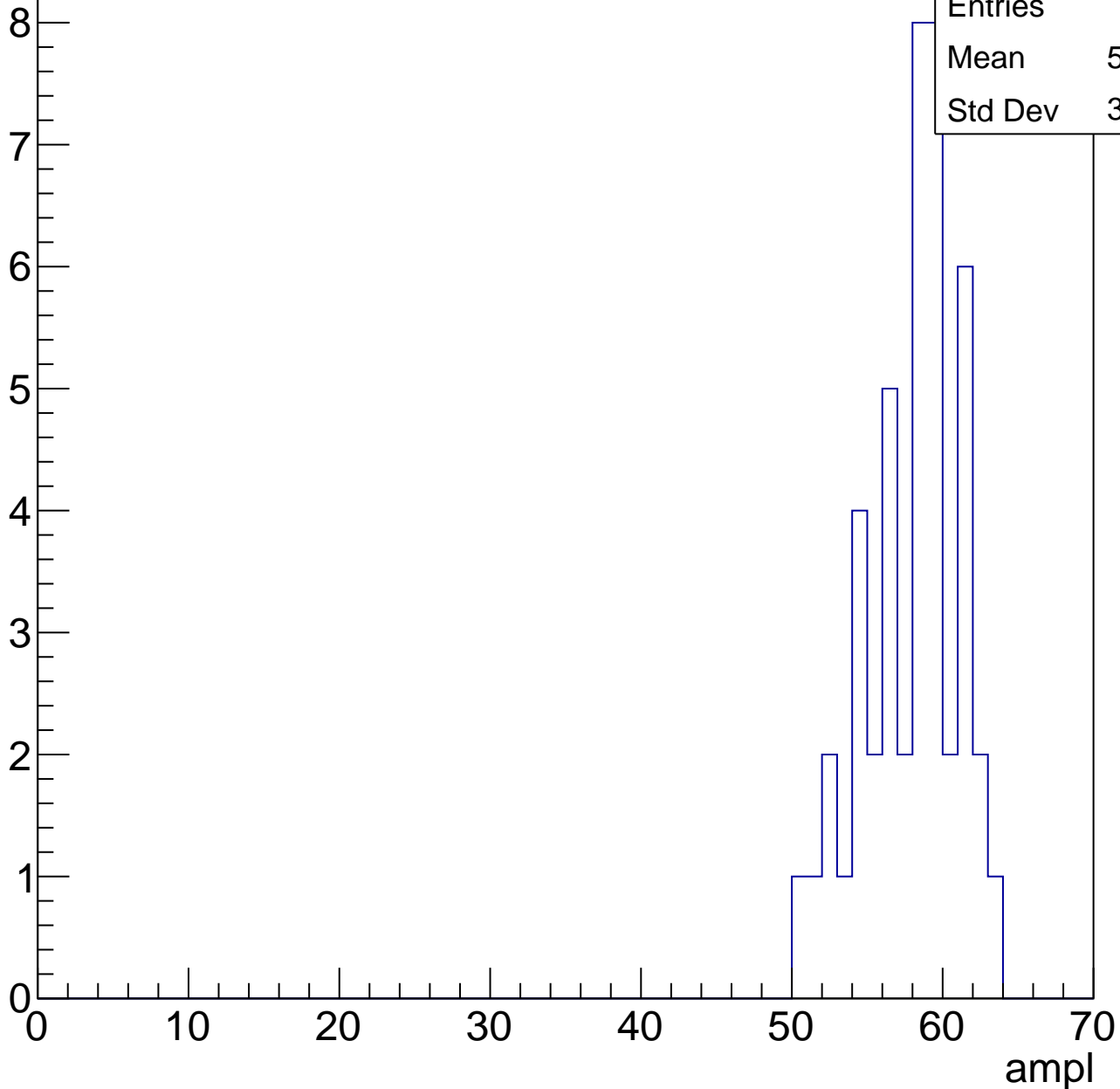


B1L102S, U11-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	57.49
Std Dev	3.088

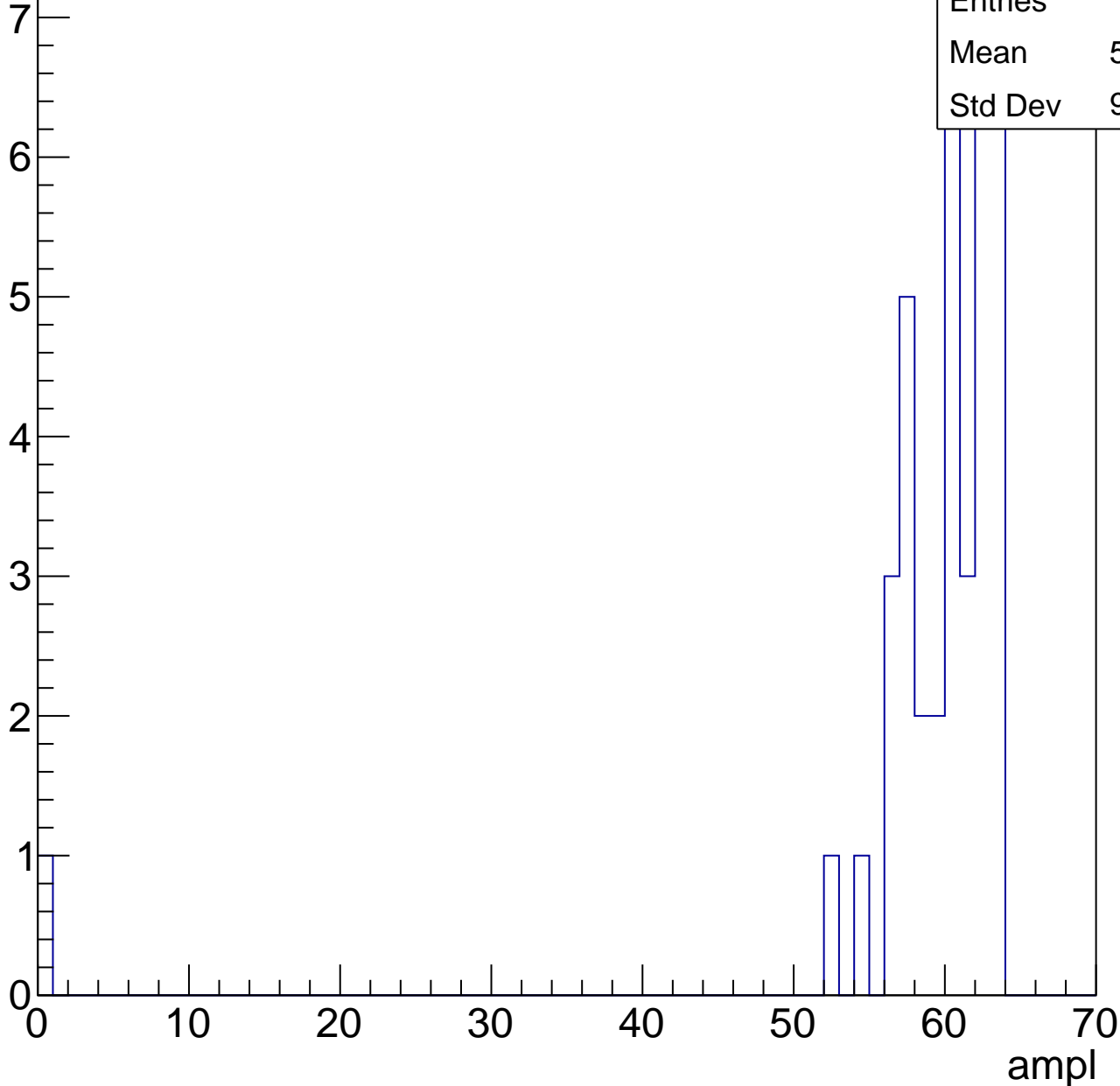


B1L102S, U11-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.23
Std Dev	9.838



B1L102S, U11-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

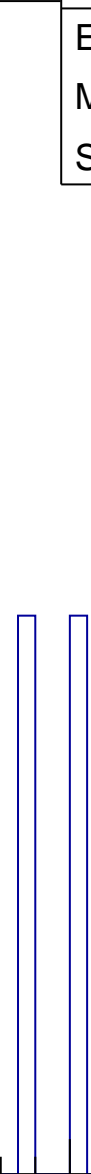
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	2.28

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch102, adc0

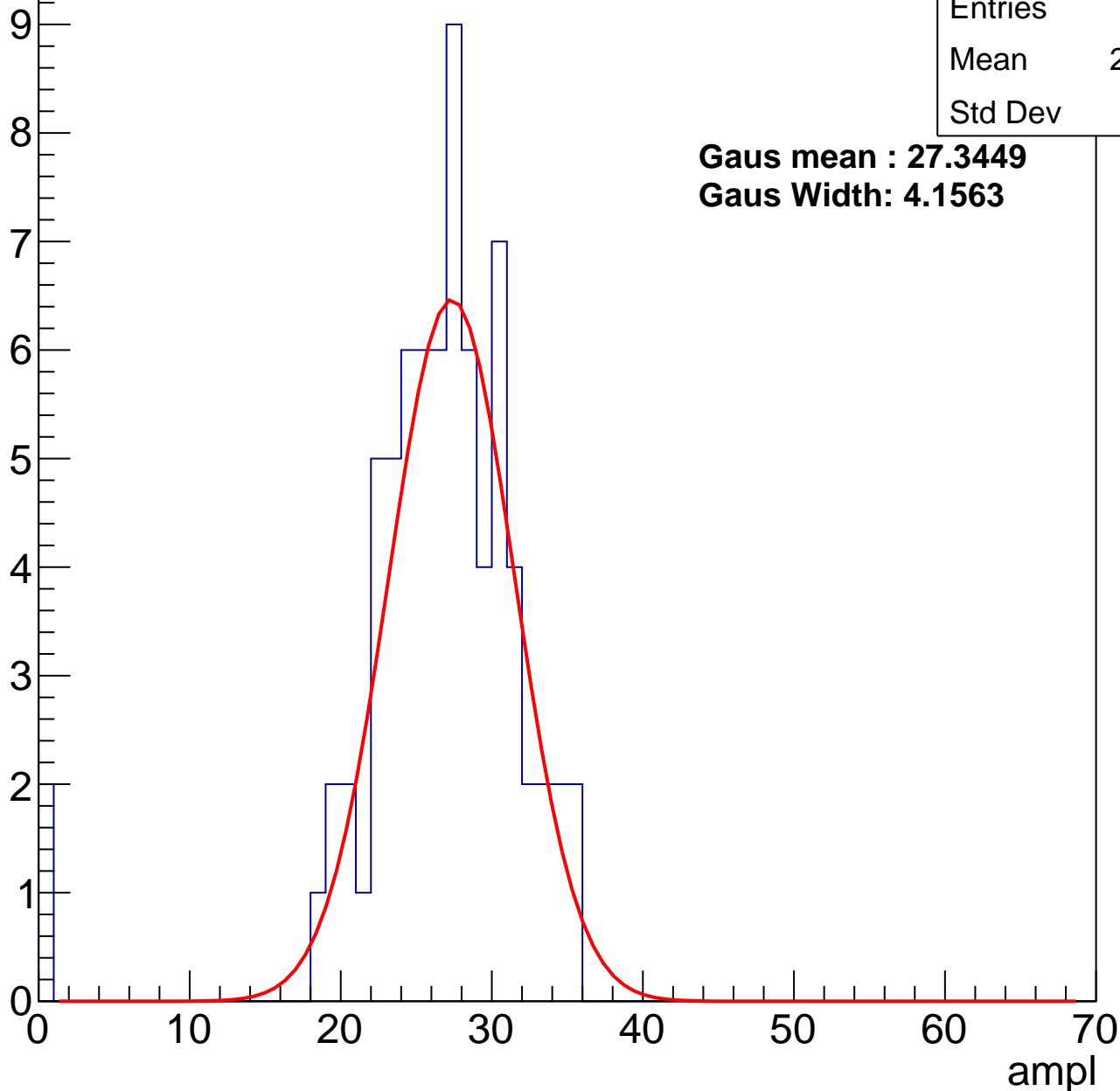
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	25.96
Std Dev	5.82

Gaus mean : 27.3449

Gaus Width: 4.1563



B1L102S, U11-ch102, adc1

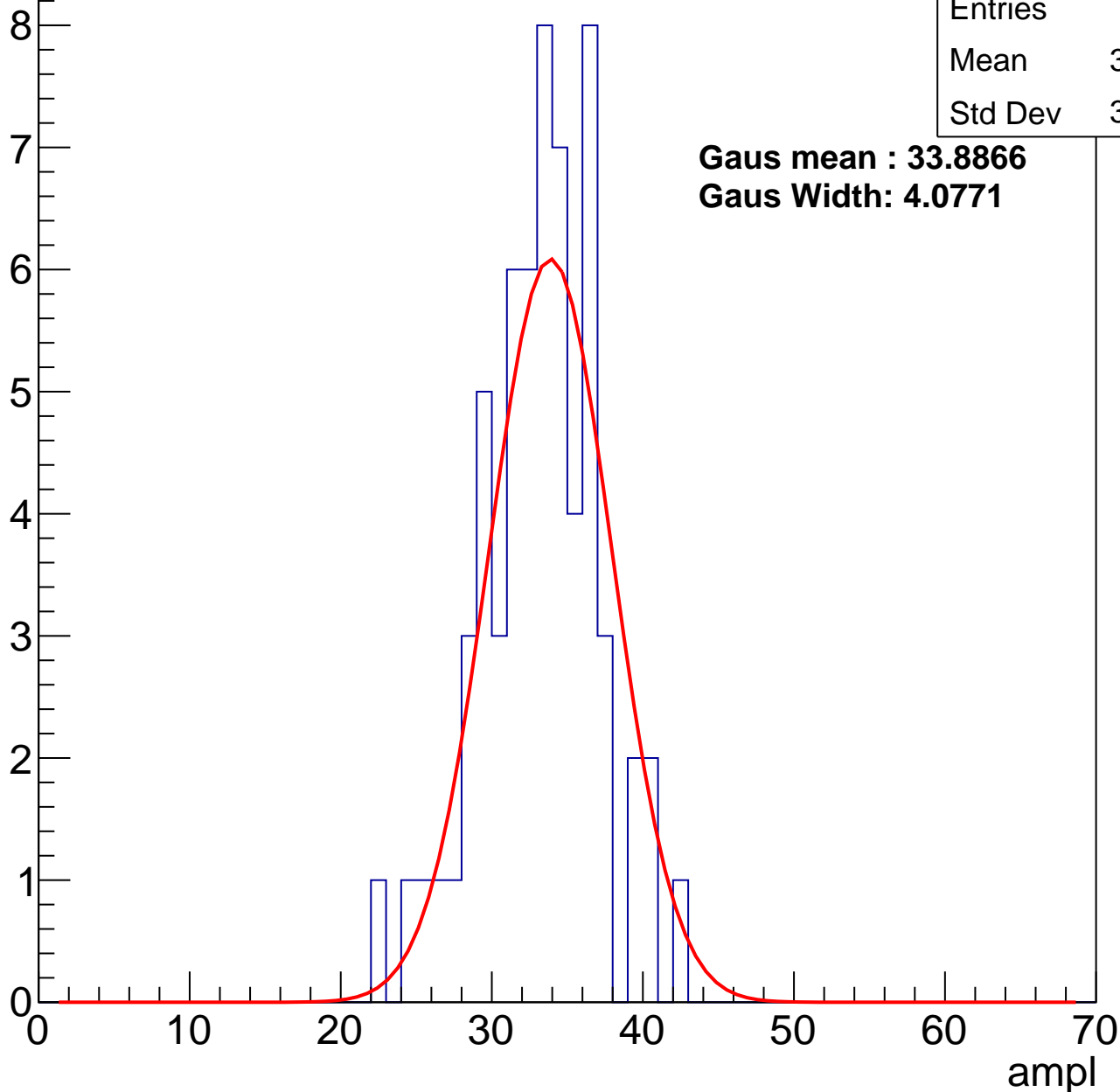
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	32.73
Std Dev	3.888

Gaus mean : 33.8866

Gaus Width: 4.0771



B1L102S, U11-ch102, adc2

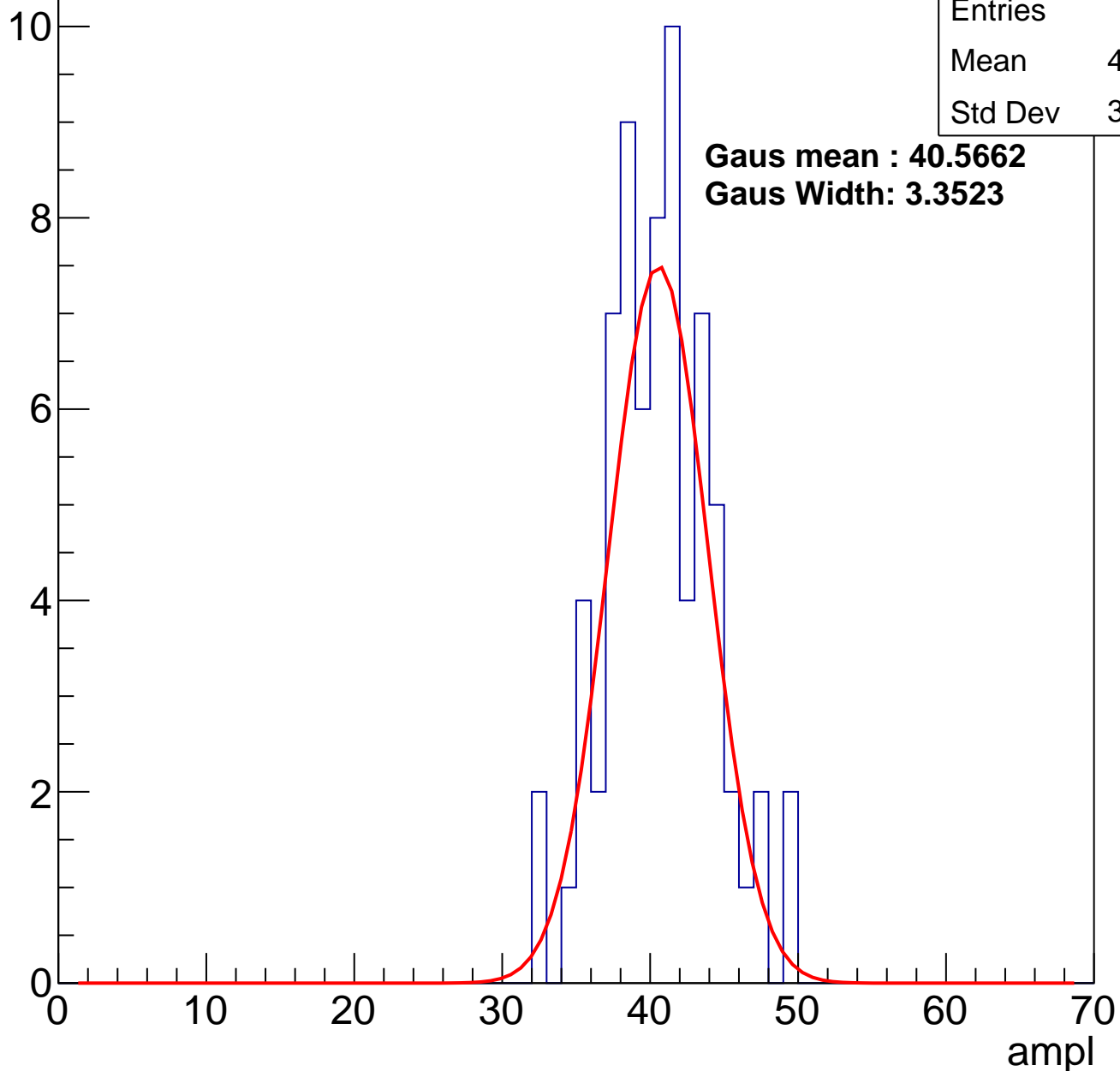
calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	40.17
Std Dev	3.563

Gaus mean : 40.5662

Gaus Width: 3.3523

Entry

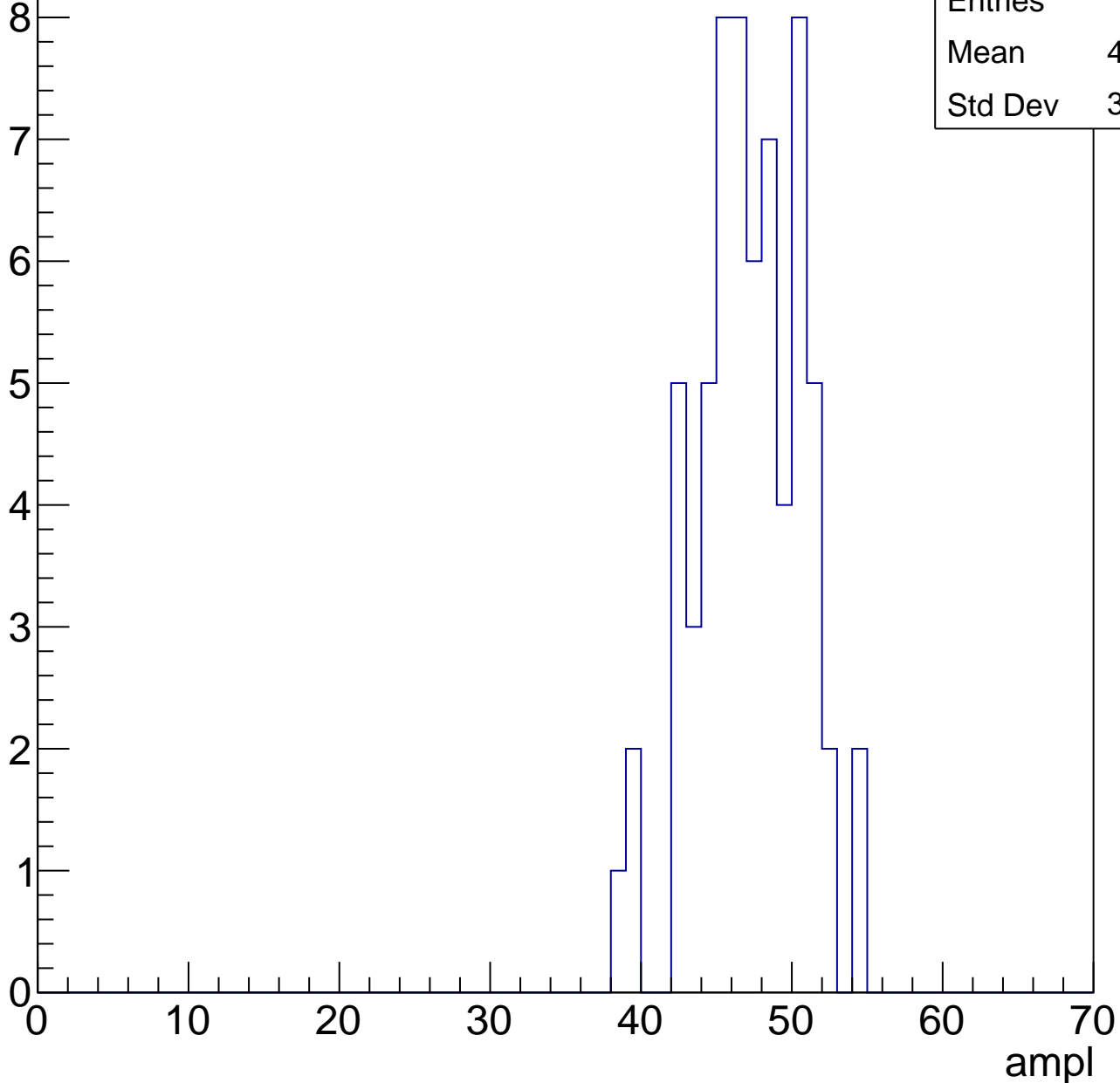


B1L102S, U11-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.73
Std Dev	3.449

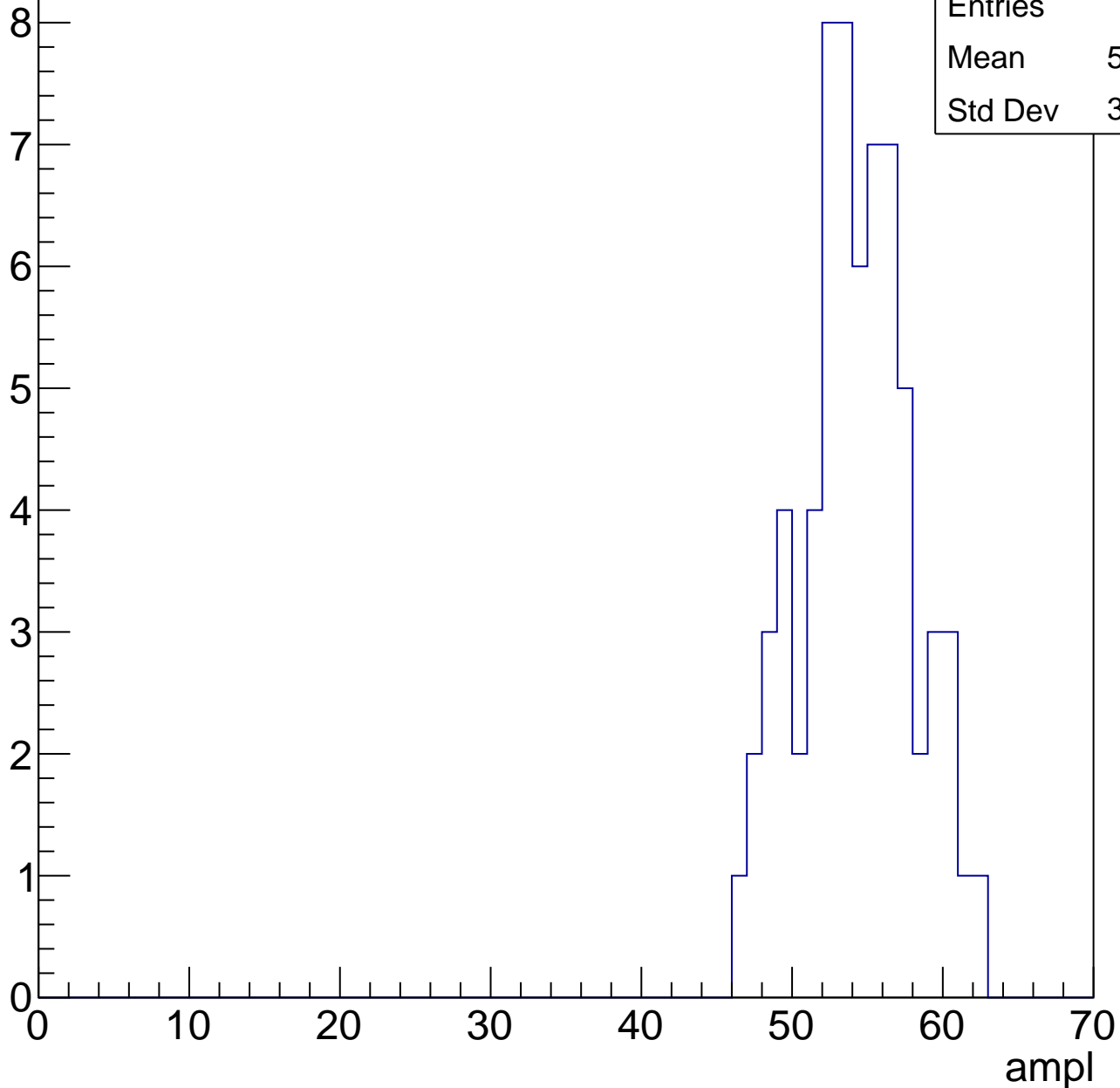


B1L102S, U11-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	53.82
Std Dev	3.632

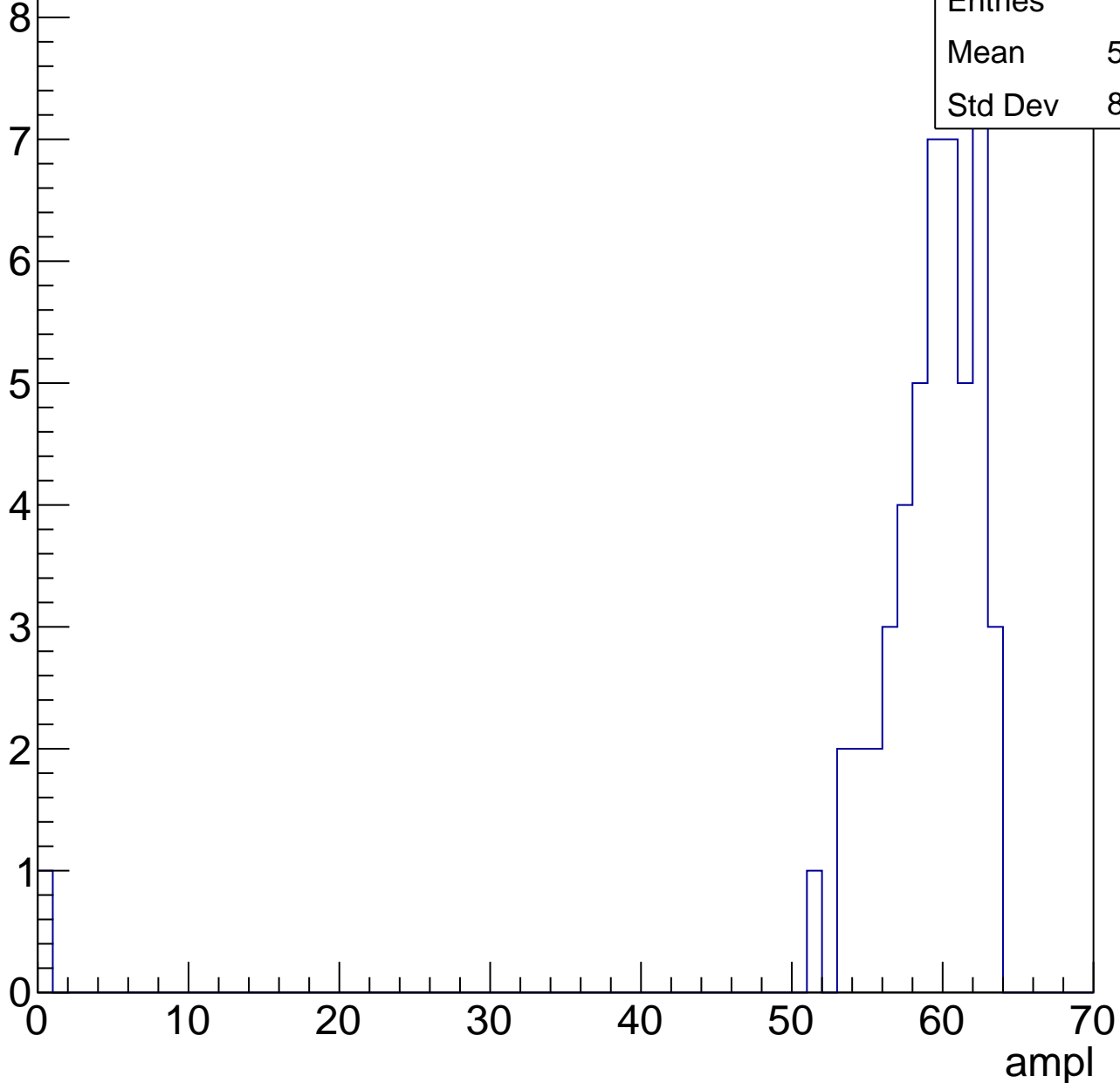


B1L102S, U11-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.68
Std Dev	8.726

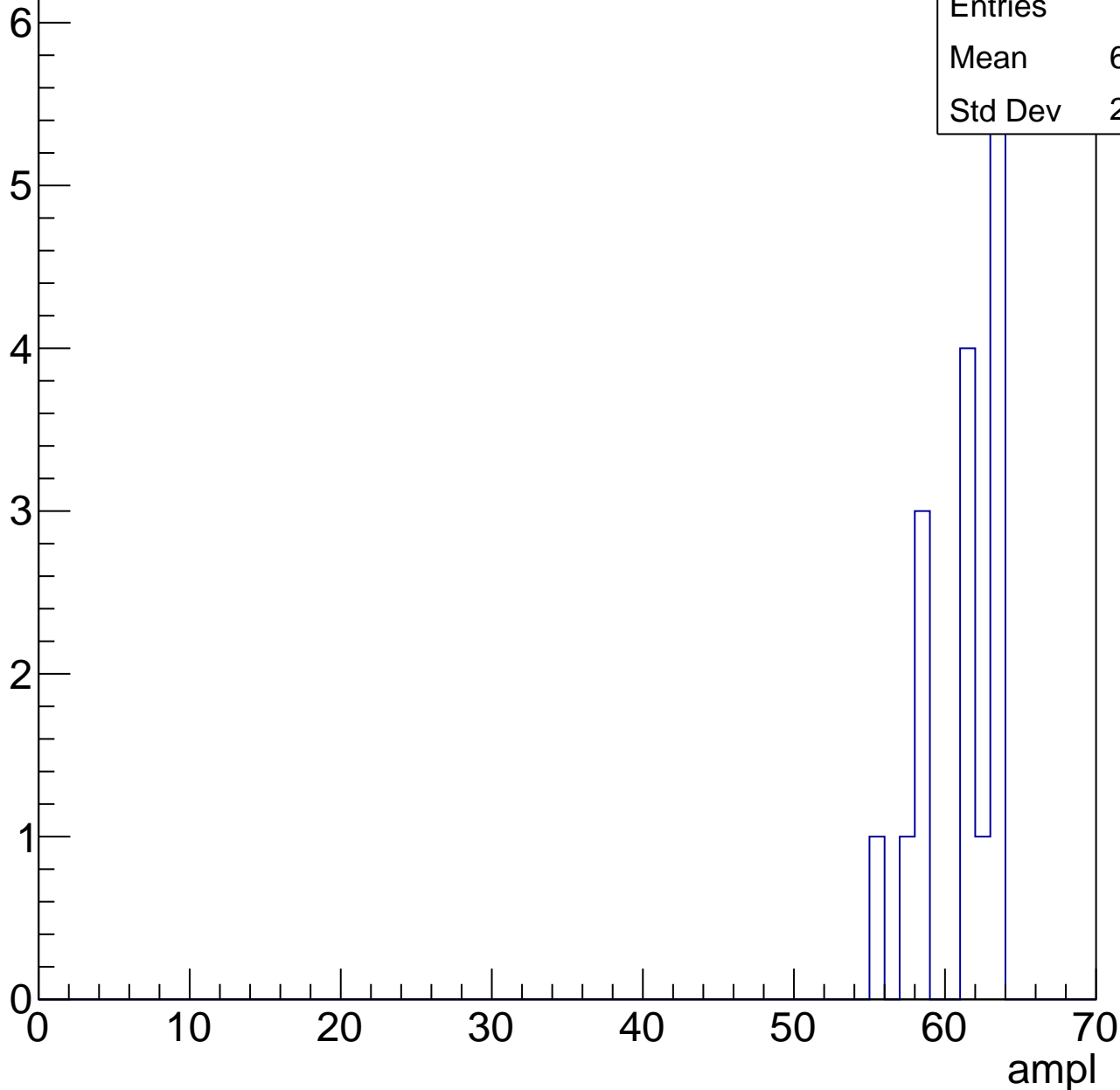


B1L102S, U11-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	60.62
Std Dev	2.522



B1L102S, U11-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U11-ch103, adc0

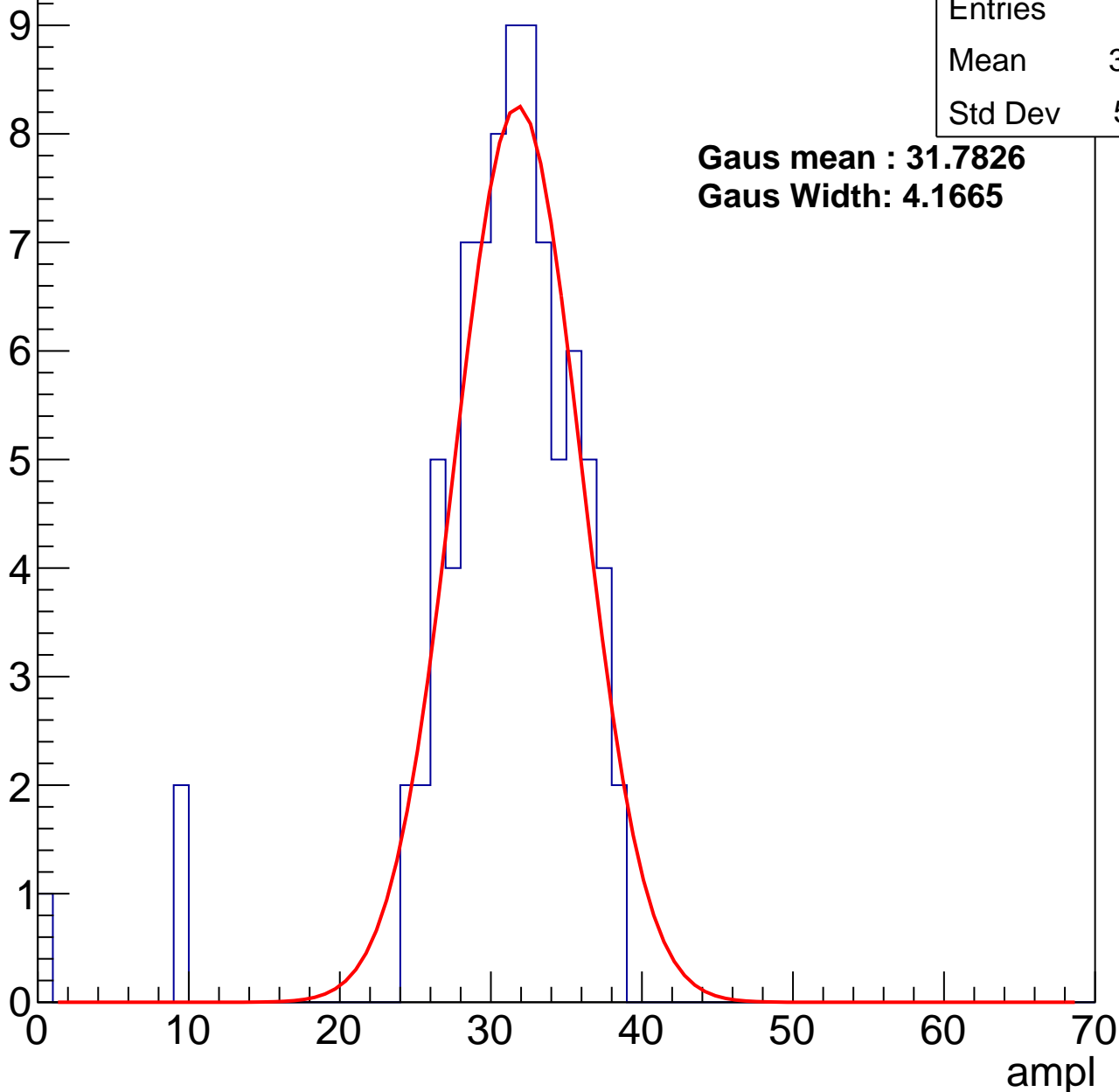
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	30.29
Std Dev	5.831

Gaus mean : 31.7826

Gaus Width: 4.1665



B1L102S, U11-ch103, adc1

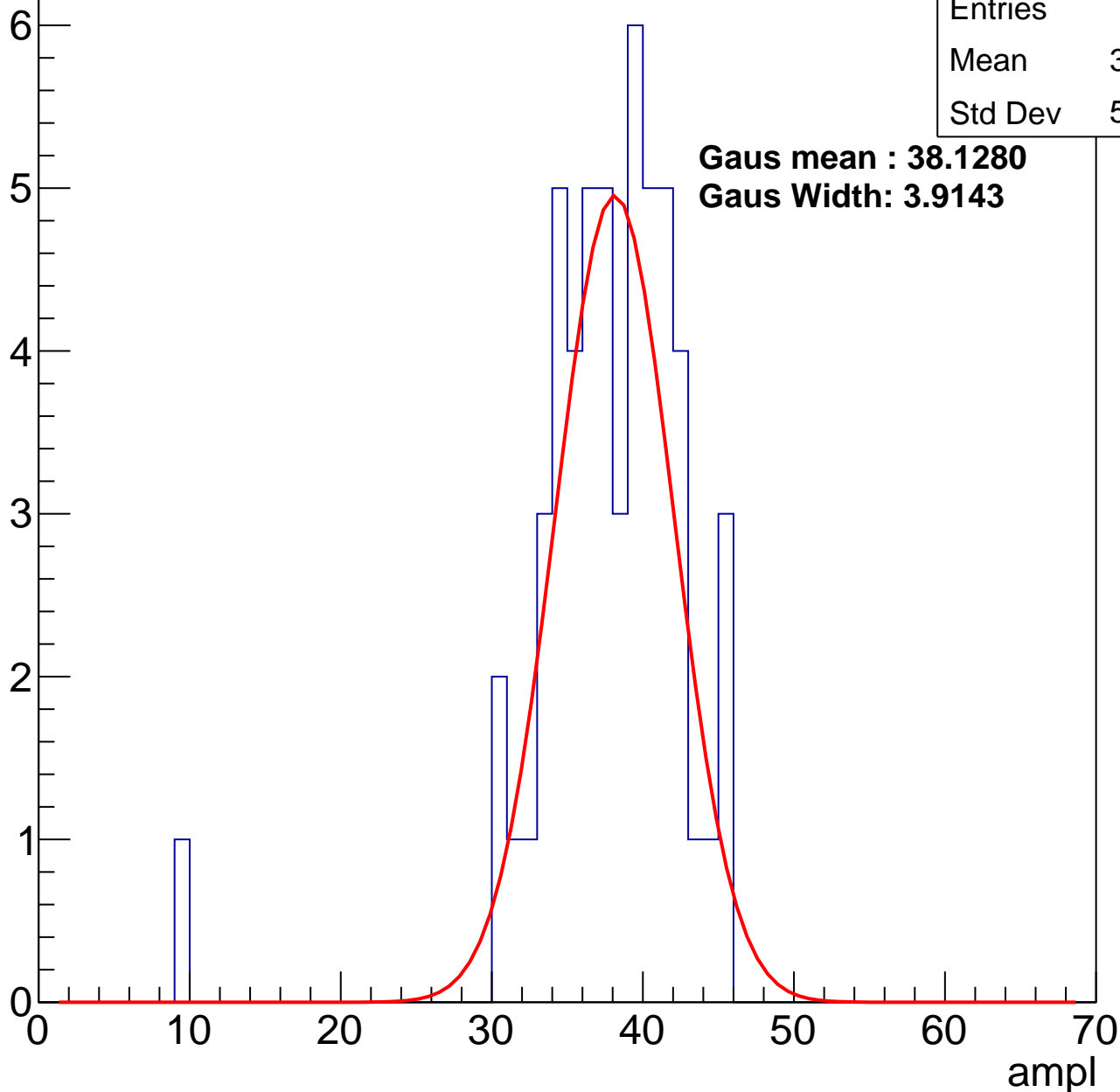
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.25
Std Dev	5.364

Gaus mean : 38.1280

Gaus Width: 3.9143



B1L102S, U11-ch103, adc2

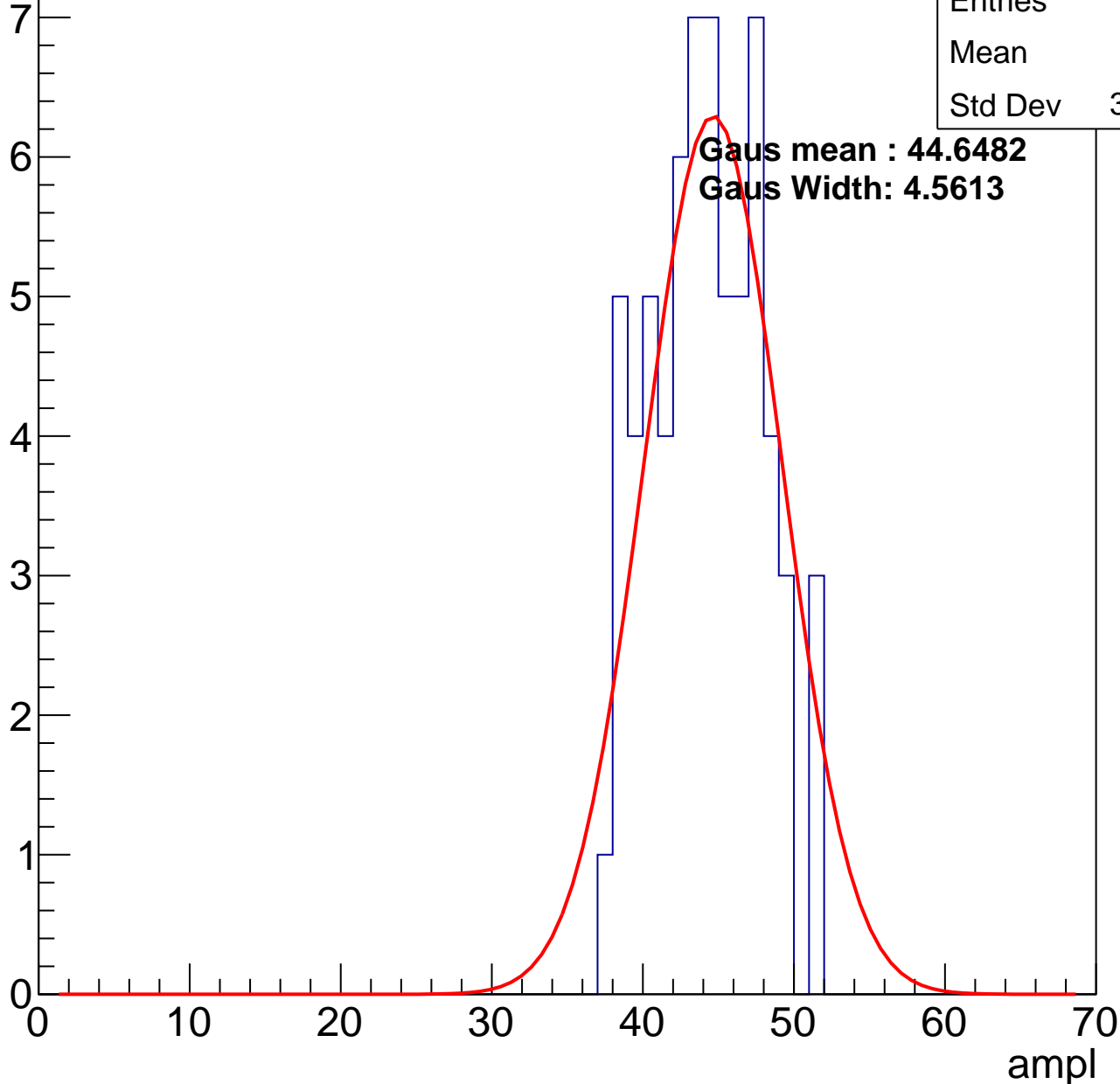
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.7
Std Dev	3.576

Gaus mean : 44.6482

Gaus Width: 4.5613

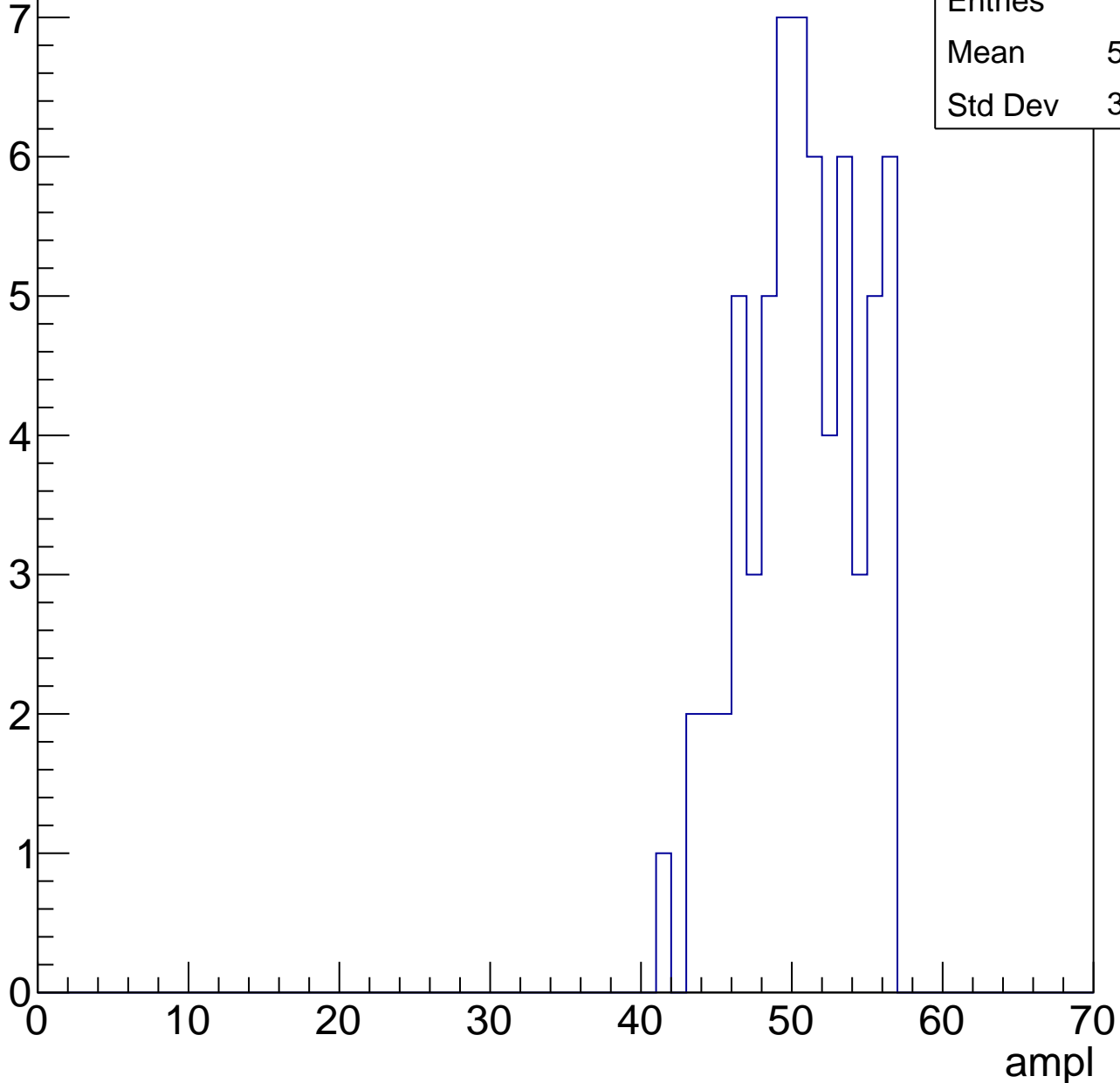


B1L102S, U11-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.22
Std Dev	3.744

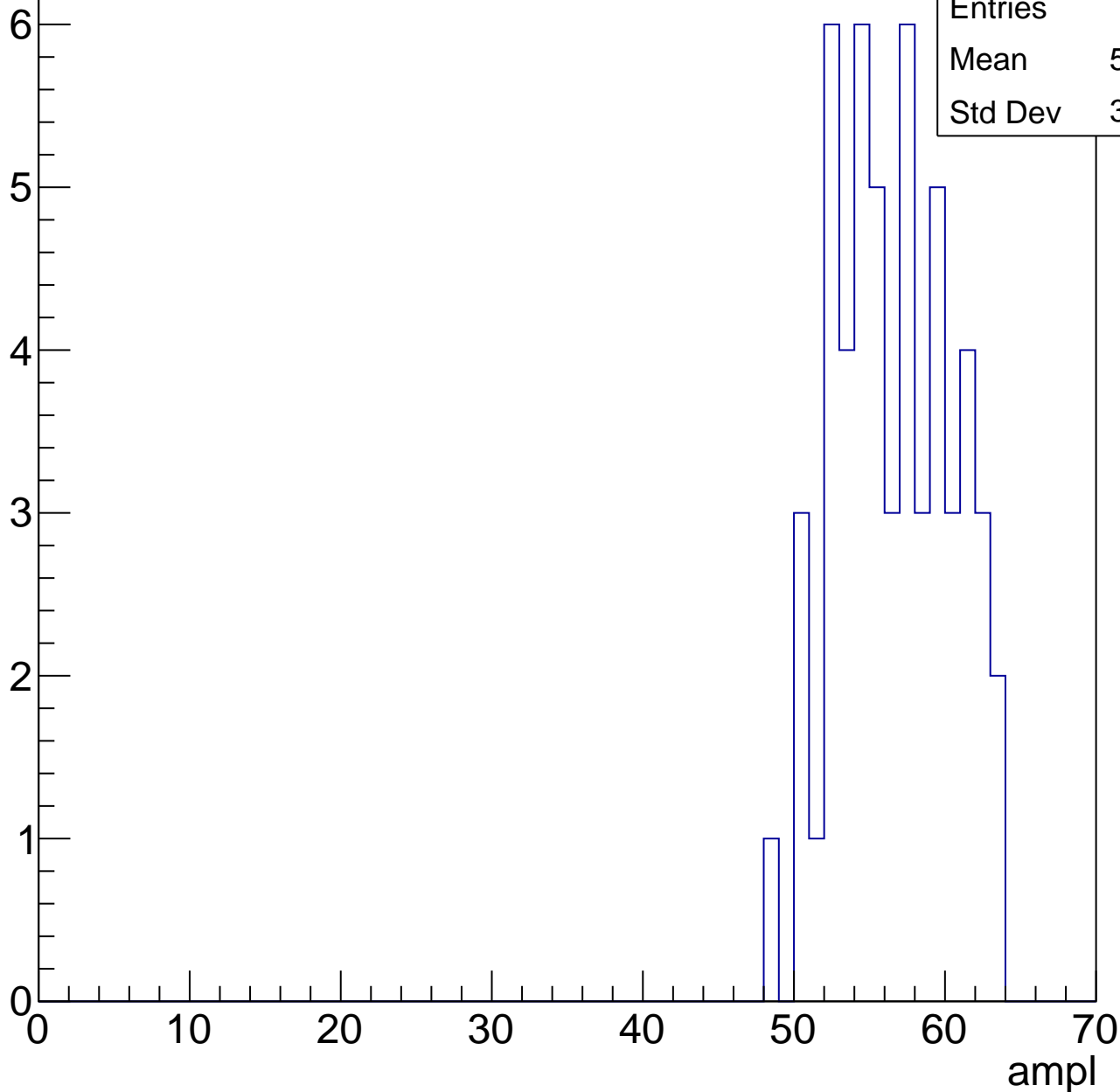


B1L102S, U11-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.13
Std Dev	3.752

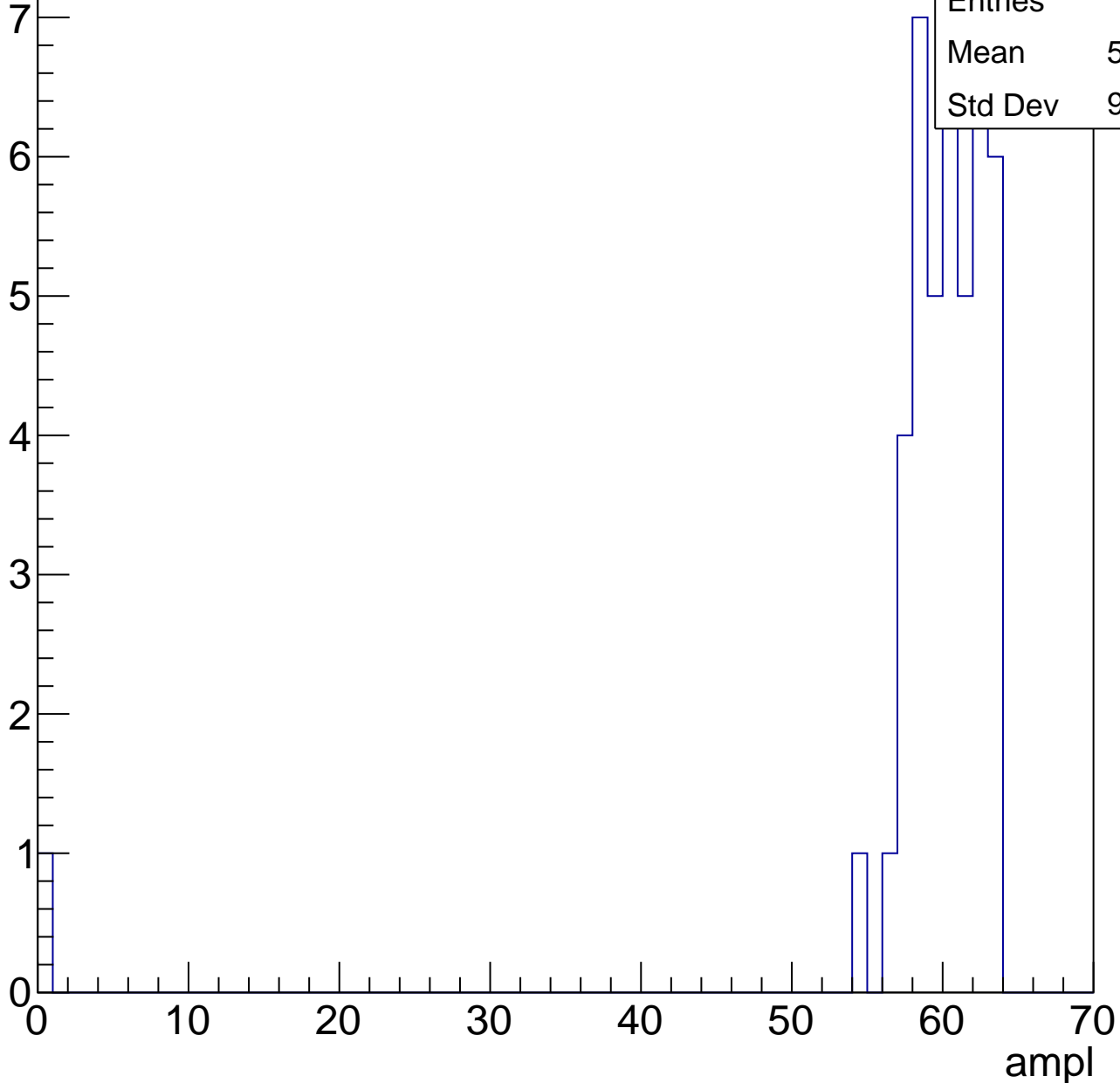


B1L102S, U11-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.55
Std Dev	9.189



B1L102S, U11-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch104, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	26.86
Std Dev	7.276

Gaus mean : 30.1561

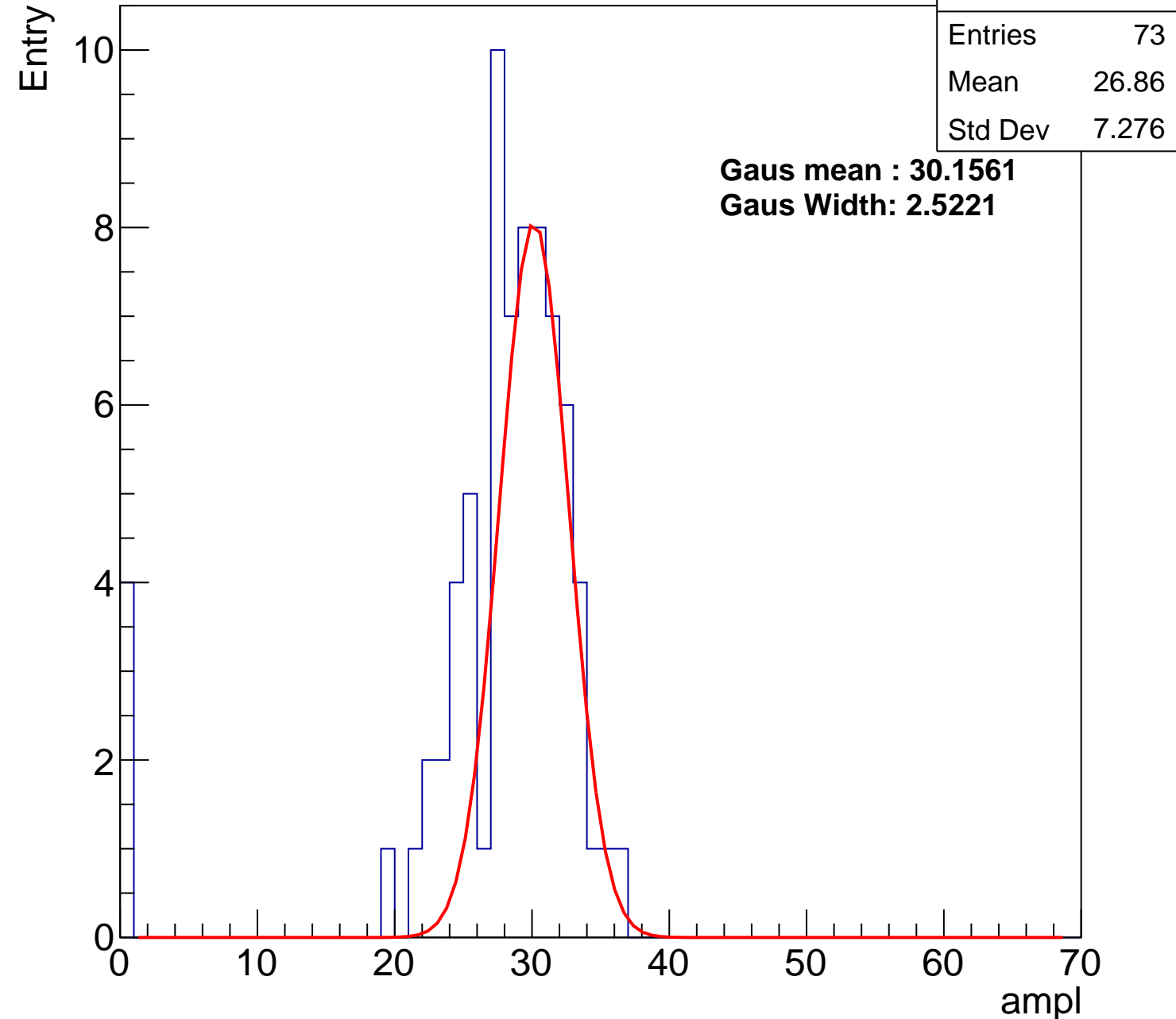
Gaus Width: 2.5221

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch104, adc1

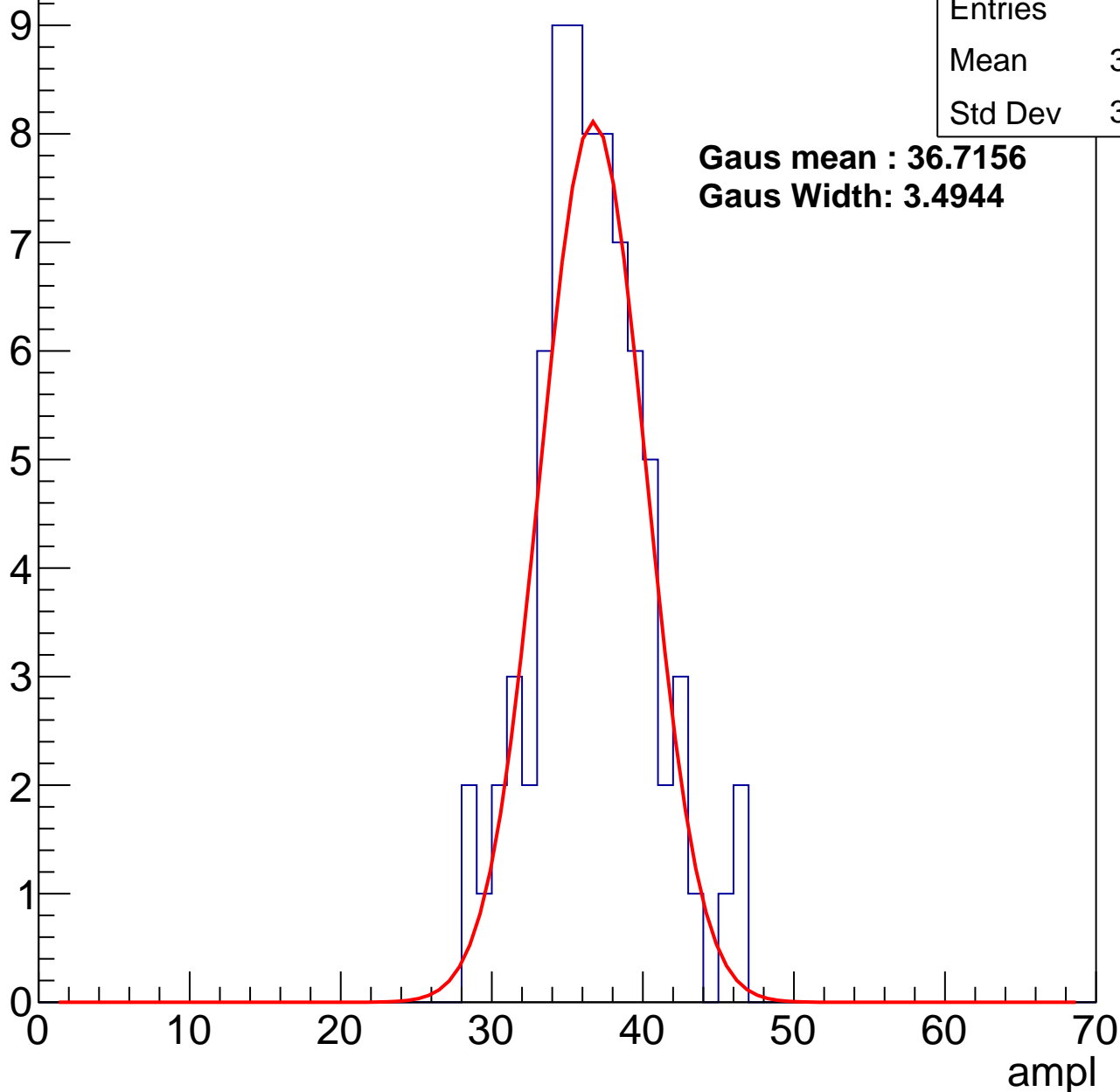
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.27
Std Dev	3.792

Gaus mean : 36.7156

Gaus Width: 3.4944



B1L102S, U11-ch104, adc2

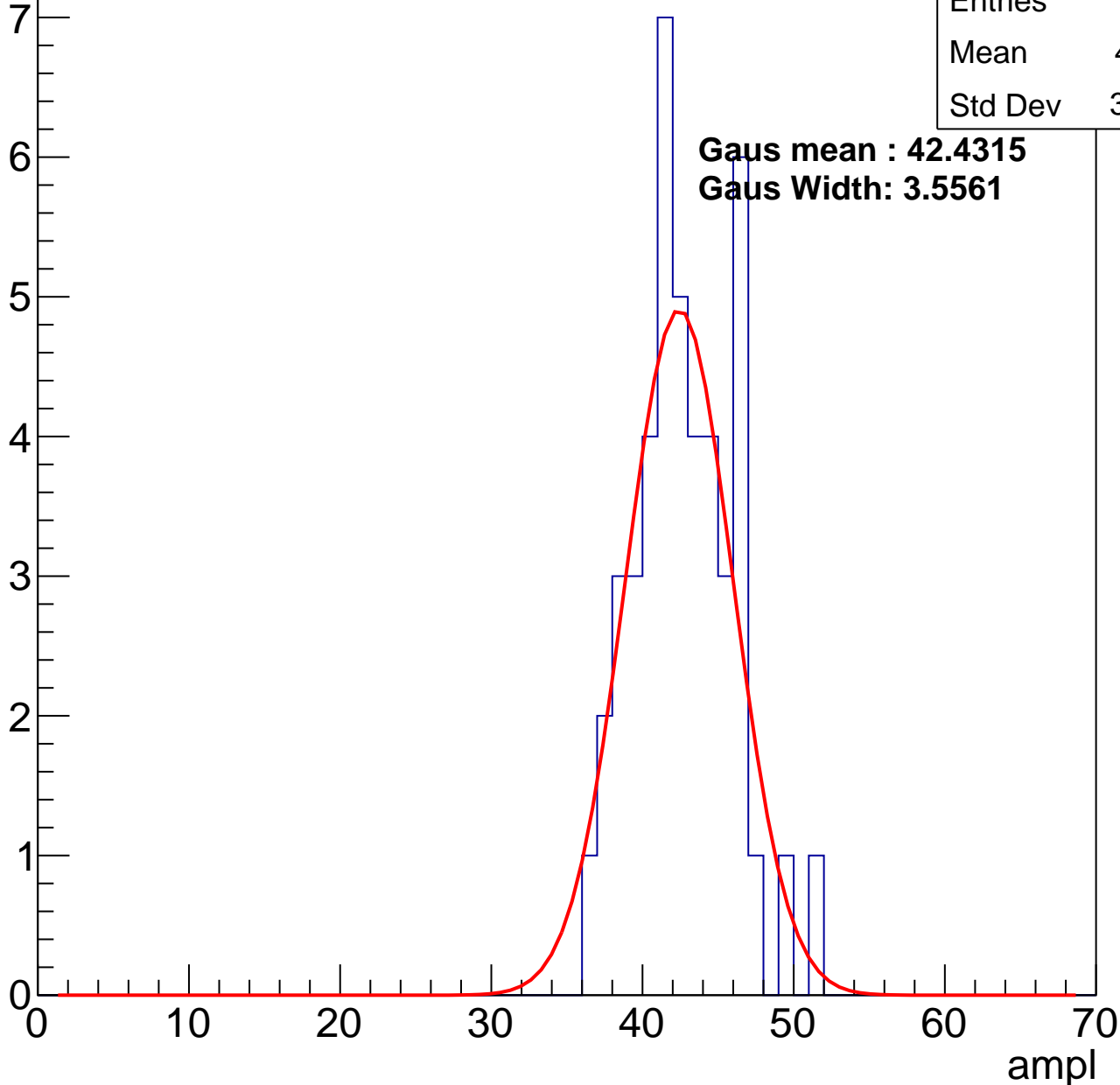
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	42.31
Std Dev	3.258

Gaus mean : 42.4315

Gaus Width: 3.5561



B1L102S, U11-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	47.93
Std Dev	3.775

Entry

10

8

6

4

2

0

0

10

20

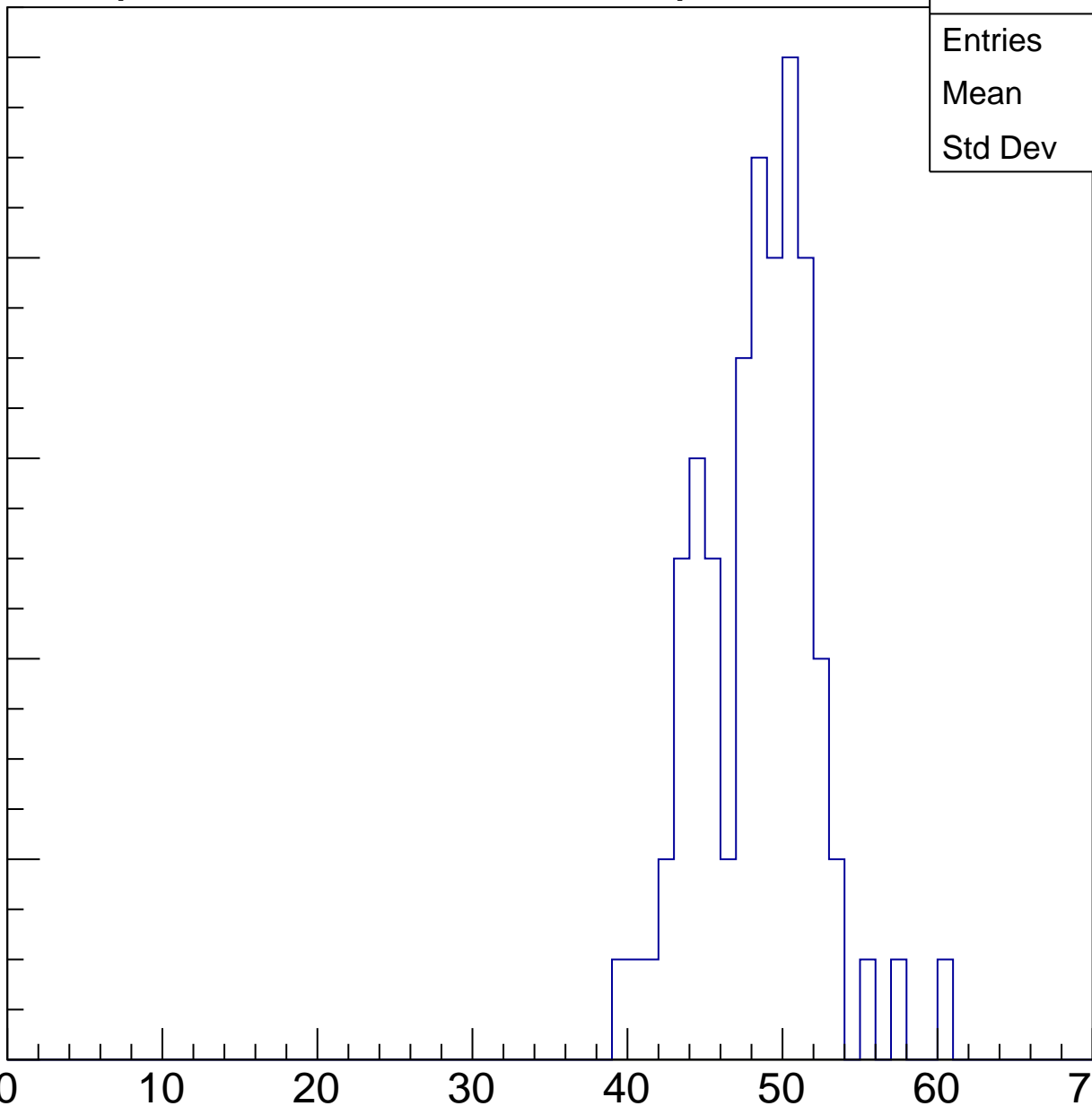
30

40

50

60

ampl

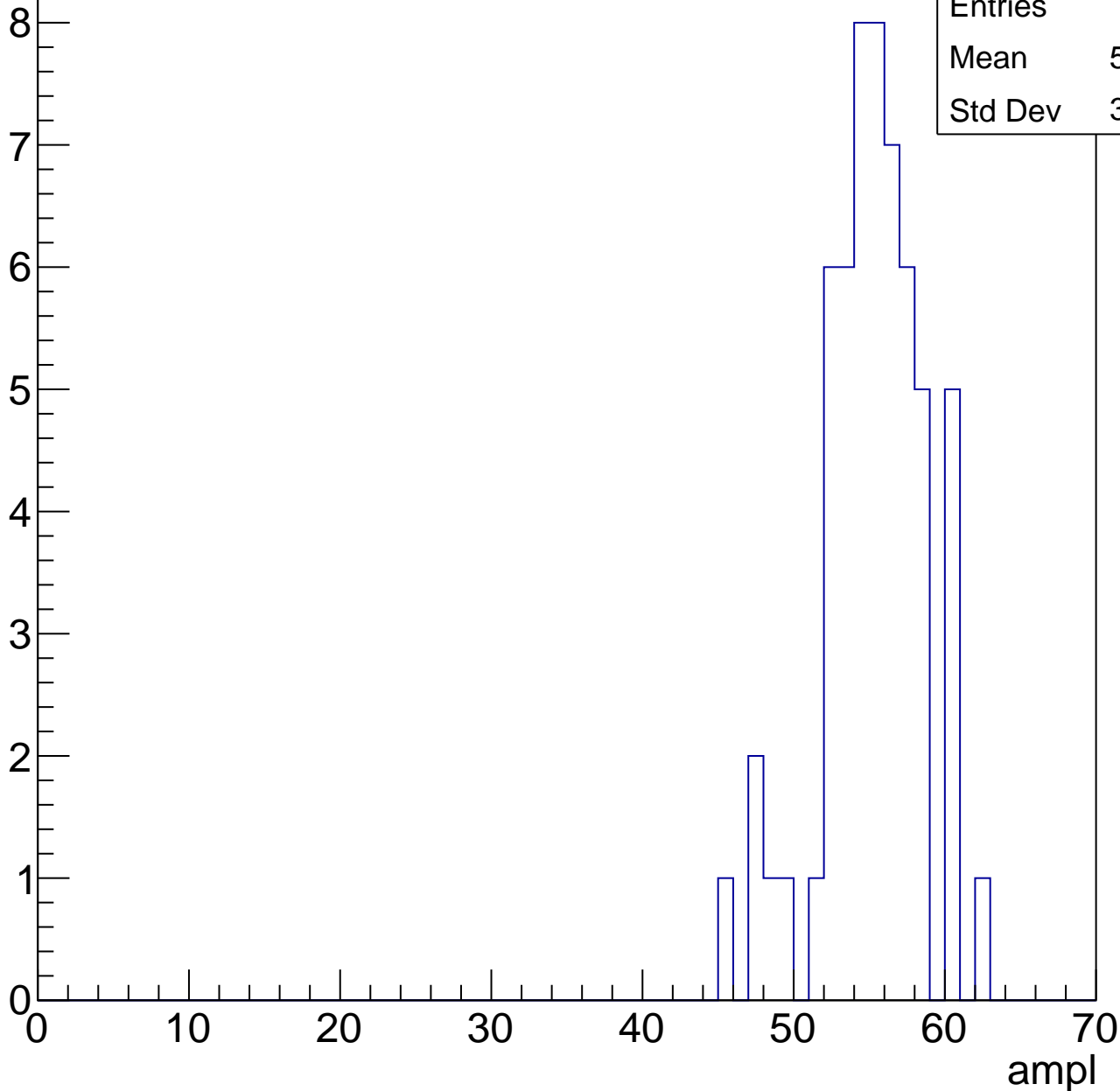


B1L102S, U11-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.74
Std Dev	3.376

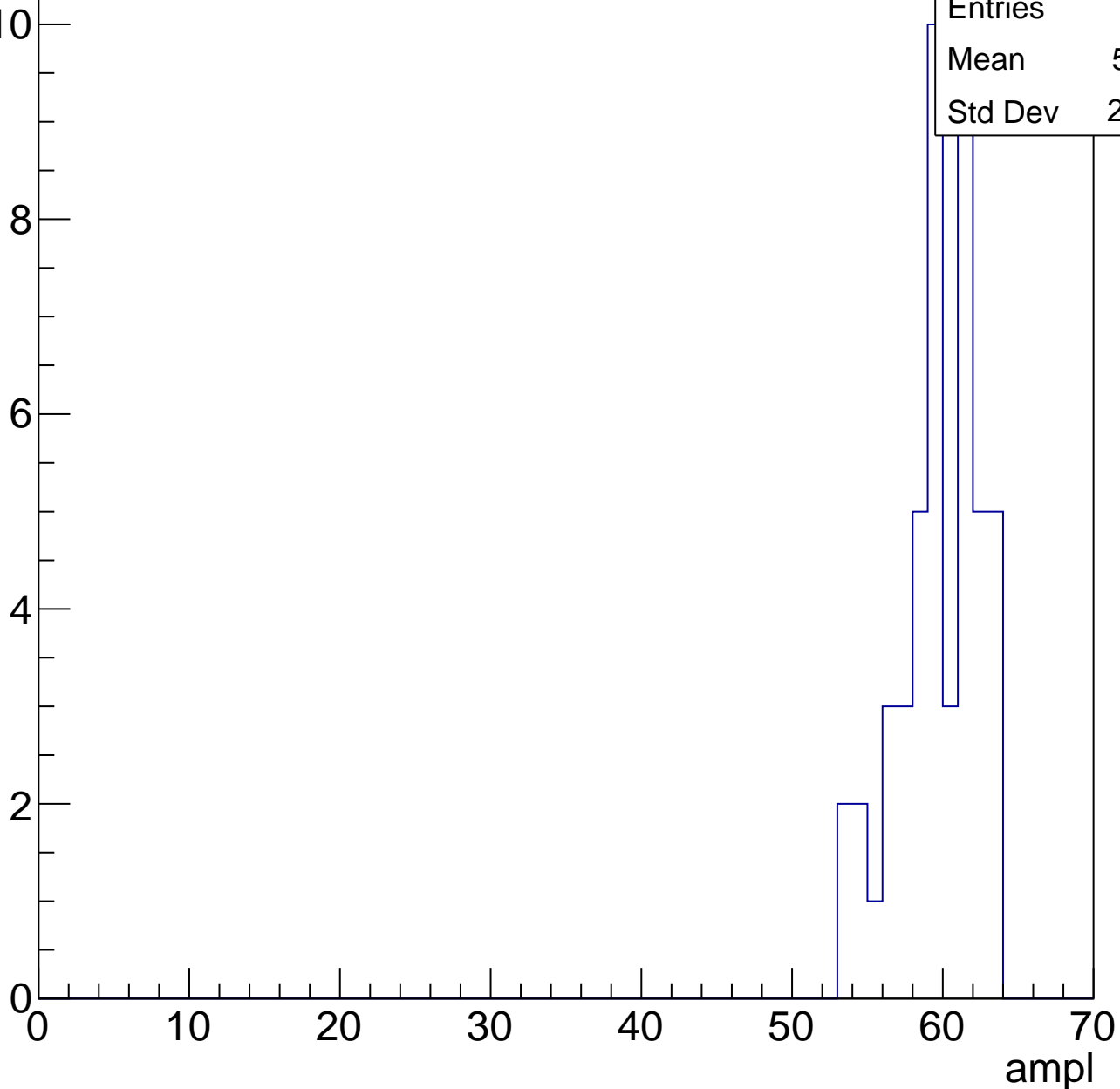


B1L102S, U11-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59.21
Std Dev	2.677

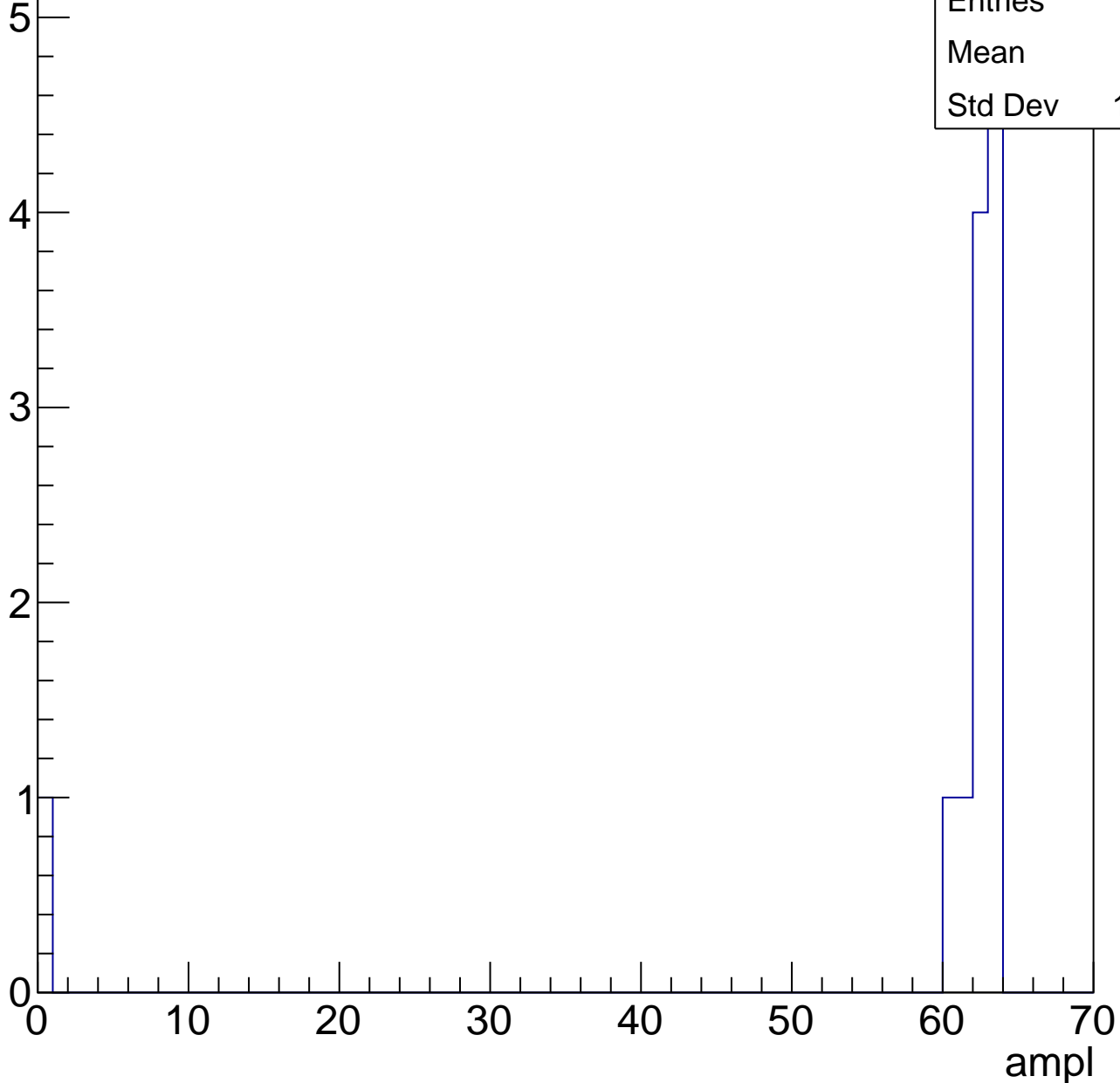


B1L102S, U11-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	57
Std Dev	17.21



B1L102S, U11-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch105, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	30.34
Std Dev	4.05

Gaus mean : 30.9590

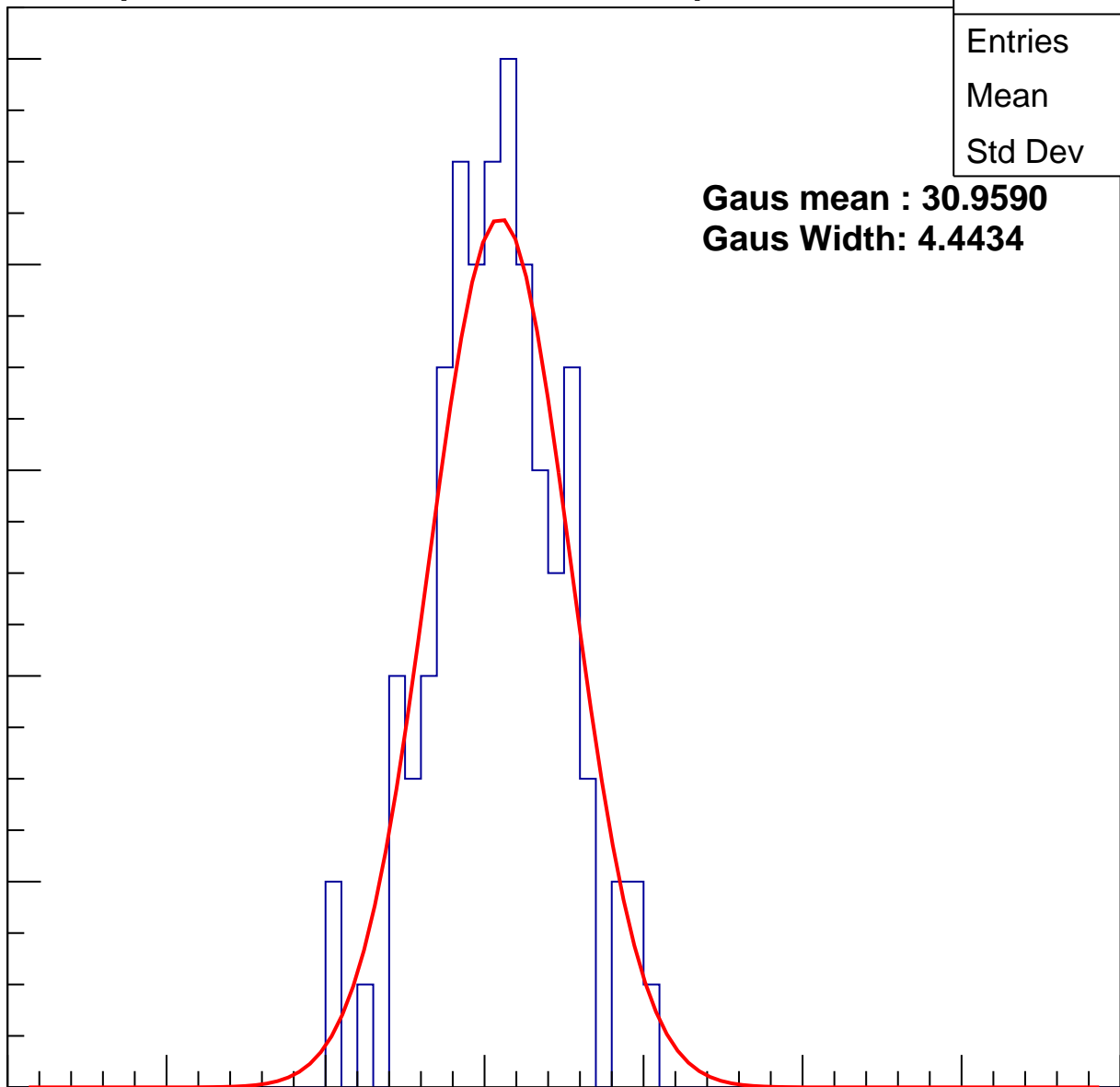
Gaus Width: 4.4434

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch105, adc1

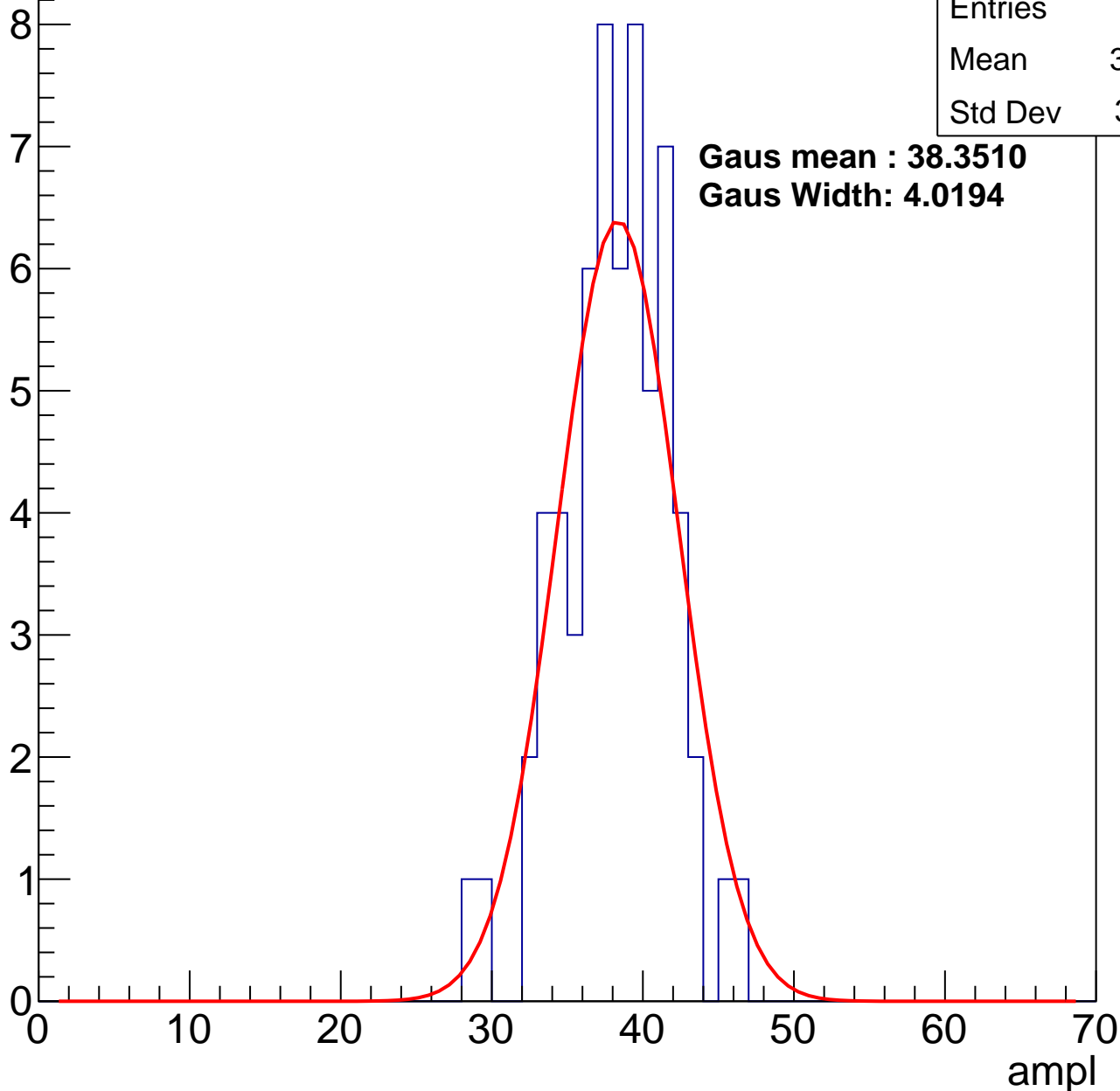
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.75
Std Dev	3.541

Gaus mean : 38.3510

Gaus Width: 4.0194



B1L102S, U11-ch105, adc2

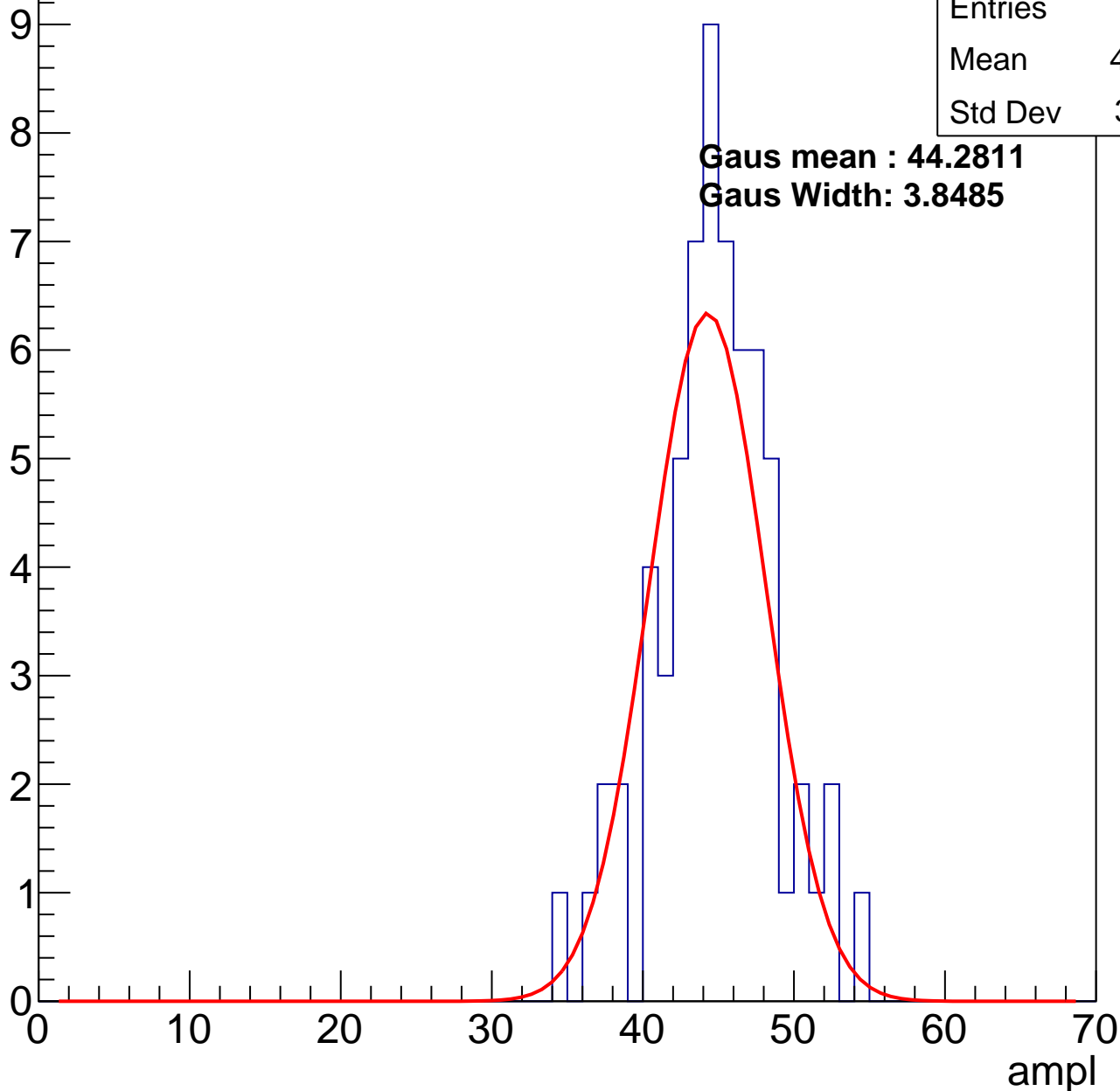
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.32
Std Dev	3.891

Gaus mean : 44.2811

Gaus Width: 3.8485

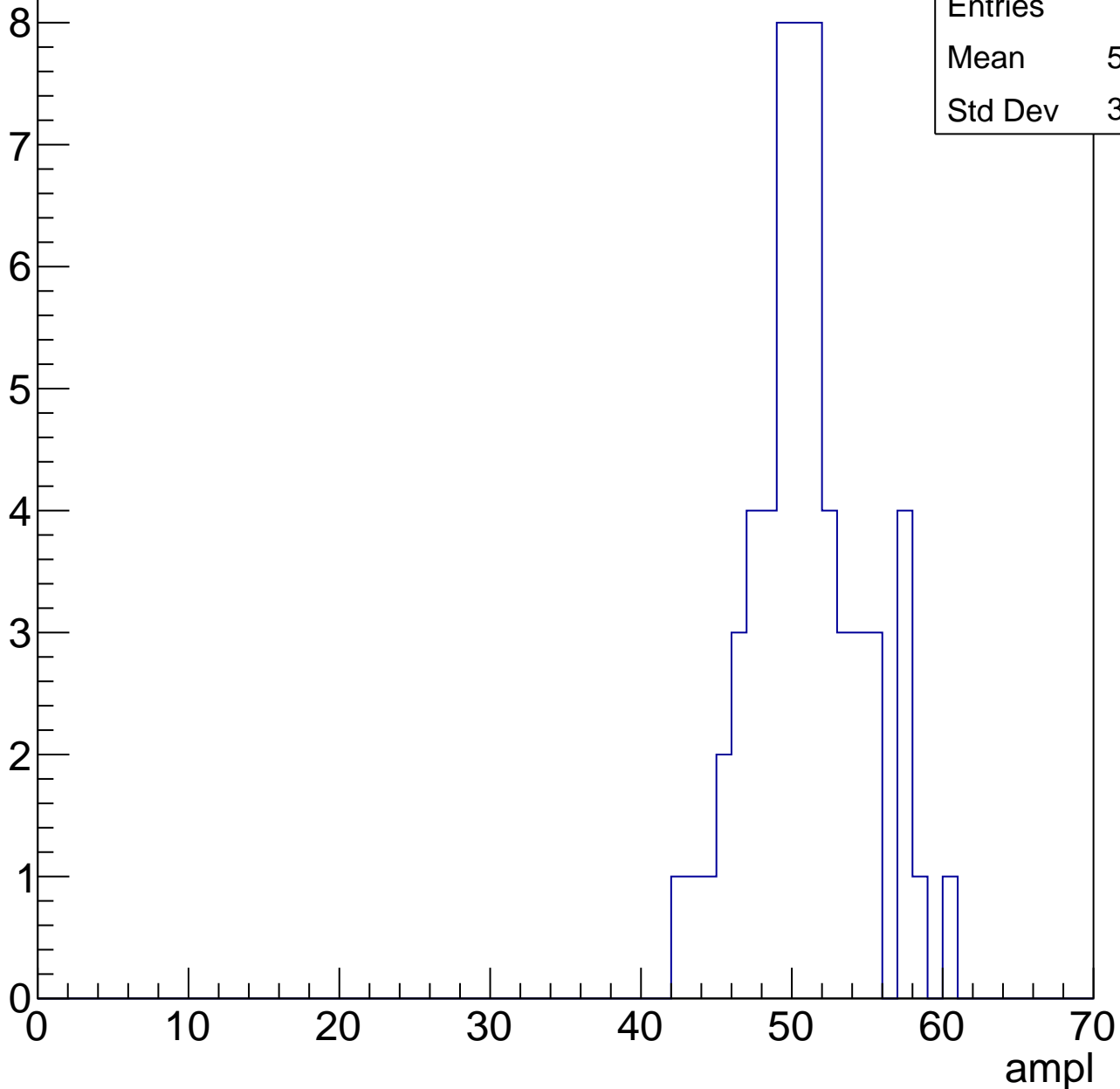


B1L102S, U11-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.46
Std Dev	3.748



B1L102S, U11-ch105, adc4

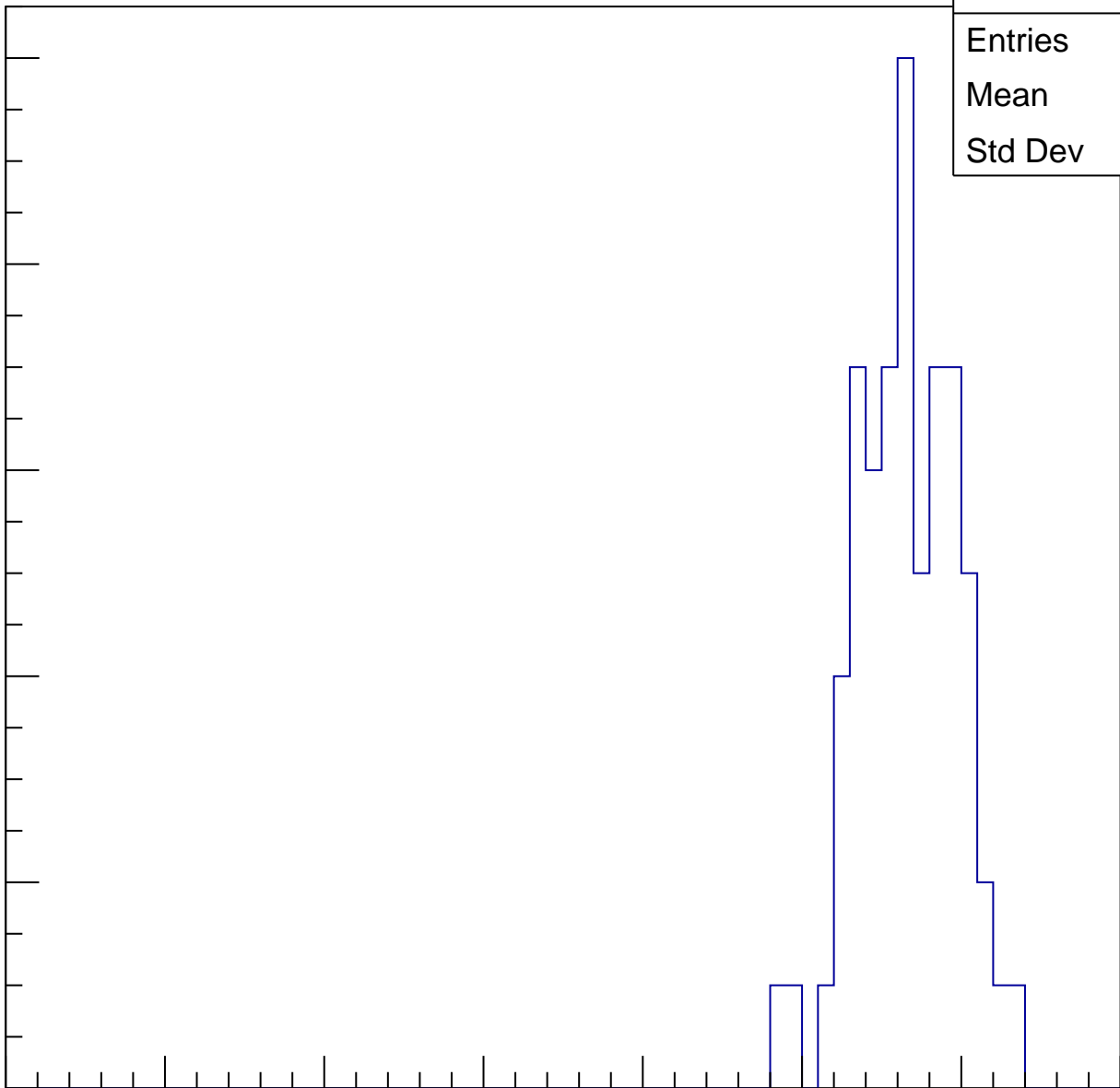
calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	56.11
Std Dev	3.054

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

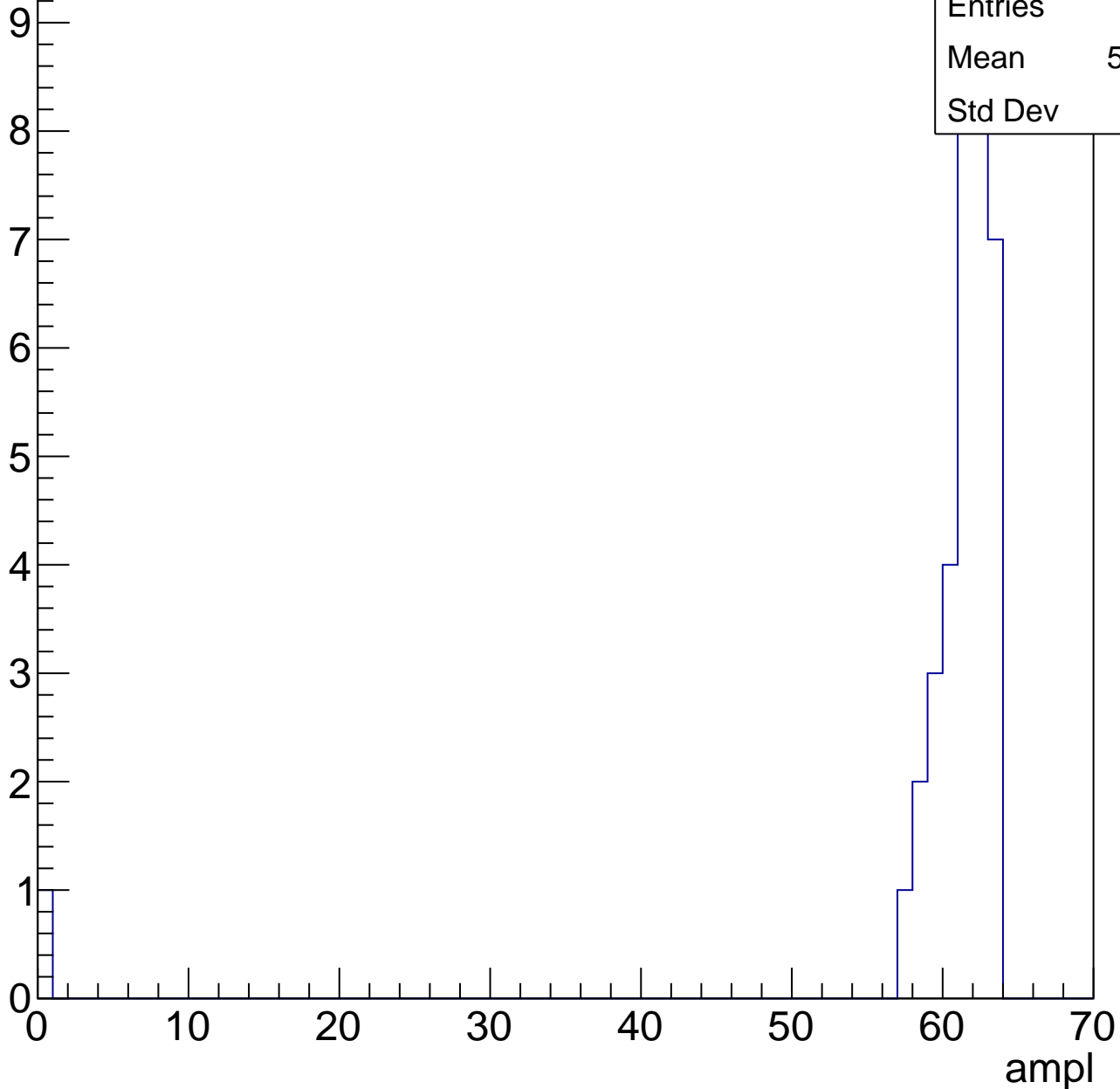


B1L102S, U11-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.34
Std Dev	10.3



B1L102S, U11-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch106, adc0

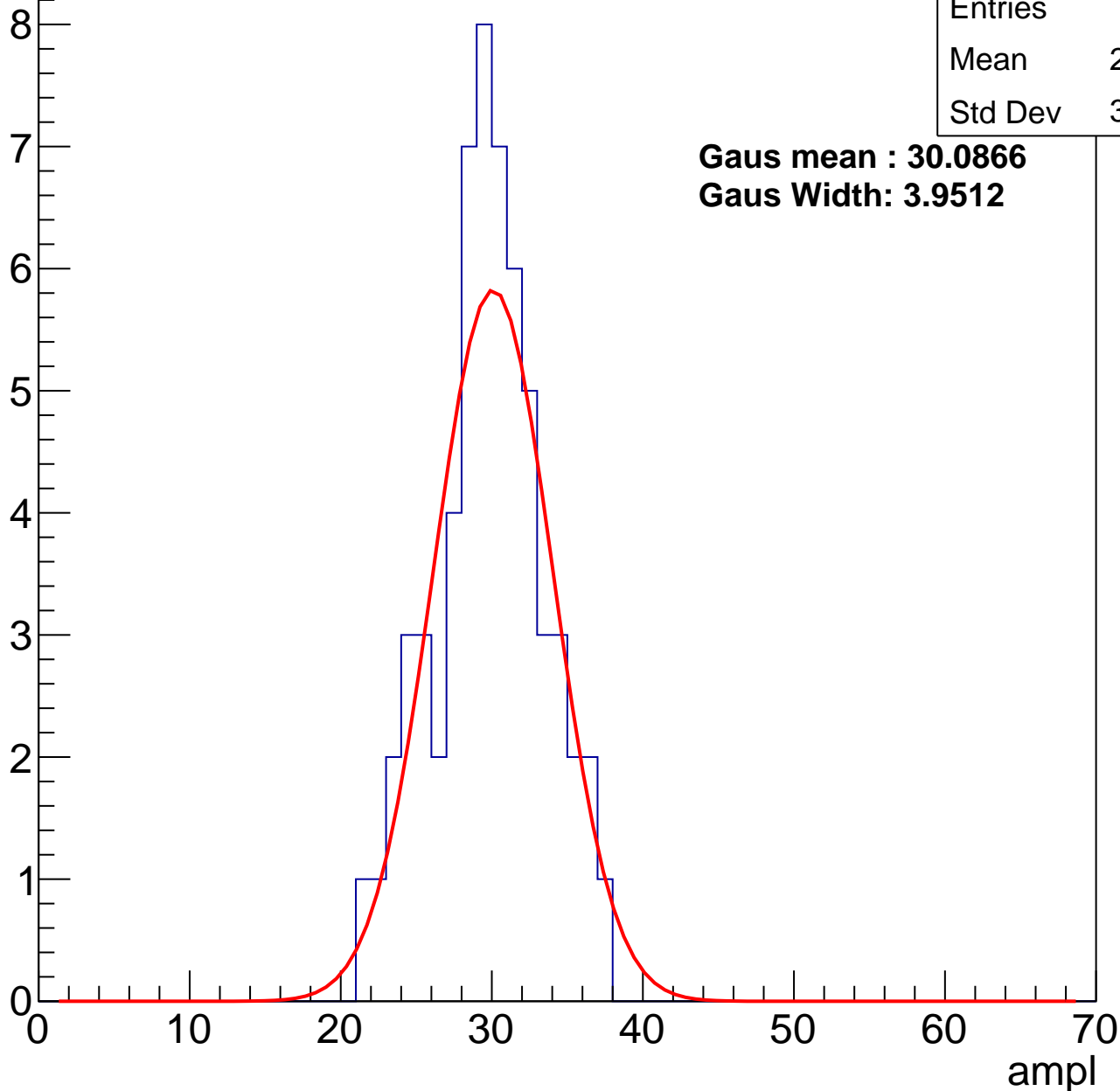
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.33
Std Dev	3.595

Gaus mean : 30.0866

Gaus Width: 3.9512



B1L102S, U11-ch106, adc1

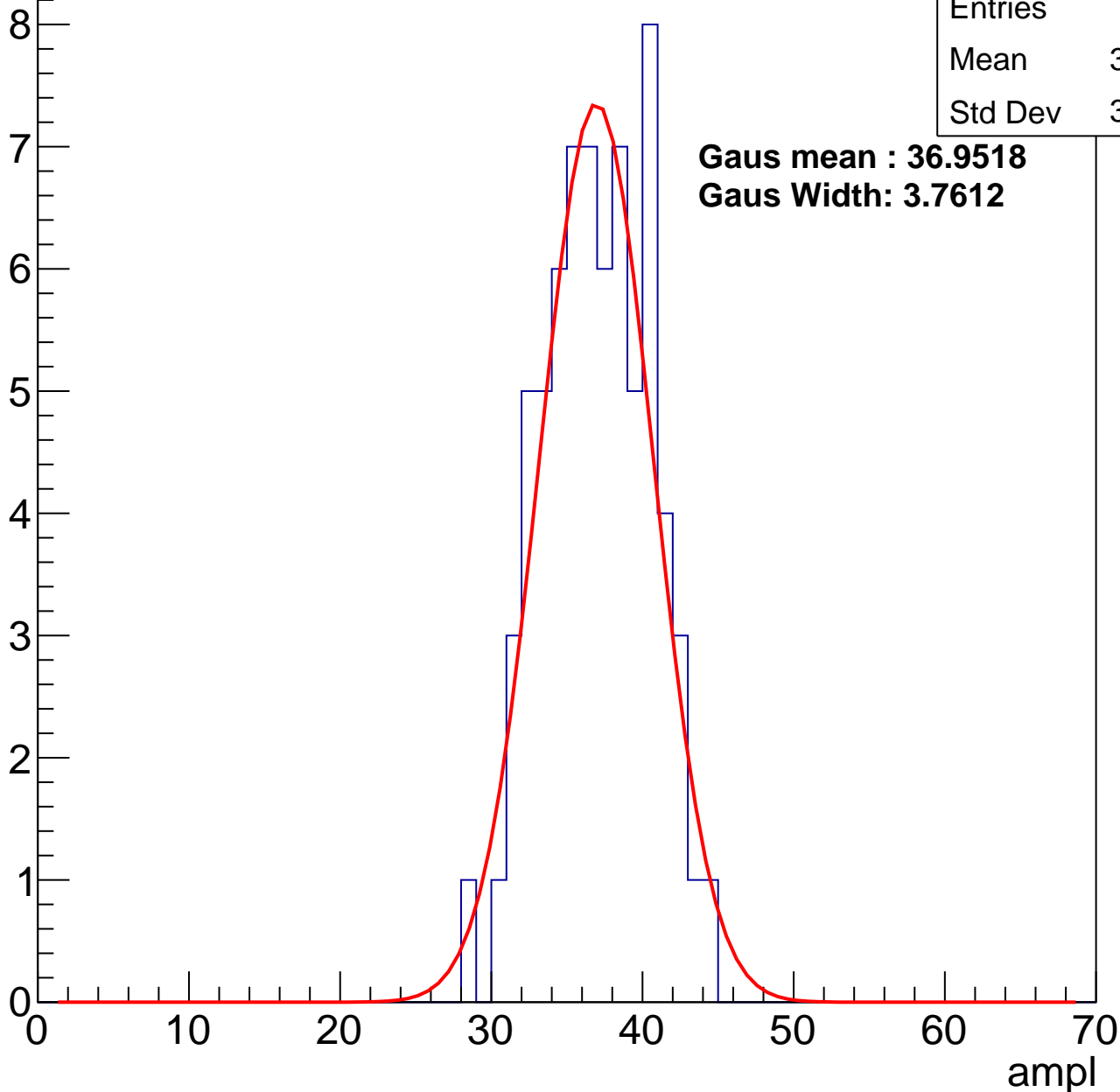
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.53
Std Dev	3.463

Gaus mean : 36.9518

Gaus Width: 3.7612



B1L102S, U11-ch106, adc2

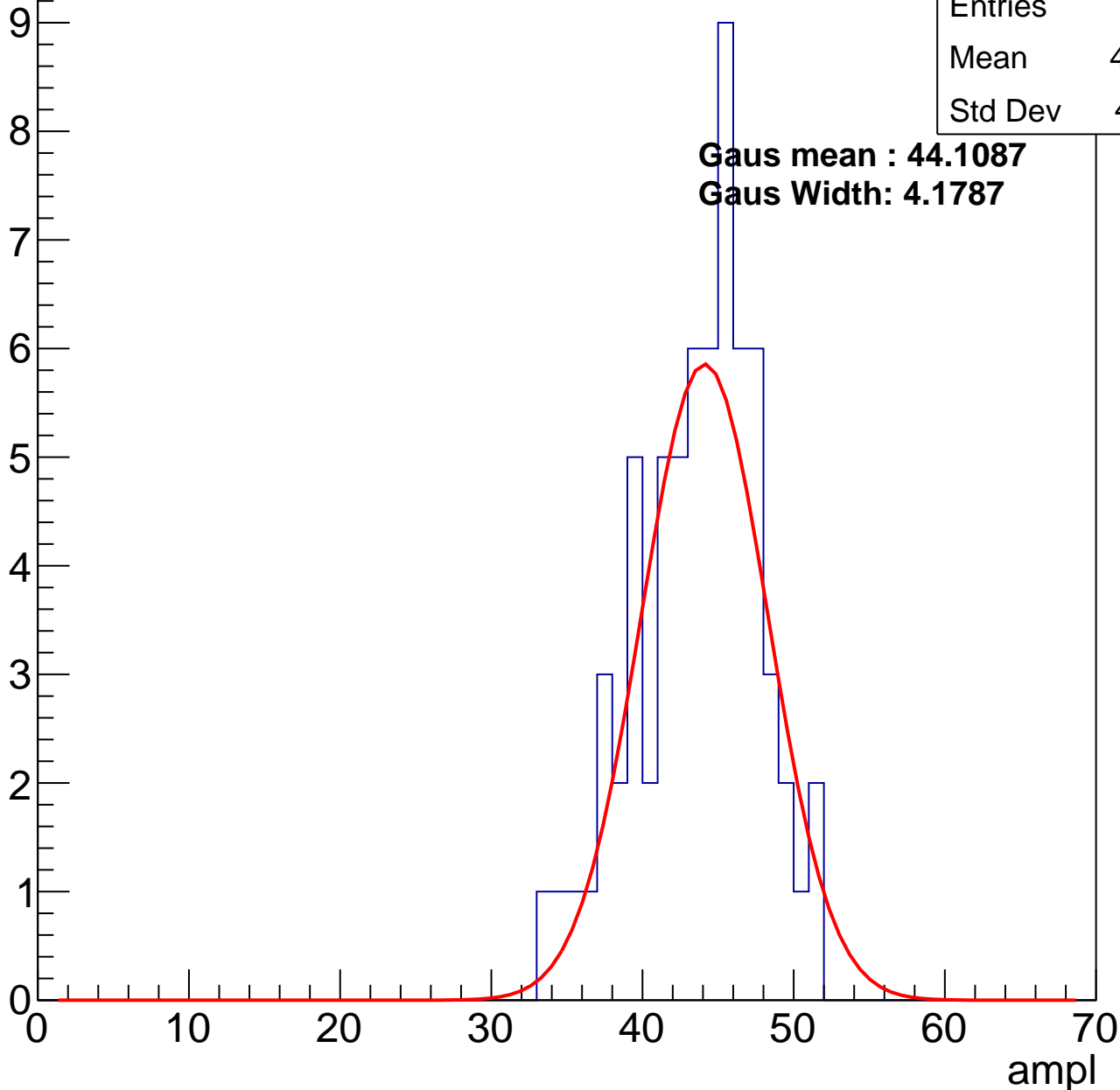
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.19
Std Dev	4.031

Gaus mean : 44.1087

Gaus Width: 4.1787

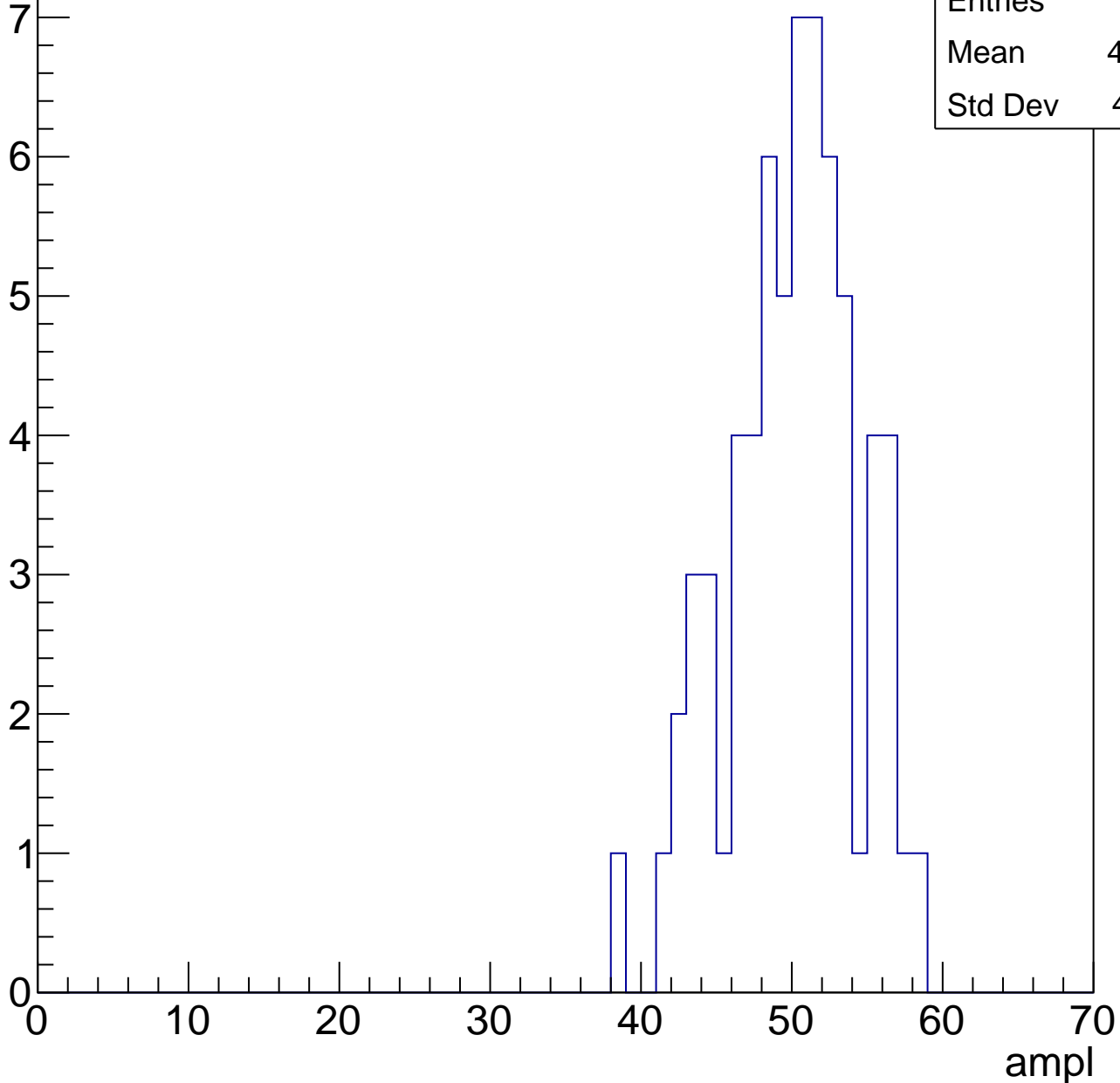


B1L102S, U11-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.56
Std Dev	4.261

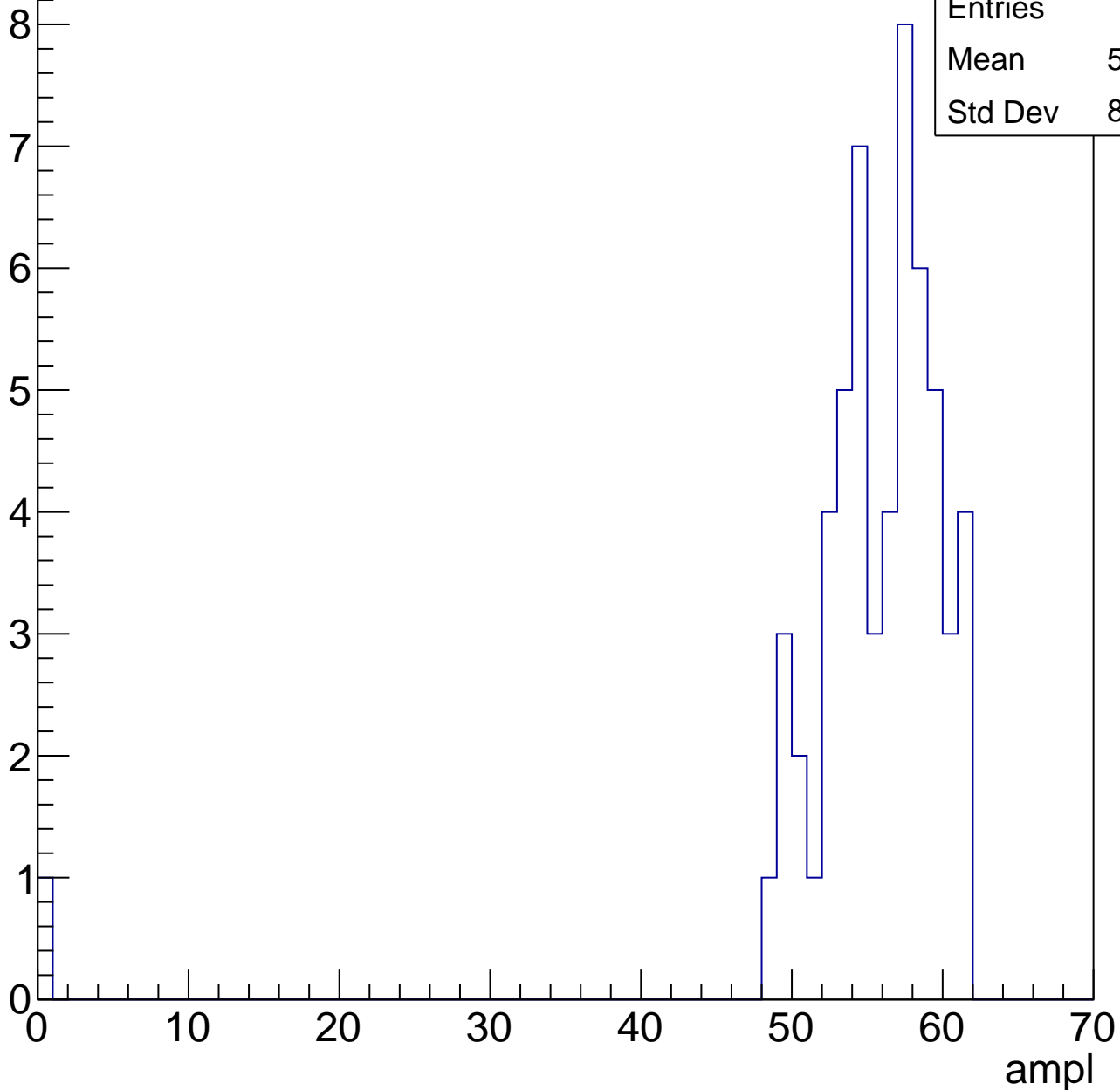


B1L102S, U11-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.54
Std Dev	8.044

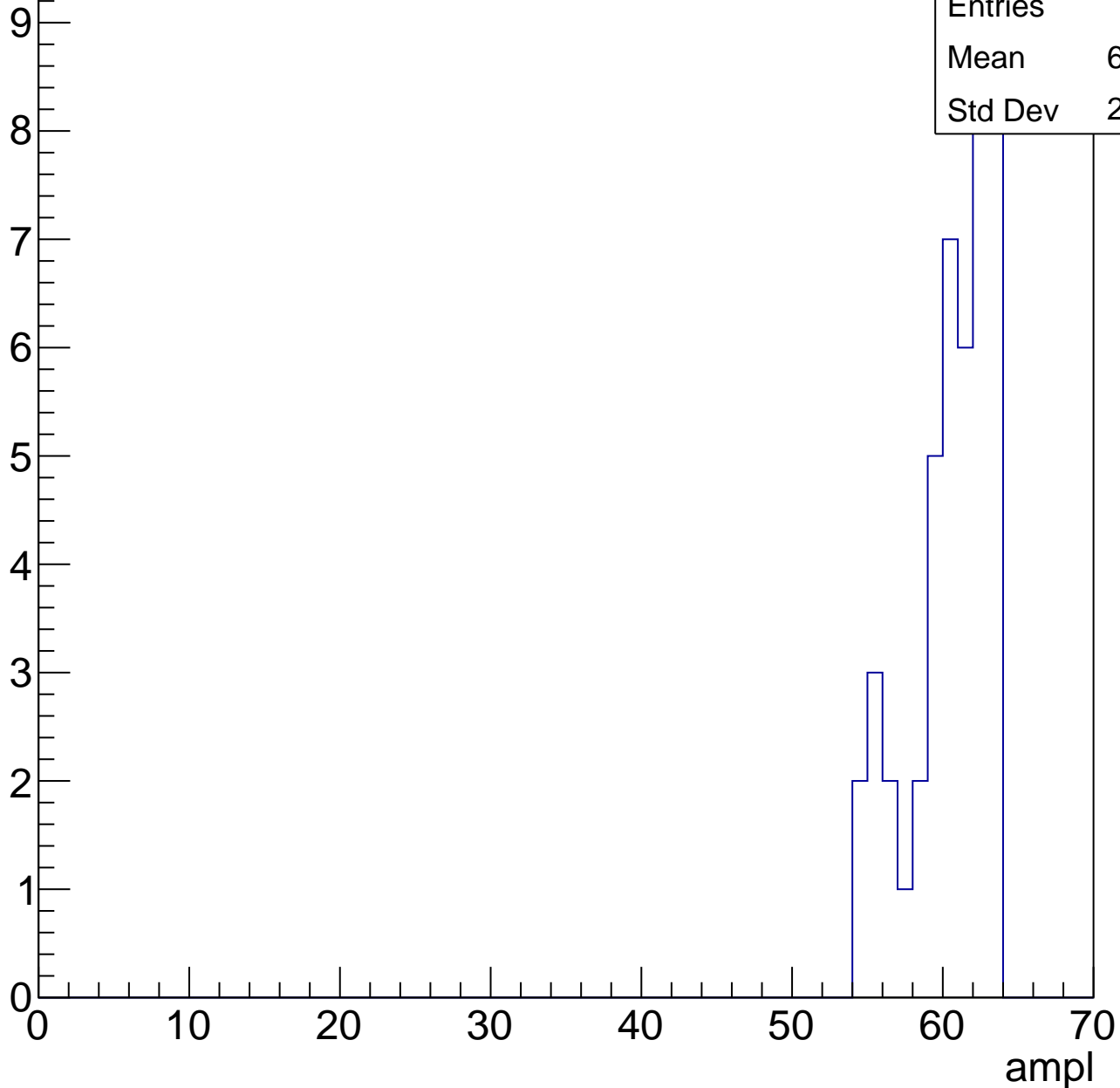


B1L102S, U11-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

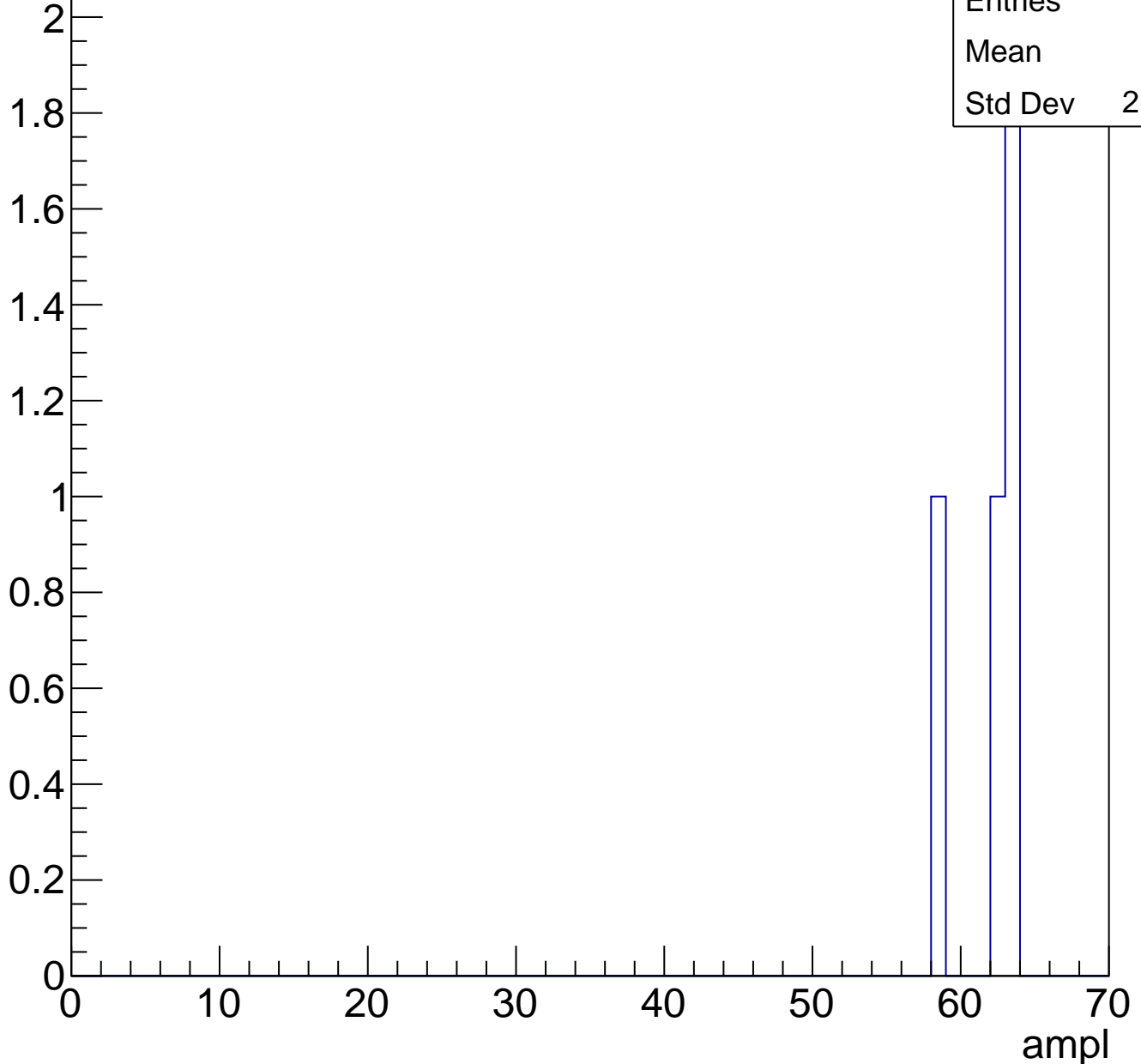
Entries	45
Mean	60.02
Std Dev	2.646



B1L102S, U11-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

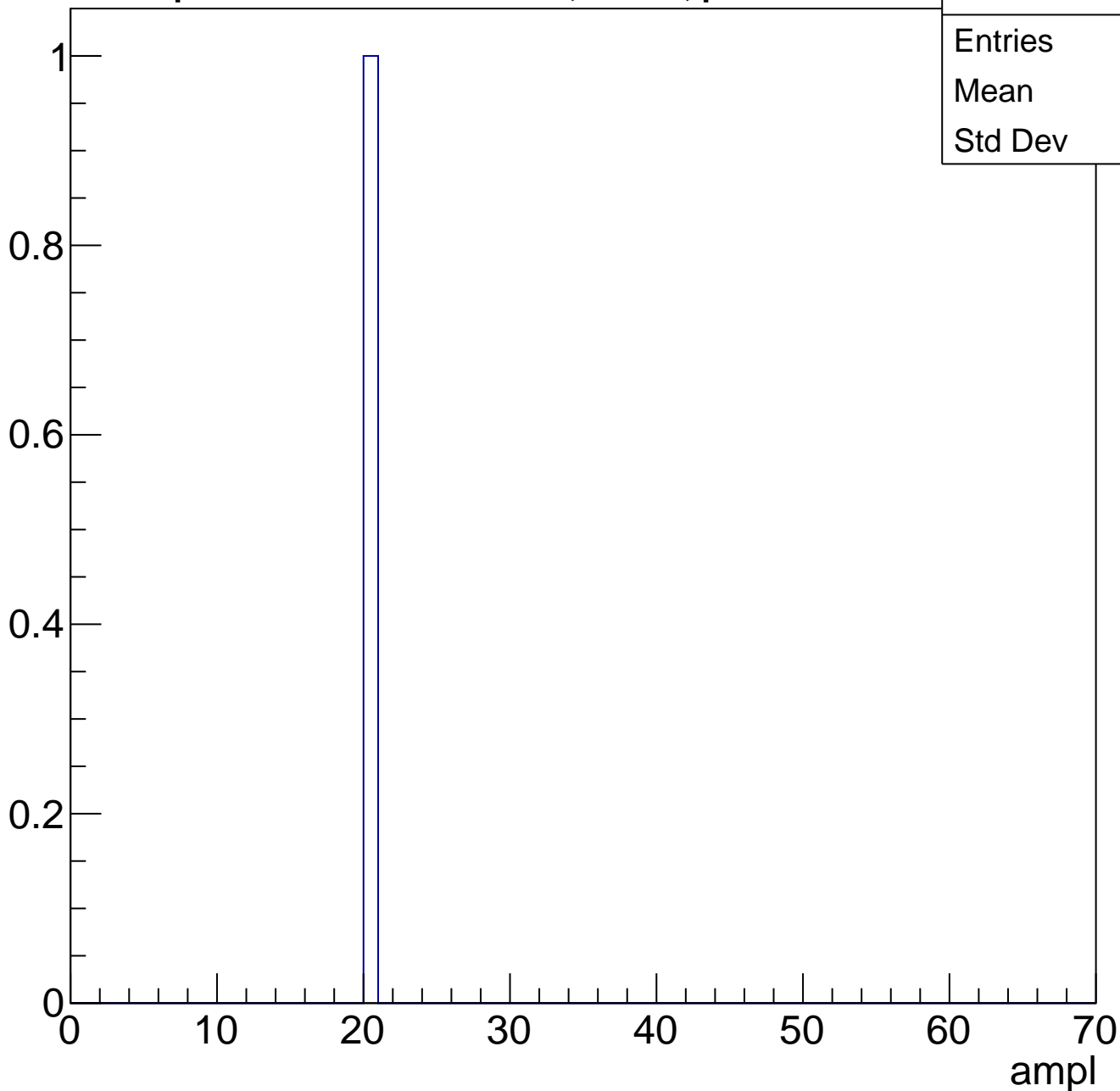


Entries	4
Mean	61.5
Std Dev	2.062

B1L102S, U11-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U11-ch107, adc0

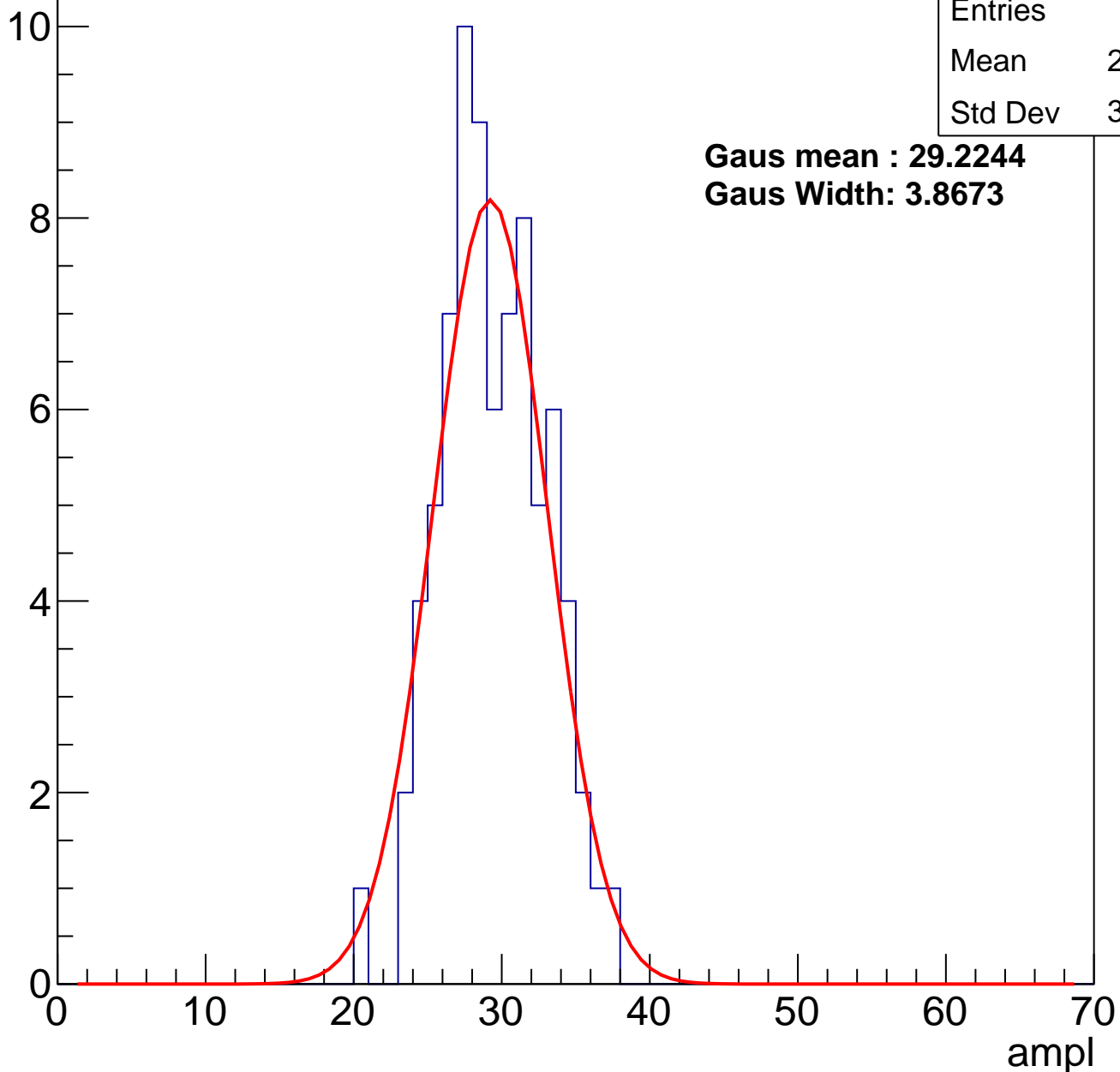
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.97
Std Dev	3.416

Gaus mean : 29.2244

Gaus Width: 3.8673

Entry



B1L102S, U11-ch107, adc1

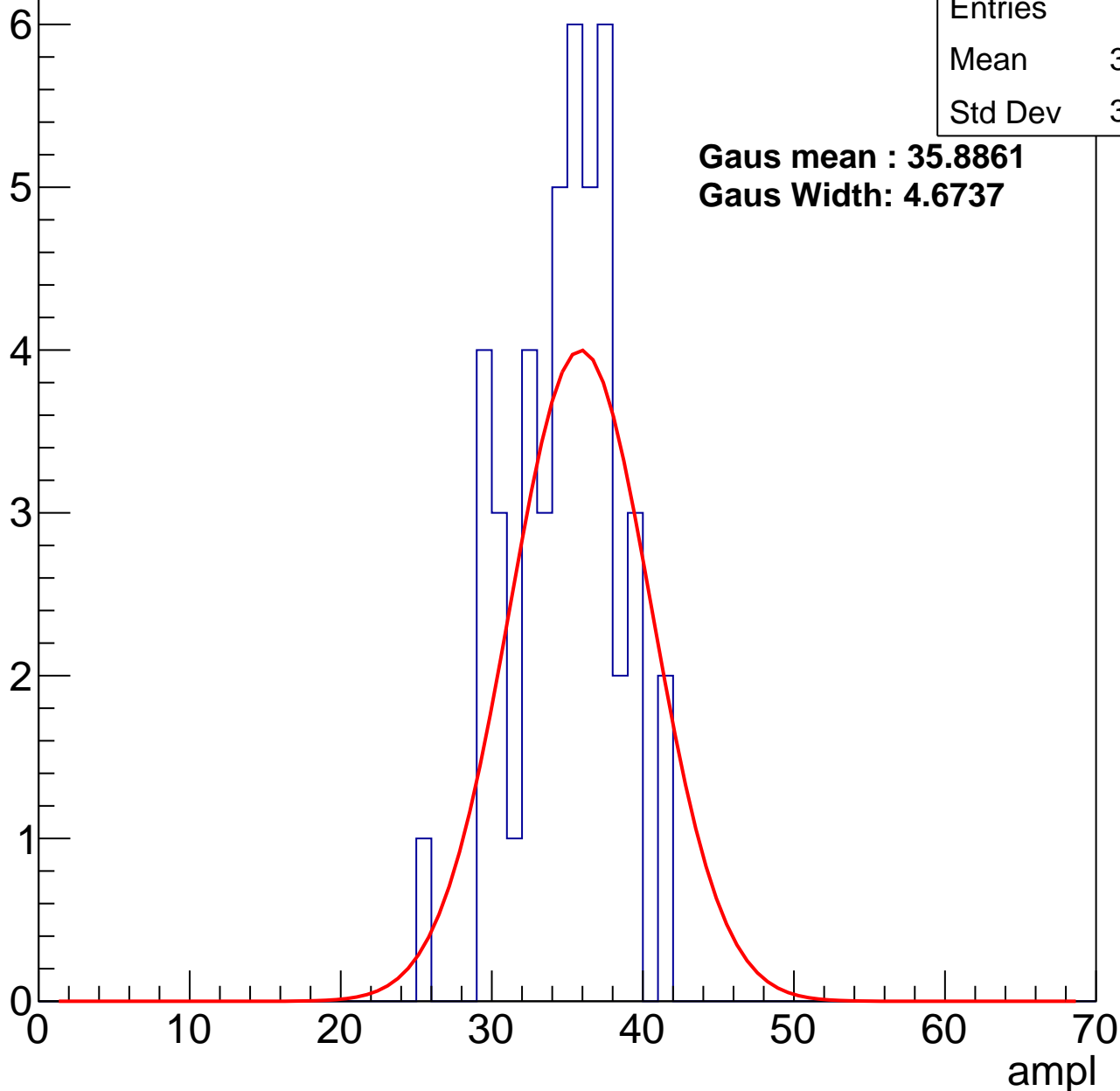
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	34.36
Std Dev	3.452

Gaus mean : 35.8861

Gaus Width: 4.6737



B1L102S, U11-ch107, adc2

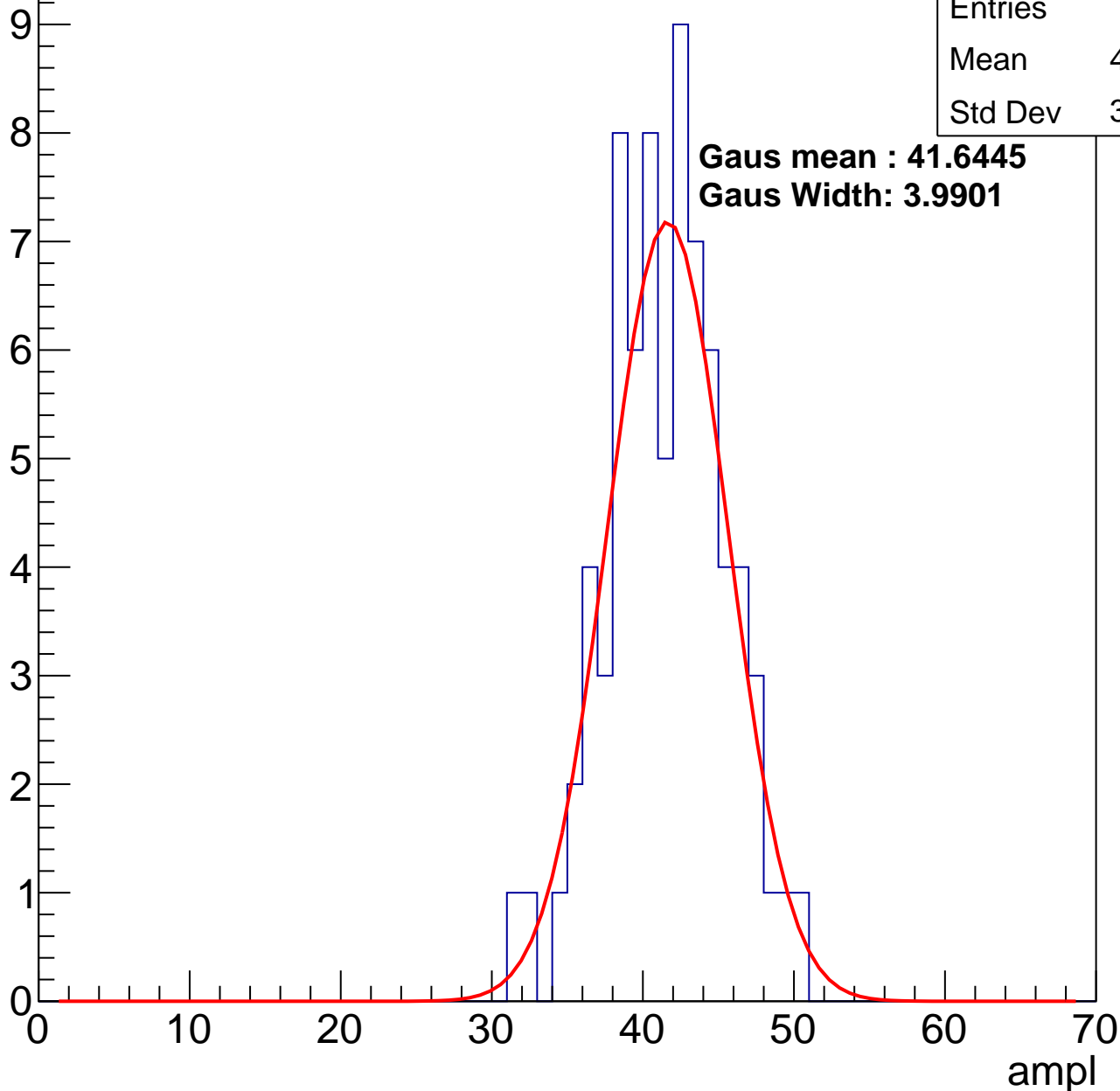
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.07
Std Dev	3.848

Gaus mean : 41.6445

Gaus Width: 3.9901

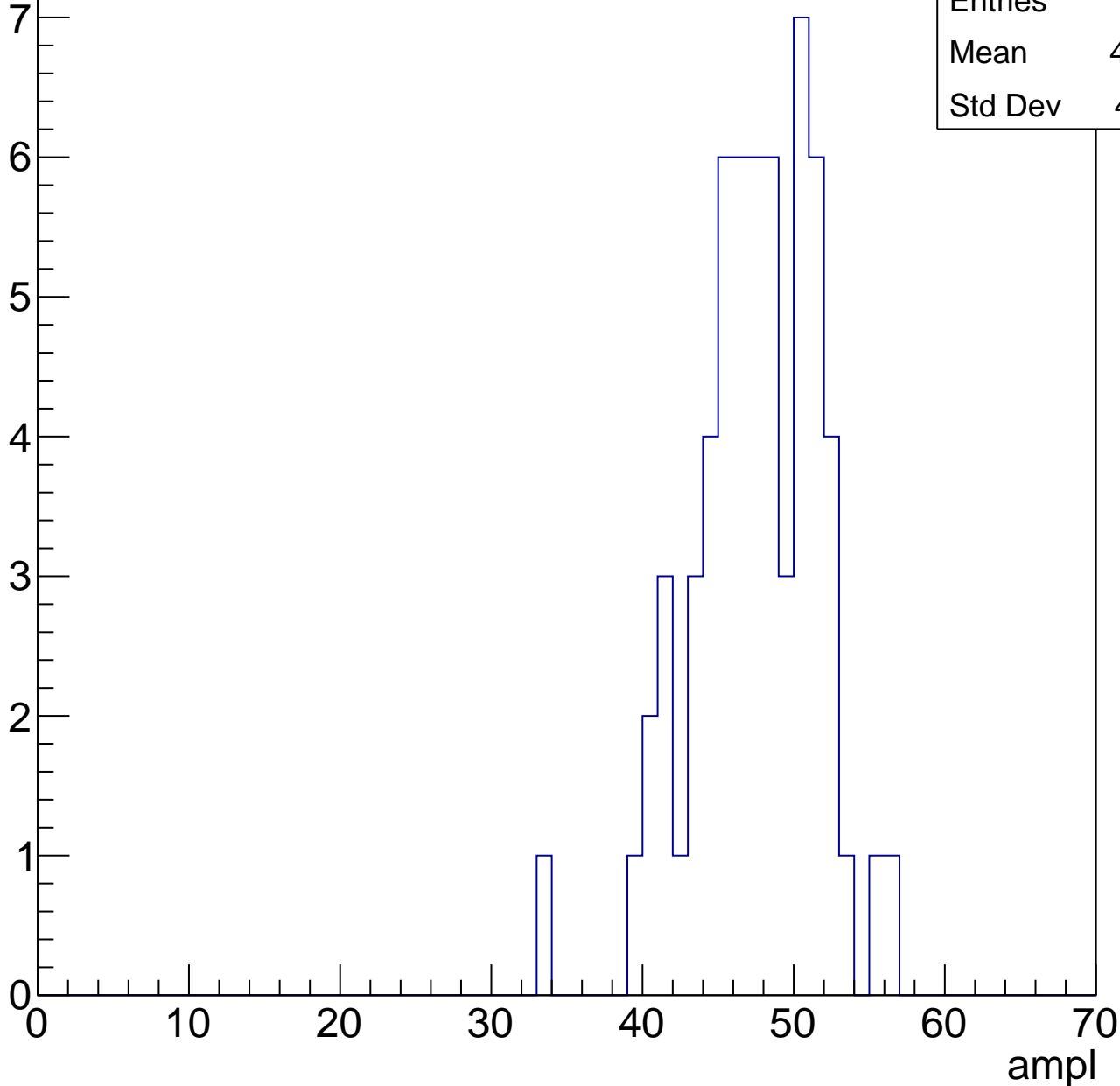


B1L102S, U11-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	46.98
Std Dev	4.141

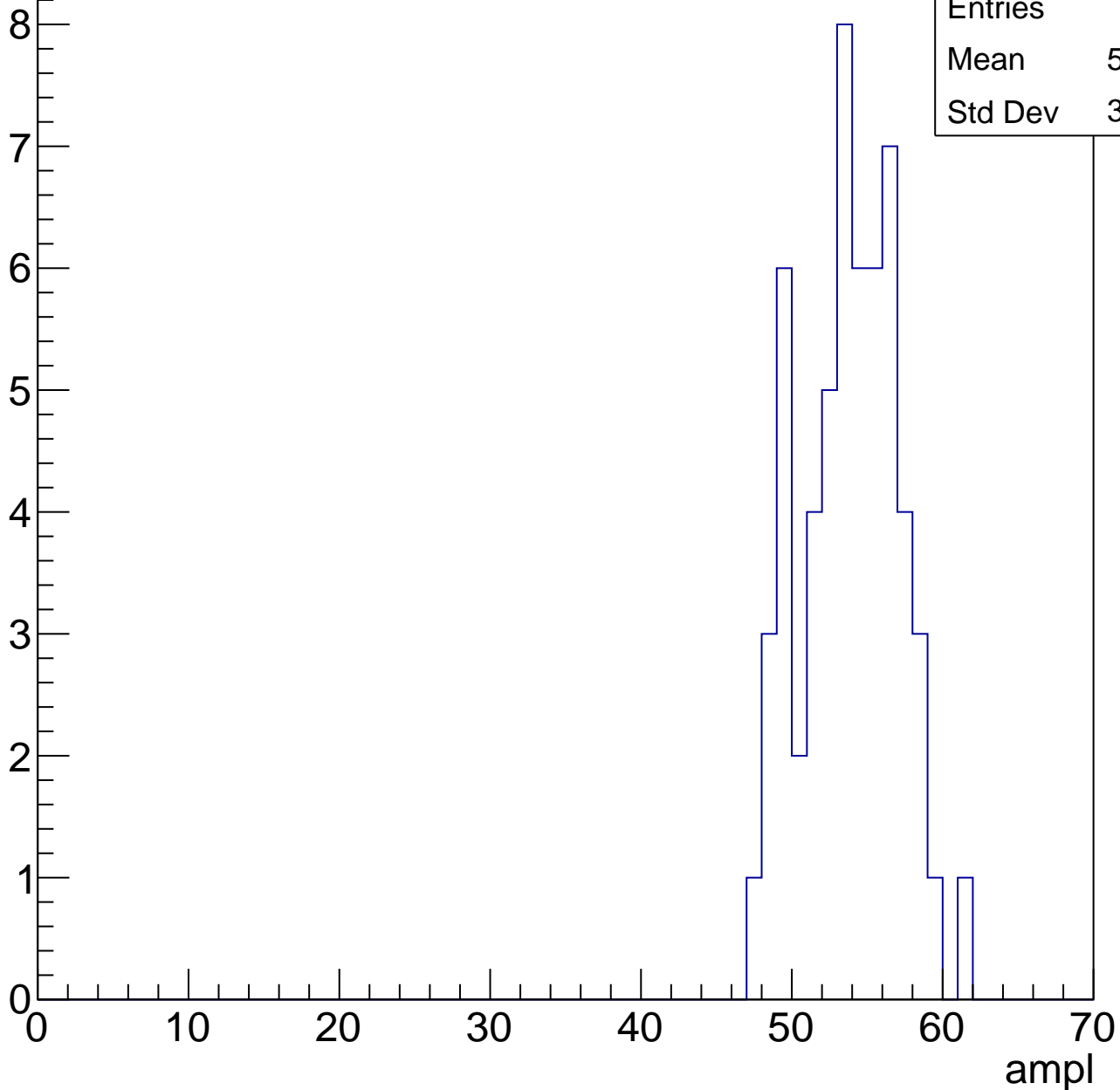


B1L102S, U11-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.35
Std Dev	3.165

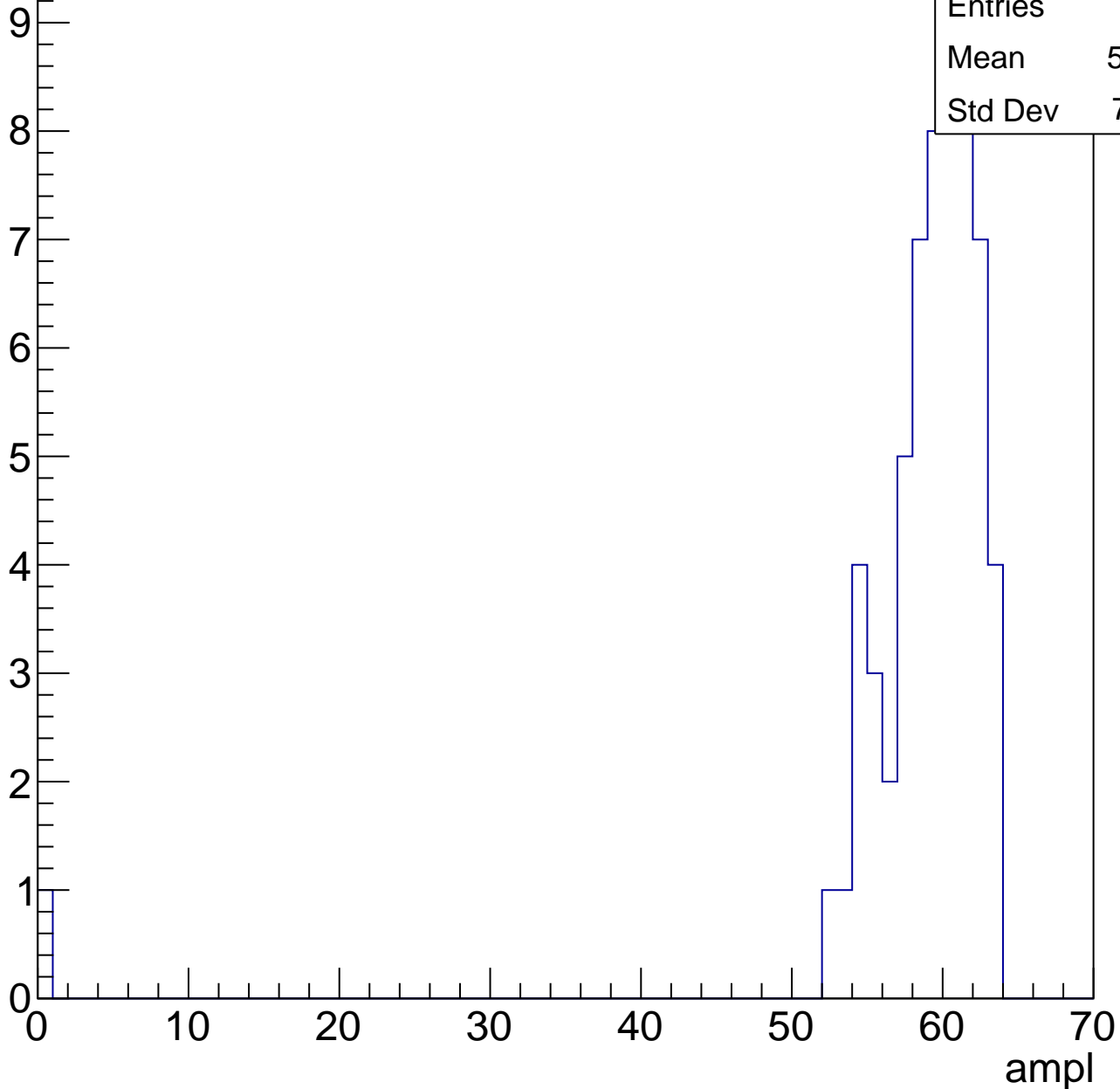


B1L102S, U11-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	57.97
Std Dev	7.961

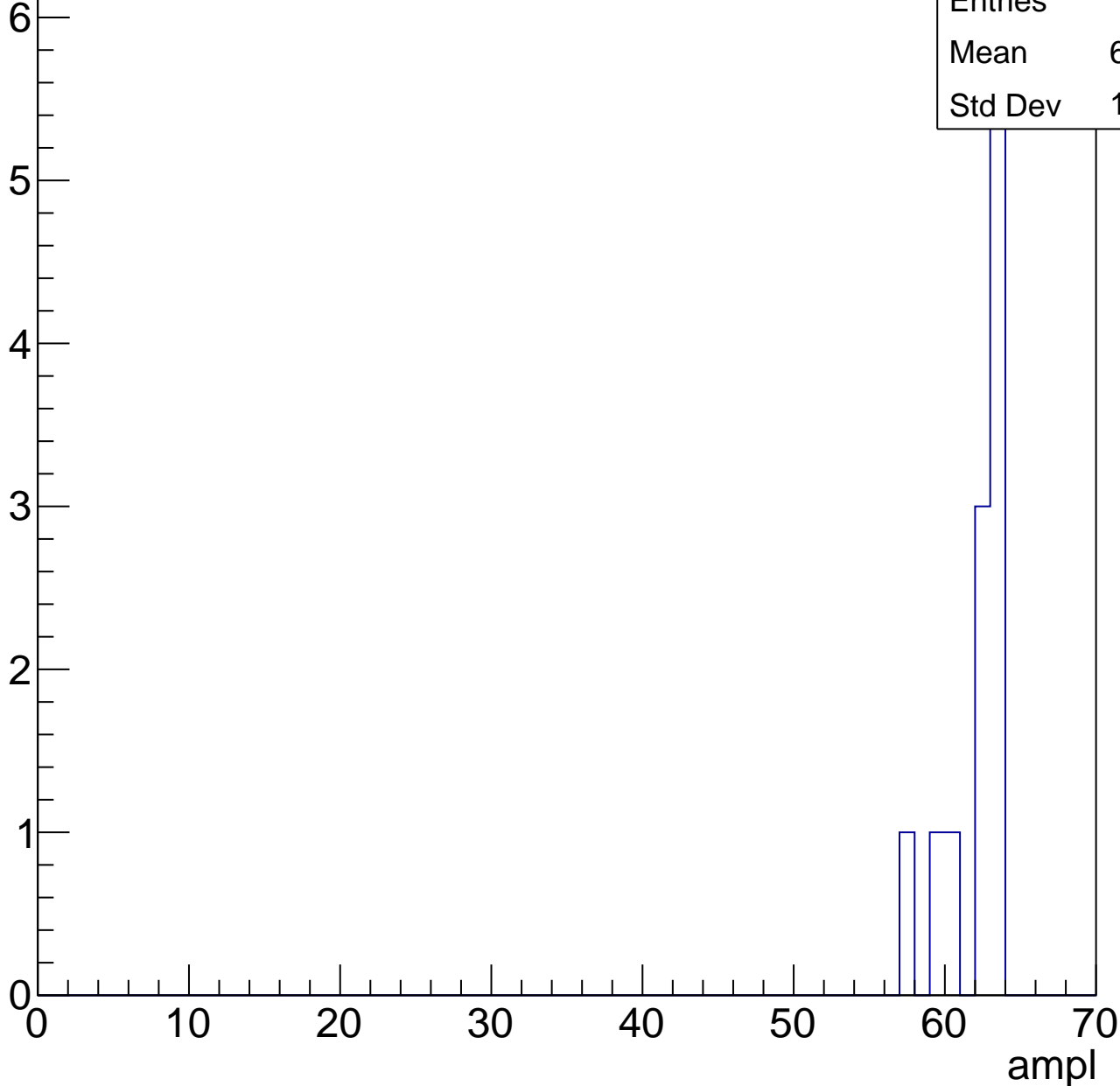


B1L102S, U11-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.67
Std Dev	1.886



B1L102S, U11-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch108, adc0

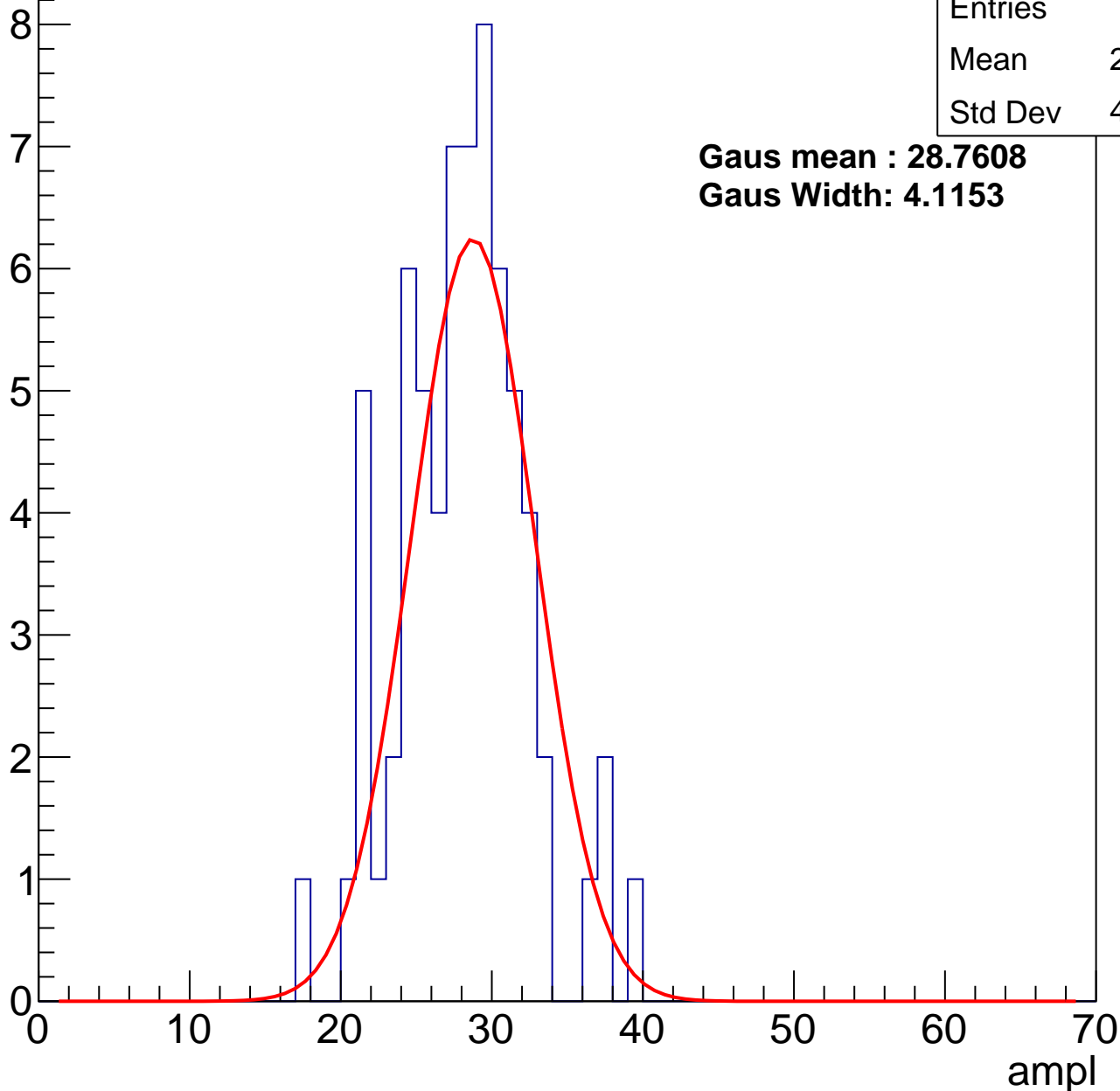
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.62
Std Dev	4.215

Gaus mean : 28.7608

Gaus Width: 4.1153



B1L102S, U11-ch108, adc1

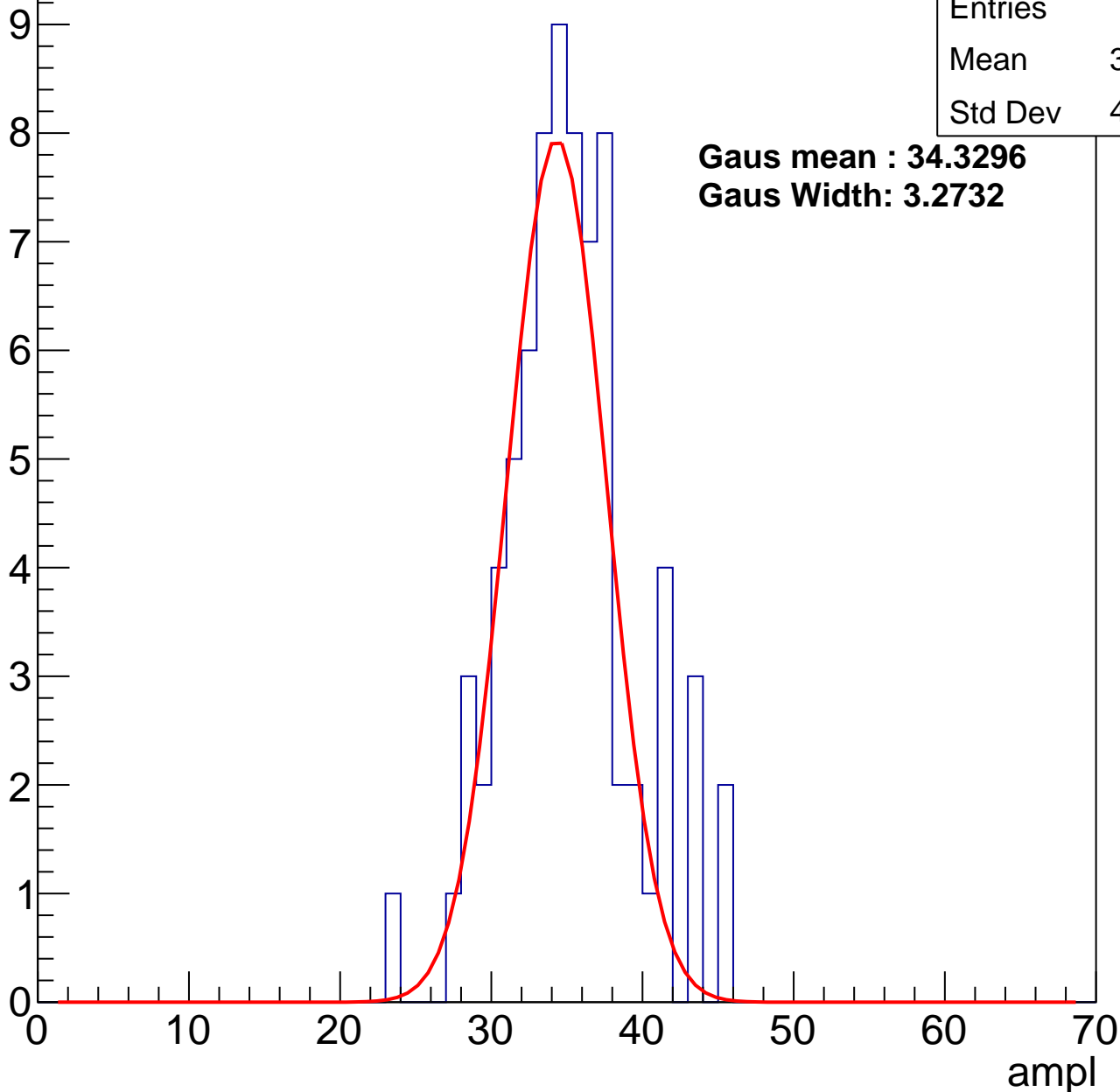
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.66
Std Dev	4.204

Gaus mean : 34.3296

Gaus Width: 3.2732



B1L102S, U11-ch108, adc2

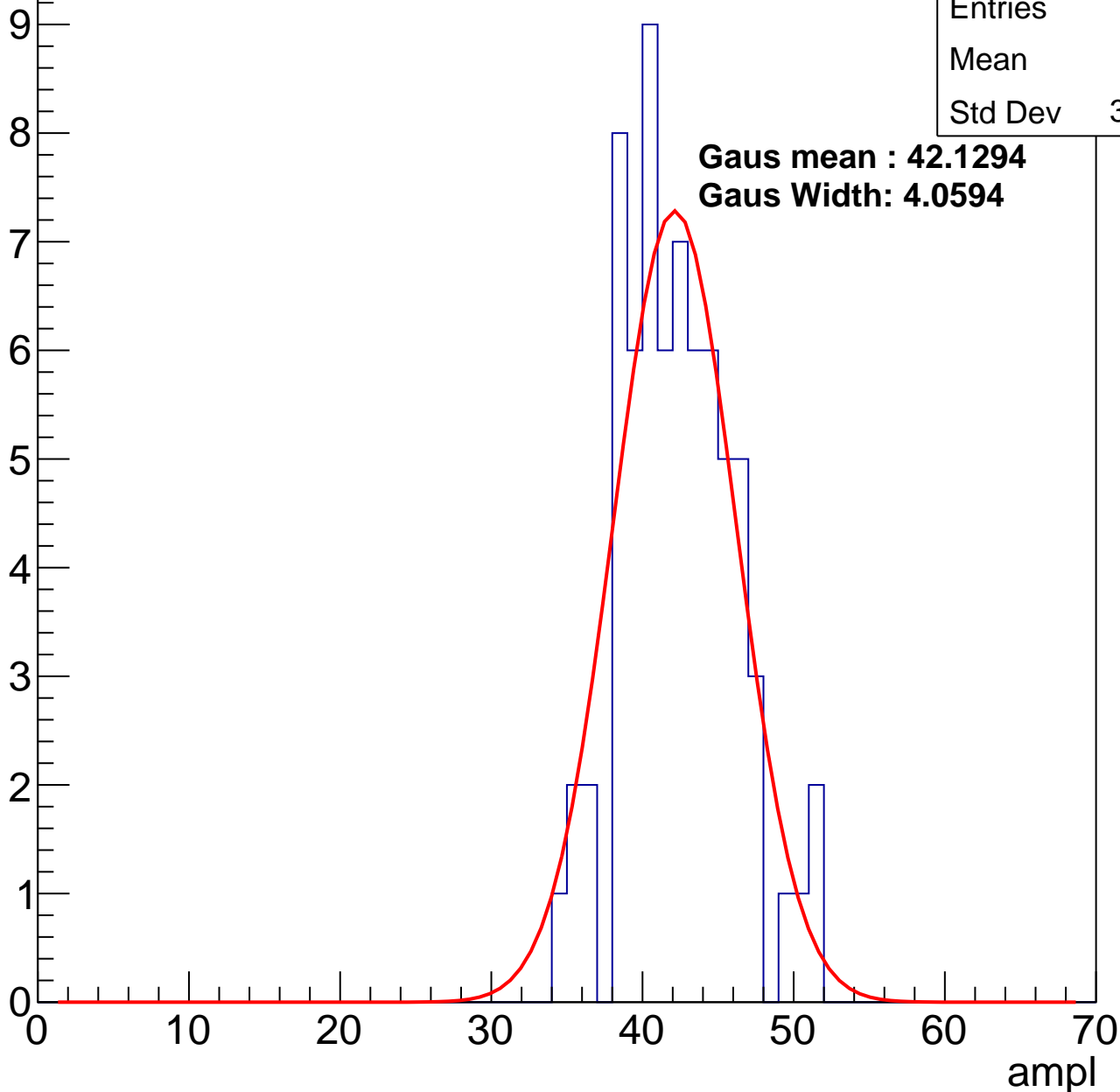
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.9
Std Dev	3.708

Gaus mean : 42.1294

Gaus Width: 4.0594

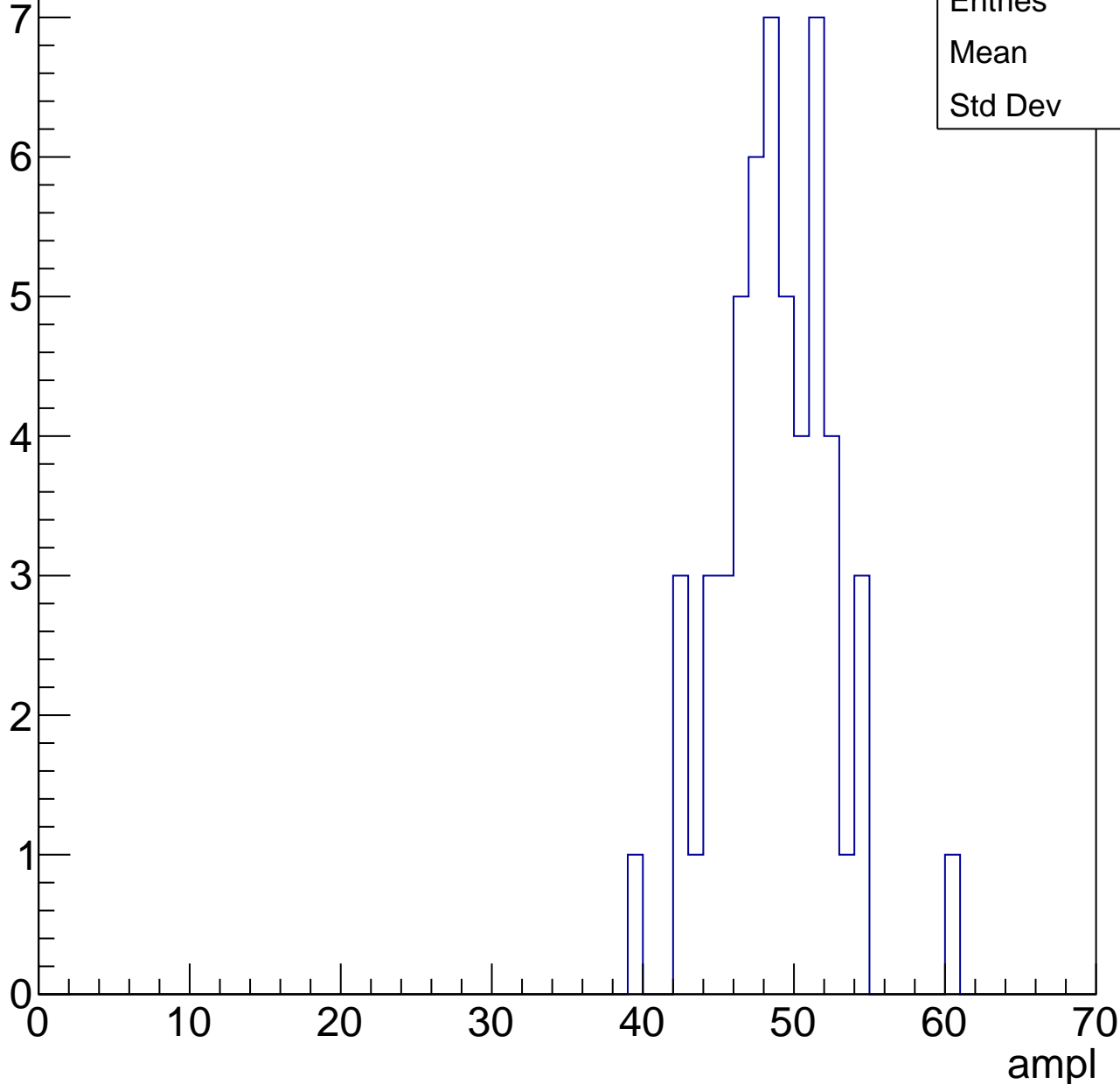


B1L102S, U11-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.3
Std Dev	3.69

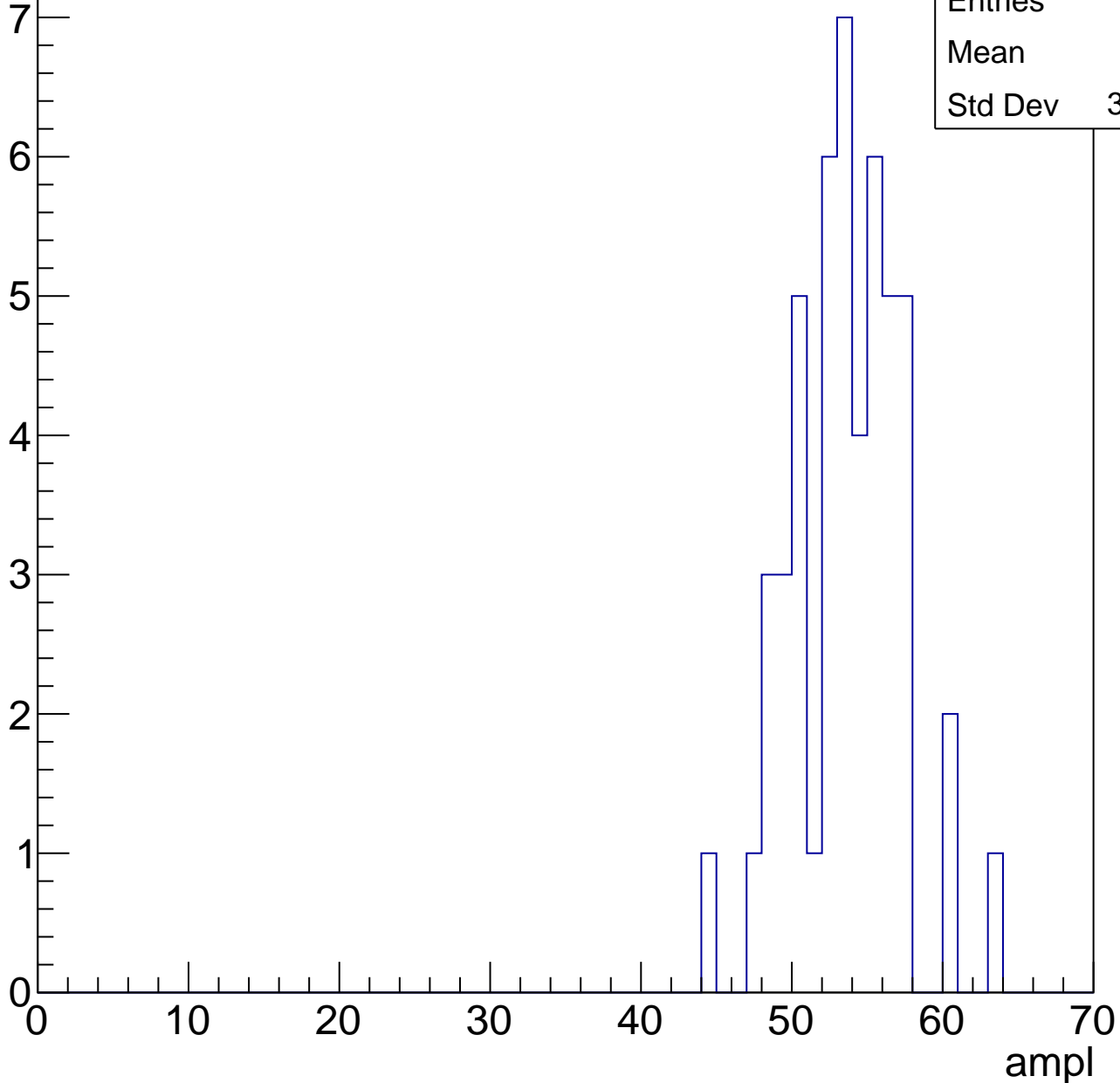


B1L102S, U11-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

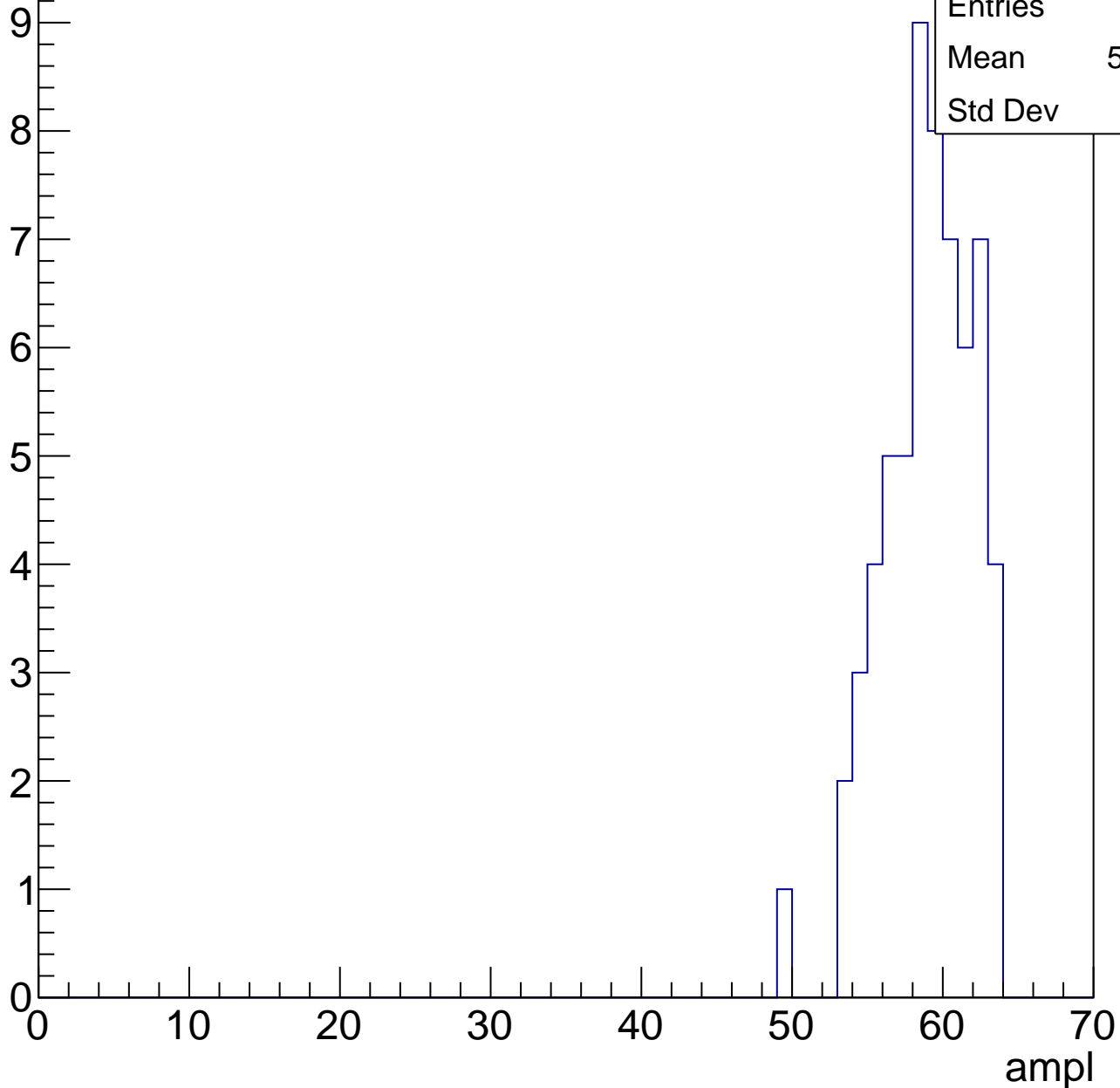
Entries	50
Mean	53.2
Std Dev	3.589



B1L102S, U11-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

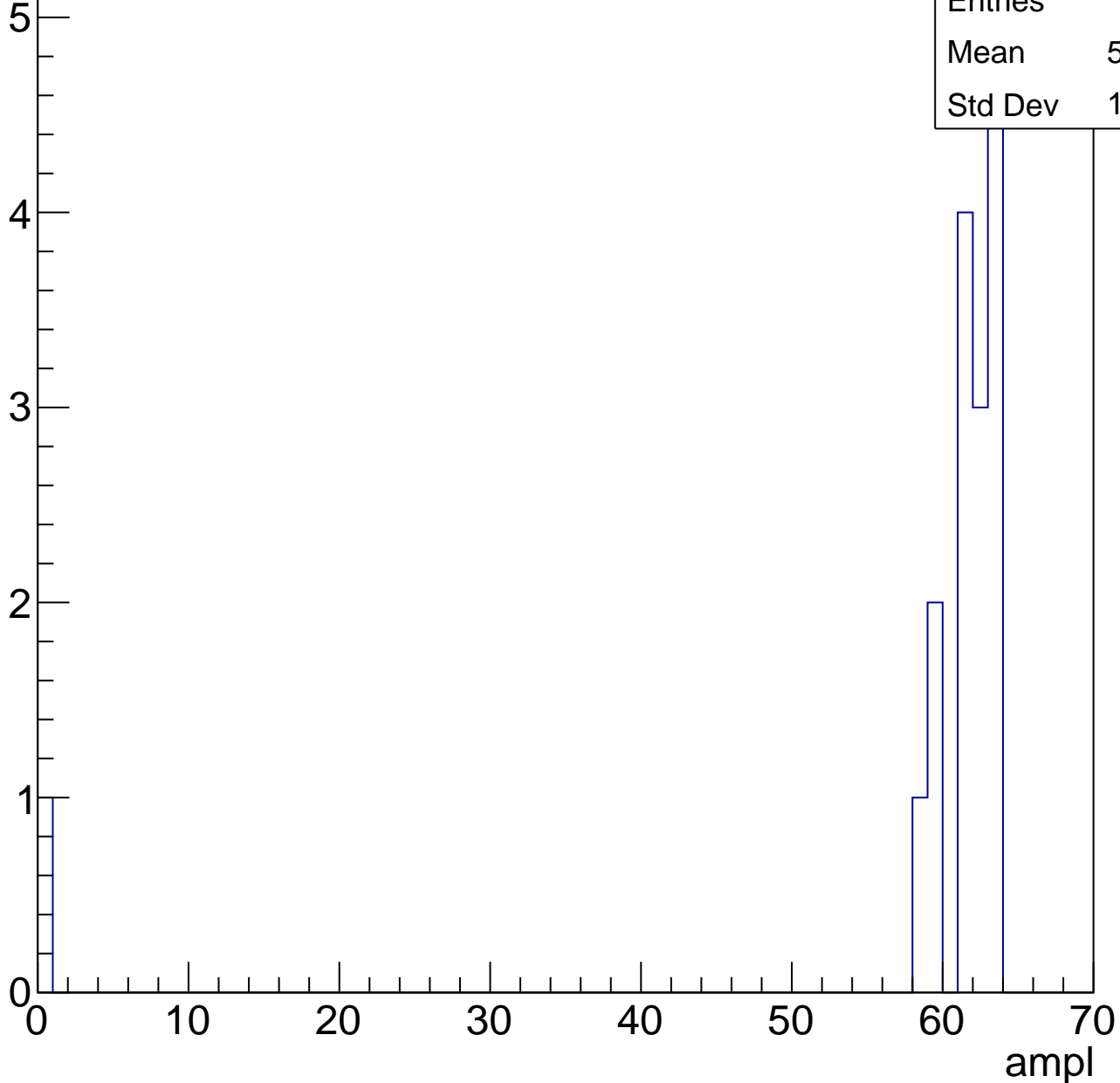


B1L102S, U11-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.56
Std Dev	14.94



B1L102S, U11-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch109, adc0

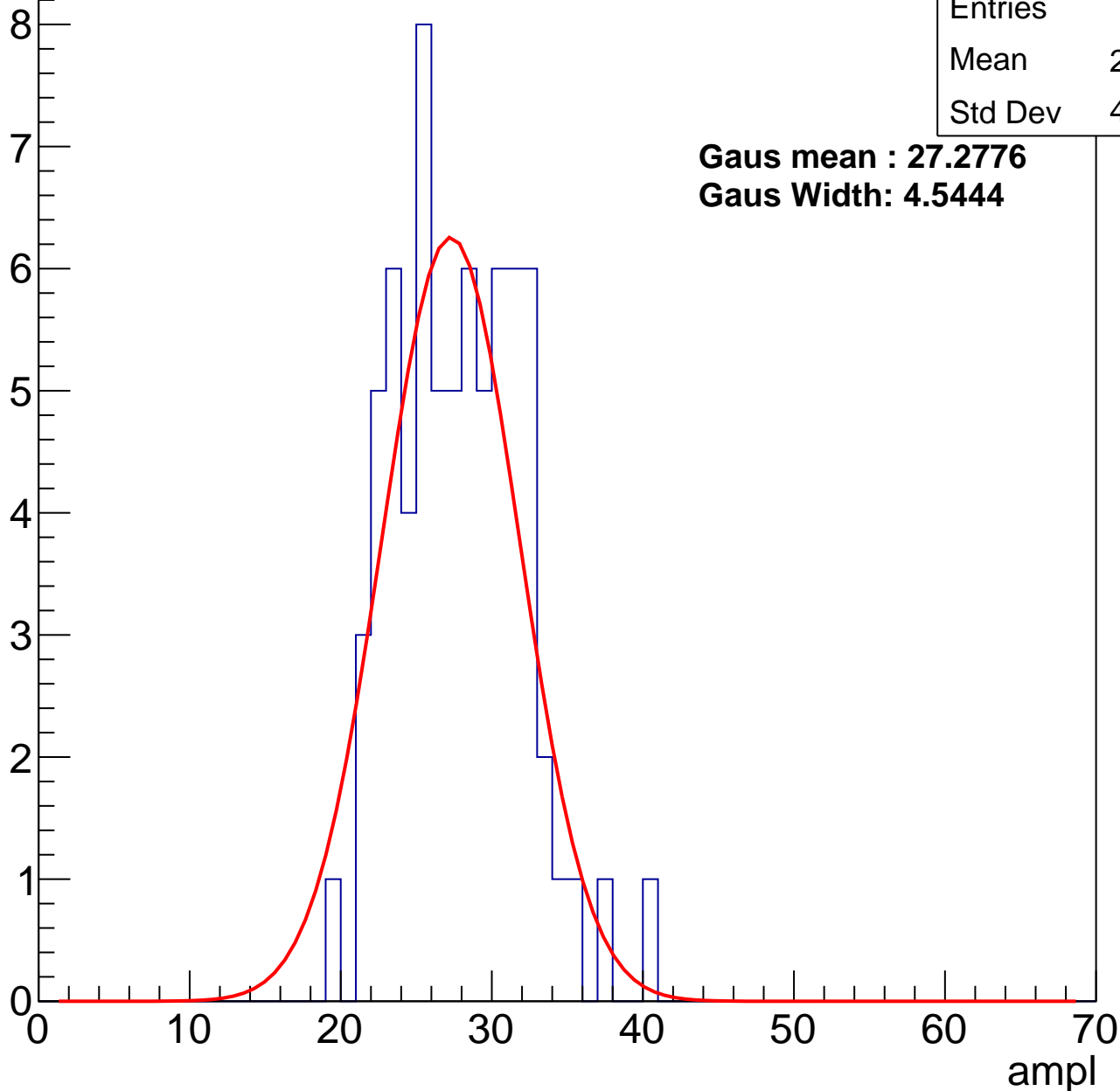
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.42
Std Dev	4.139

Gaus mean : 27.2776

Gaus Width: 4.5444



B1L102S, U11-ch109, adc1

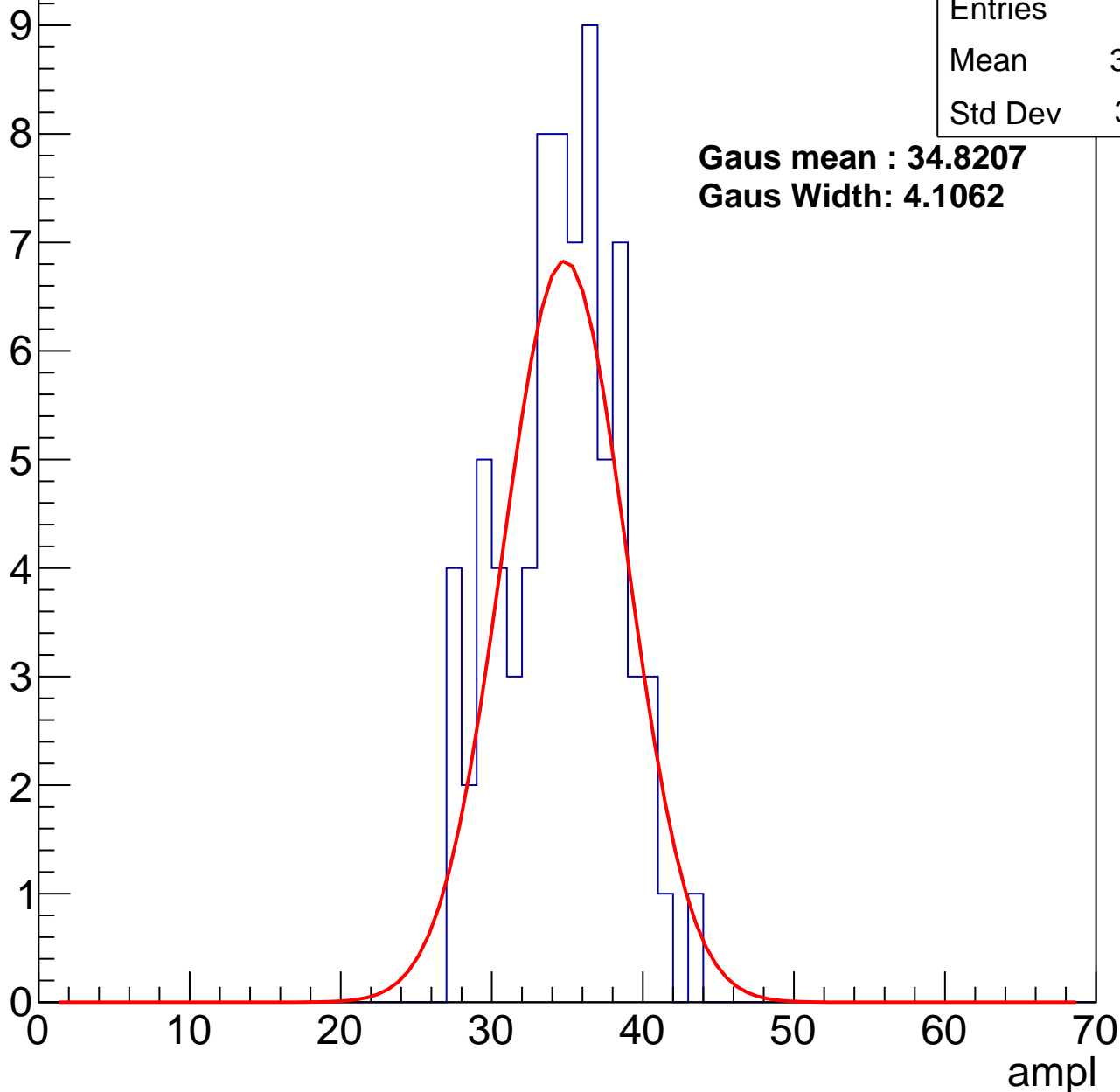
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.15
Std Dev	3.711

Gaus mean : 34.8207

Gaus Width: 4.1062



B1L102S, U11-ch109, adc2

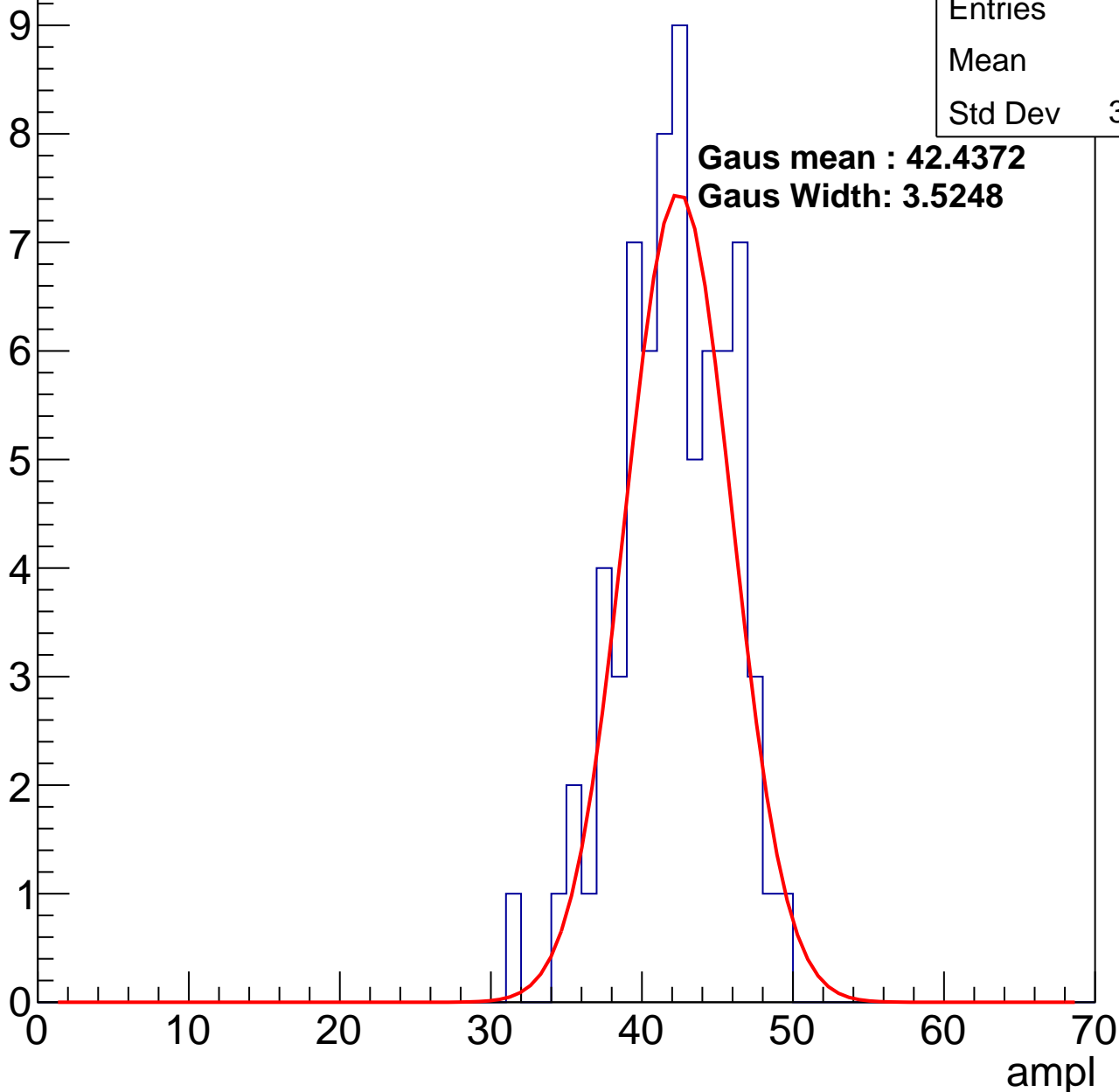
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.7
Std Dev	3.582

Gaus mean : 42.4372

Gaus Width: 3.5248

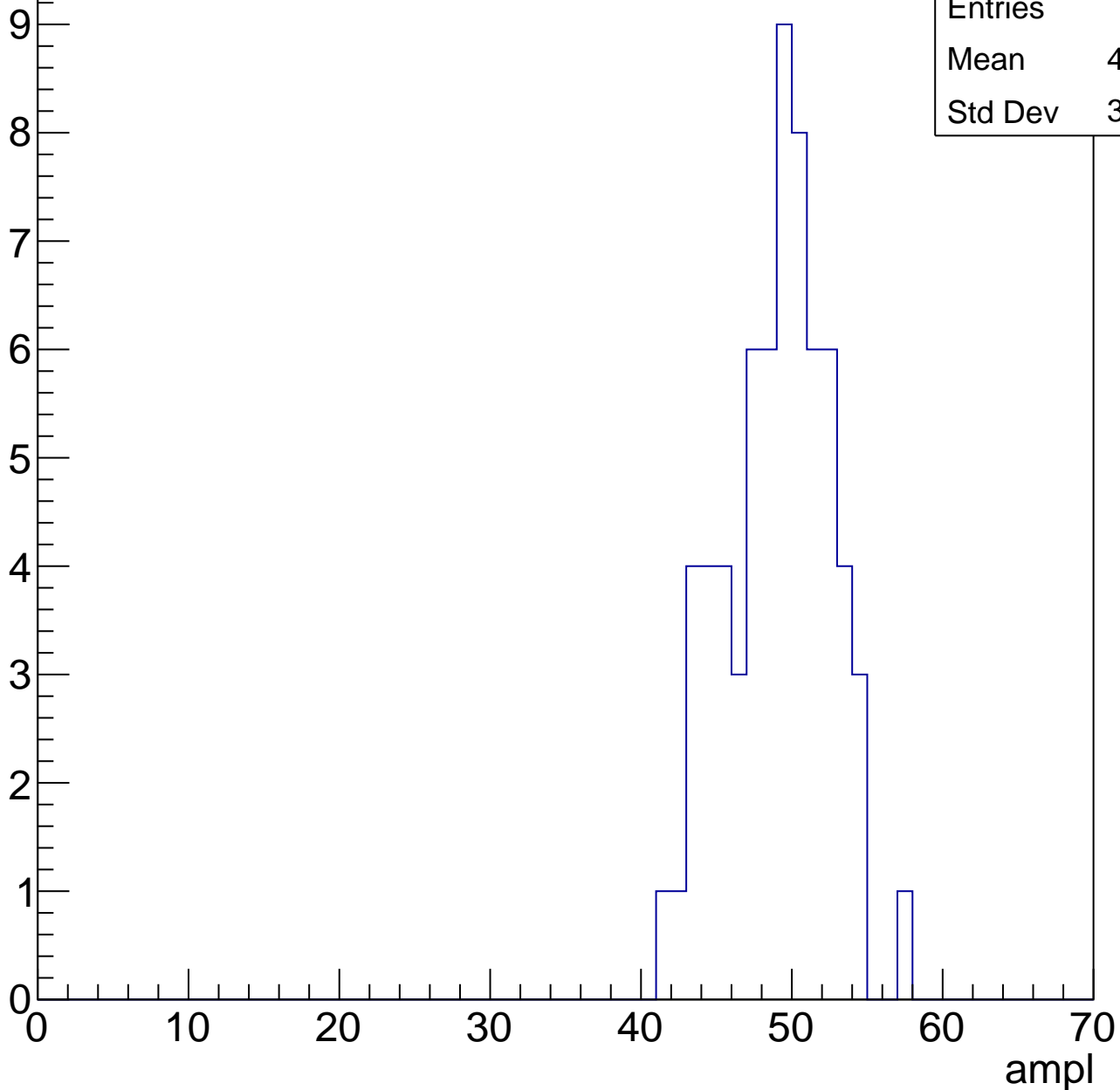


B1L102S, U11-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.62
Std Dev	3.388

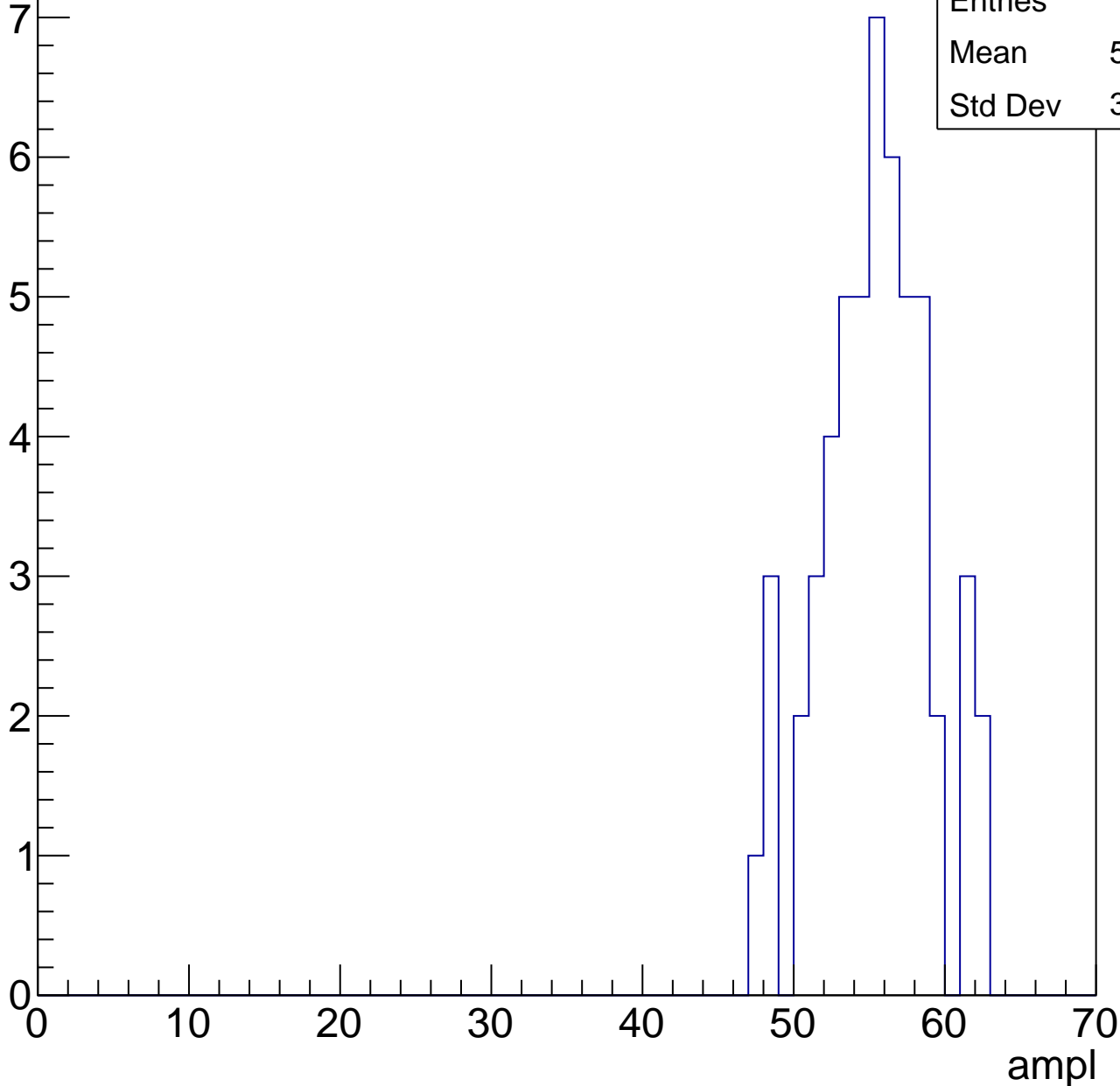


B1L102S, U11-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.87
Std Dev	3.577

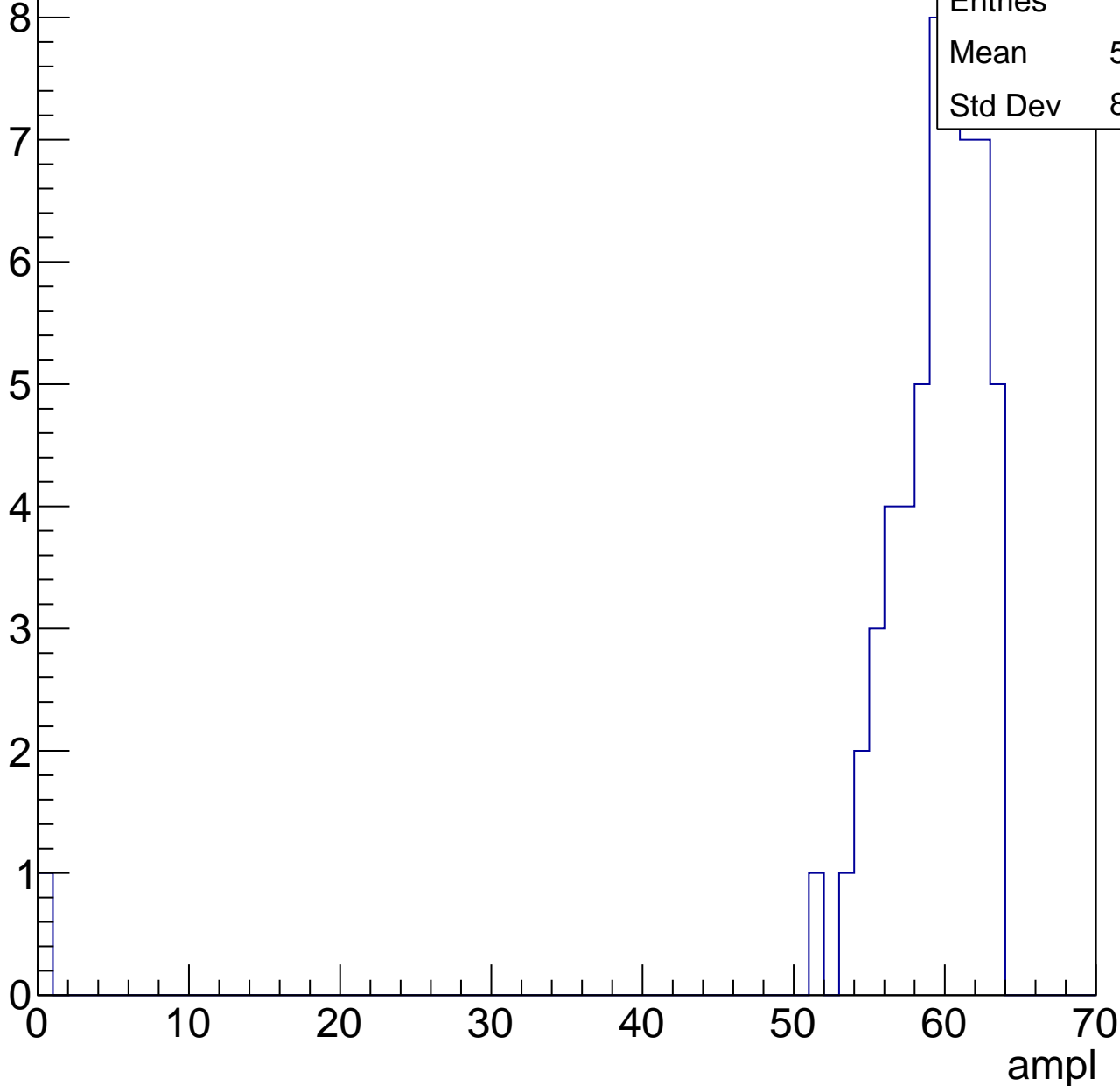


B1L102S, U11-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

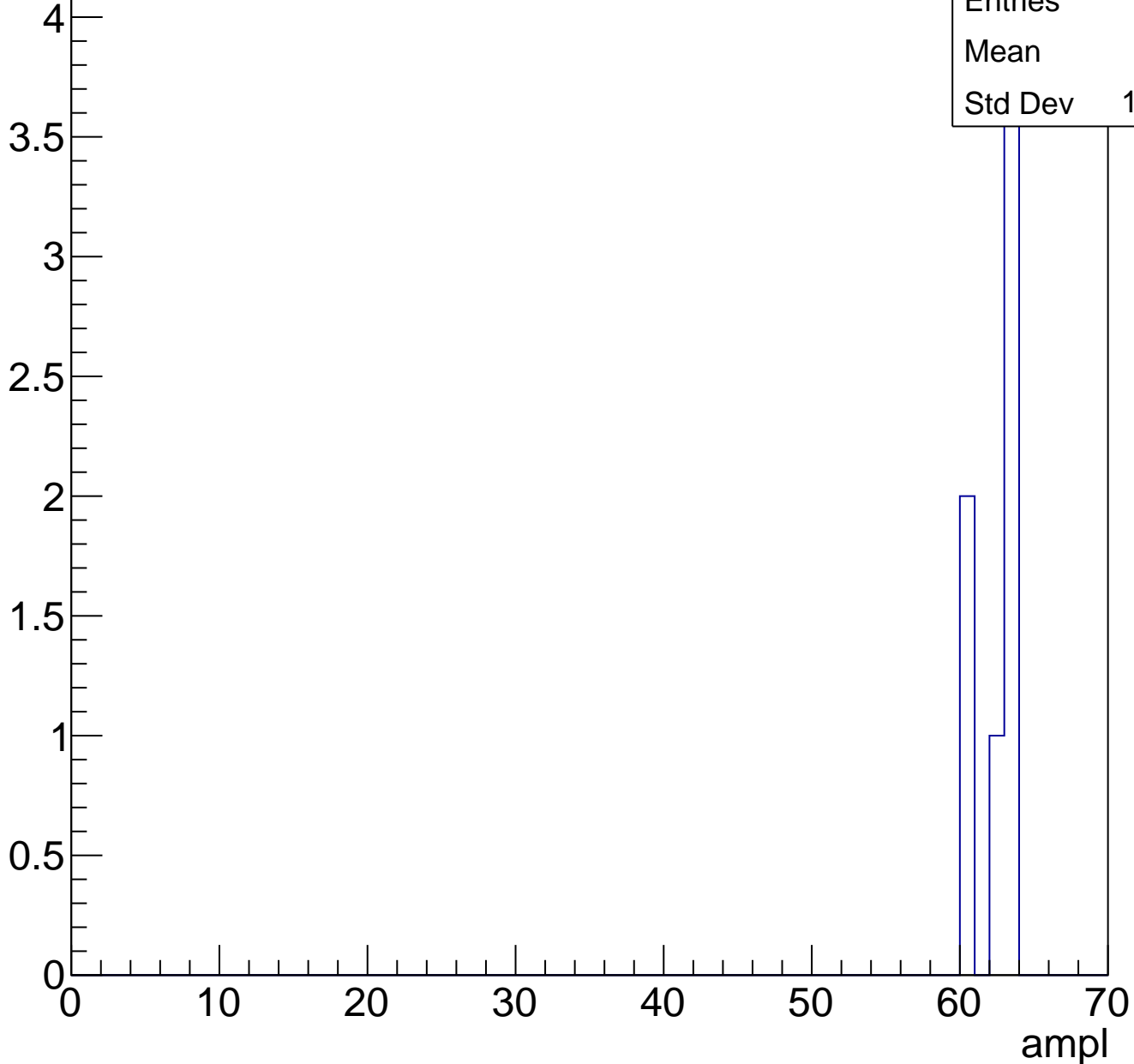
Entries	56
Mean	57.98
Std Dev	8.299



B1L102S, U11-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

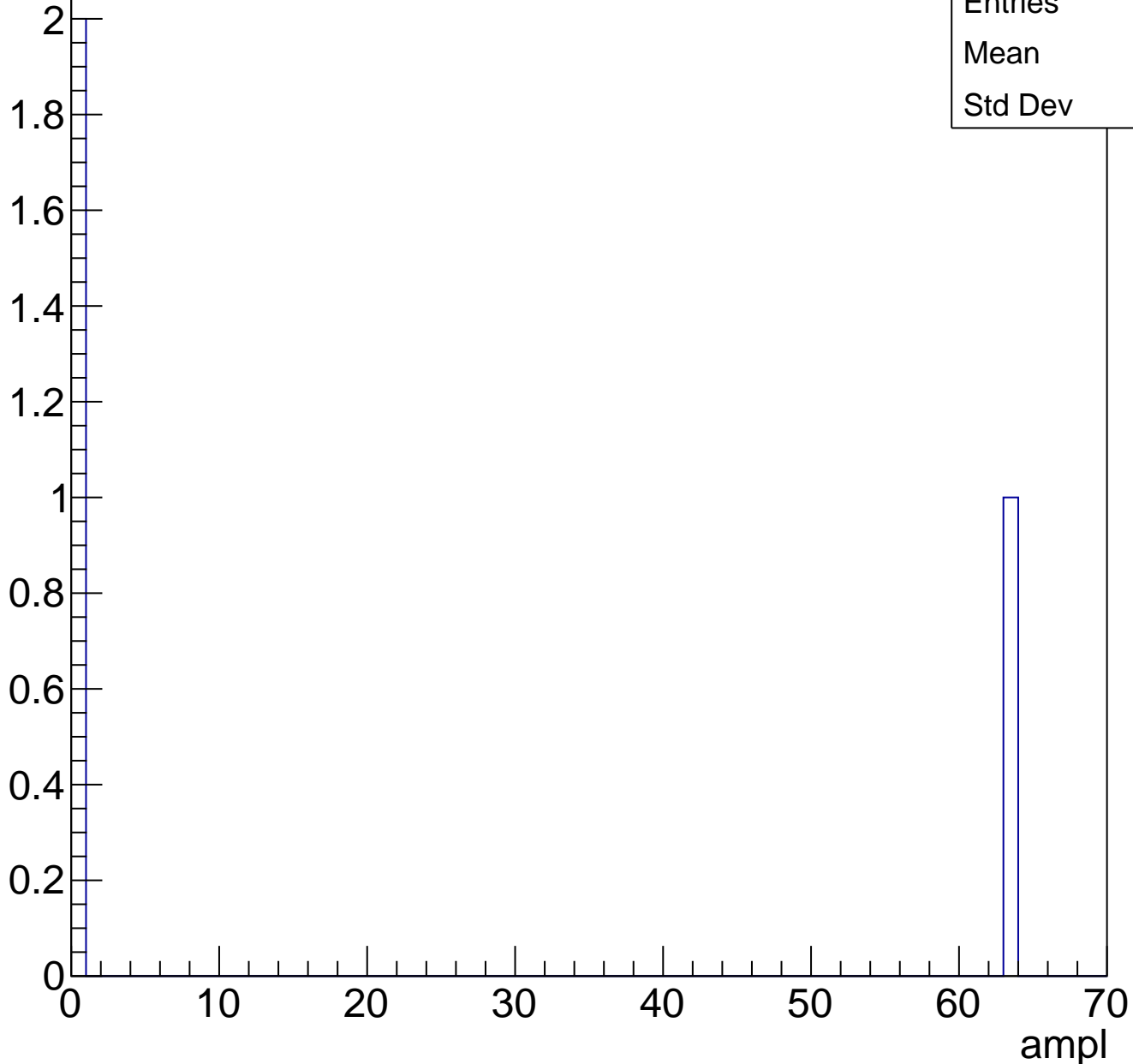
Entry



B1L102S, U11-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U11-ch110, adc0

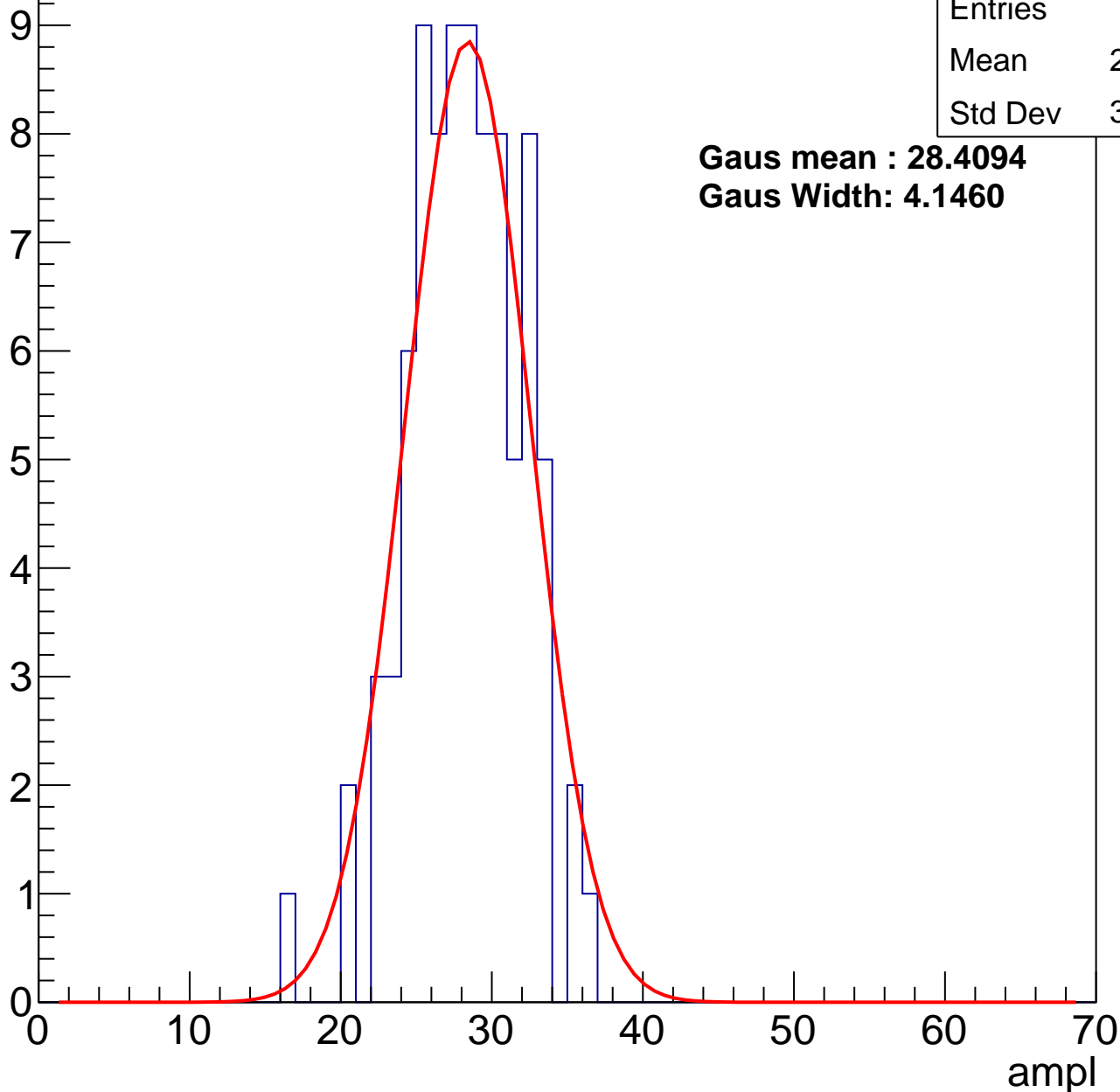
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	27.78
Std Dev	3.665

Gaus mean : 28.4094

Gaus Width: 4.1460



B1L102S, U11-ch110, adc1

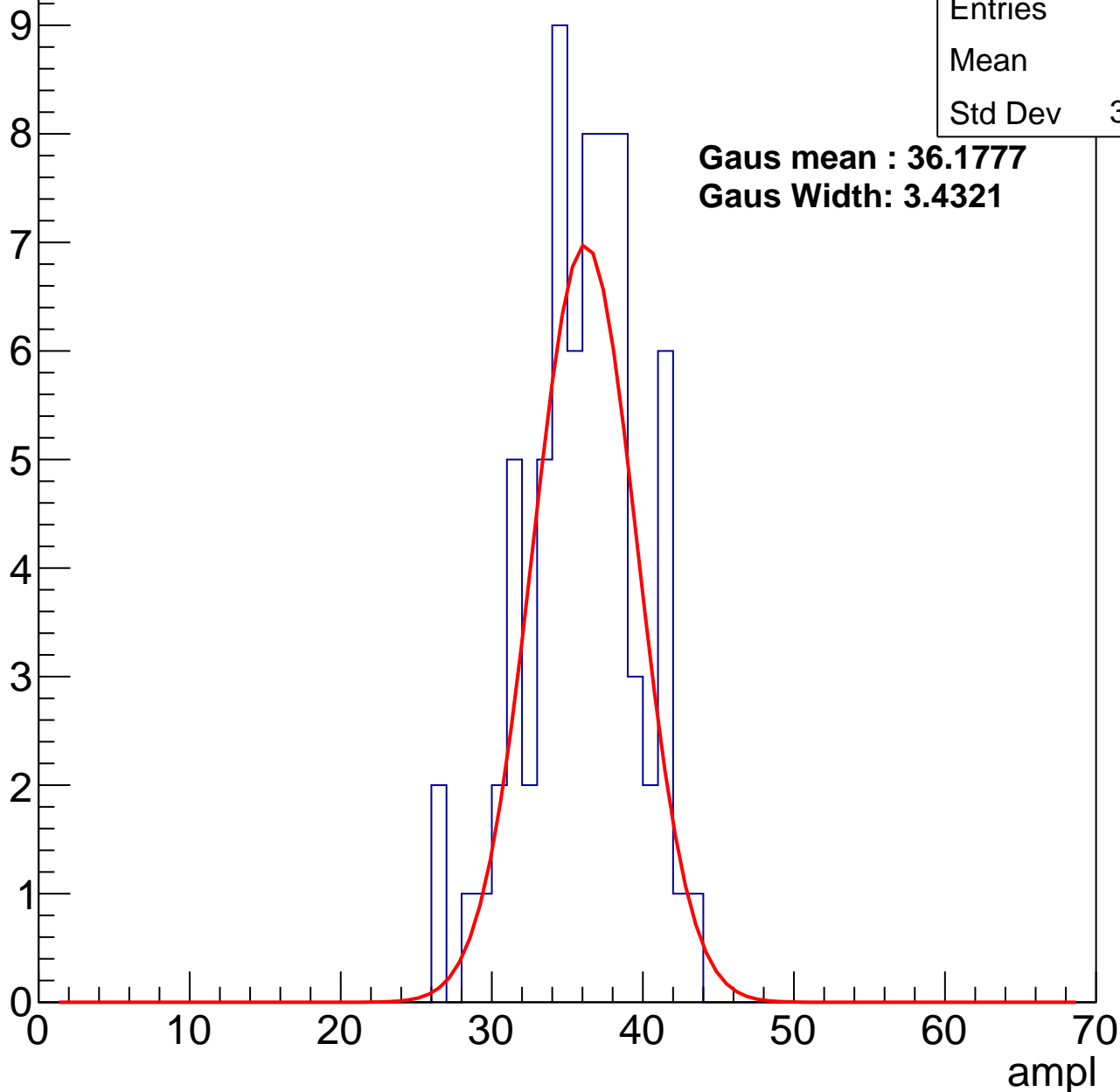
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.5
Std Dev	3.675

Gaus mean : 36.1777

Gaus Width: 3.4321



B1L102S, U11-ch110, adc2

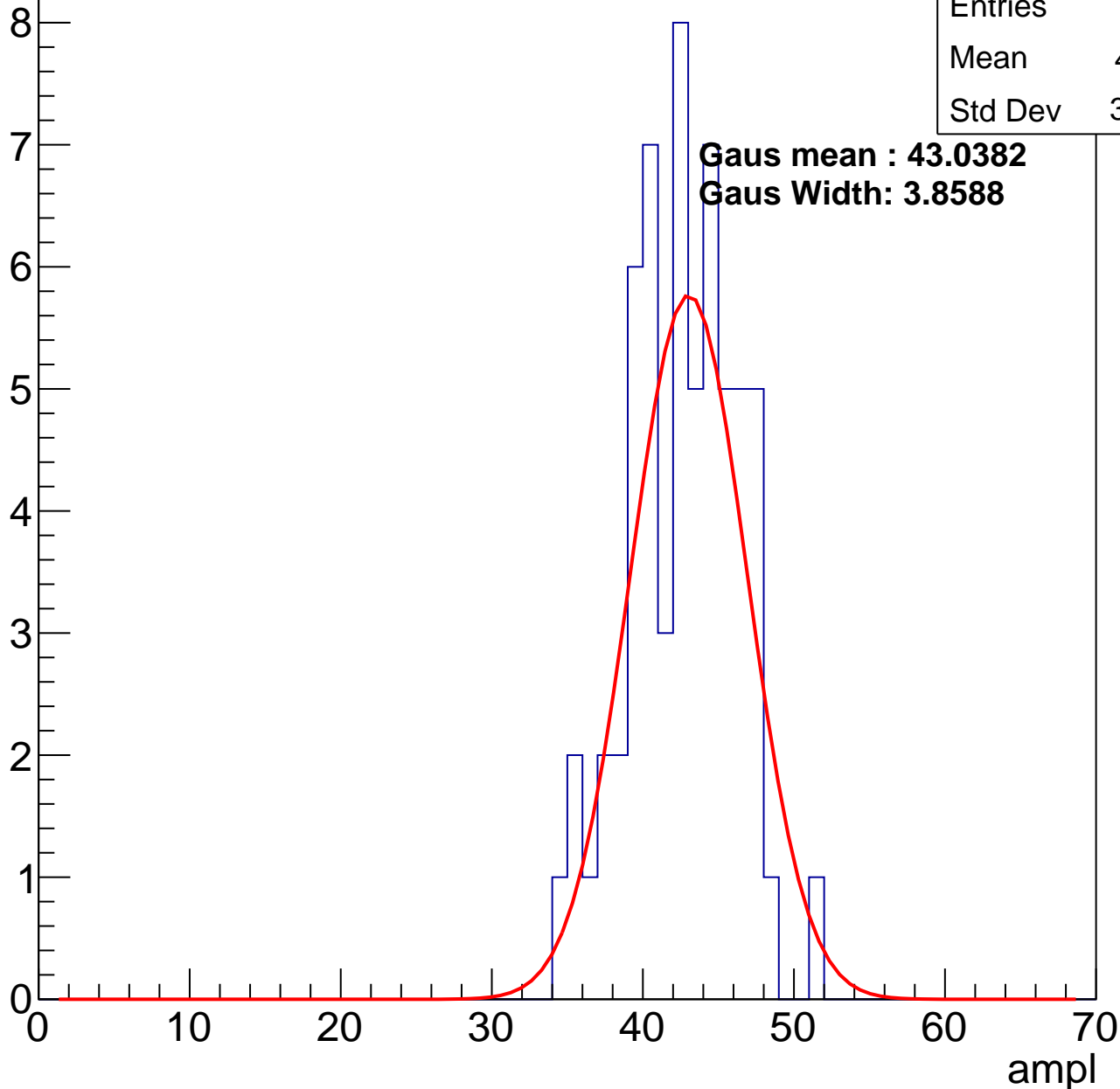
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.21
Std Dev	3.535

Gaus mean : 43.0382

Gaus Width: 3.8588

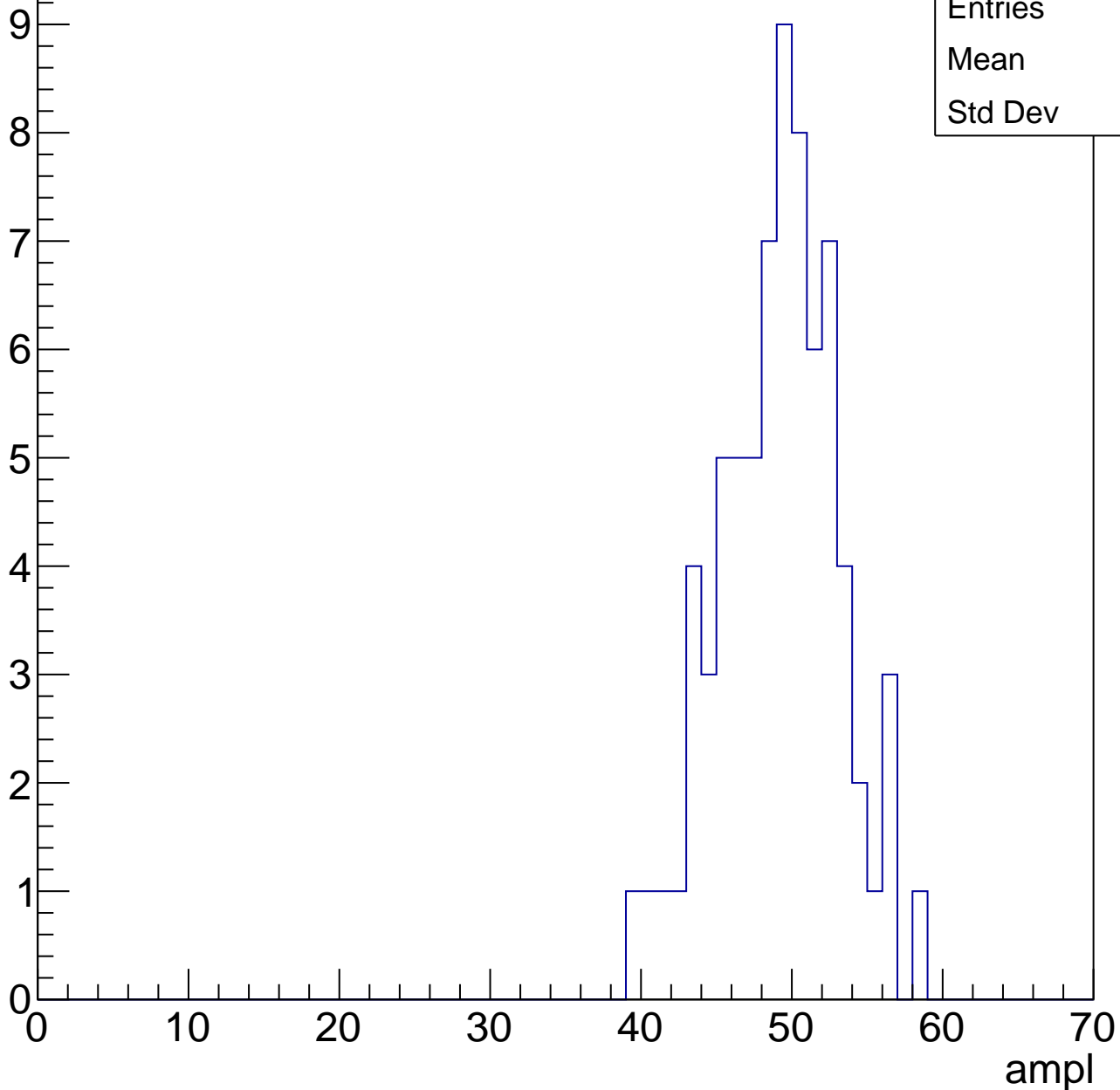


B1L102S, U11-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.7
Std Dev	3.91

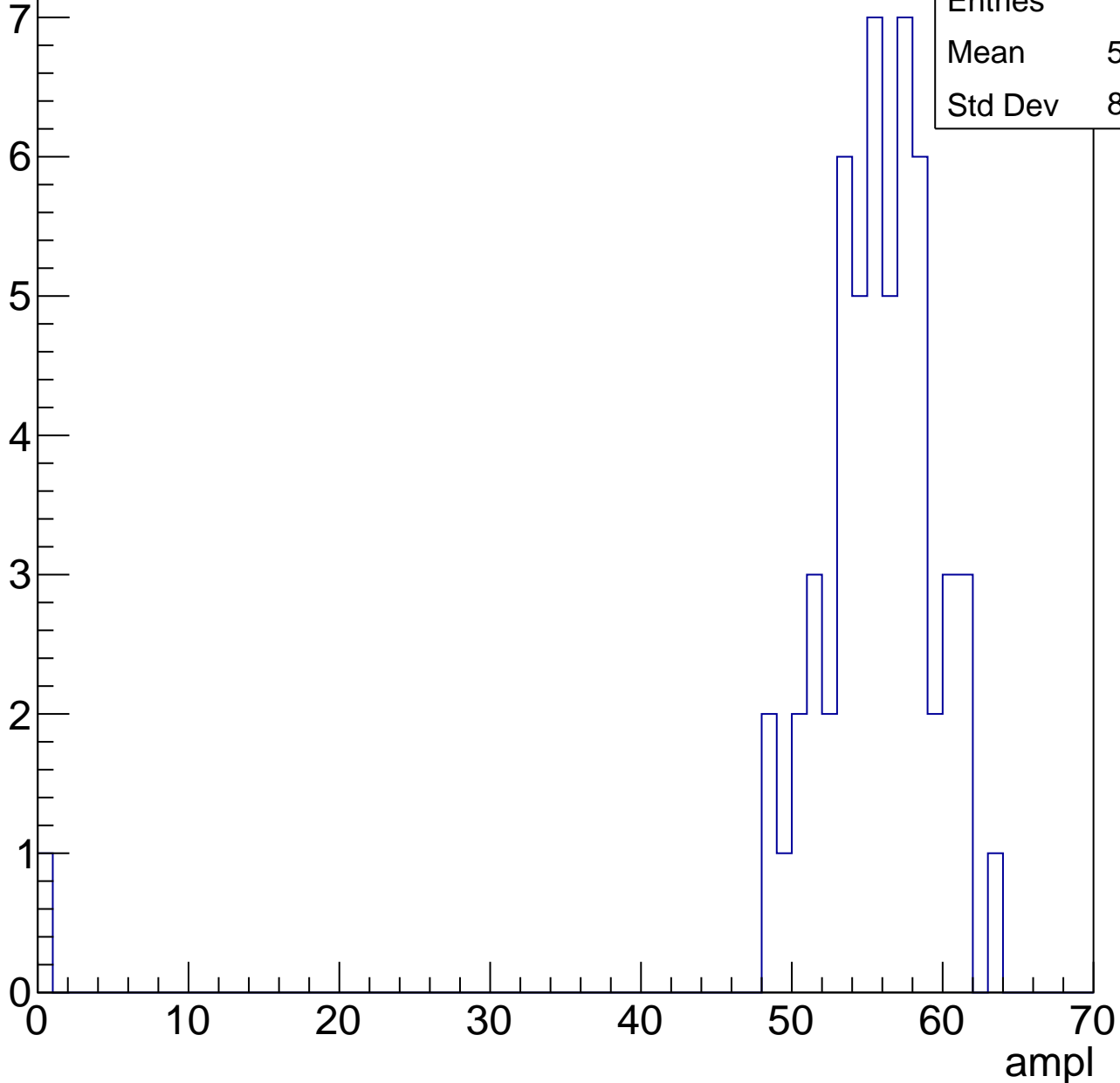


B1L102S, U11-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

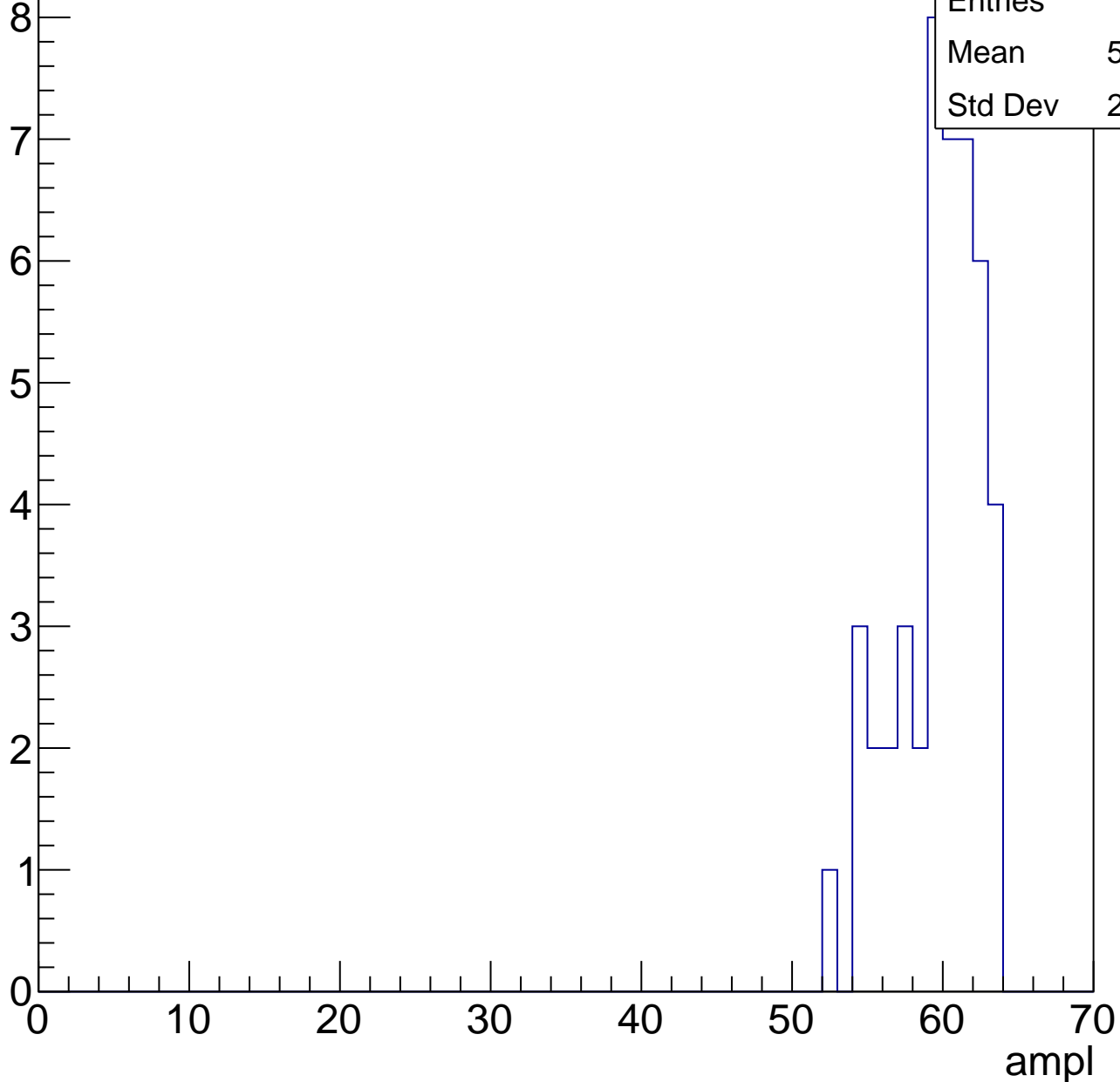
Entries	56
Mean	54.39
Std Dev	8.073



B1L102S, U11-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

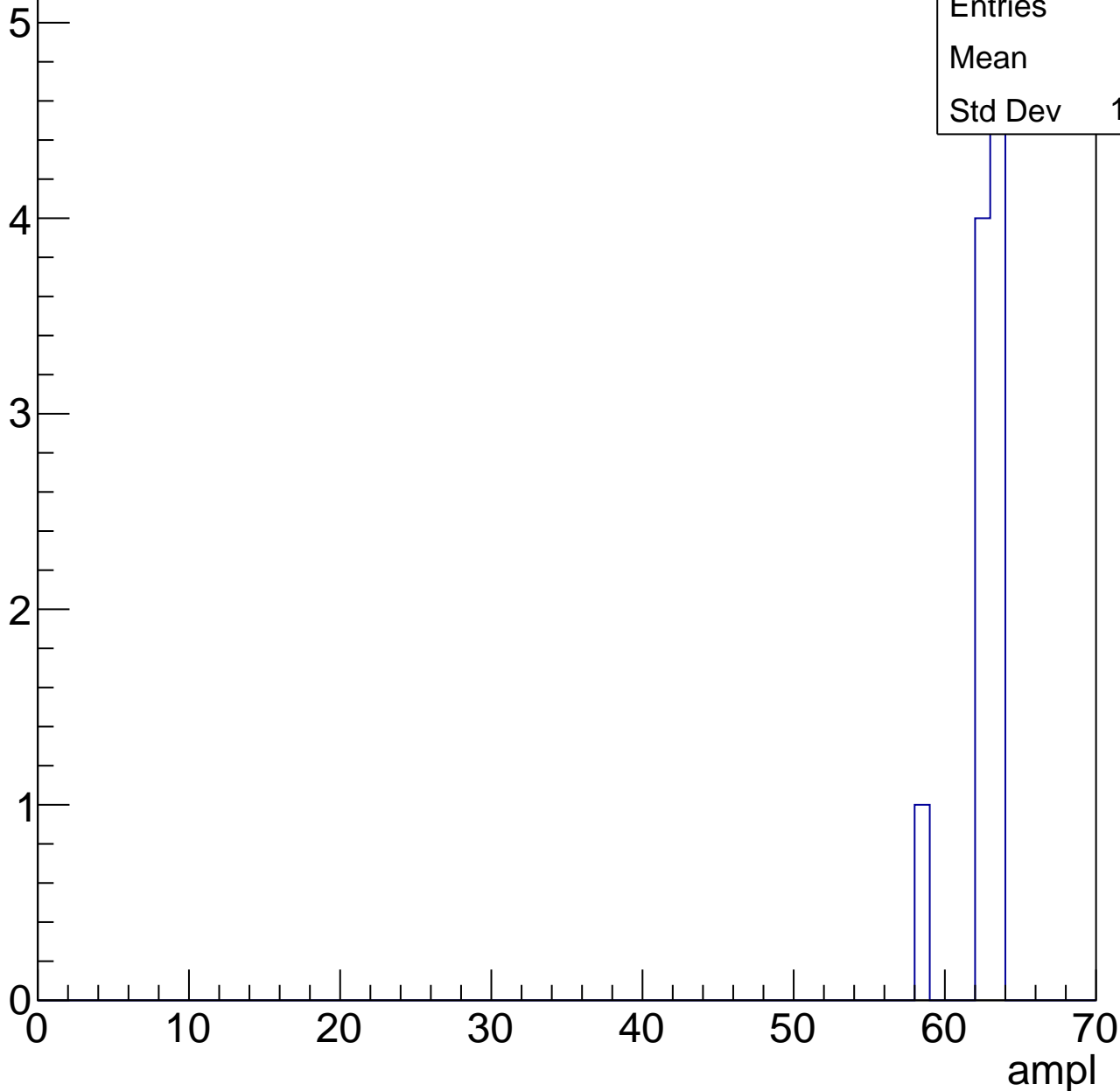


B1L102S, U11-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.1
Std Dev	1.446



B1L102S, U11-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch111, adc0

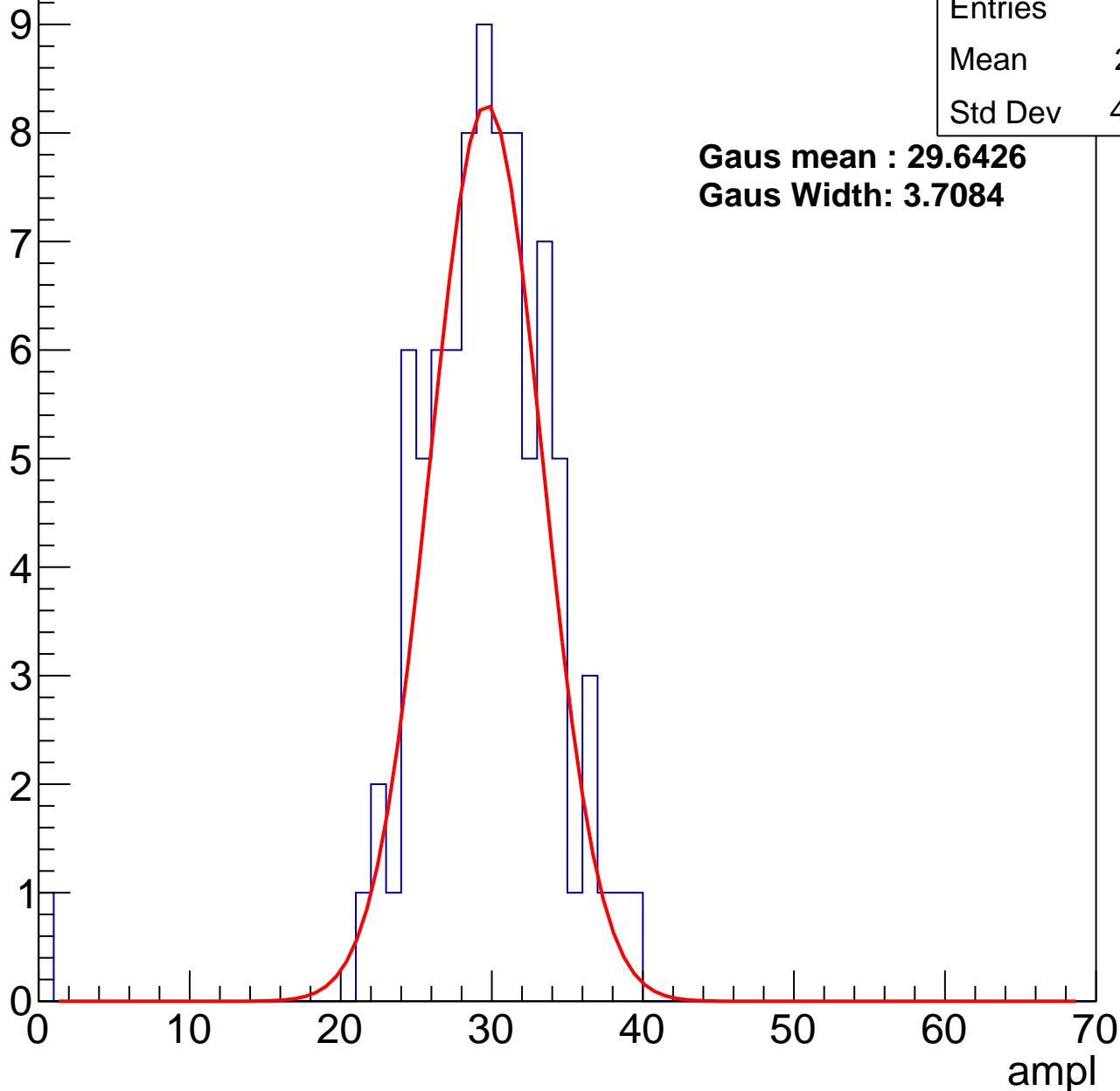
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.01
Std Dev	4.976

Gaus mean : 29.6426

Gaus Width: 3.7084



B1L102S, U11-ch111, adc1

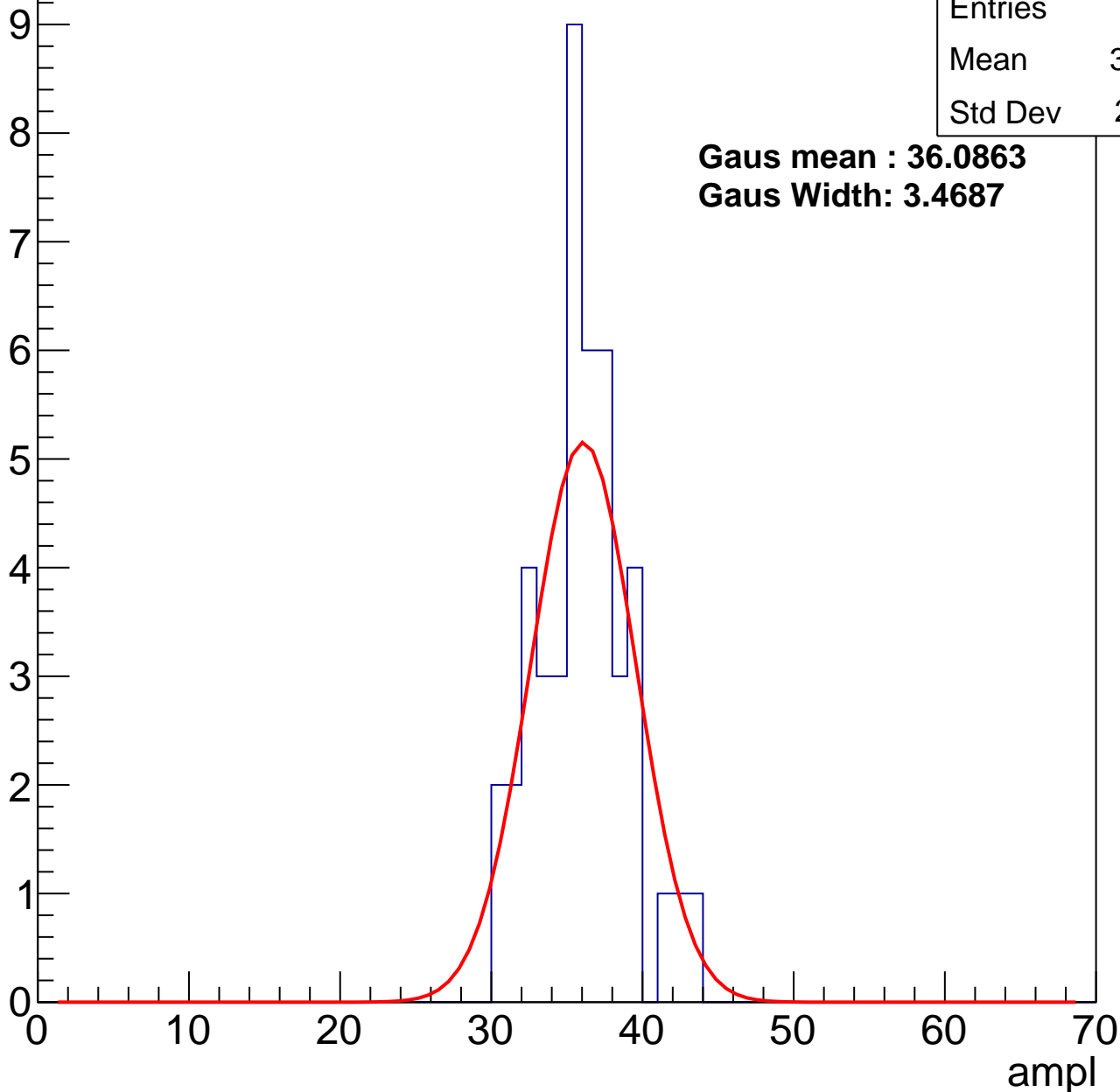
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	35.56
Std Dev	2.941

Gaus mean : 36.0863

Gaus Width: 3.4687



B1L102S, U11-ch111, adc2

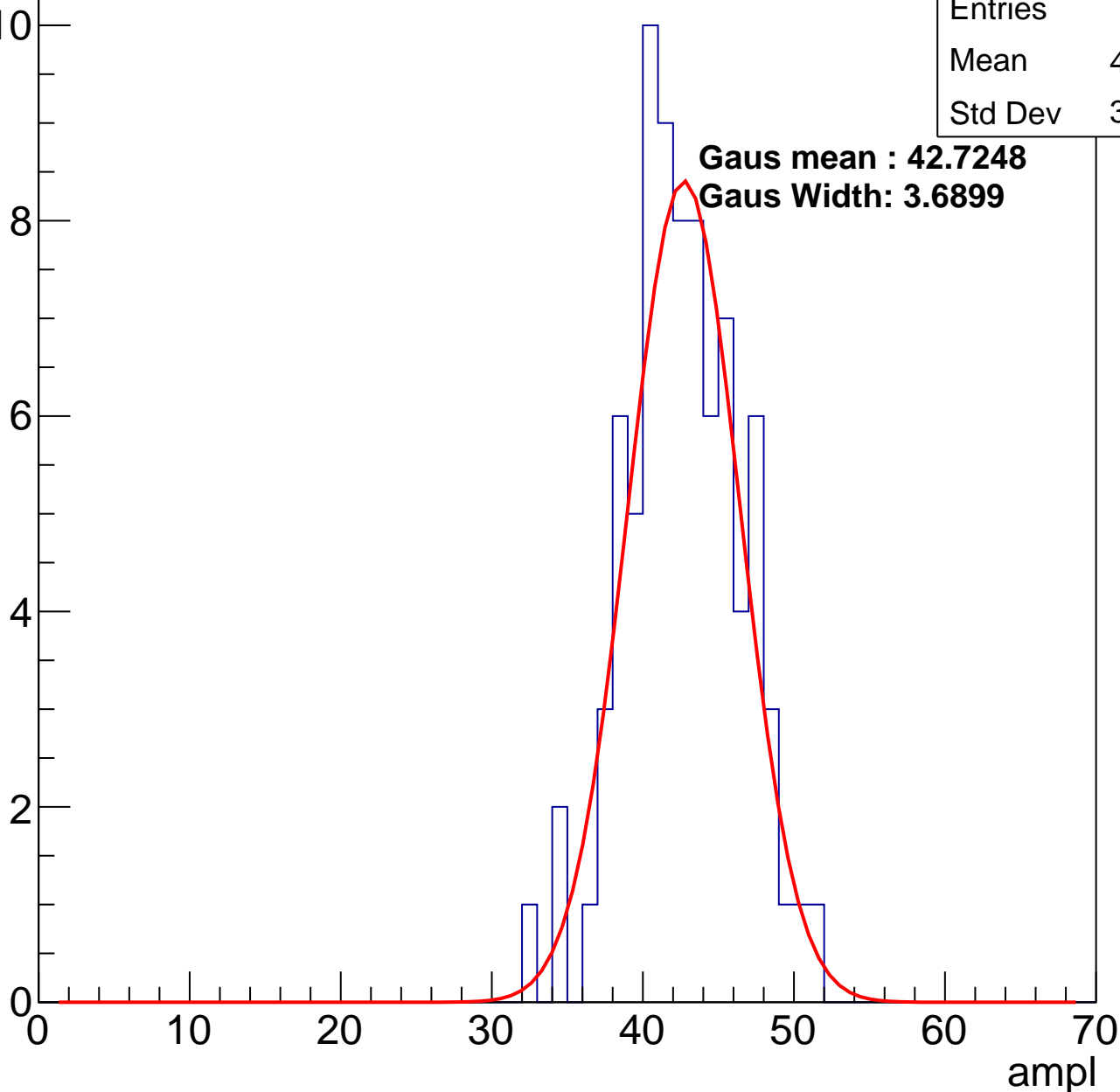
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	42.17
Std Dev	3.725

Gaus mean : 42.7248

Gaus Width: 3.6899

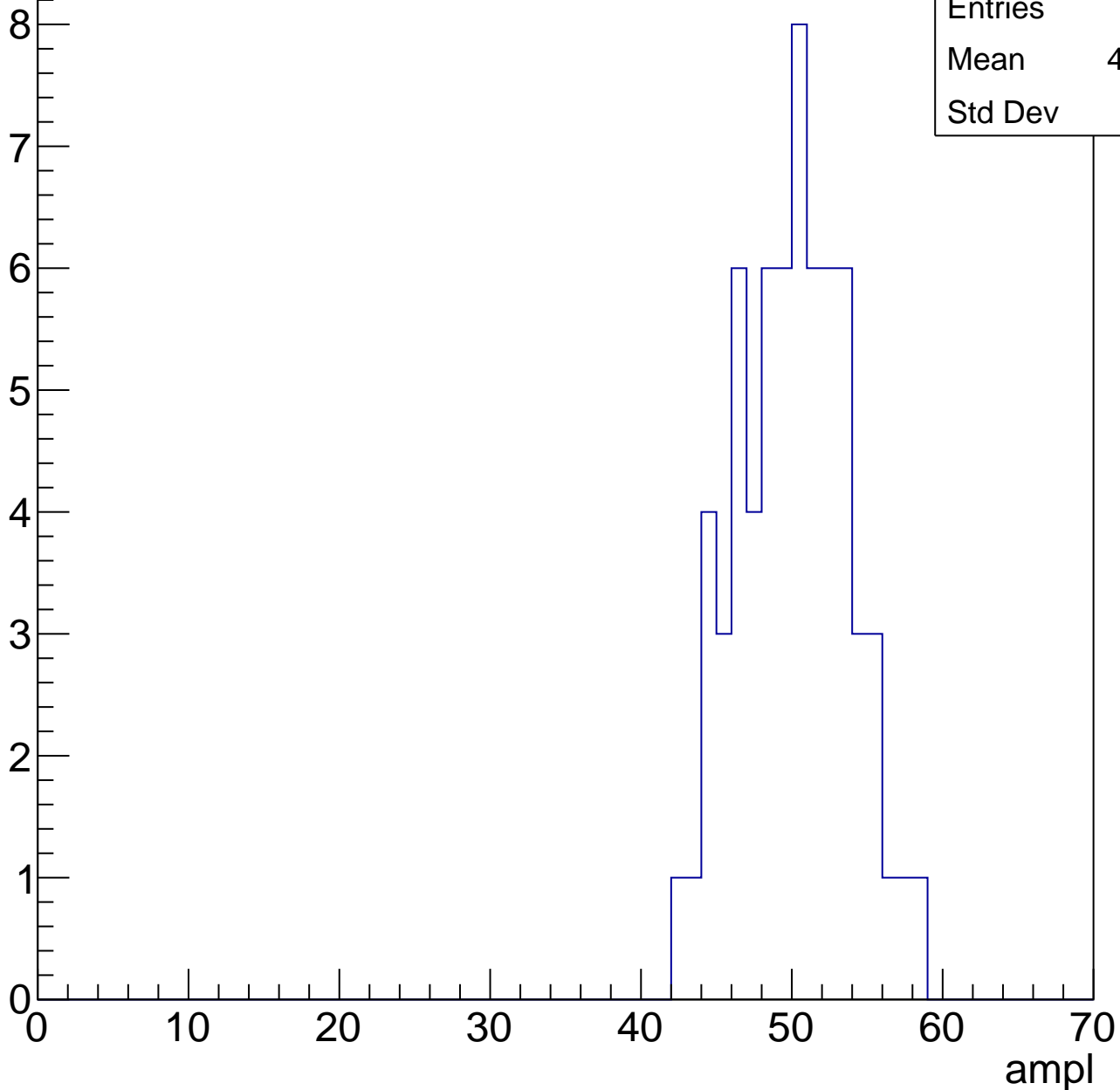


B1L102S, U11-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.64
Std Dev	3.57

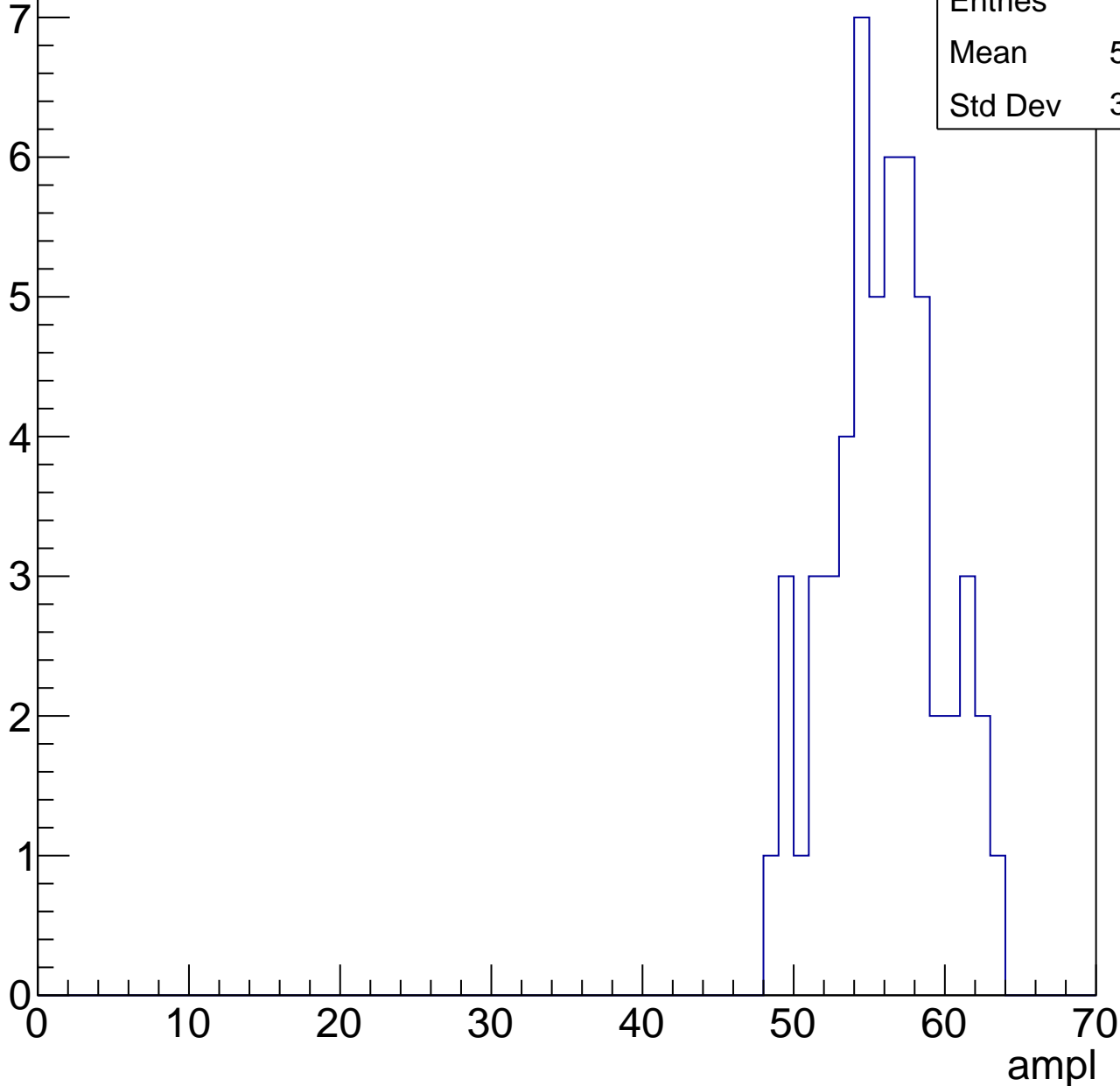


B1L102S, U11-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.46
Std Dev	3.589

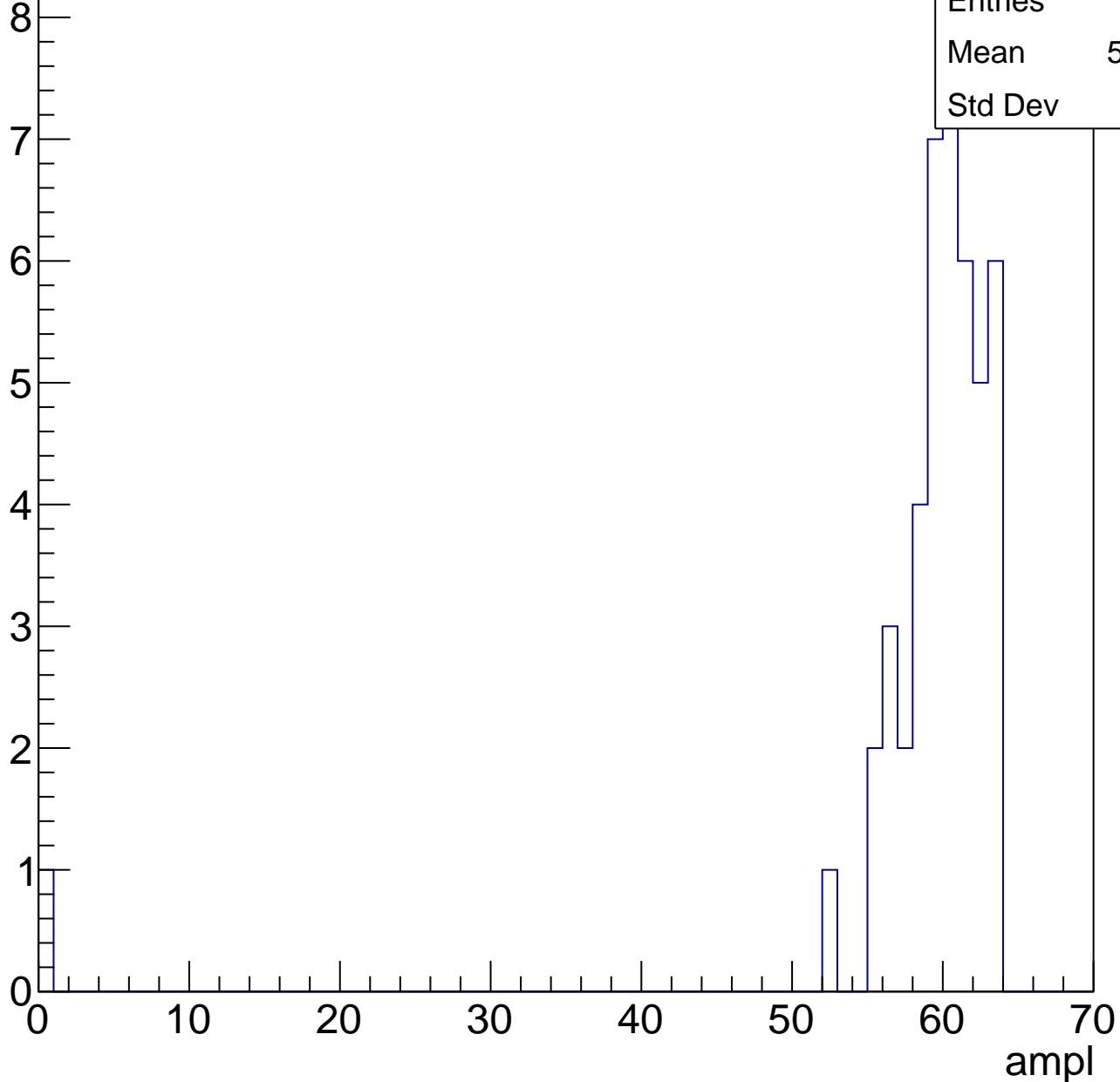


B1L102S, U11-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

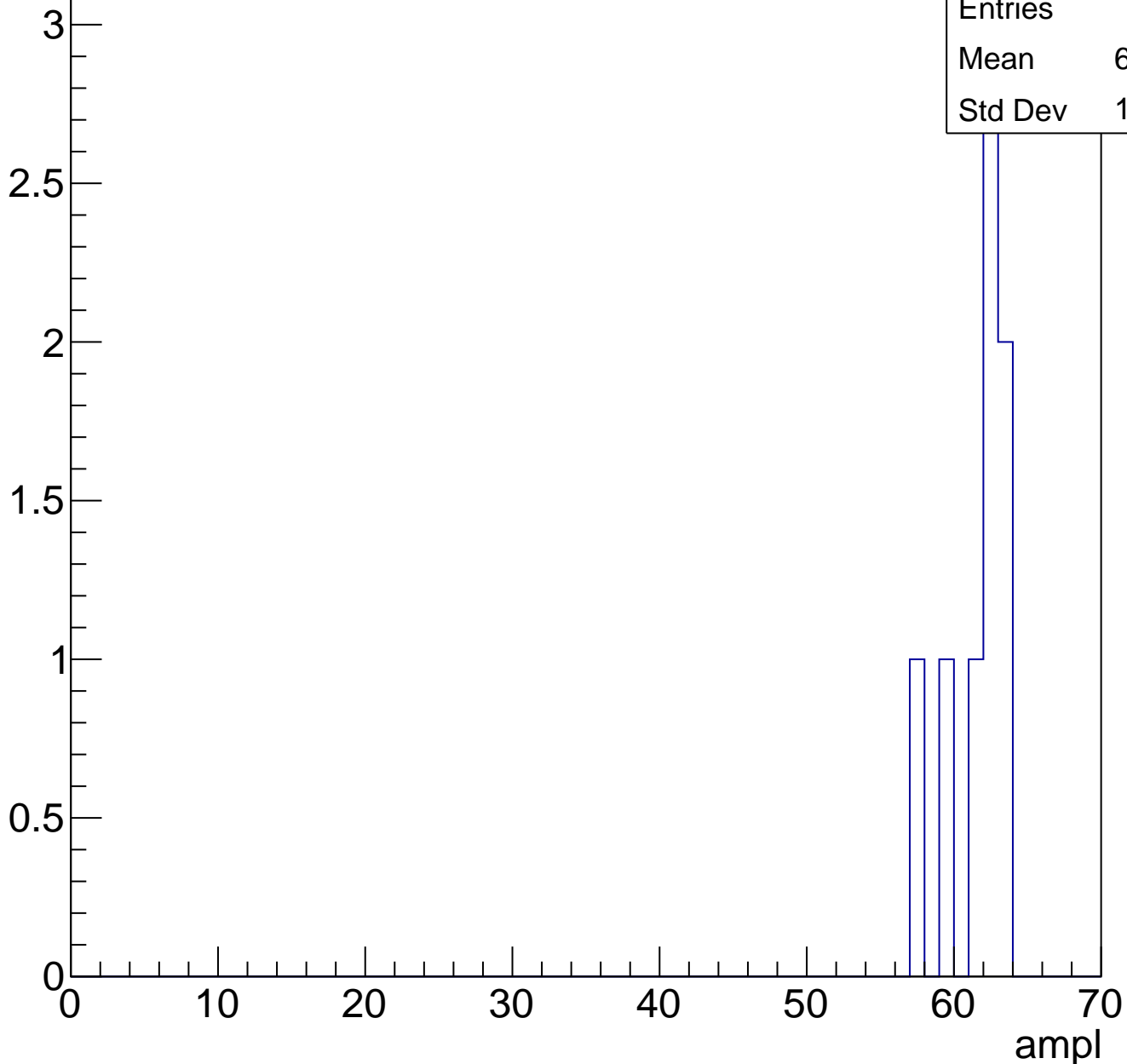
Entries	45
Mean	58.29
Std Dev	9.13



B1L102S, U11-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	8
Mean	61.12
Std Dev	1.965

B1L102S, U11-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch112, adc0

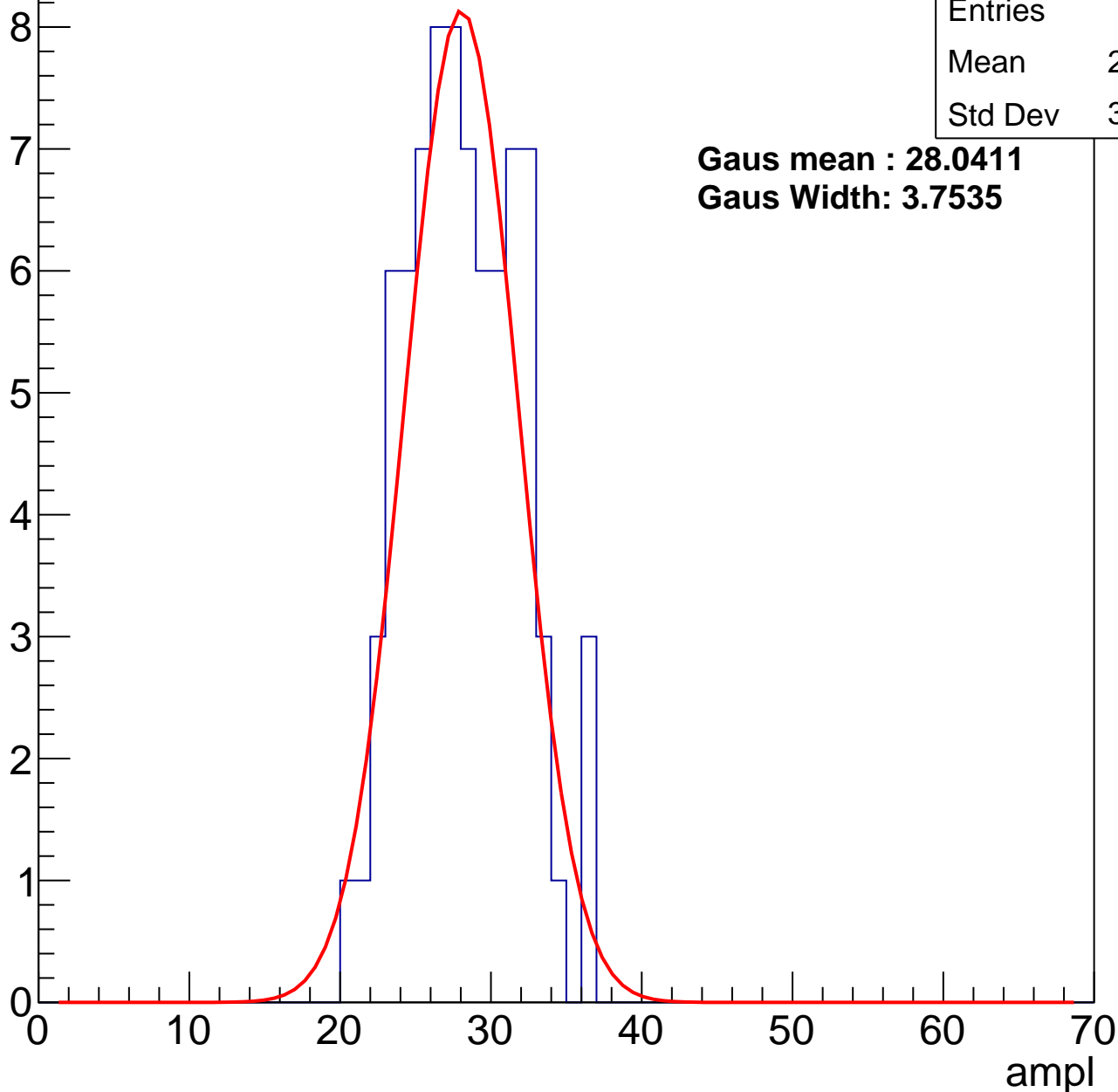
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.75
Std Dev	3.662

Gaus mean : 28.0411

Gaus Width: 3.7535



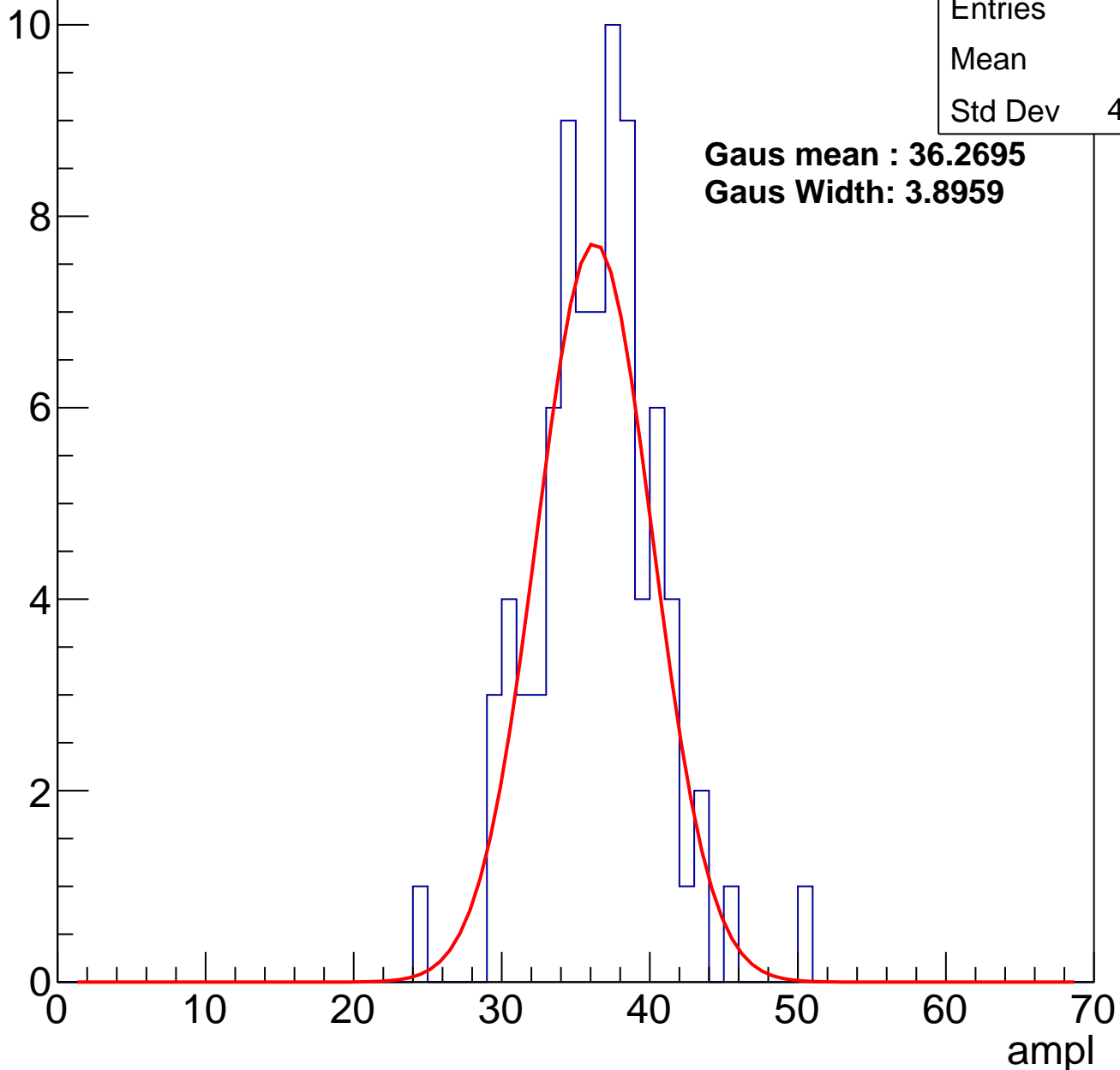
B1L102S, U11-ch112, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	36
Std Dev	4.073

Gaus mean : 36.2695
Gaus Width: 3.8959

Entry



B1L102S, U11-ch112, adc2

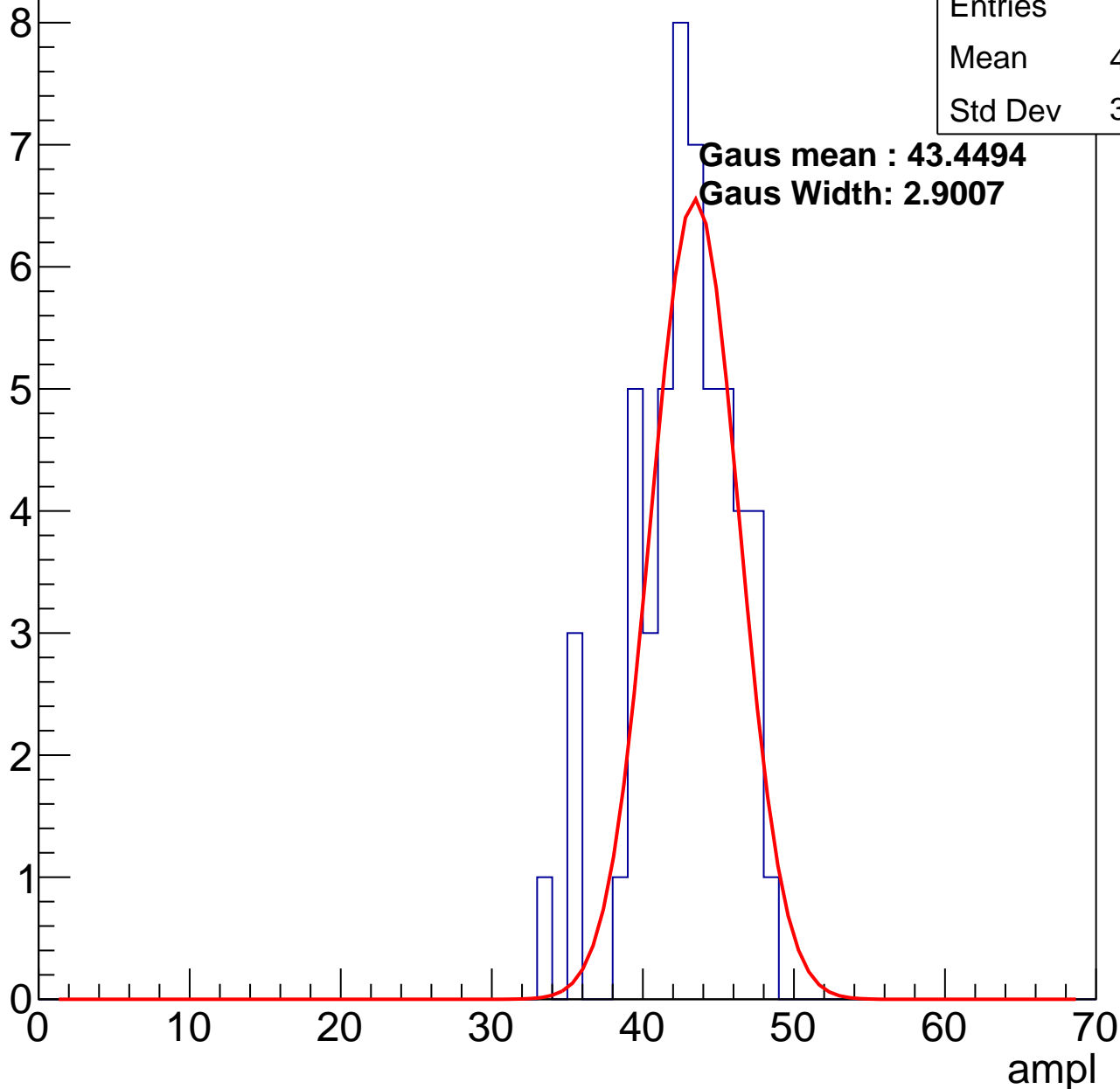
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.27
Std Dev	3.323

Gaus mean : 43.4494

Gaus Width: 2.9007

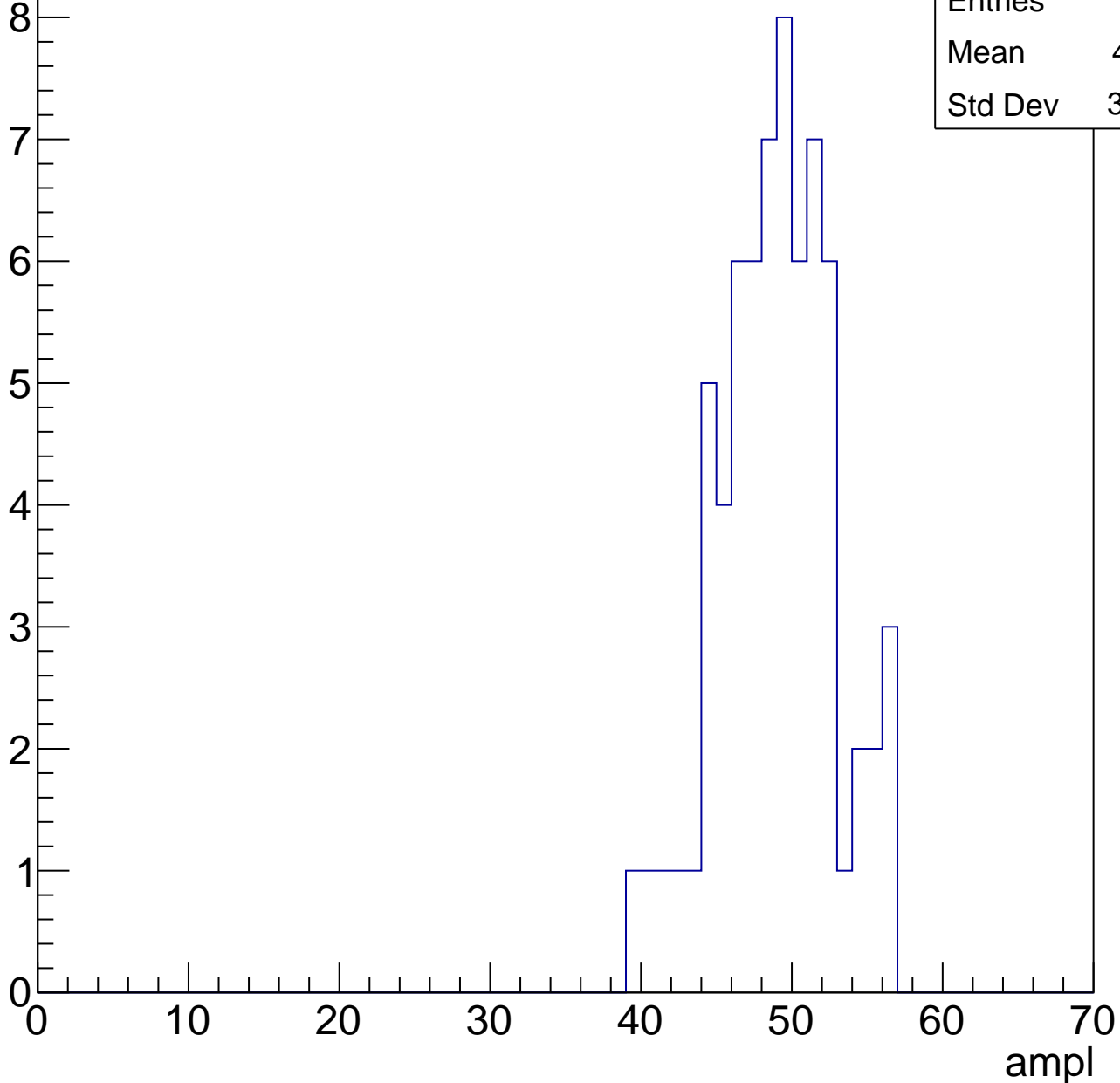


B1L102S, U11-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.51
Std Dev	3.759

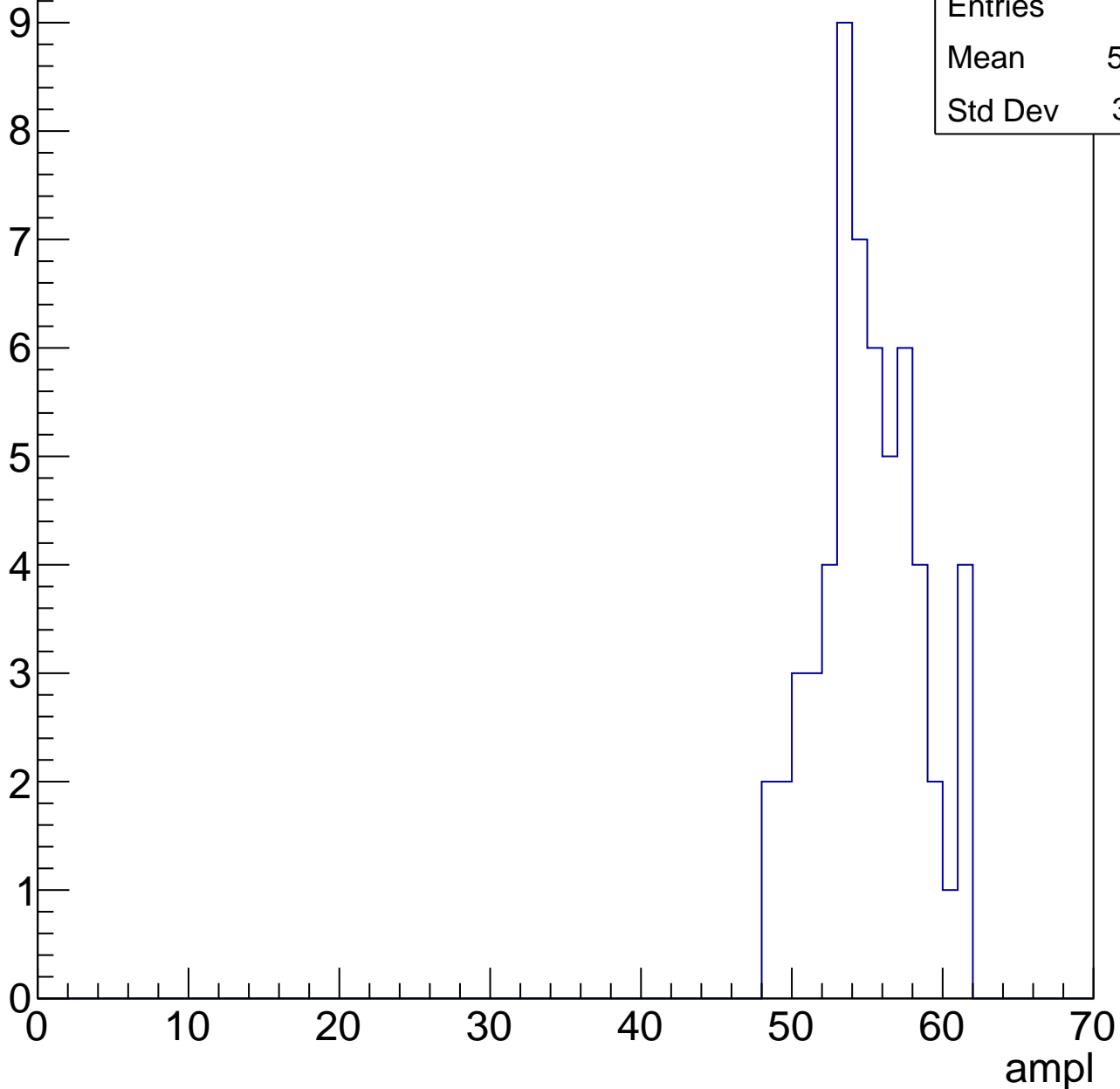


B1L102S, U11-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.59
Std Dev	3.291

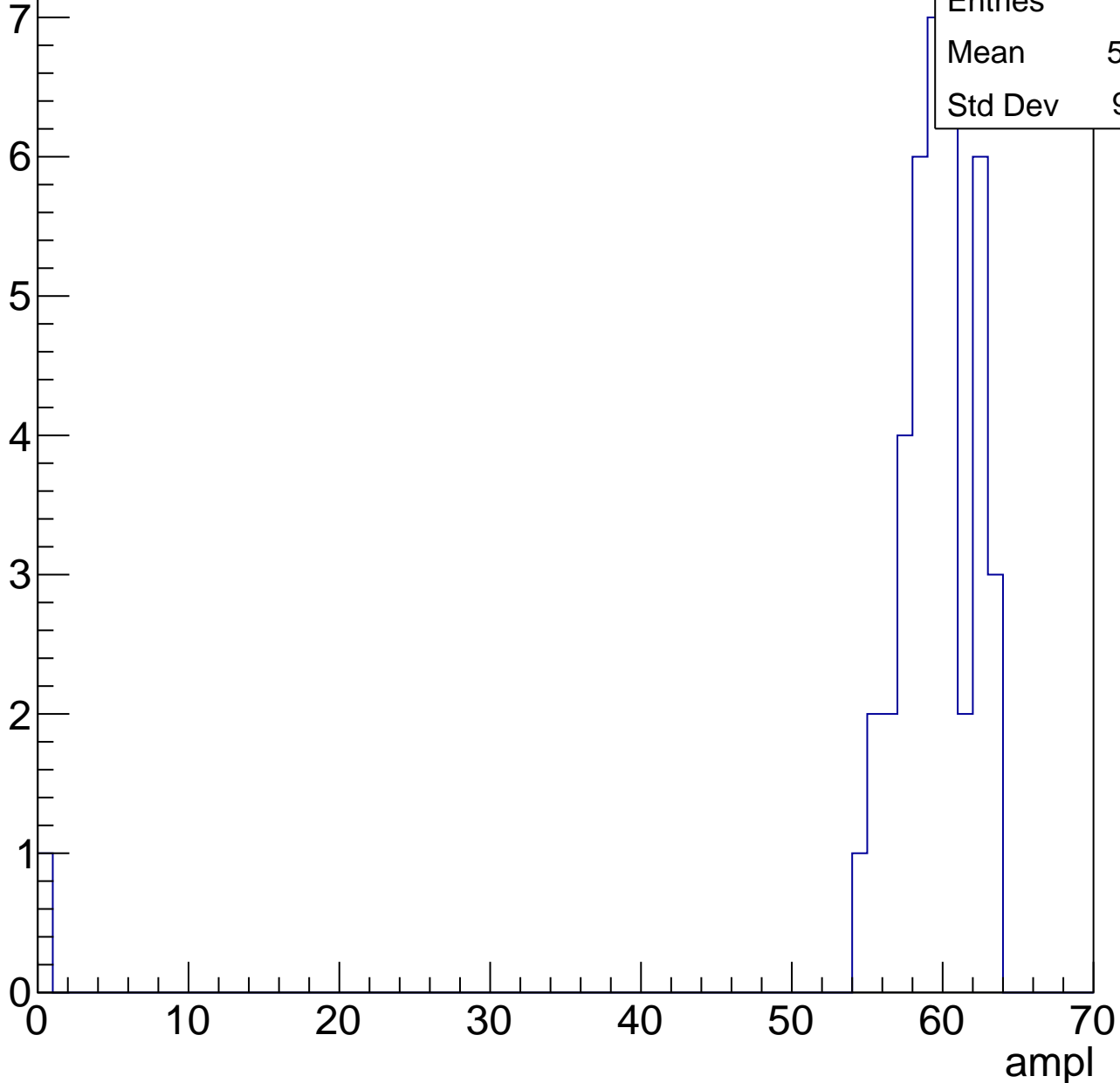


B1L102S, U11-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	57.76
Std Dev	9.411

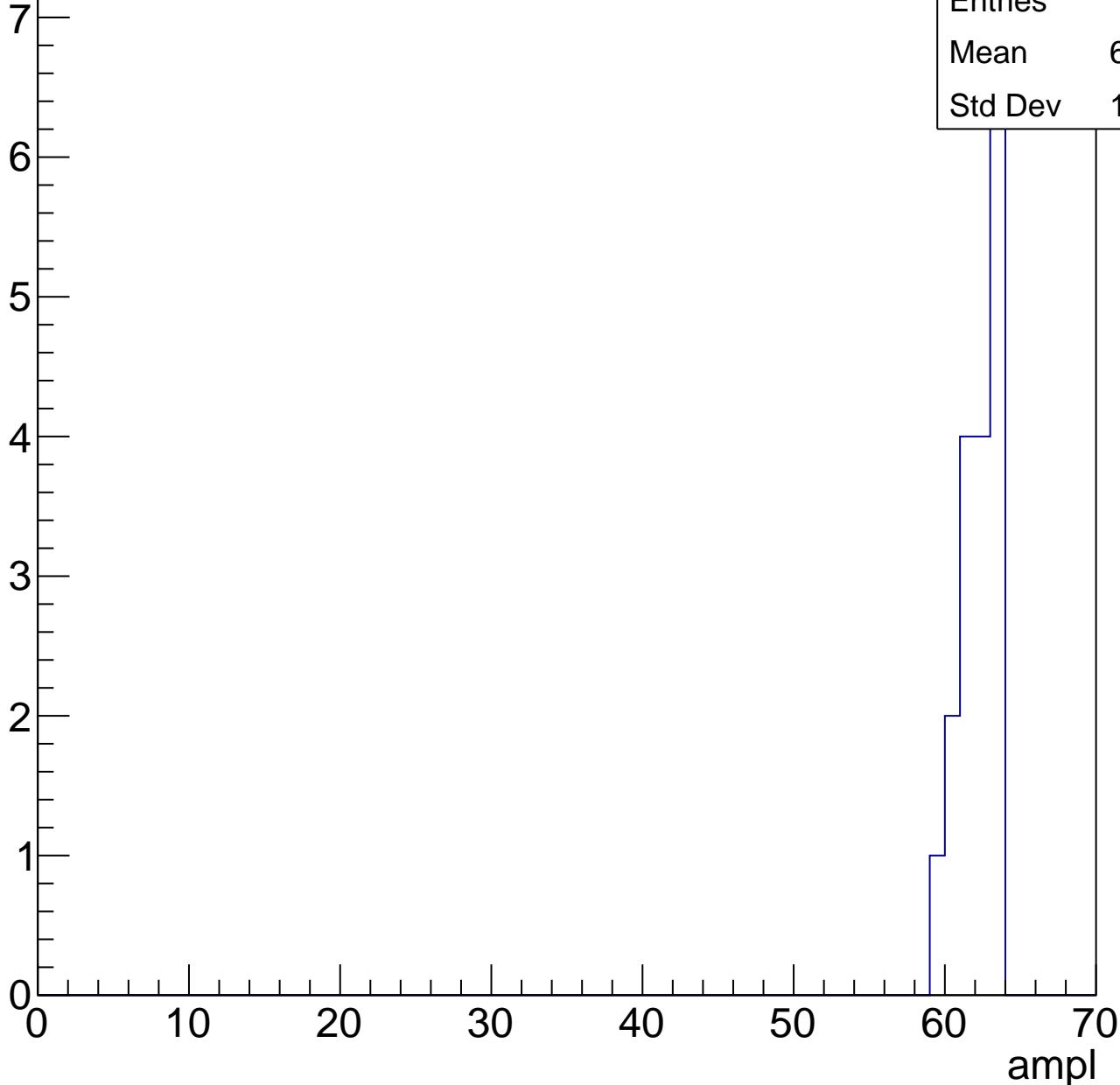


B1L102S, U11-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	61.78
Std Dev	1.227



B1L102S, U11-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch113, adc0

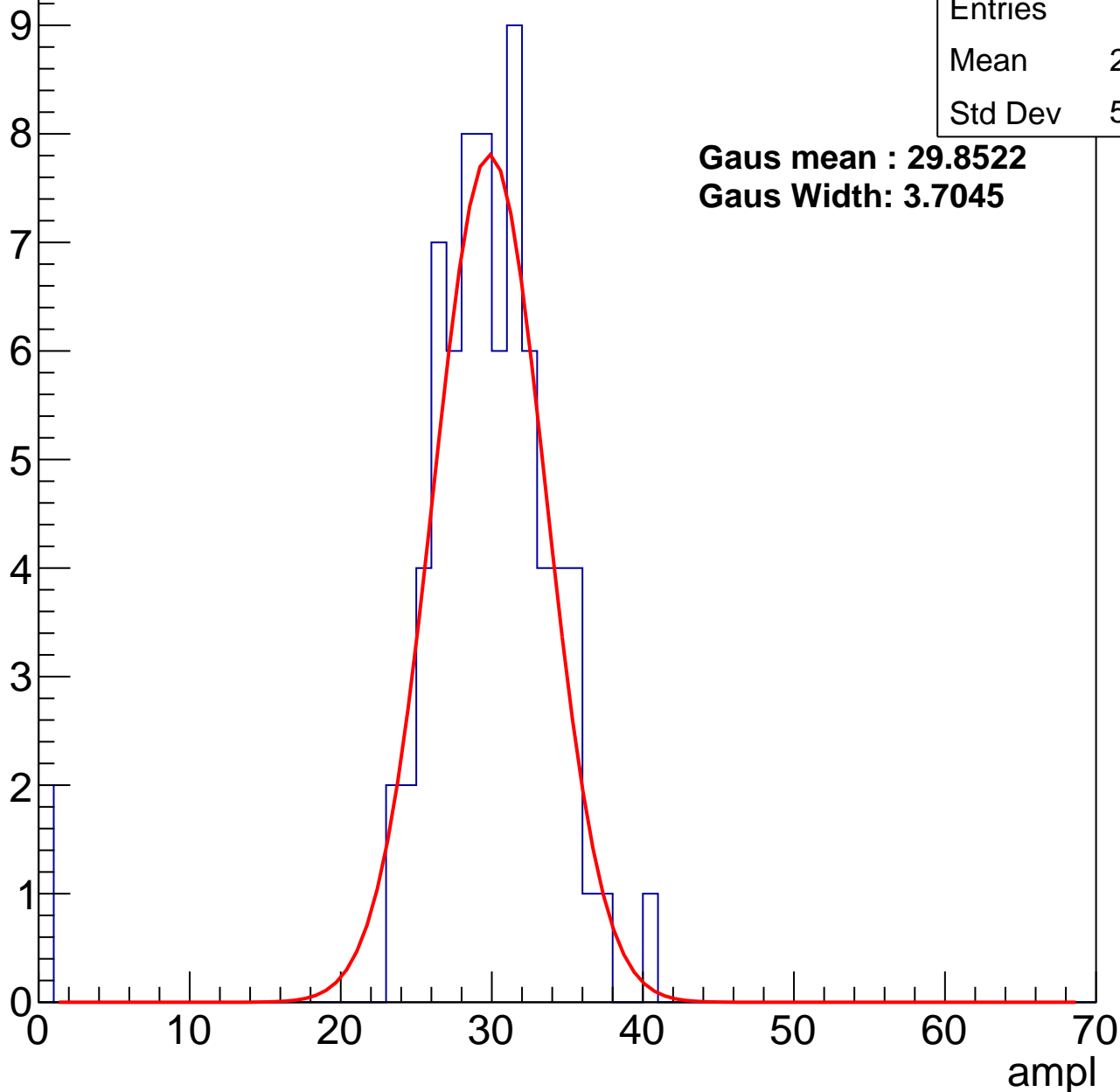
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.88
Std Dev	5.888

Gaus mean : 29.8522

Gaus Width: 3.7045



B1L102S, U11-ch113, adc1

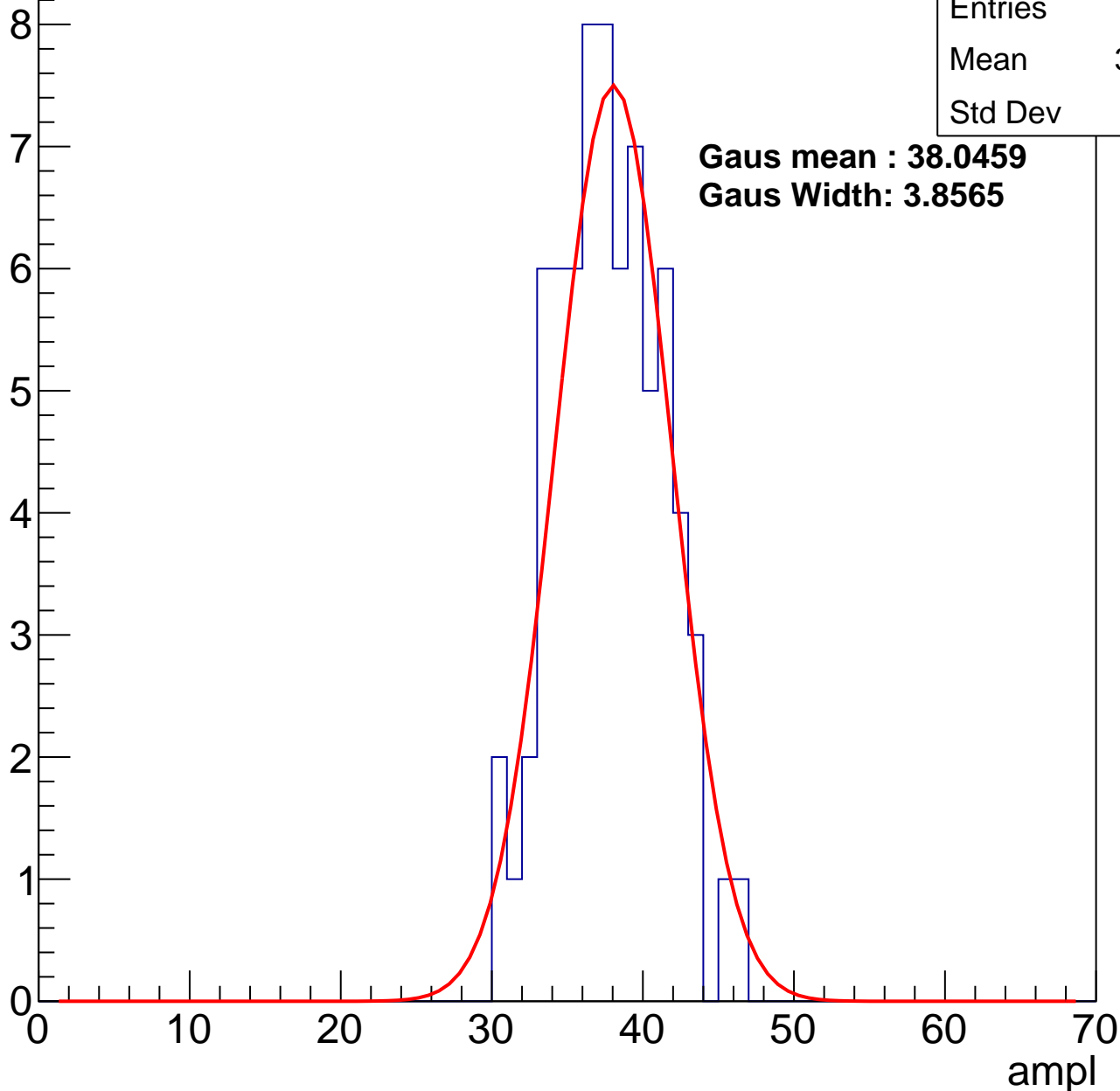
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.31
Std Dev	3.51

Gaus mean : 38.0459

Gaus Width: 3.8565



B1L102S, U11-ch113, adc2

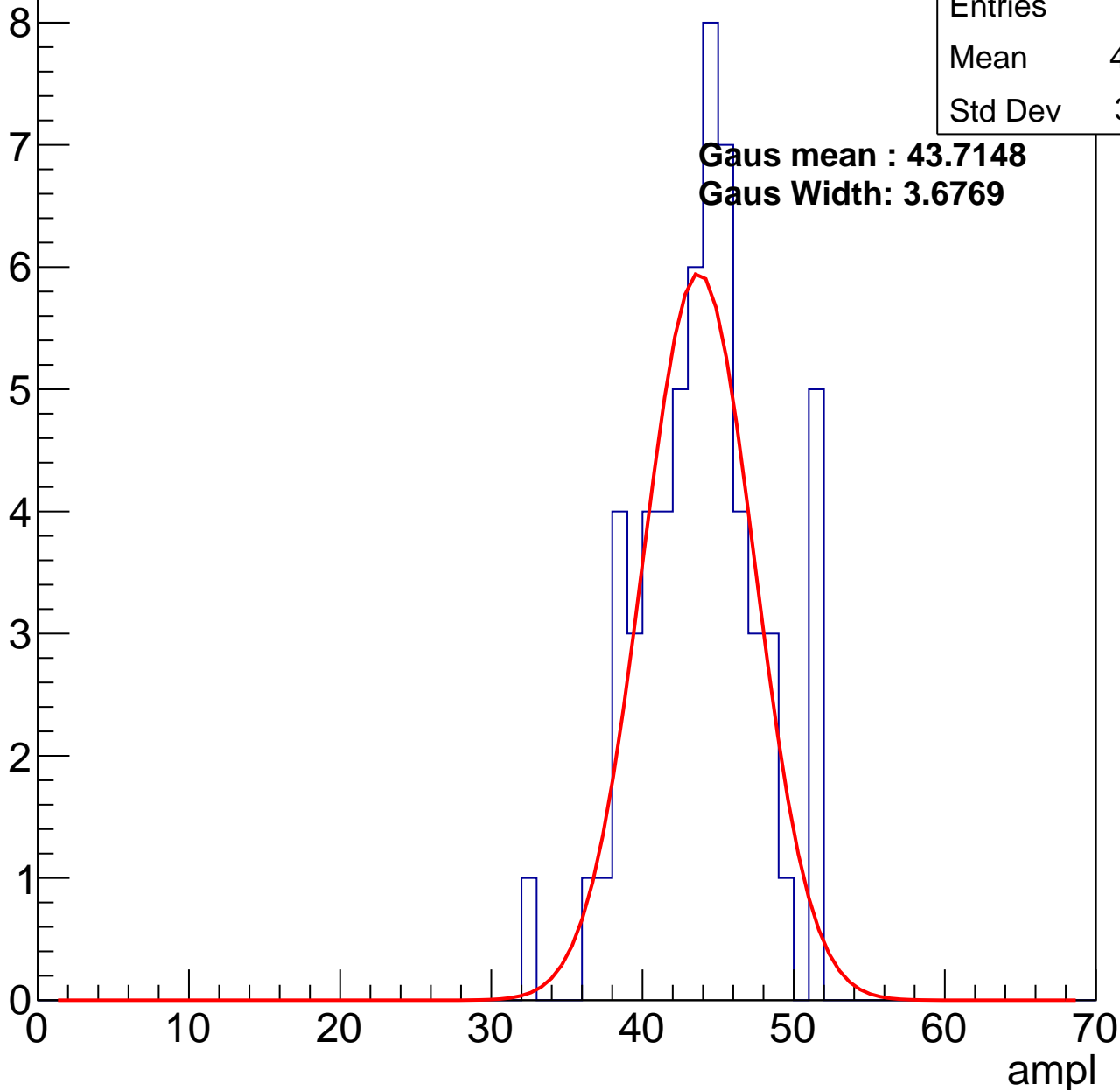
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.43
Std Dev	3.981

Gaus mean : 43.7148

Gaus Width: 3.6769

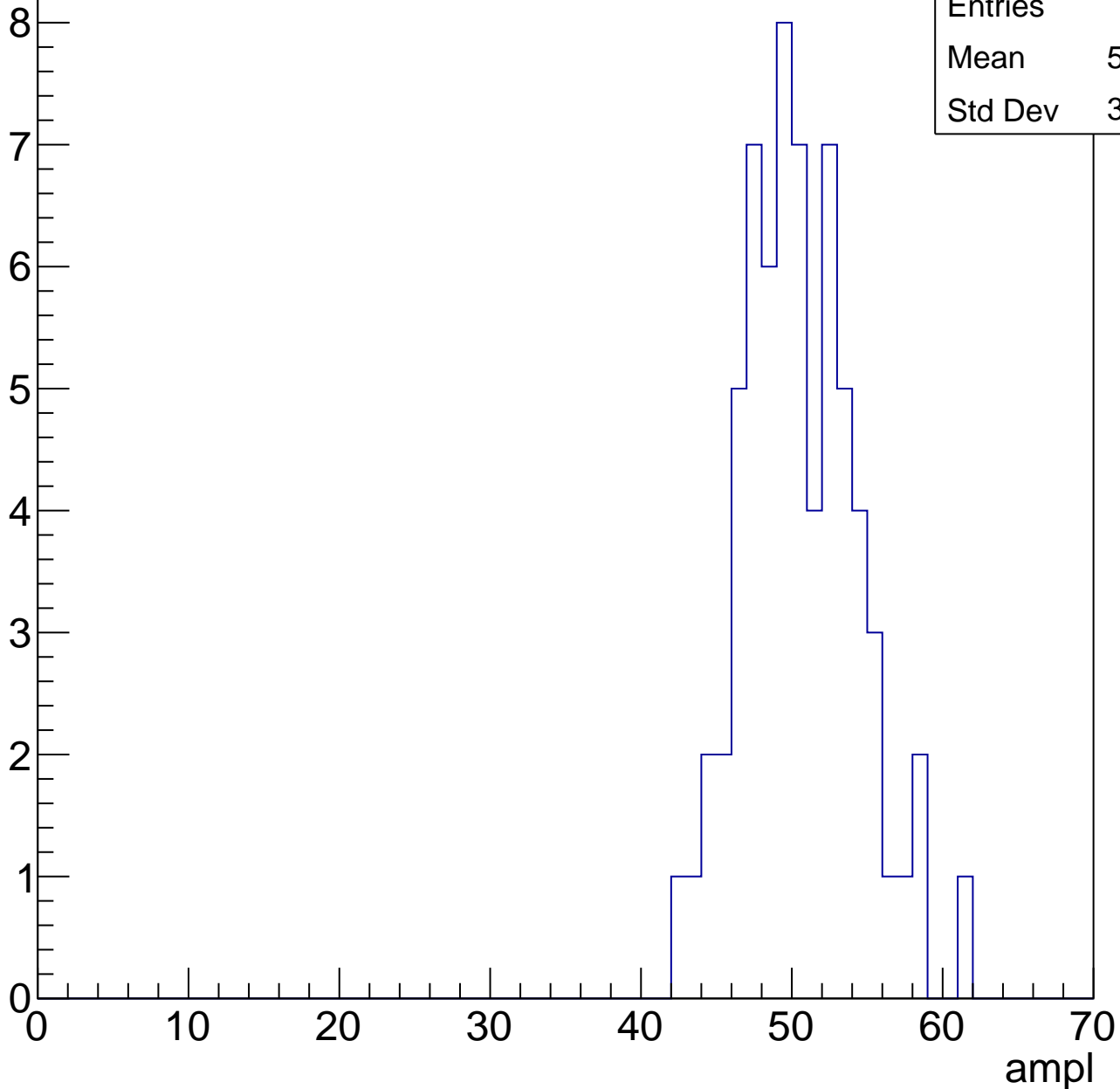


B1L102S, U11-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.09
Std Dev	3.784

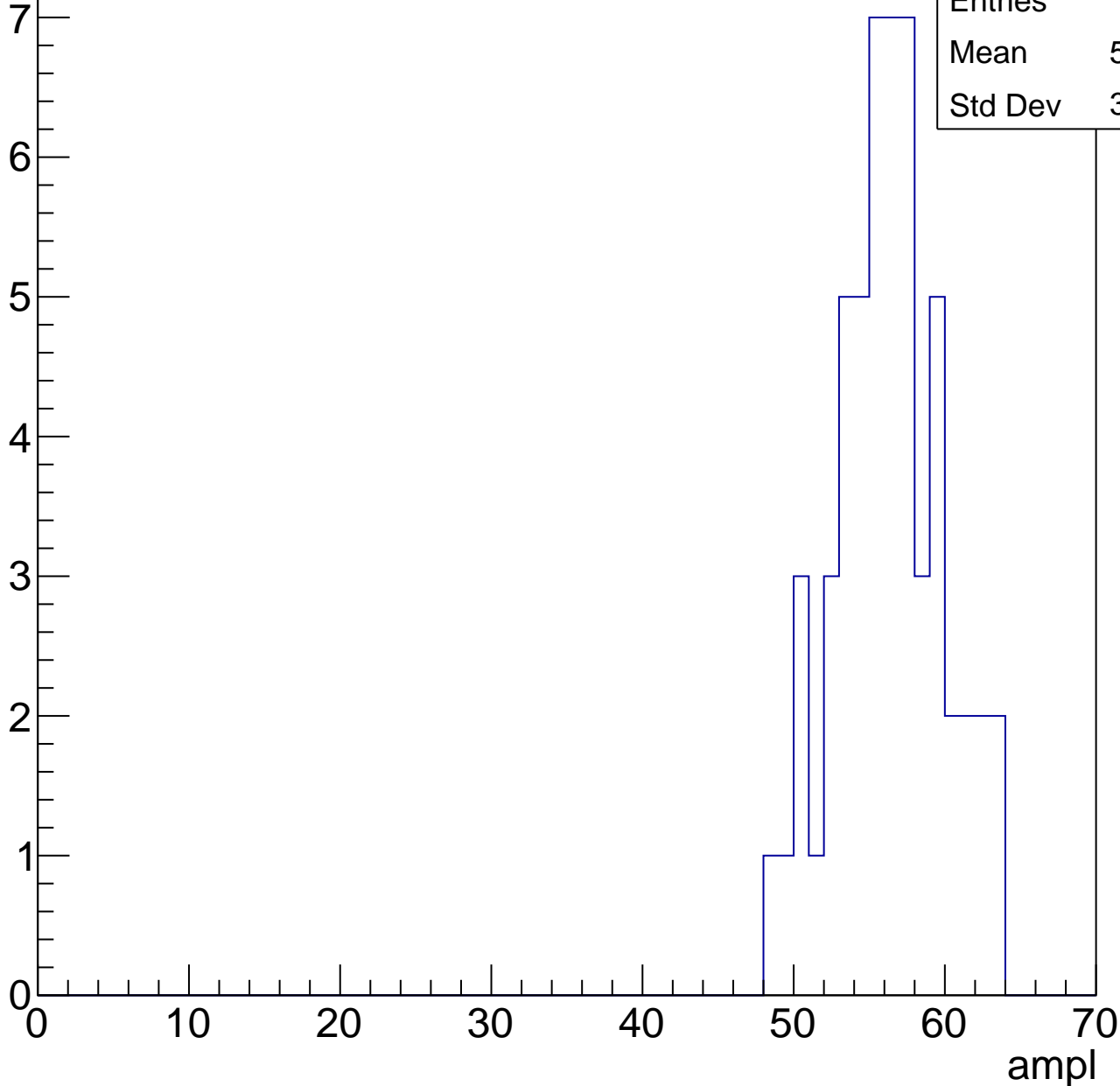


B1L102S, U11-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.82
Std Dev	3.485

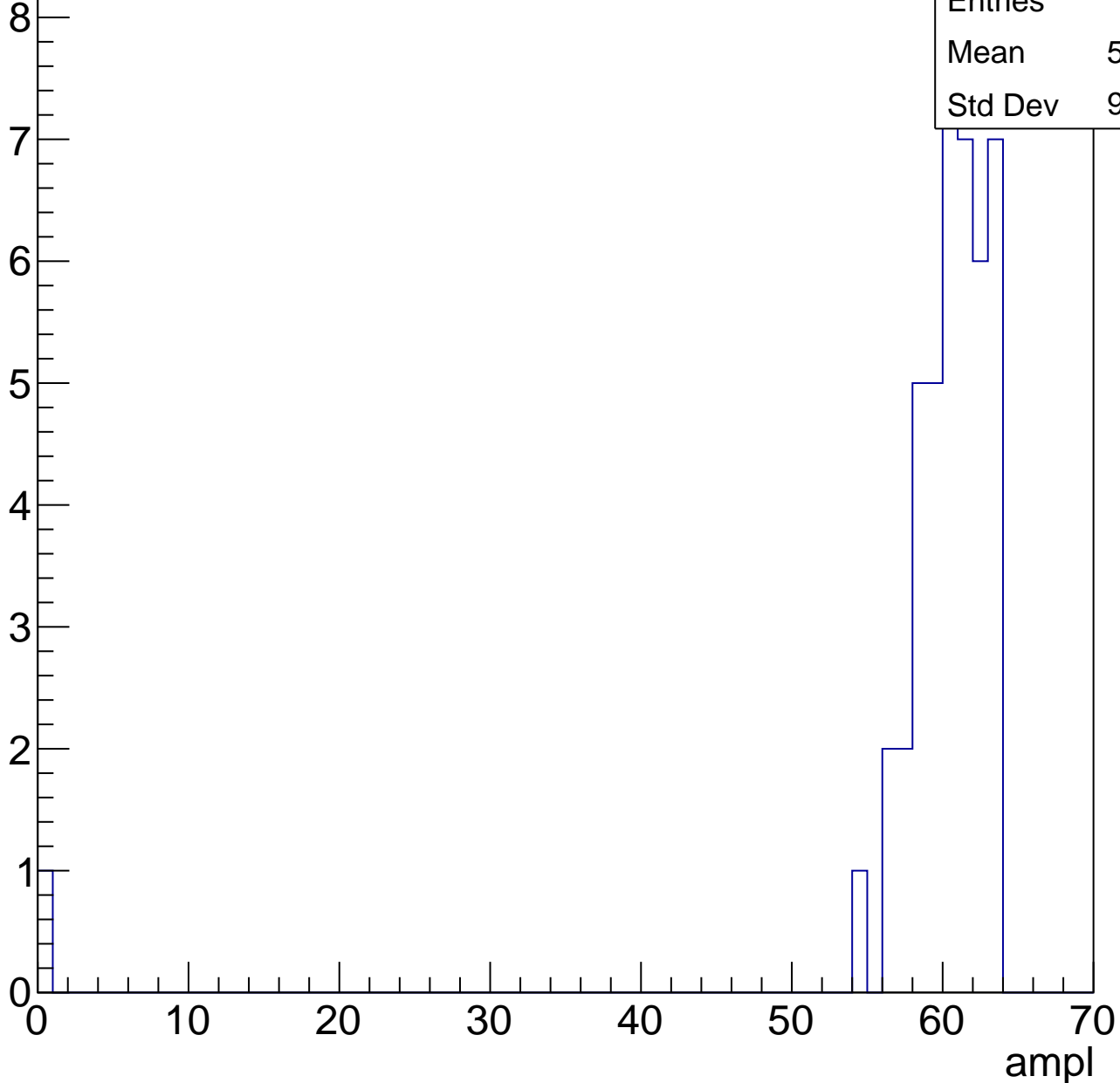


B1L102S, U11-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.75
Std Dev	9.215



B1L102S, U11-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch114, adc0

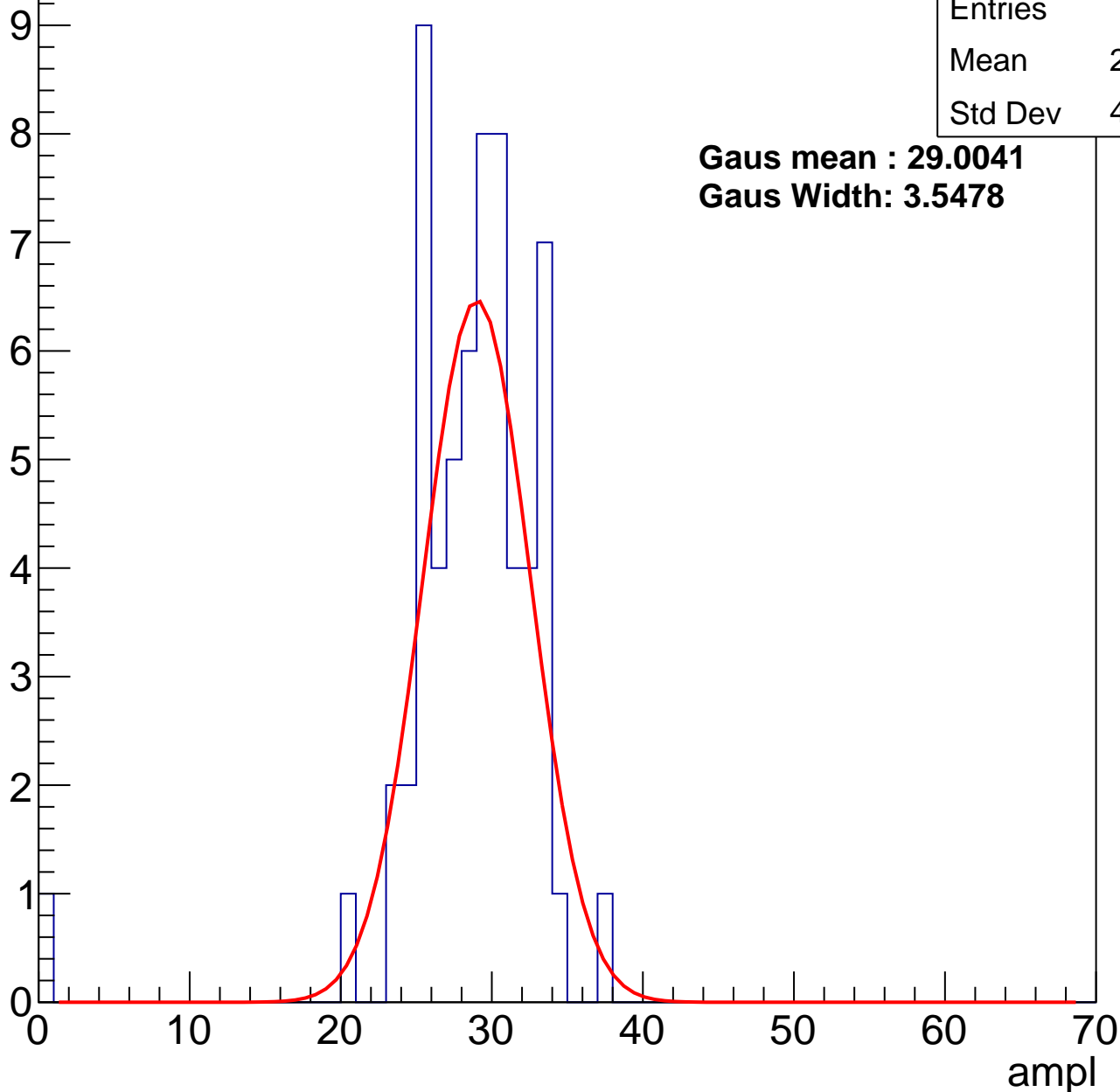
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.13
Std Dev	4.819

Gaus mean : 29.0041

Gaus Width: 3.5478



B1L102S, U11-ch114, adc1

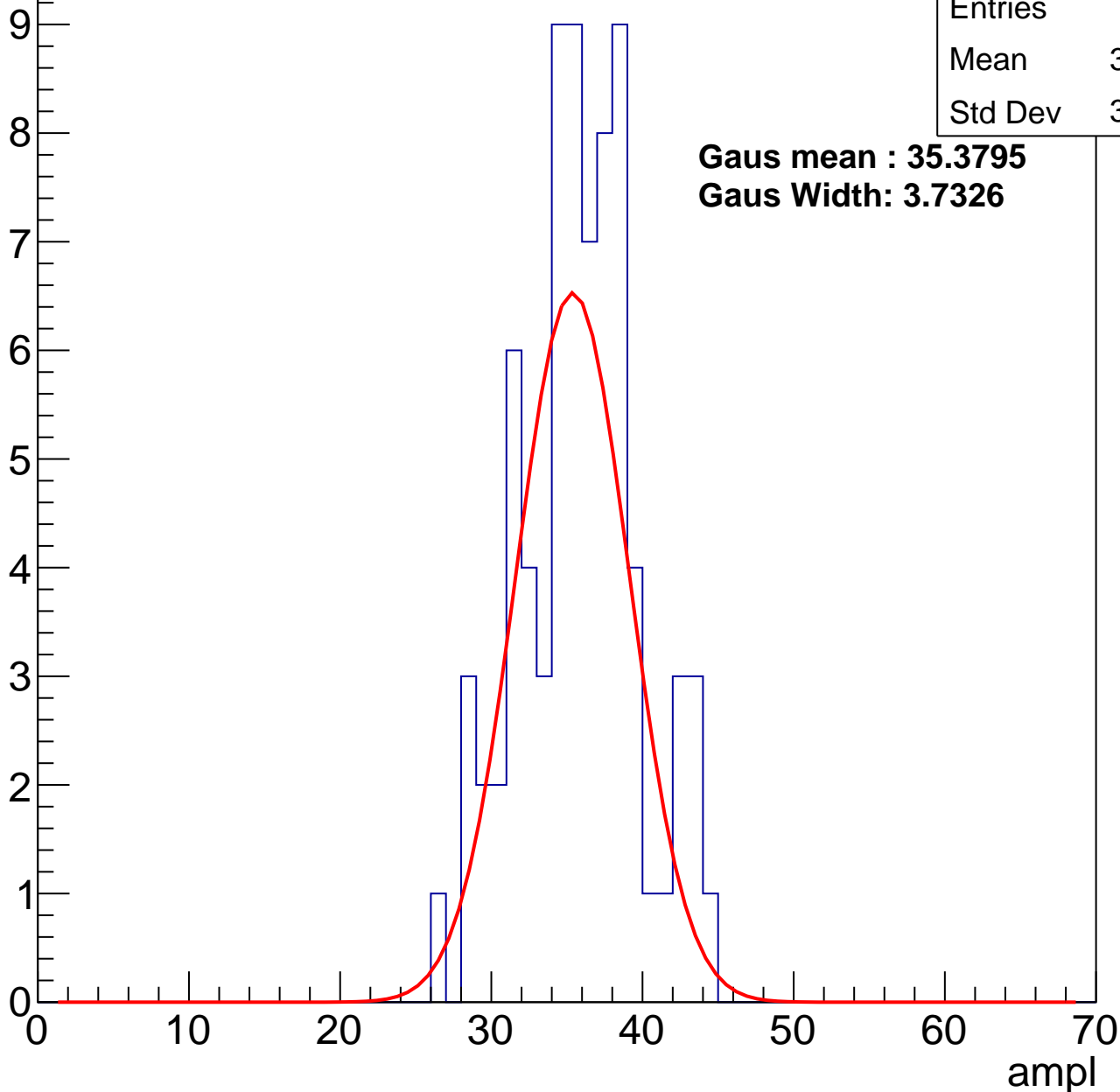
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.37
Std Dev	3.903

Gaus mean : 35.3795

Gaus Width: 3.7326



B1L102S, U11-ch114, adc2

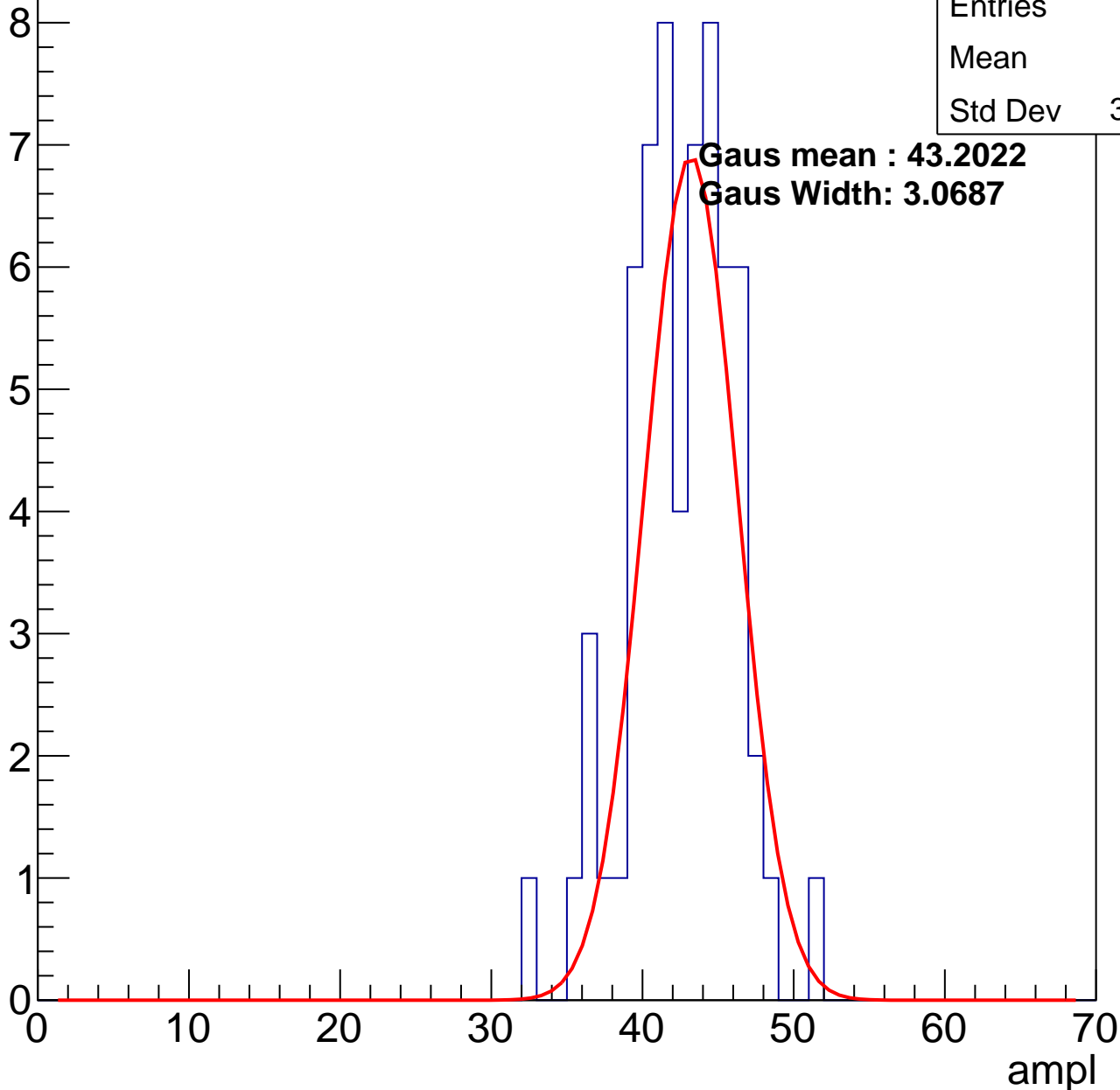
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.1
Std Dev	3.435

Gaus mean : 43.2022

Gaus Width: 3.0687

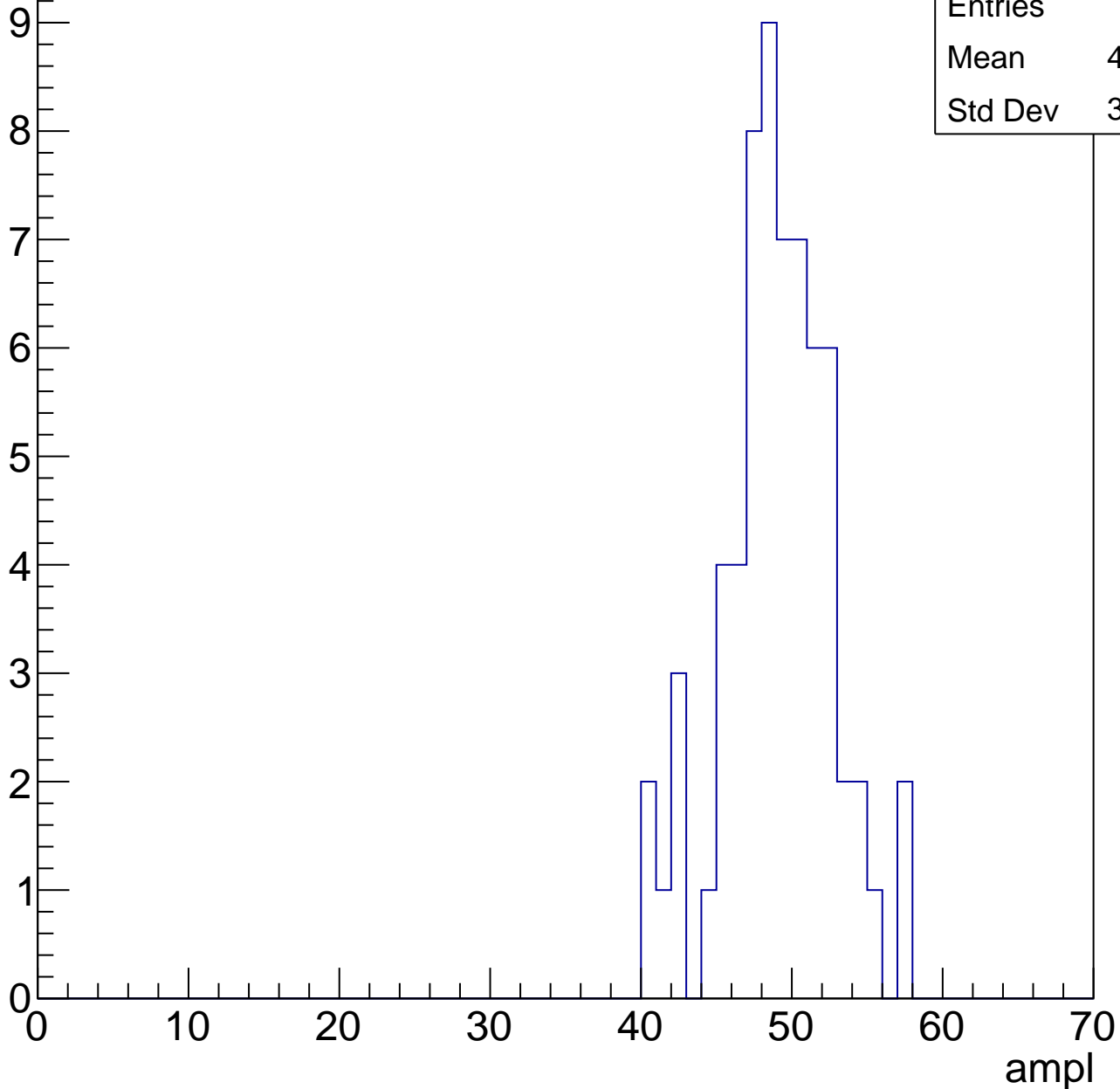


B1L102S, U11-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.57
Std Dev	3.633

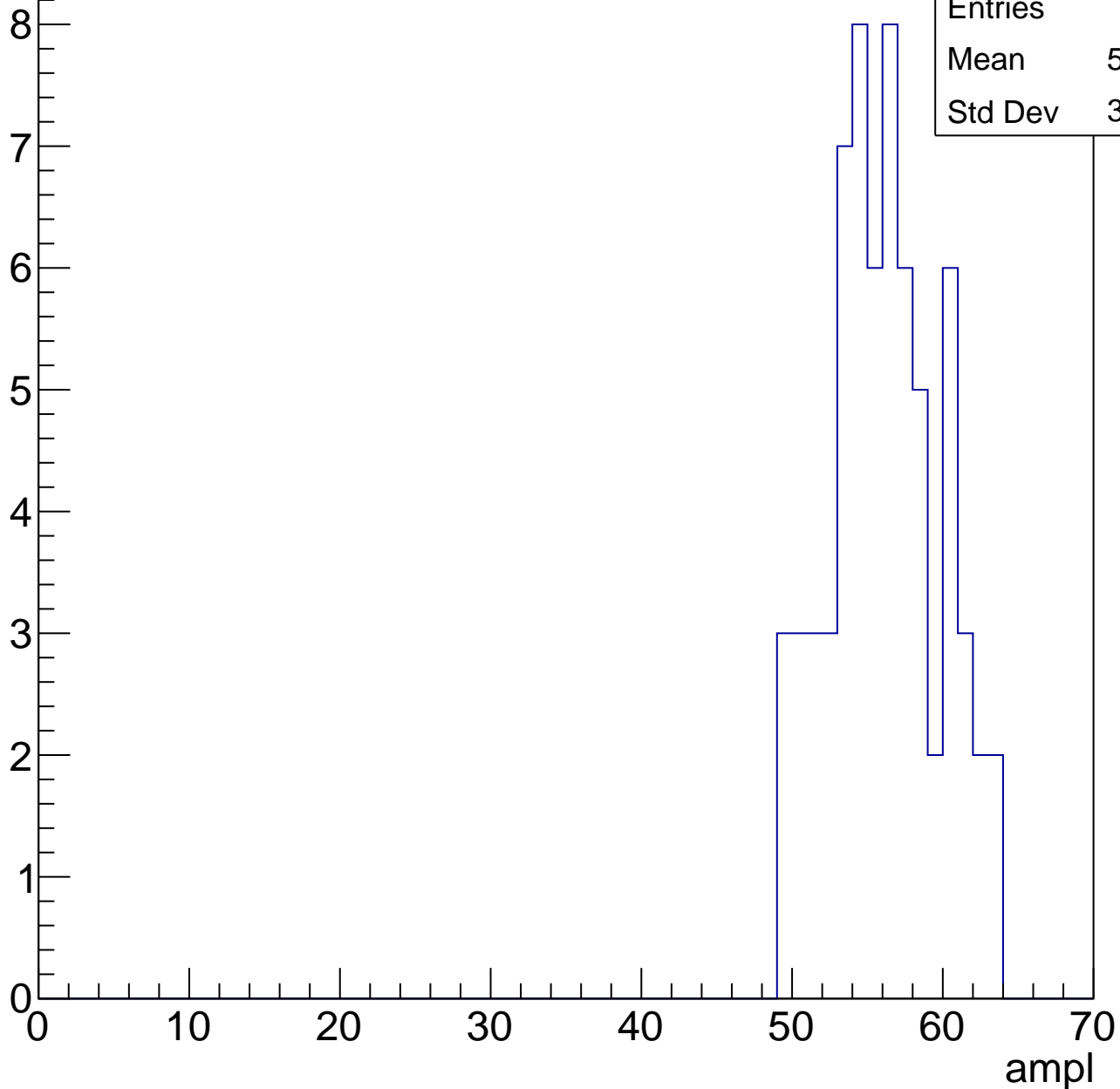


B1L102S, U11-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55.67
Std Dev	3.576

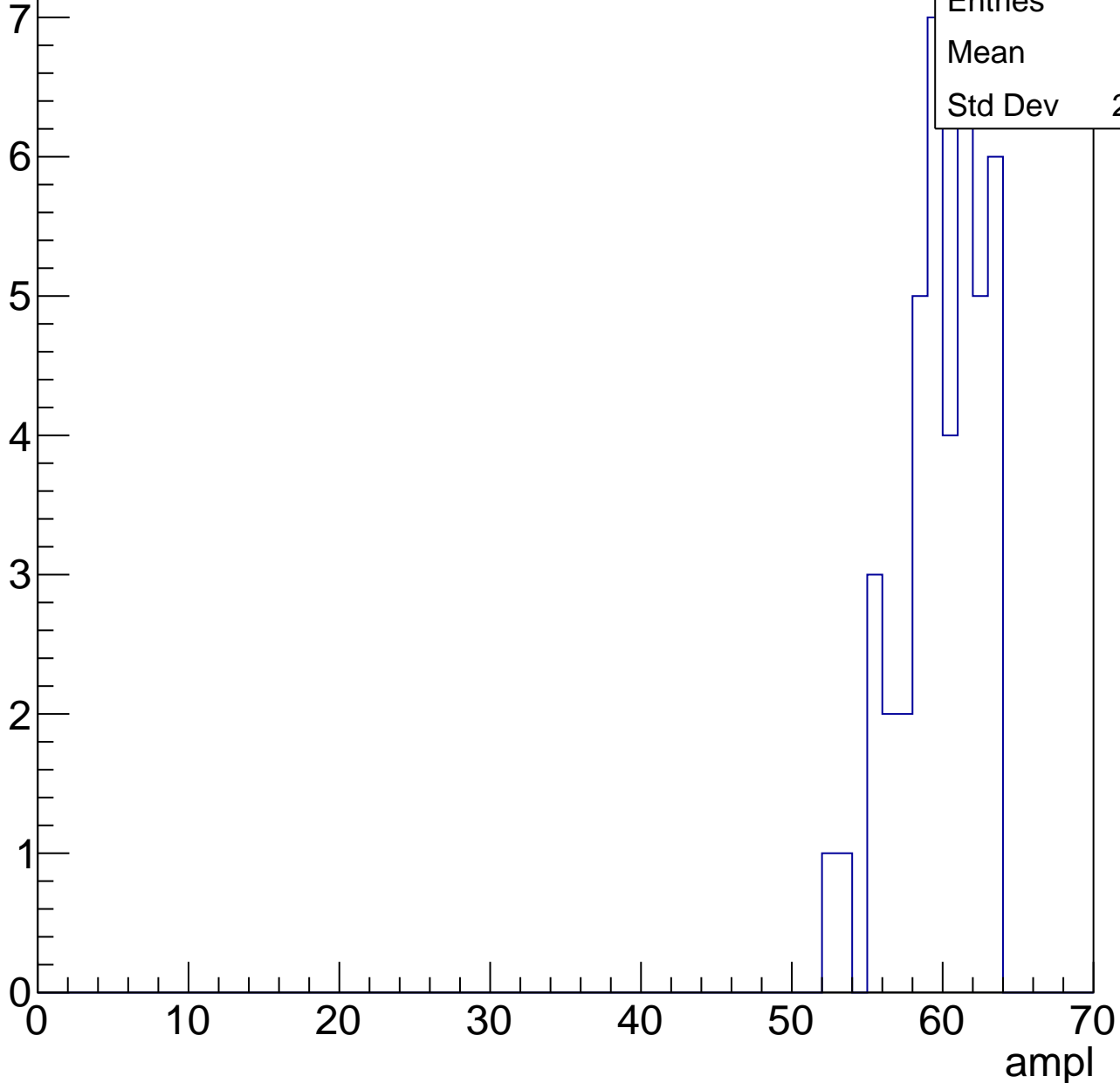


B1L102S, U11-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

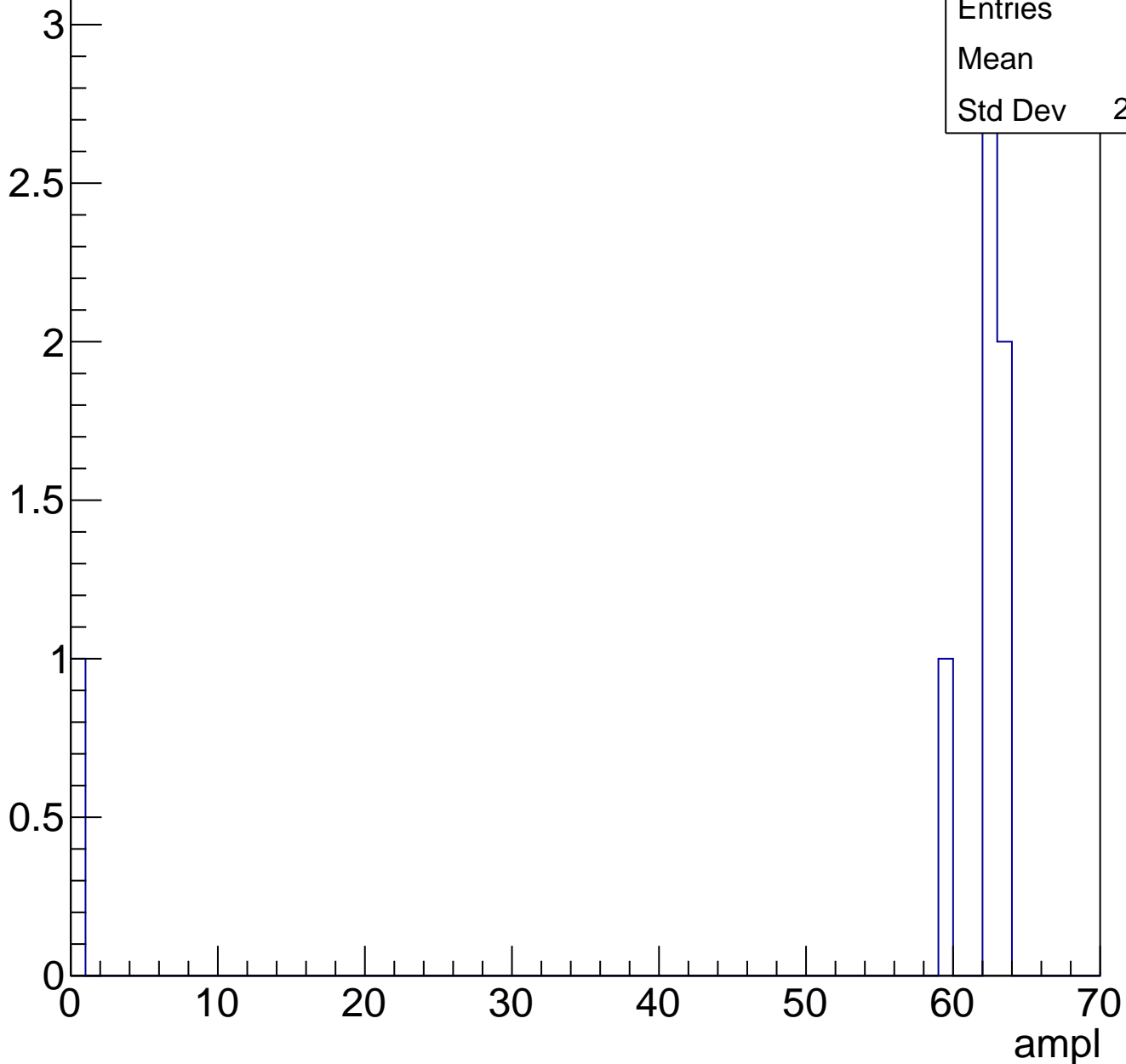
Entries	43
Mean	59.4
Std Dev	2.771



B1L102S, U11-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch115, adc0

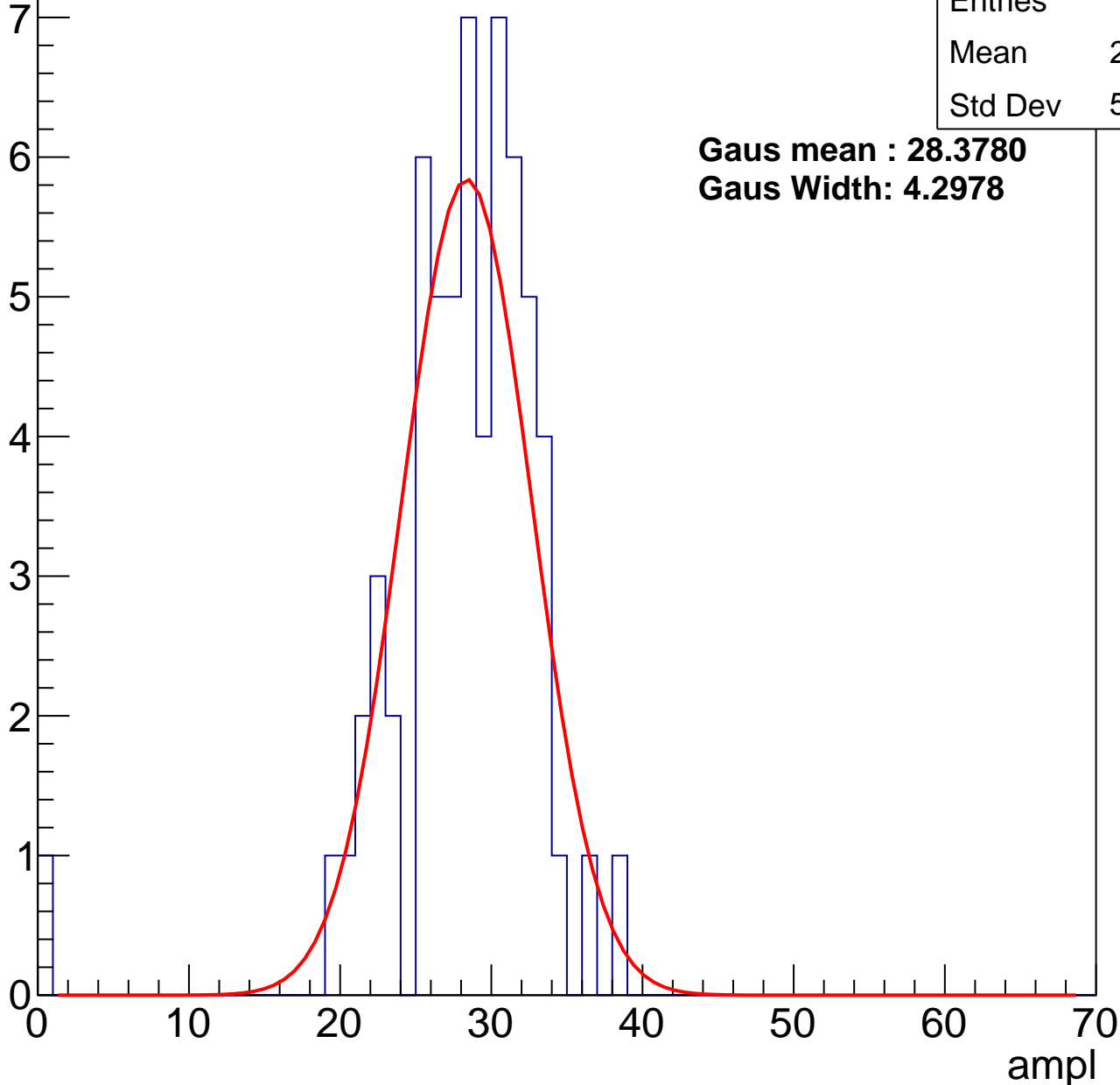
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.68
Std Dev	5.272

Gaus mean : 28.3780

Gaus Width: 4.2978



B1L102S, U11-ch115, adc1

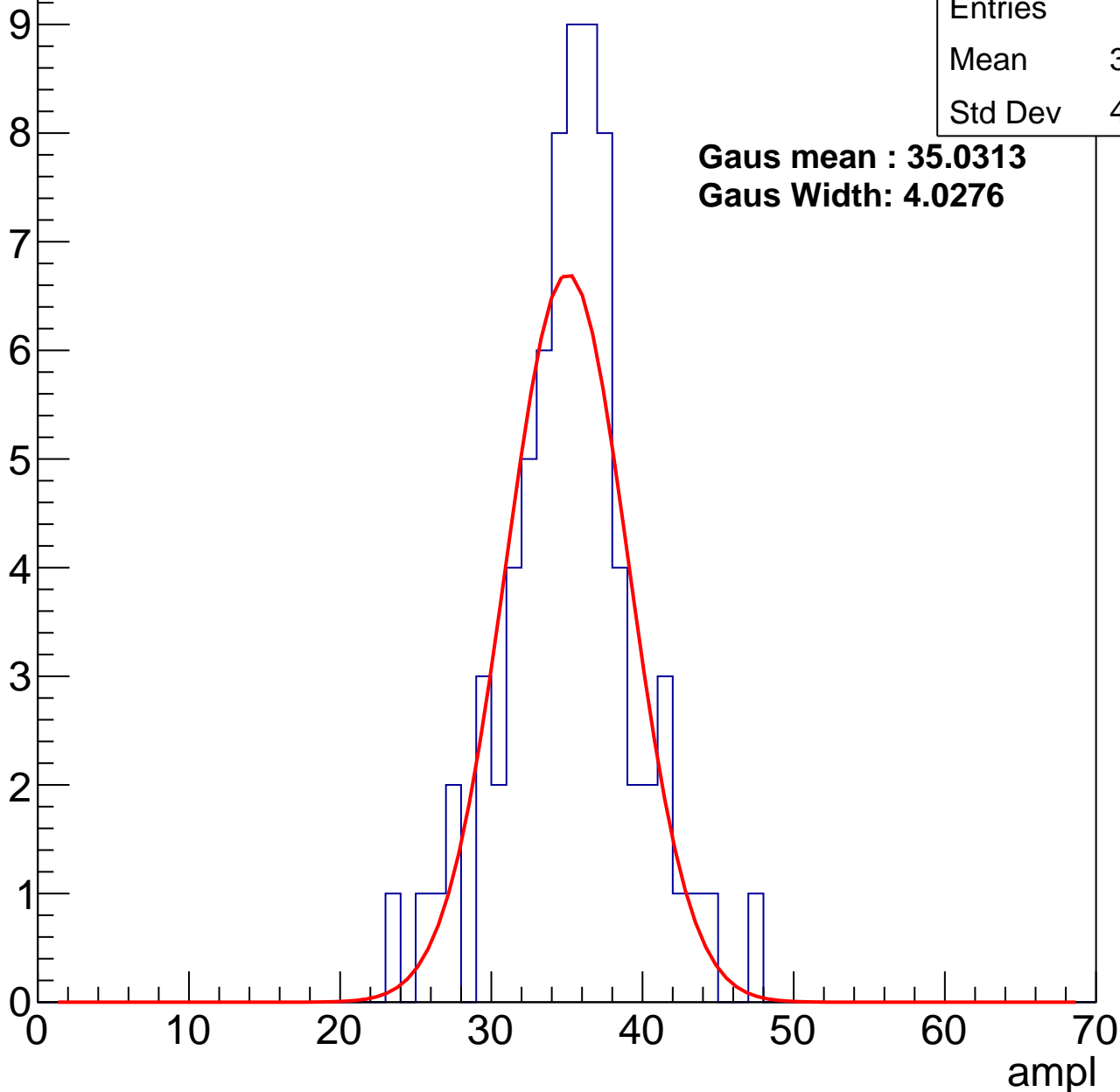
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.77
Std Dev	4.276

Gaus mean : 35.0313

Gaus Width: 4.0276



B1L102S, U11-ch115, adc2

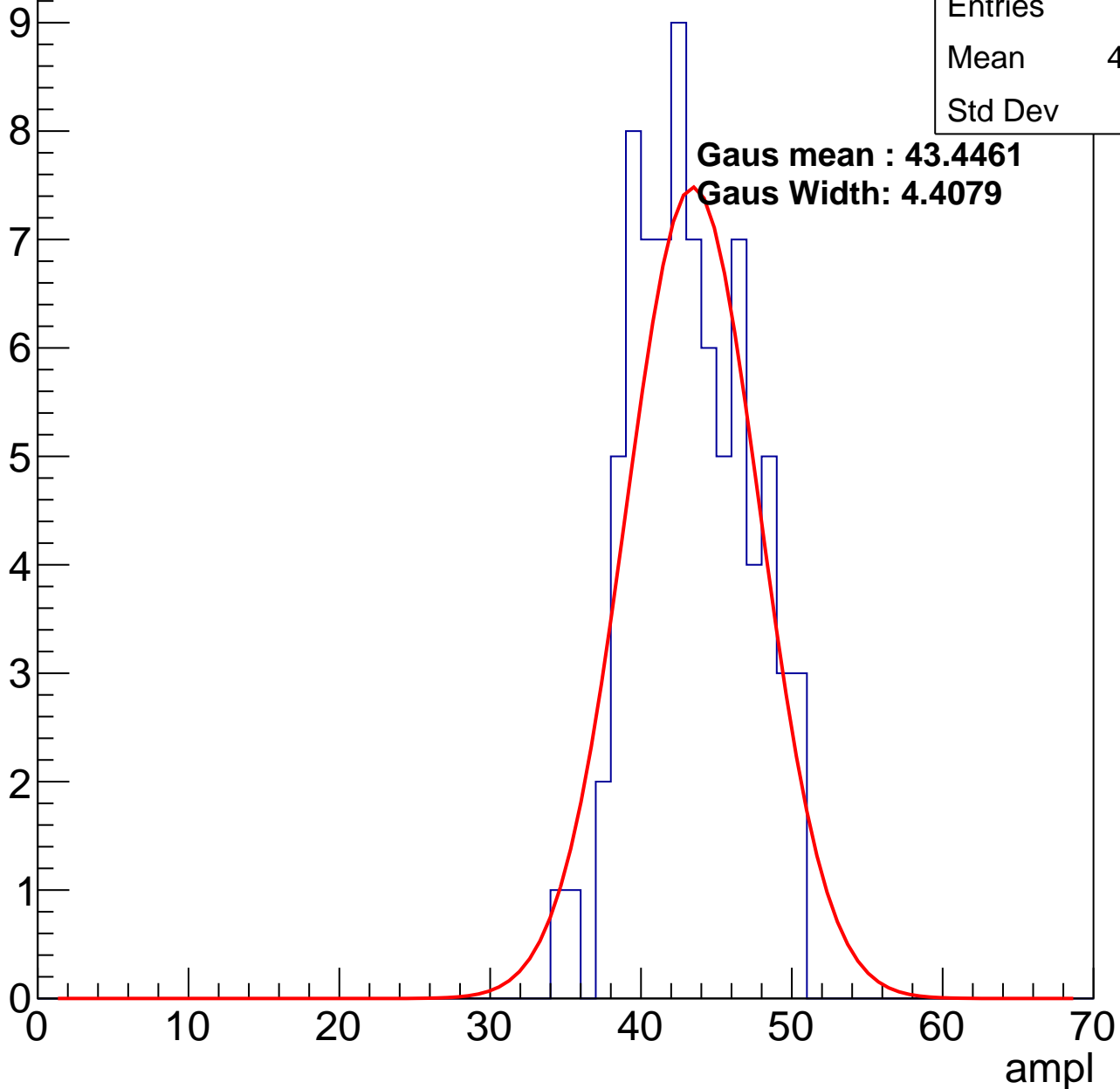
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	42.84
Std Dev	3.71

Gaus mean : 43.4461

Gaus Width: 4.4079

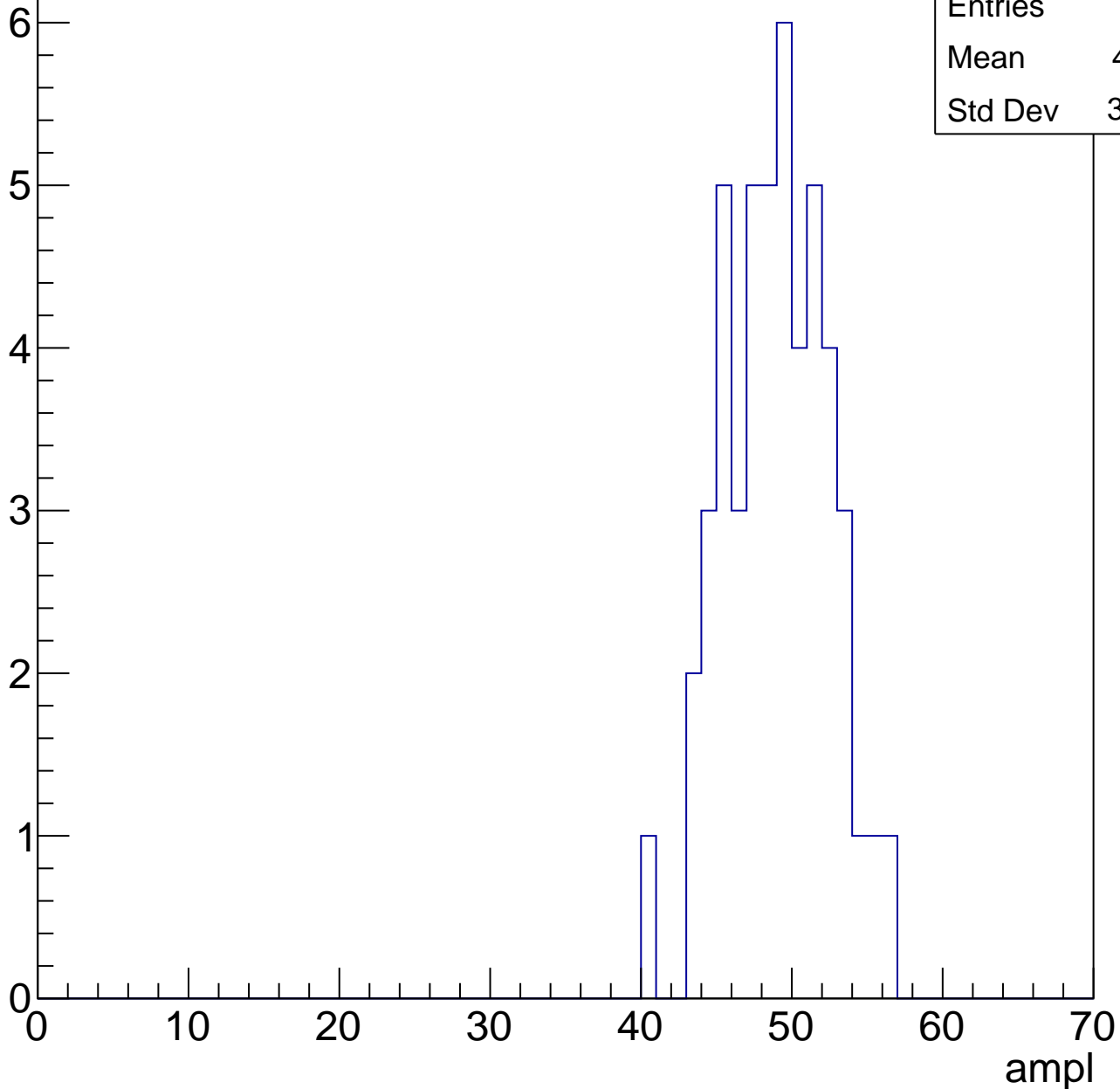


B1L102S, U11-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	48.51
Std Dev	3.399



B1L102S, U11-ch115, adc4

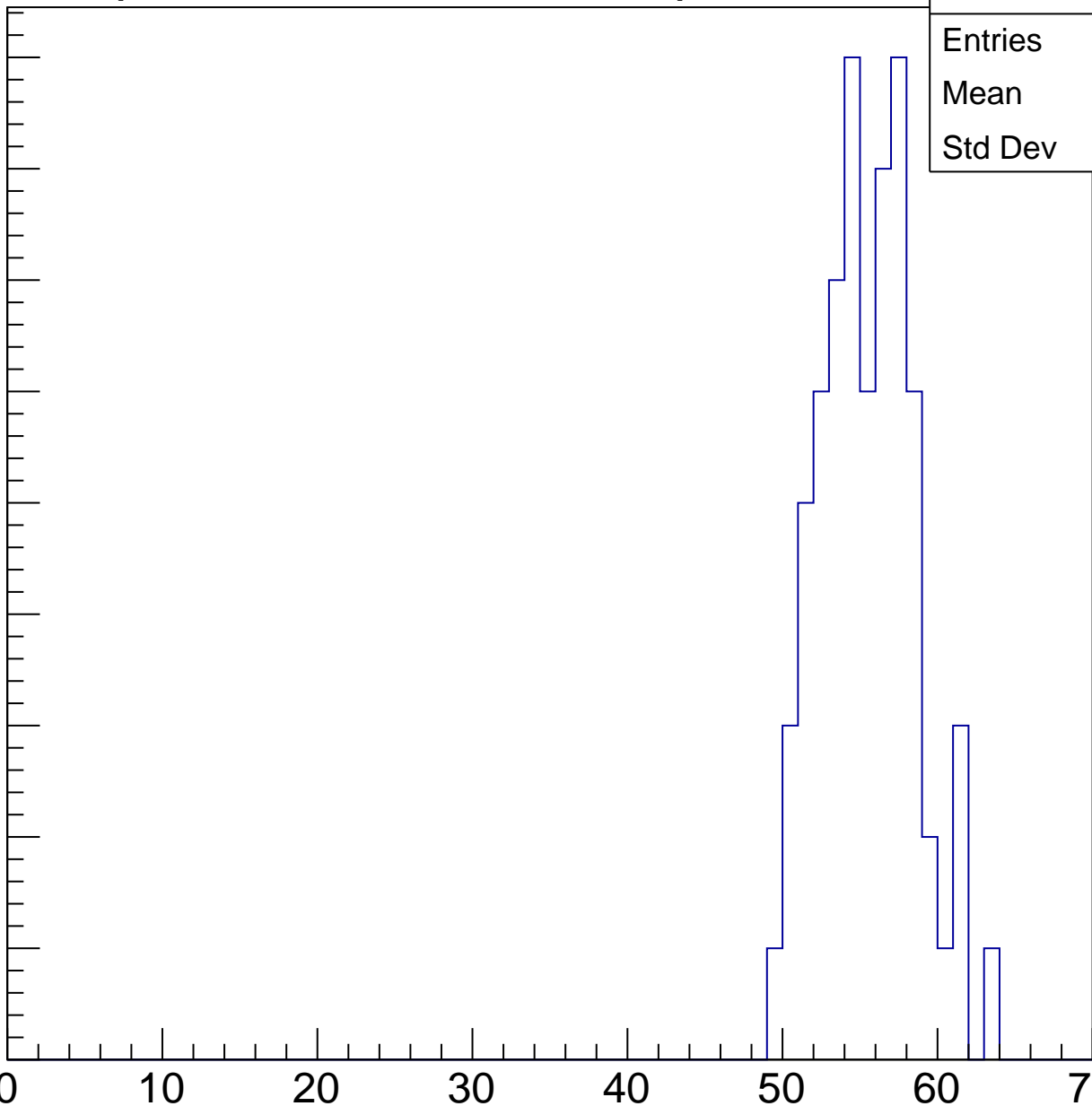
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	55.01
Std Dev	3.015

ampl

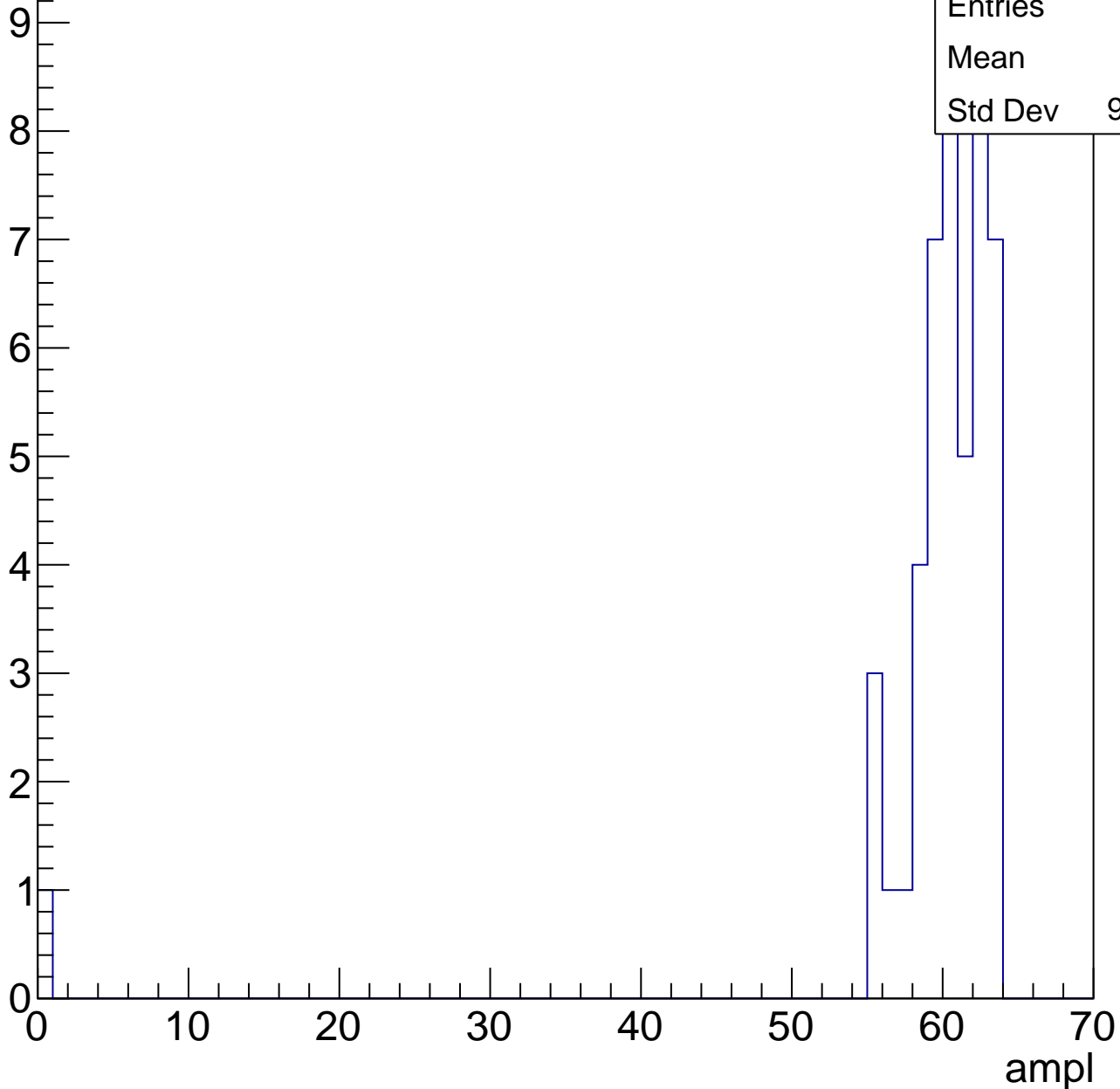


B1L102S, U11-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

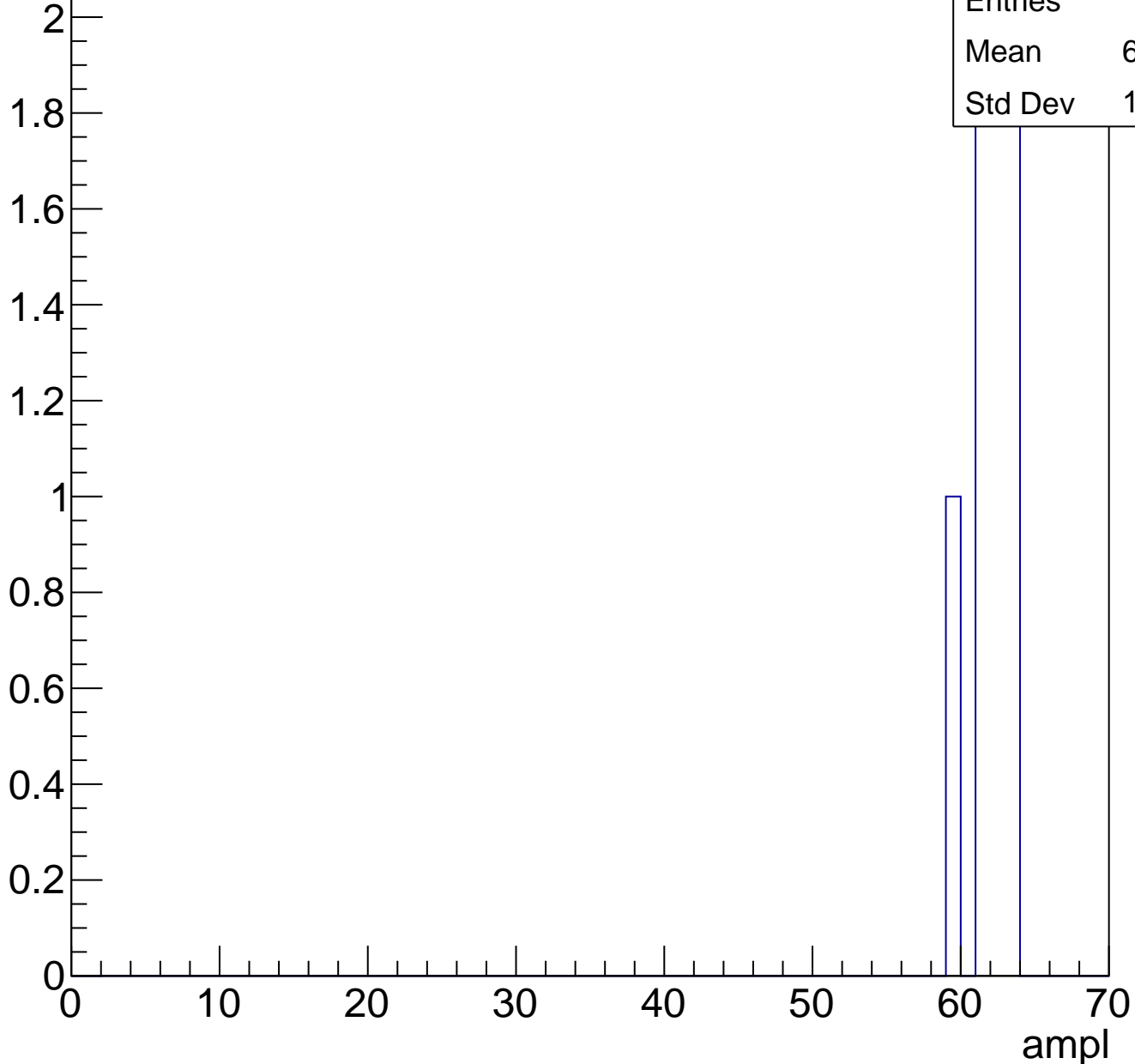
Entries	46
Mean	58.8
Std Dev	9.038



B1L102S, U11-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch116, adc0

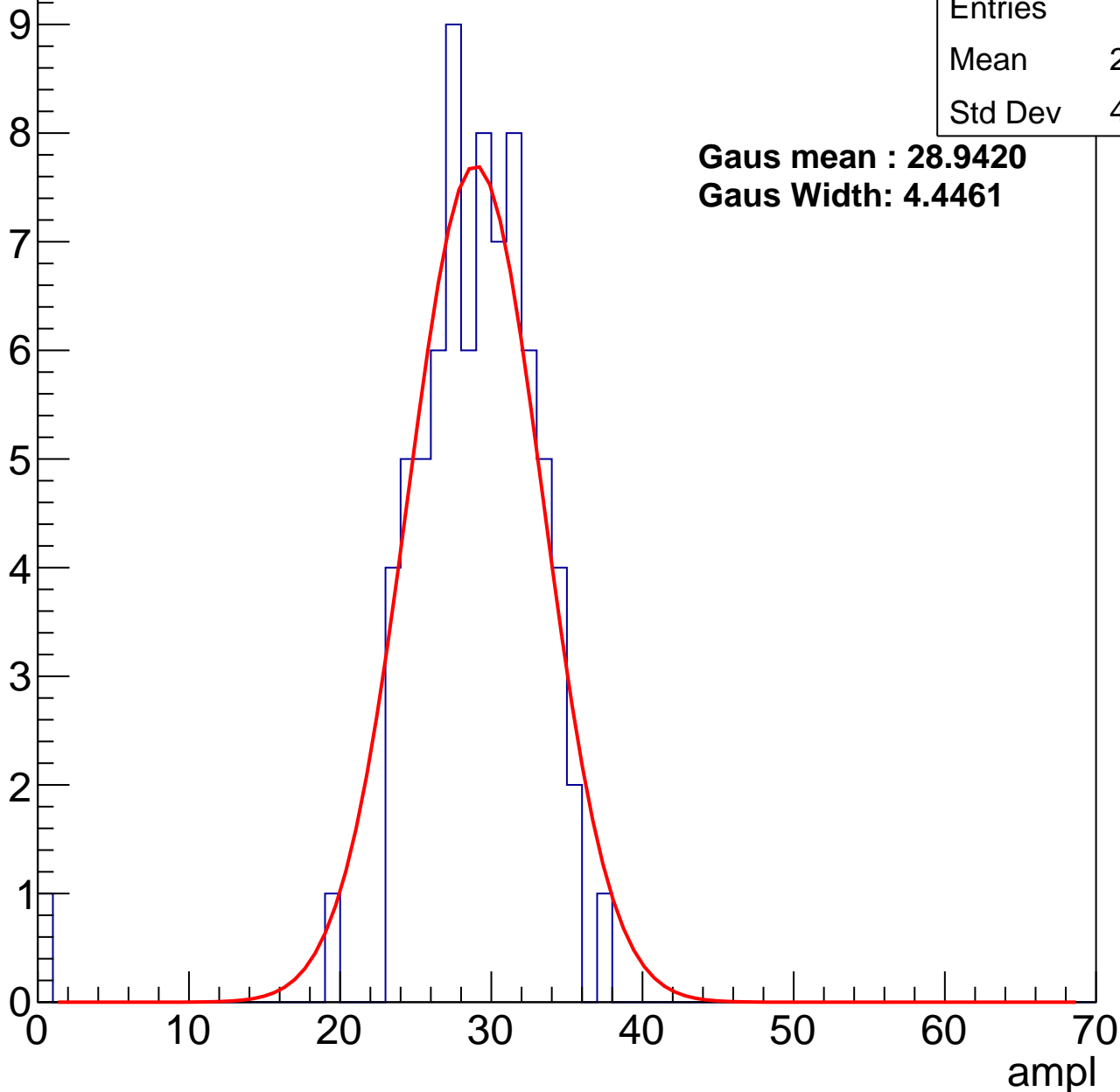
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.37
Std Dev	4.756

Gaus mean : 28.9420

Gaus Width: 4.4461



B1L102S, U11-ch116, adc1

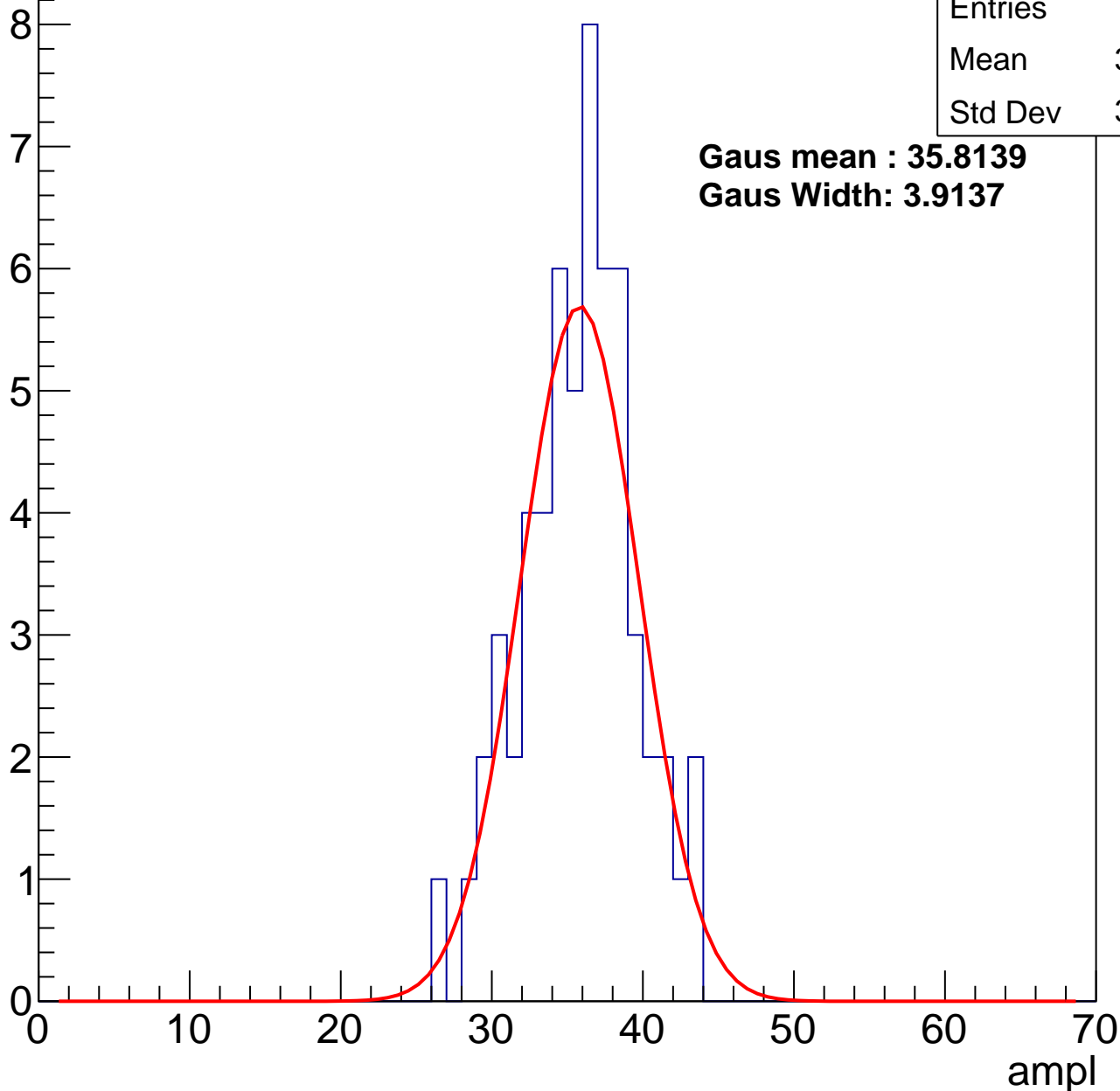
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.31
Std Dev	3.701

Gaus mean : 35.8139

Gaus Width: 3.9137



B1L102S, U11-ch116, adc2

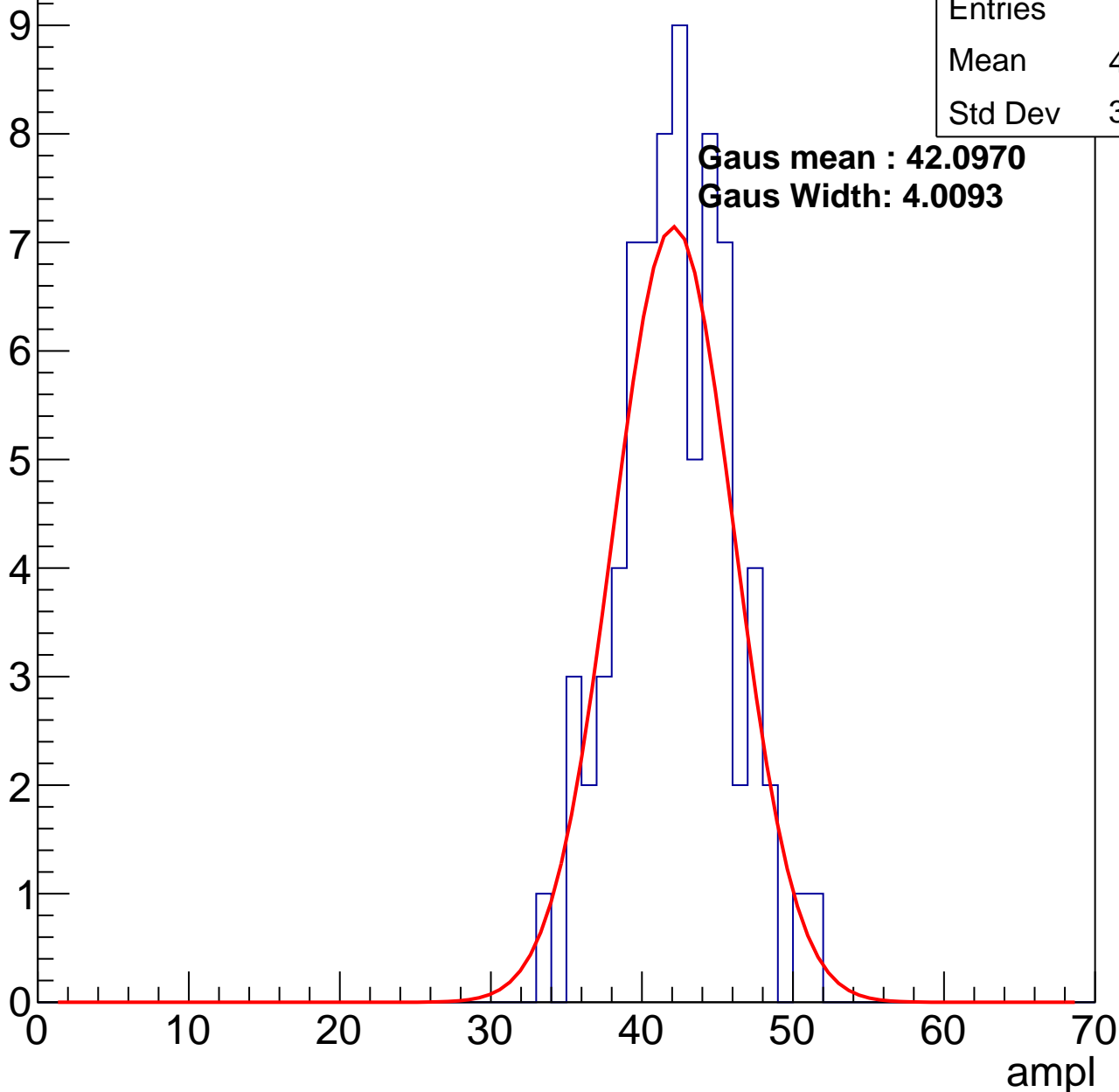
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.77
Std Dev	3.645

Gaus mean : 42.0970

Gaus Width: 4.0093

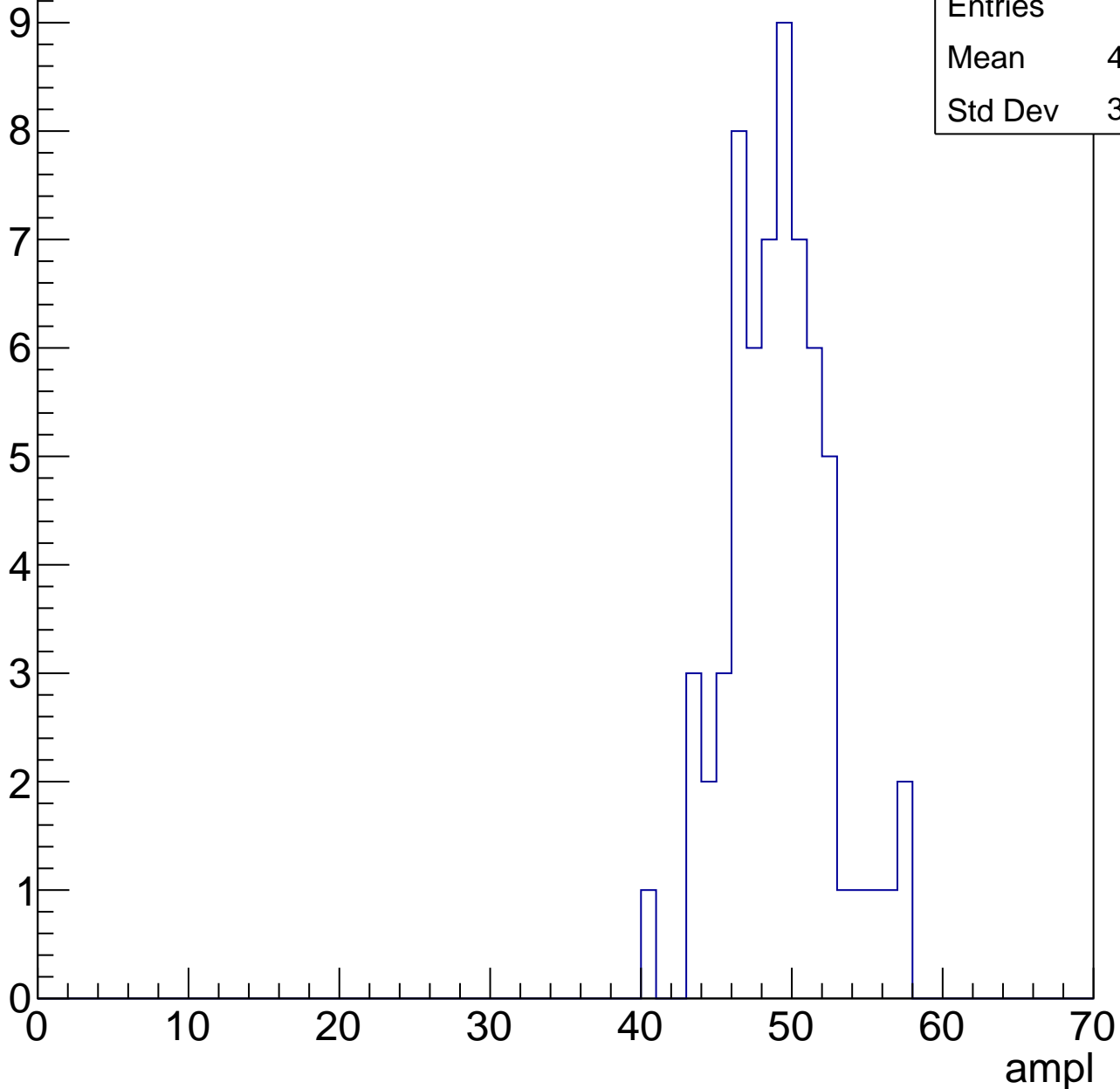


B1L102S, U11-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.68
Std Dev	3.366

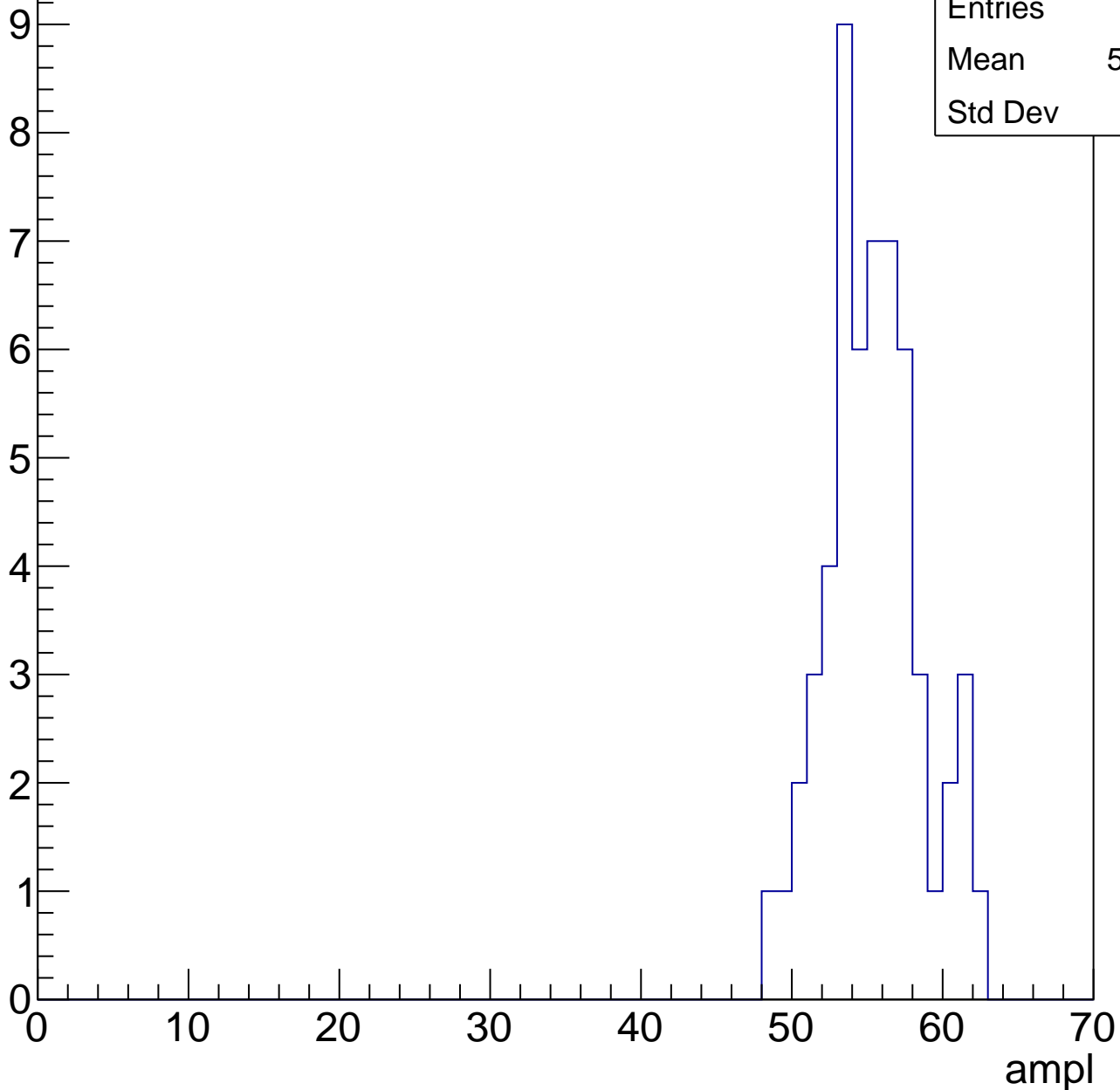


B1L102S, U11-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.93
Std Dev	3.11

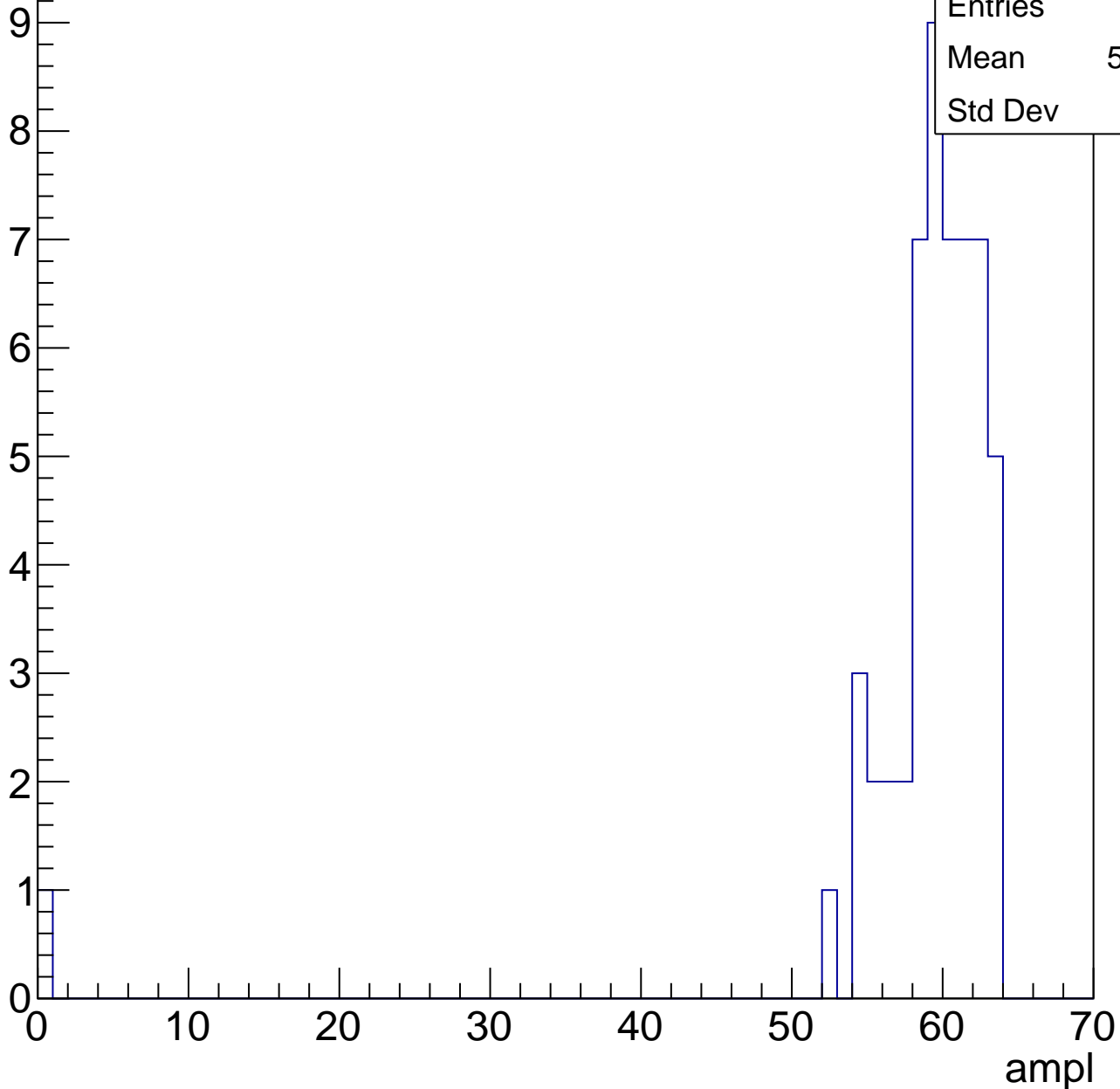


B1L102S, U11-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.17
Std Dev	8.48

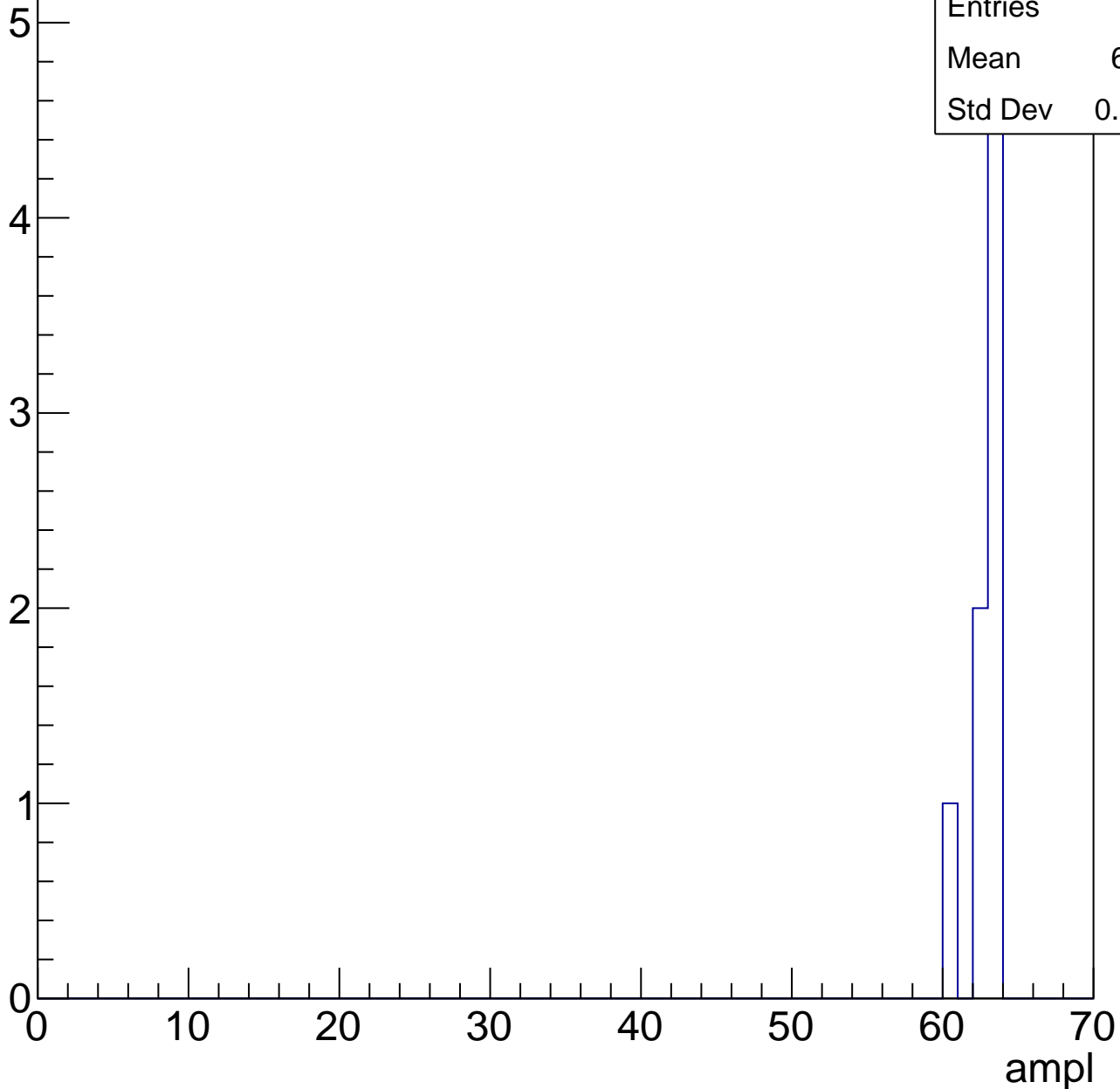


B1L102S, U11-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L102S, U11-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch117, adc0

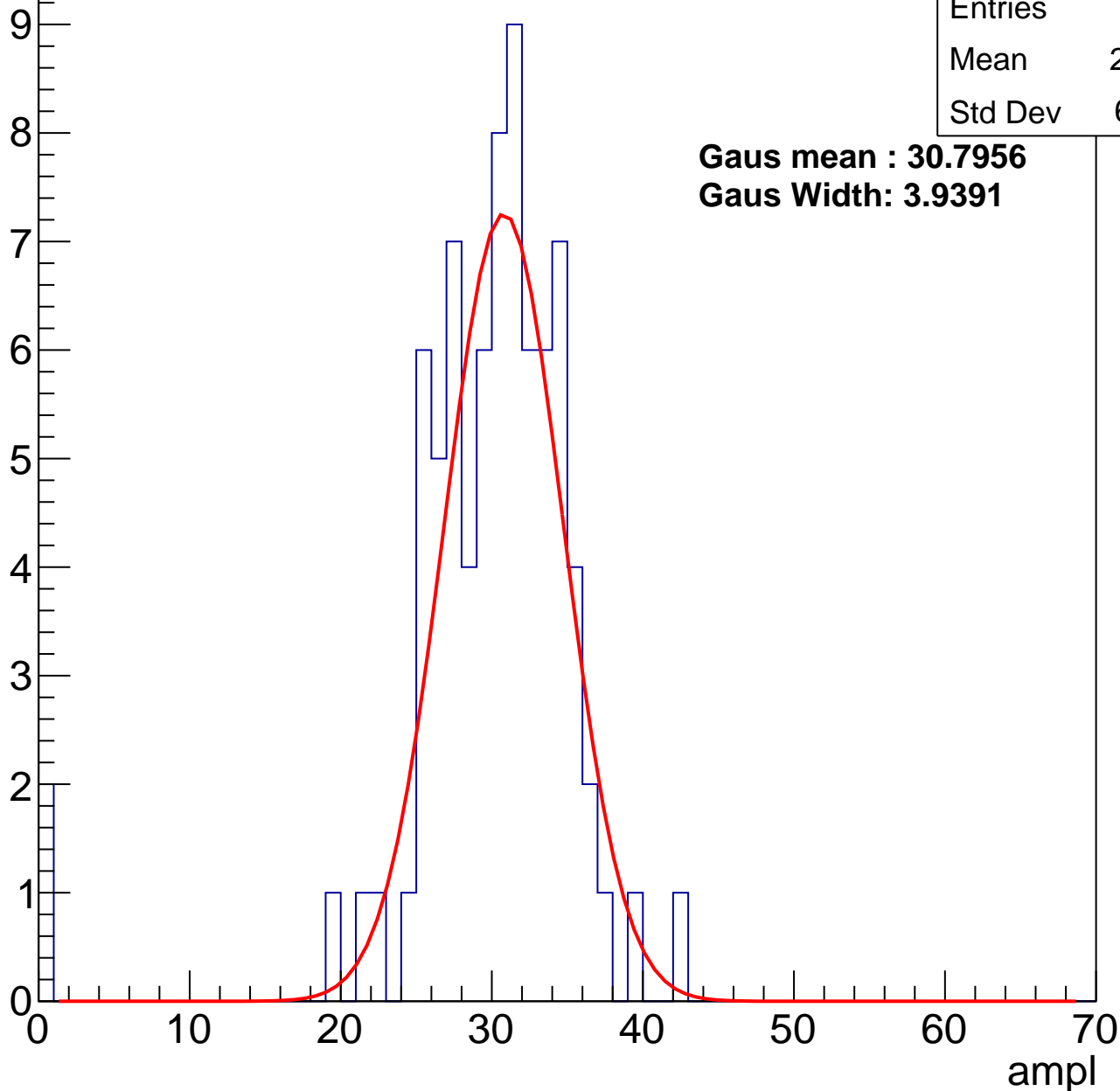
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.34
Std Dev	6.191

Gaus mean : 30.7956

Gaus Width: 3.9391



B1L102S, U11-ch117, adc1

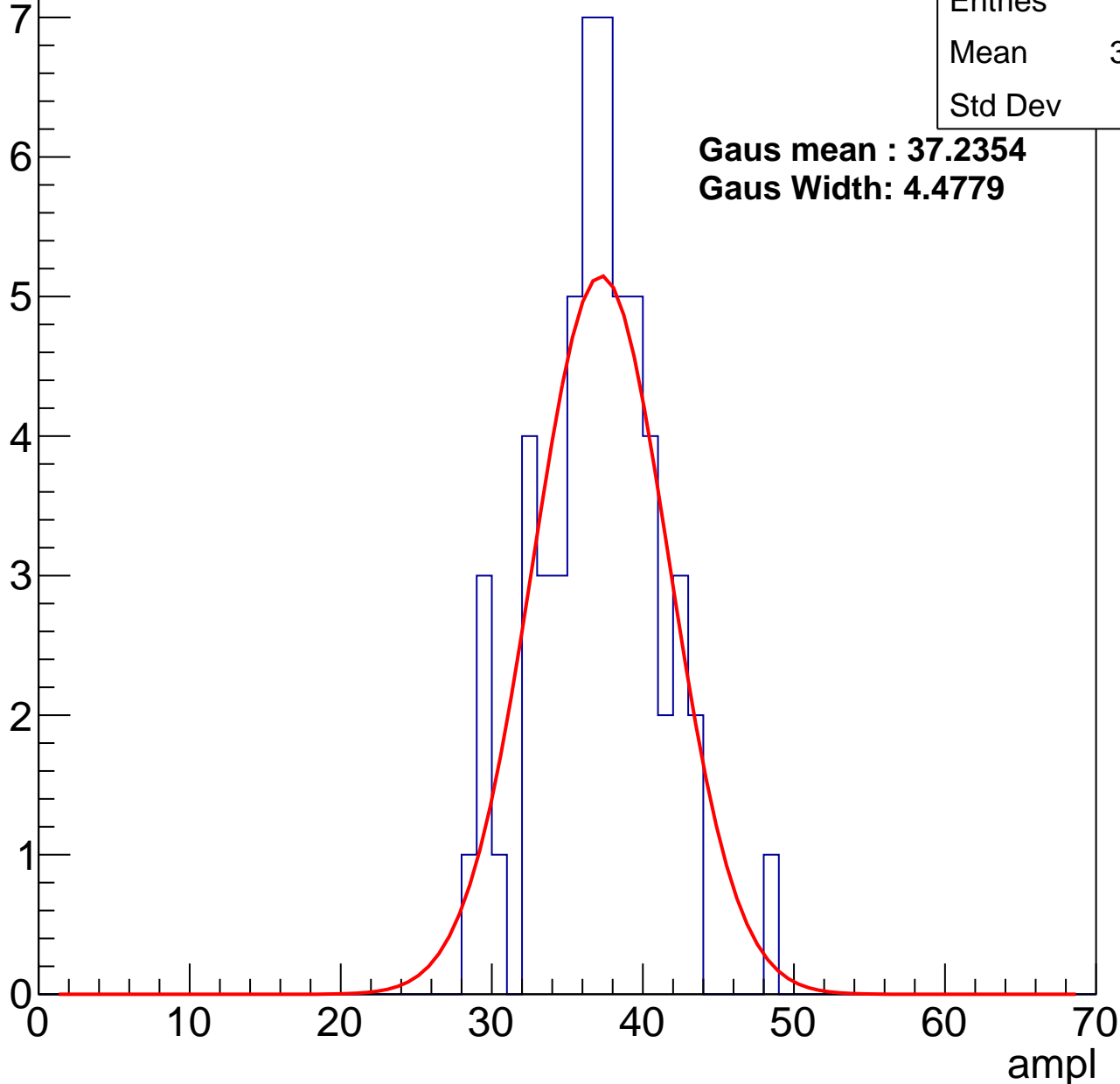
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	36.55
Std Dev	3.95

Gaus mean : 37.2354

Gaus Width: 4.4779



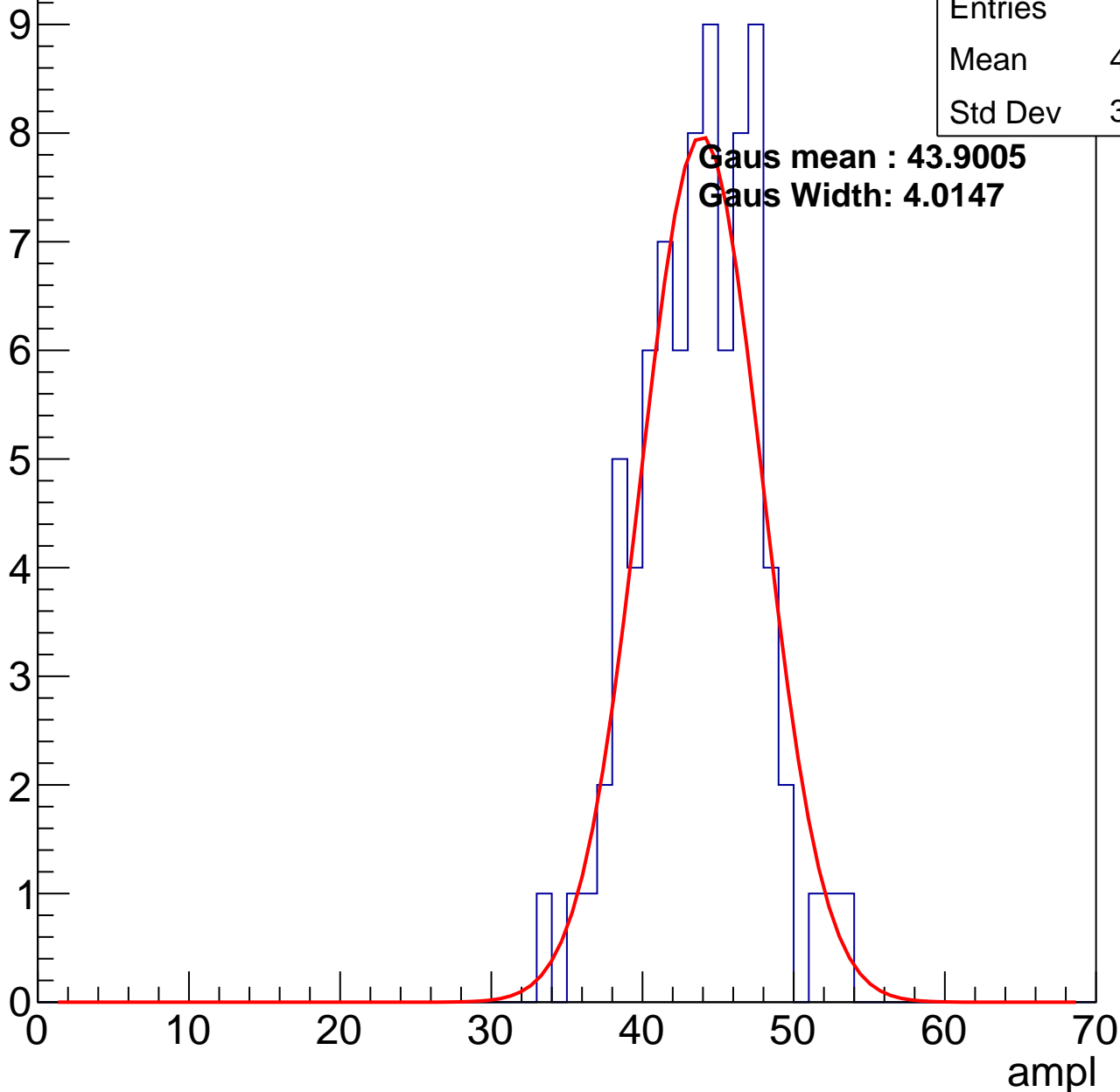
B1L102S, U11-ch117, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	43.29
Std Dev	3.859

Gaus mean : 43.9005
Gaus Width: 4.0147

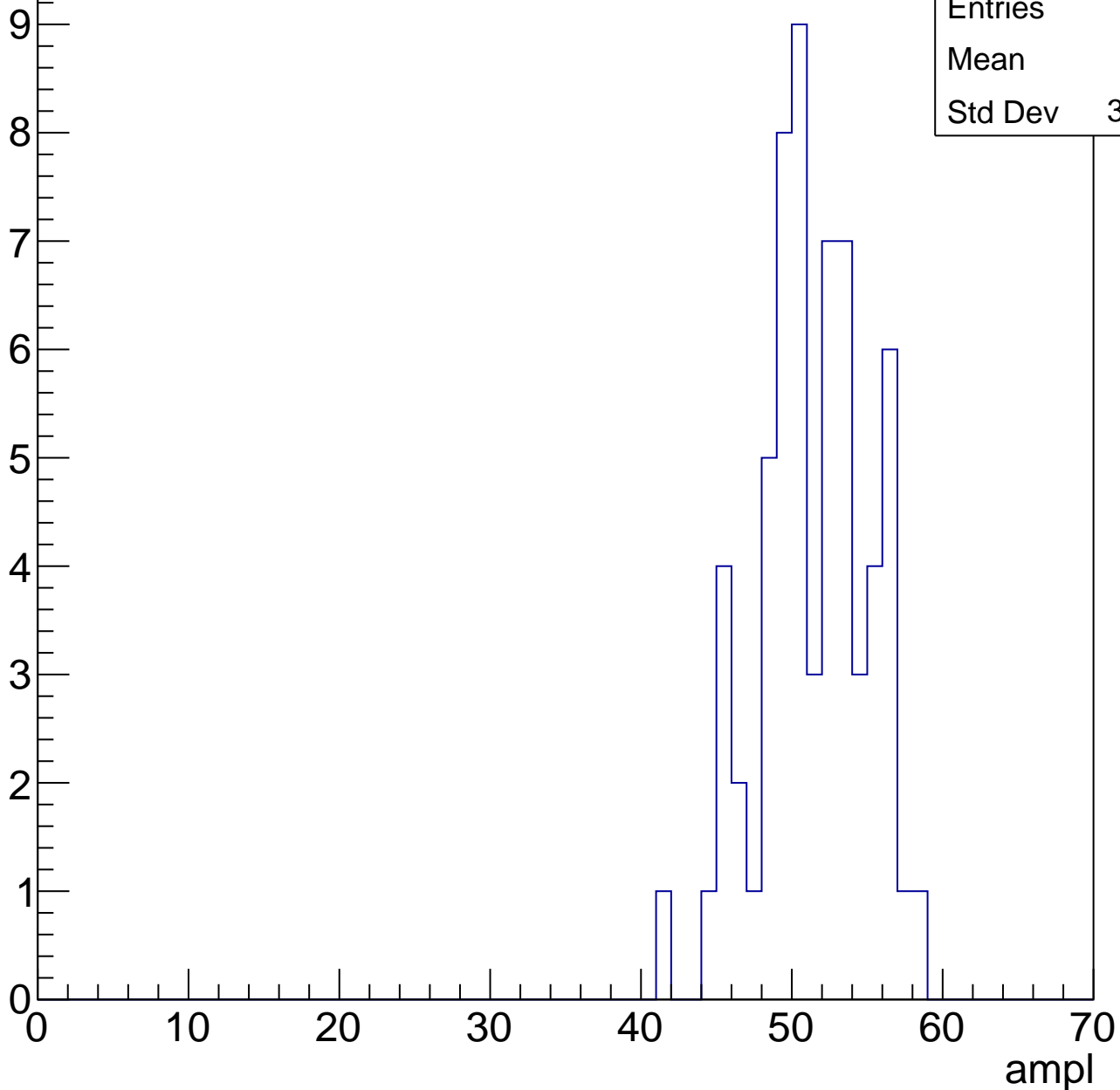


B1L102S, U11-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.9
Std Dev	3.576

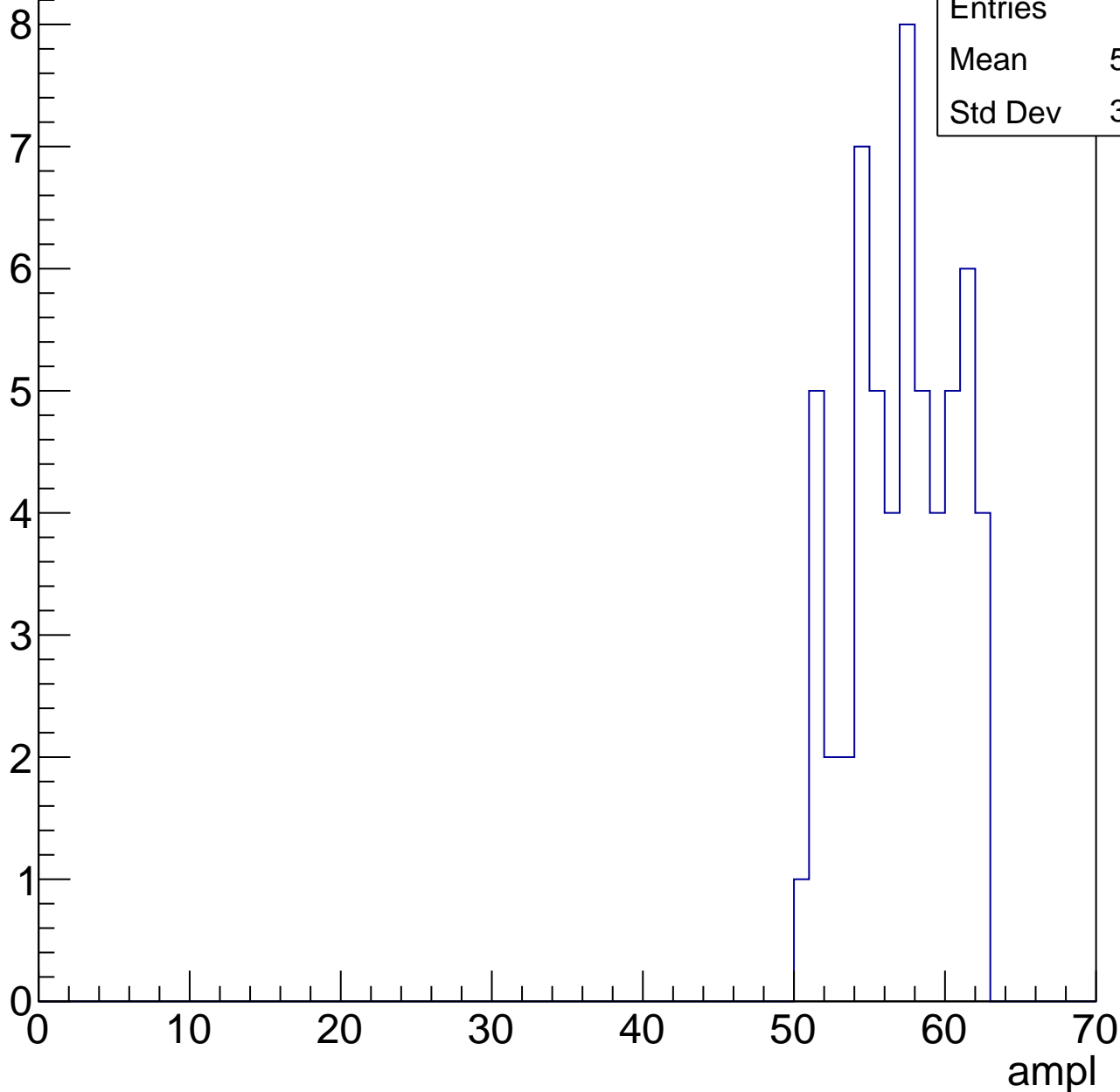


B1L102S, U11-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.69
Std Dev	3.359

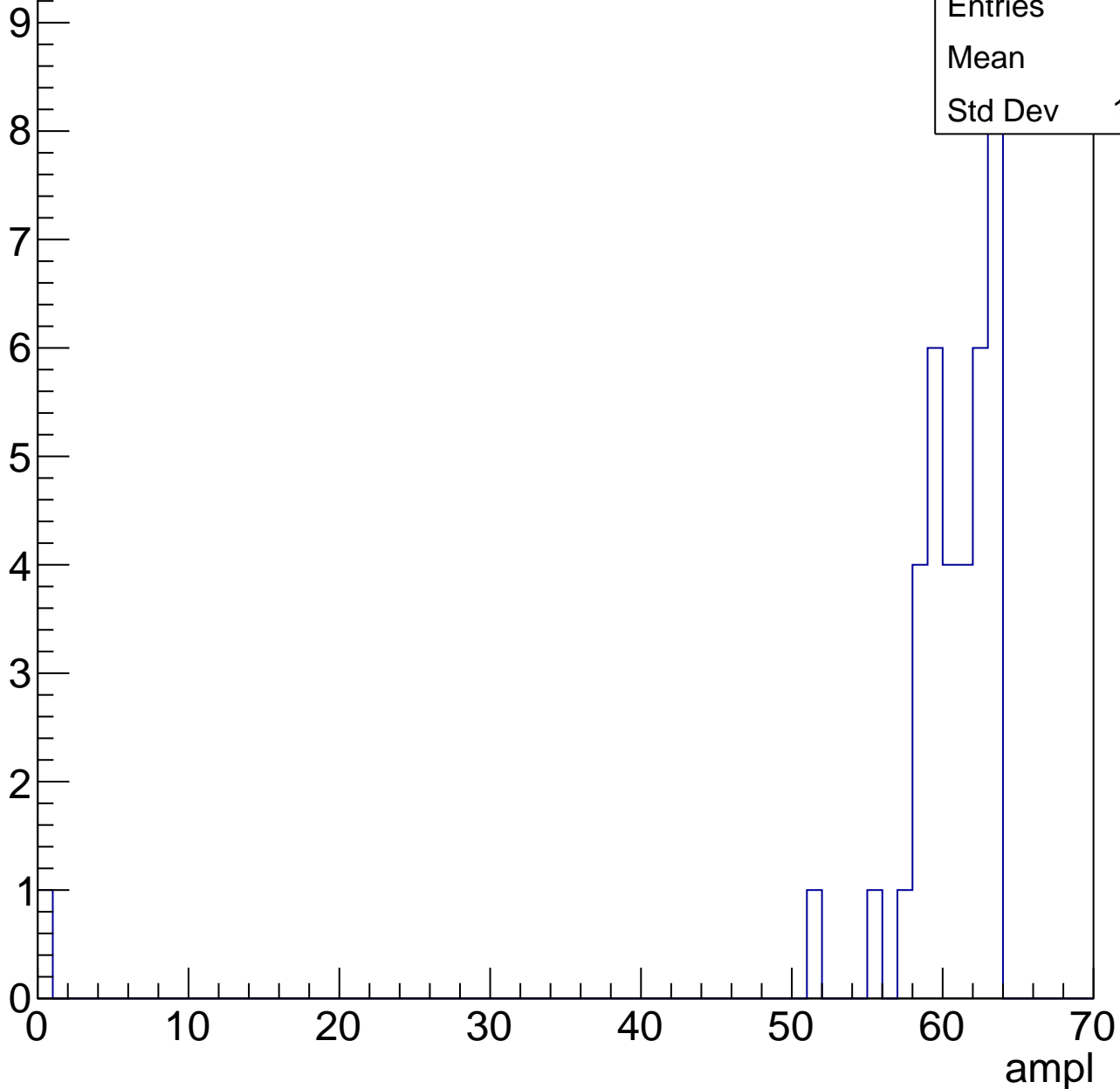


B1L102S, U11-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.7
Std Dev	10.11



B1L102S, U11-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U11-ch118, adc0

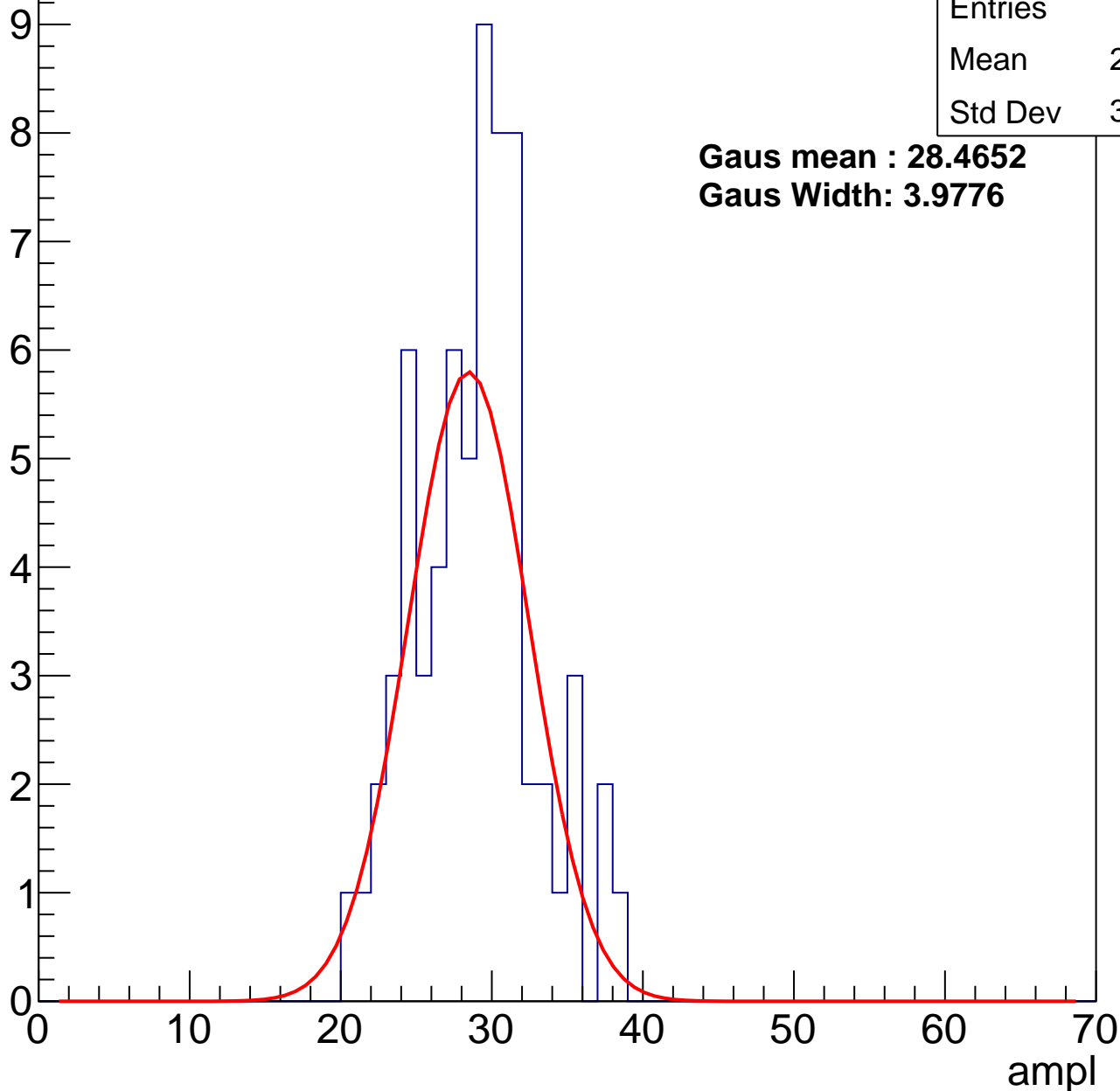
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.49
Std Dev	3.918

Gaus mean : 28.4652

Gaus Width: 3.9776



B1L102S, U11-ch118, adc1

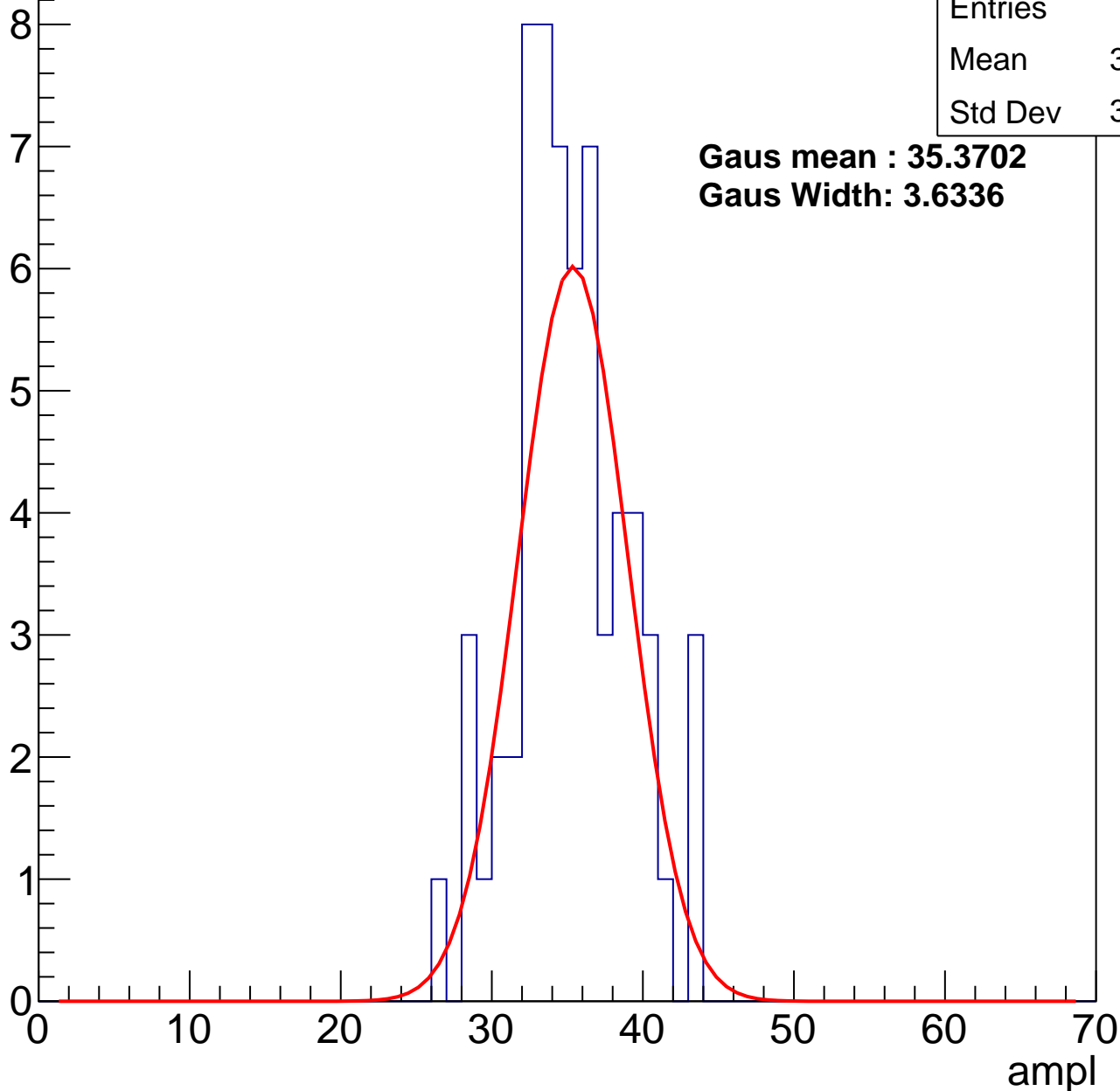
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.76
Std Dev	3.728

Gaus mean : 35.3702

Gaus Width: 3.6336



B1L102S, U11-ch118, adc2

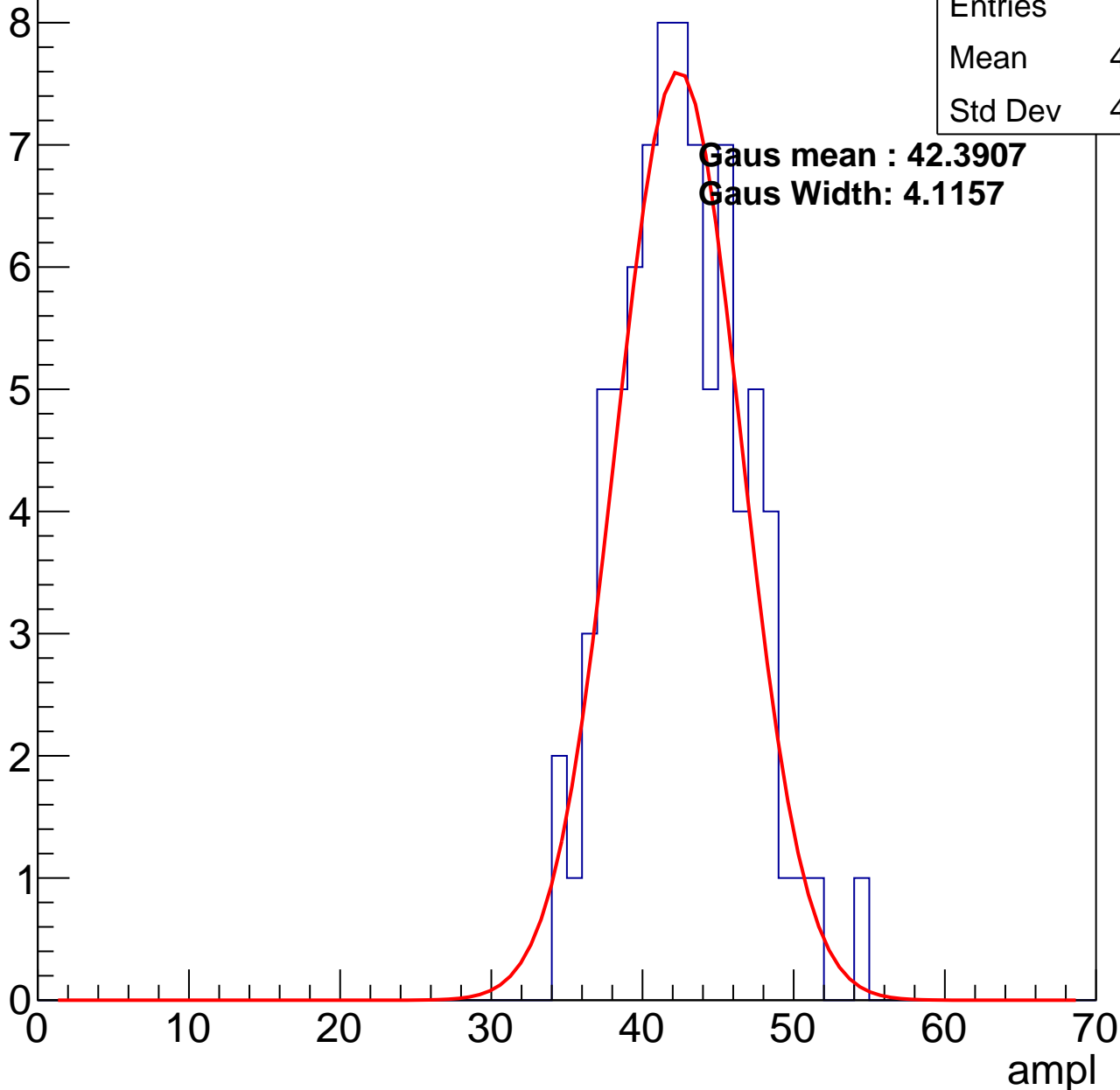
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.16
Std Dev	4.066

Gaus mean : 42.3907

Gaus Width: 4.1157

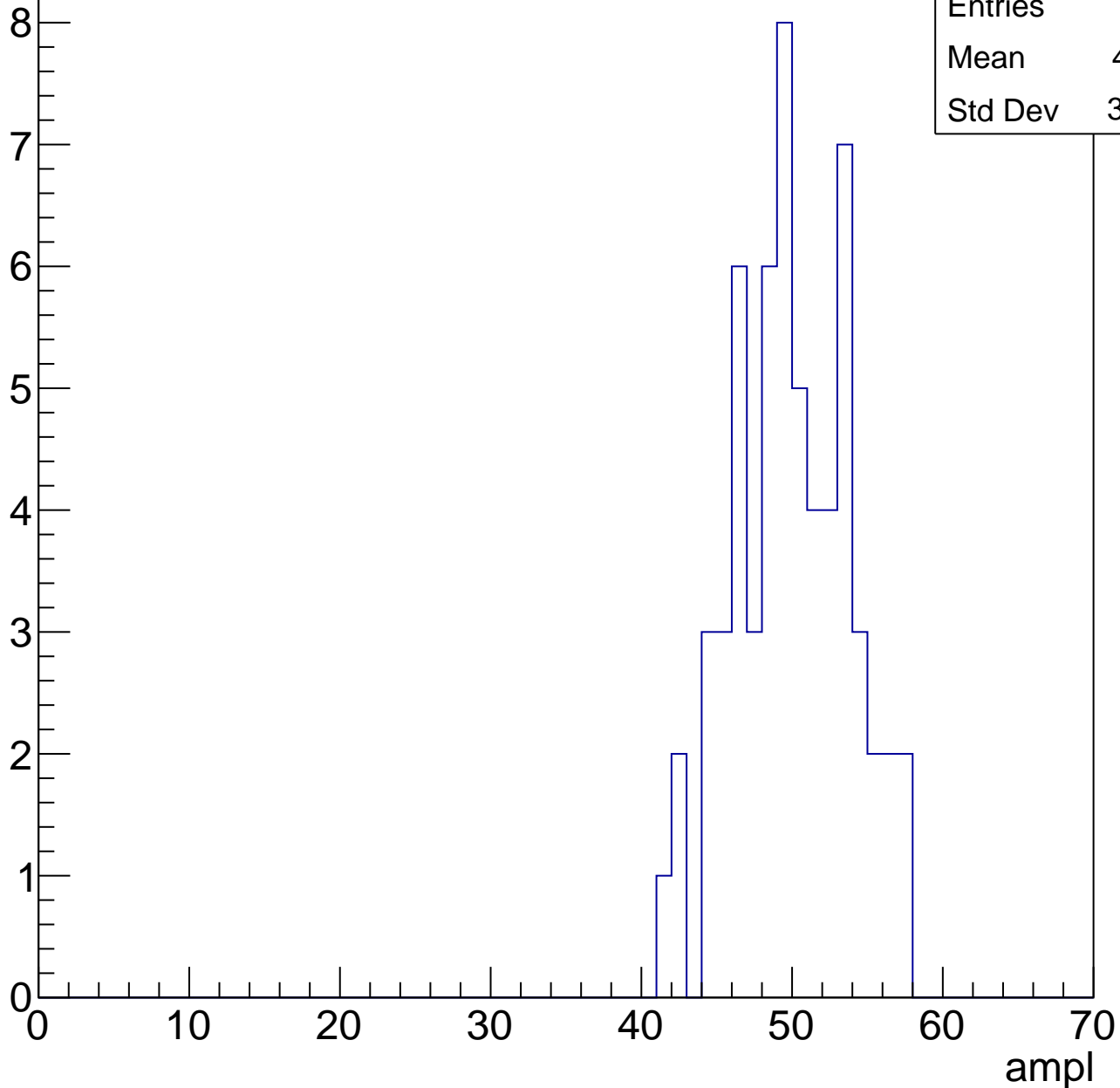


B1L102S, U11-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.51
Std Dev	3.805

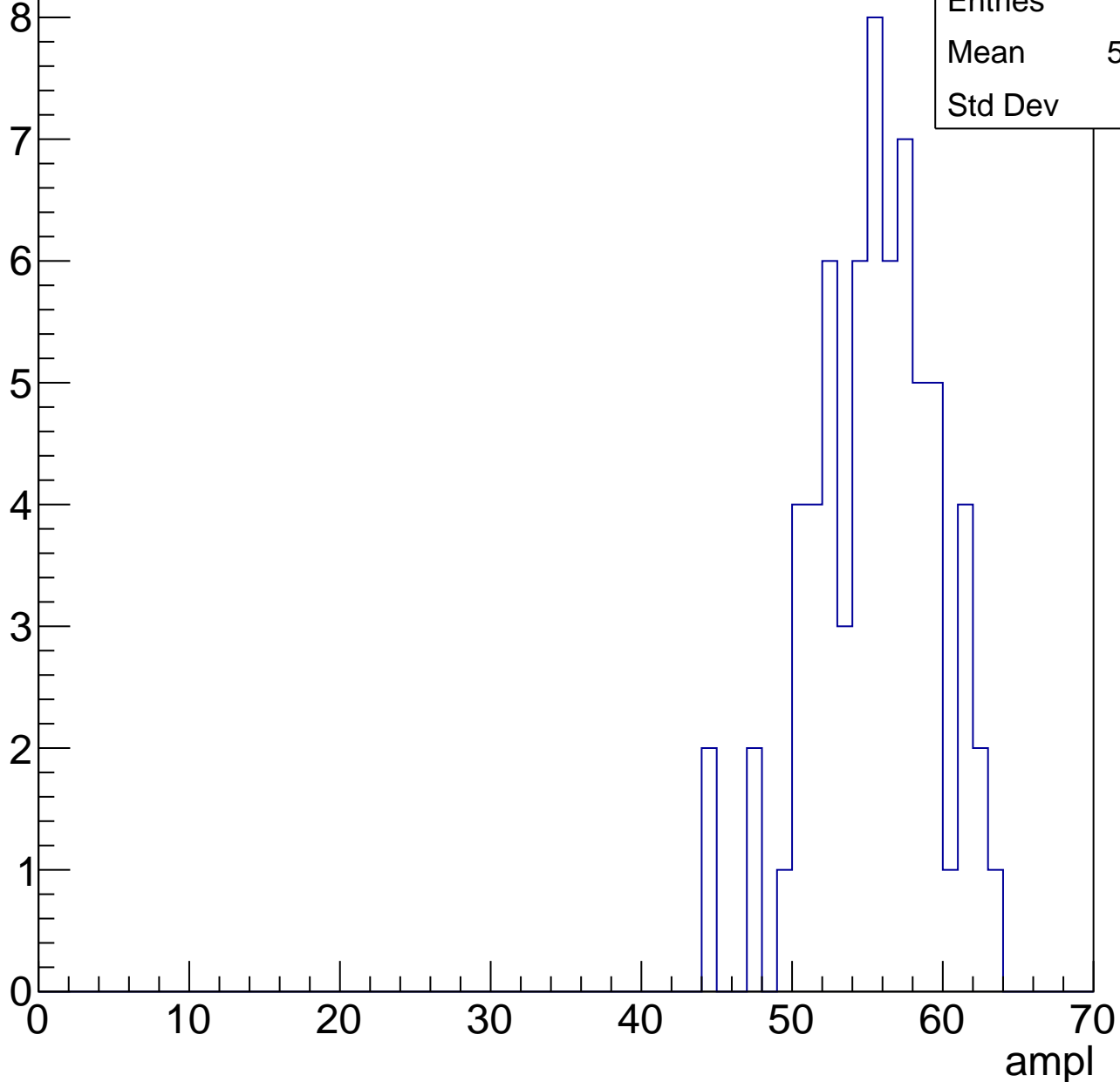


B1L102S, U11-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

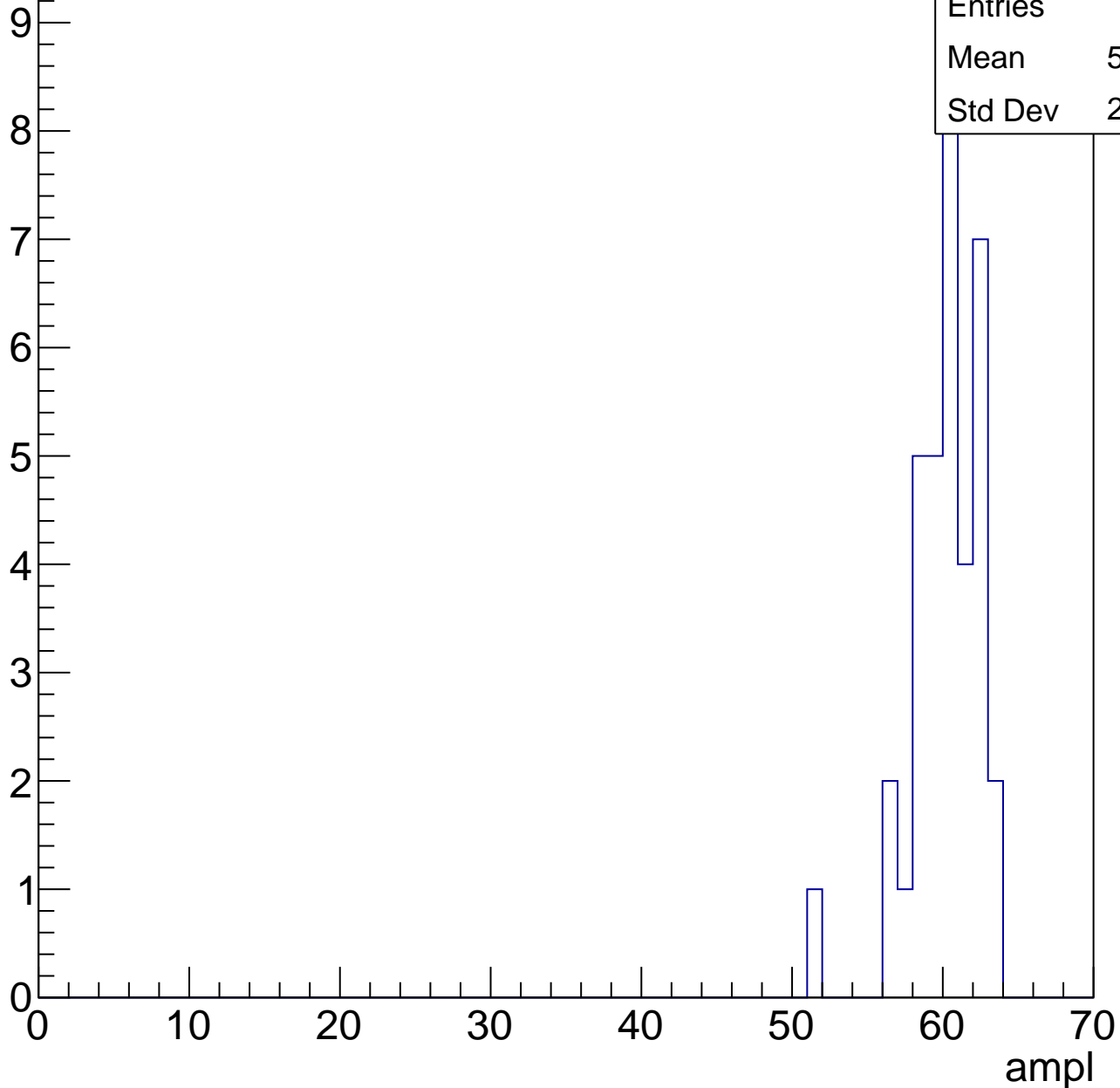
Entries	67
Mean	54.94
Std Dev	4.11



B1L102S, U11-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

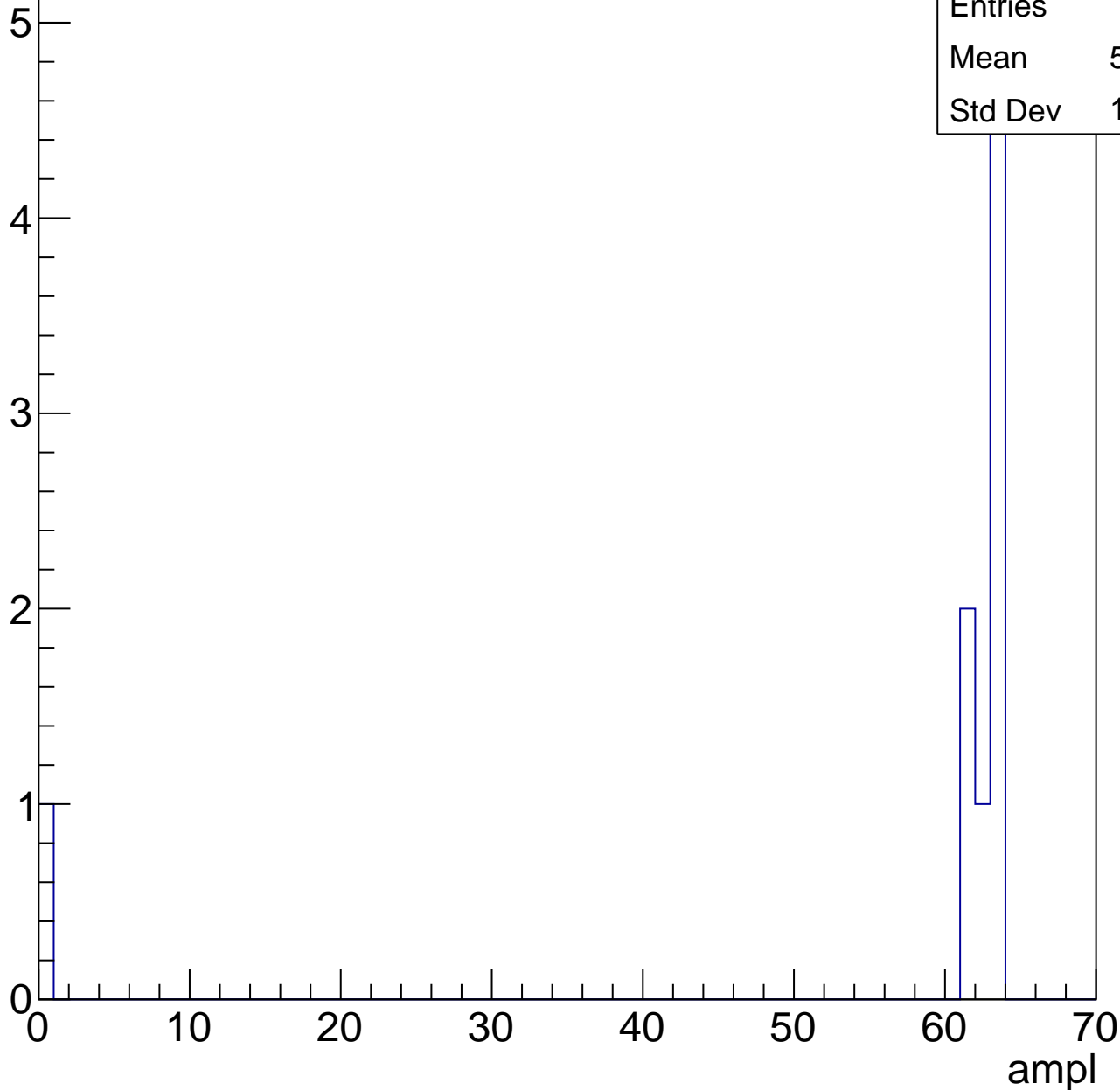


B1L102S, U11-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	55.44
Std Dev	19.62



B1L102S, U11-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch119, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	28.59
Std Dev	5.787

Gaus mean : 29.7903

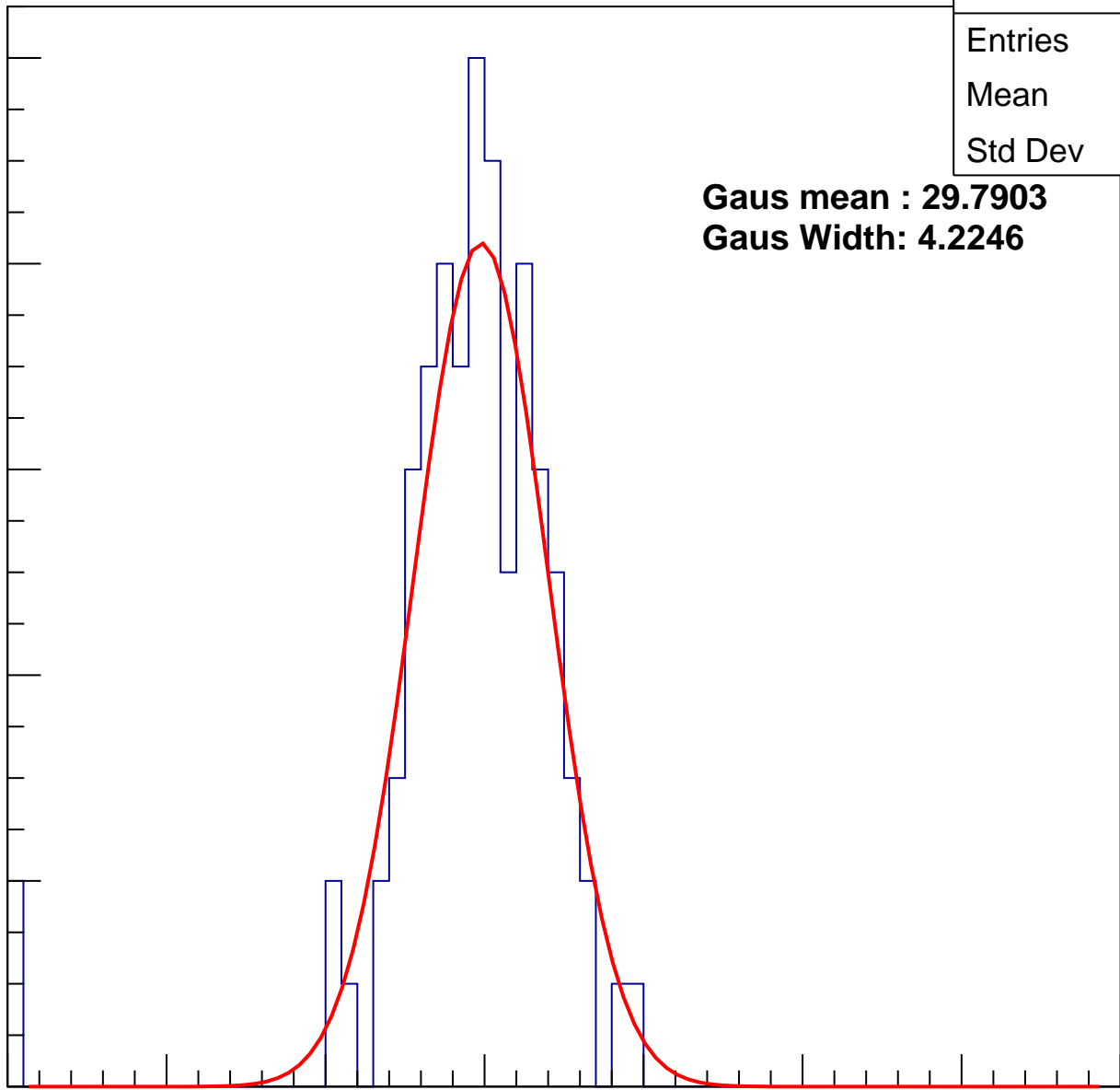
Gaus Width: 4.2246

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U11-ch119, adc1

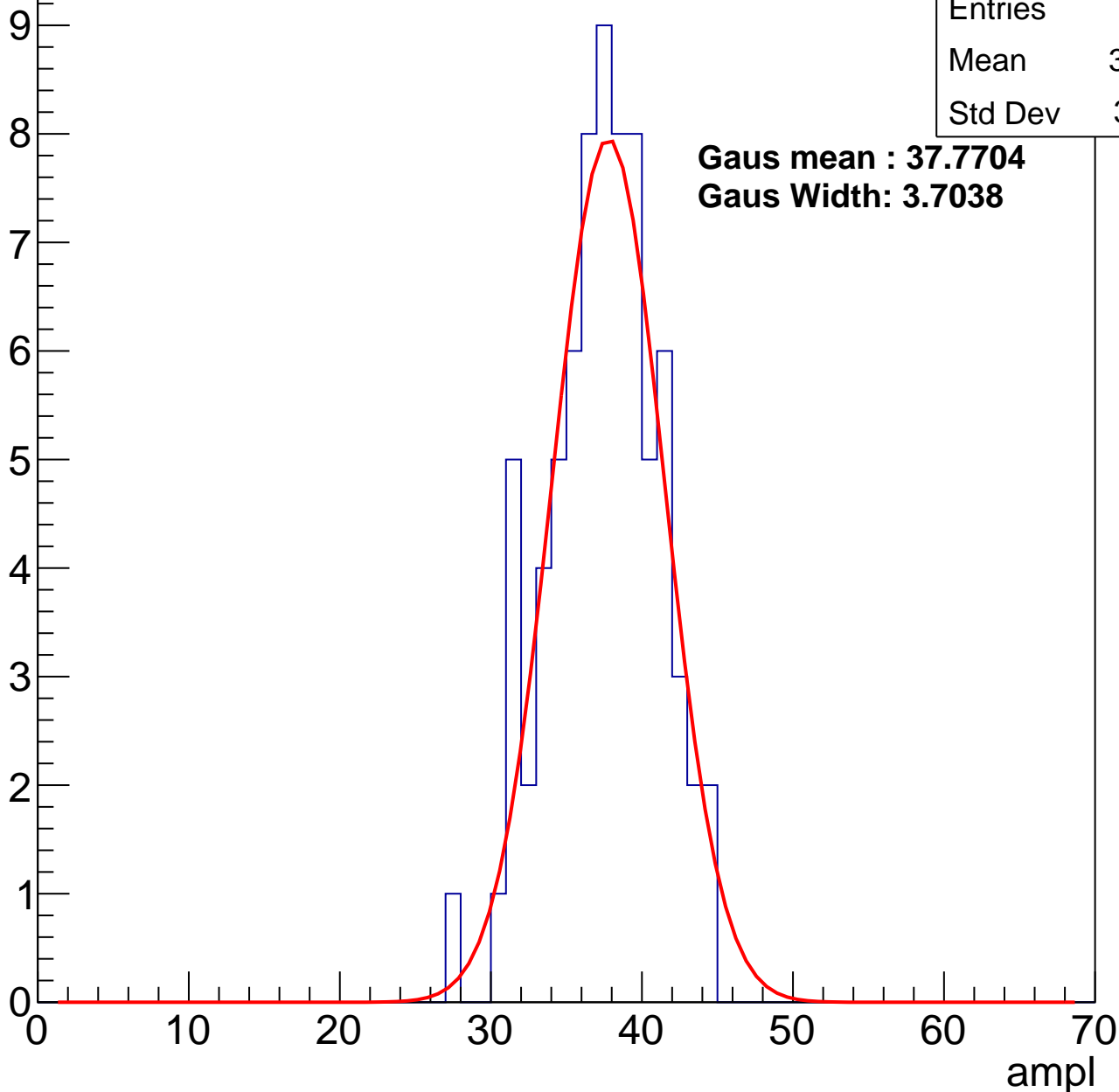
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.95
Std Dev	3.551

Gaus mean : 37.7704

Gaus Width: 3.7038



B1L102S, U11-ch119, adc2

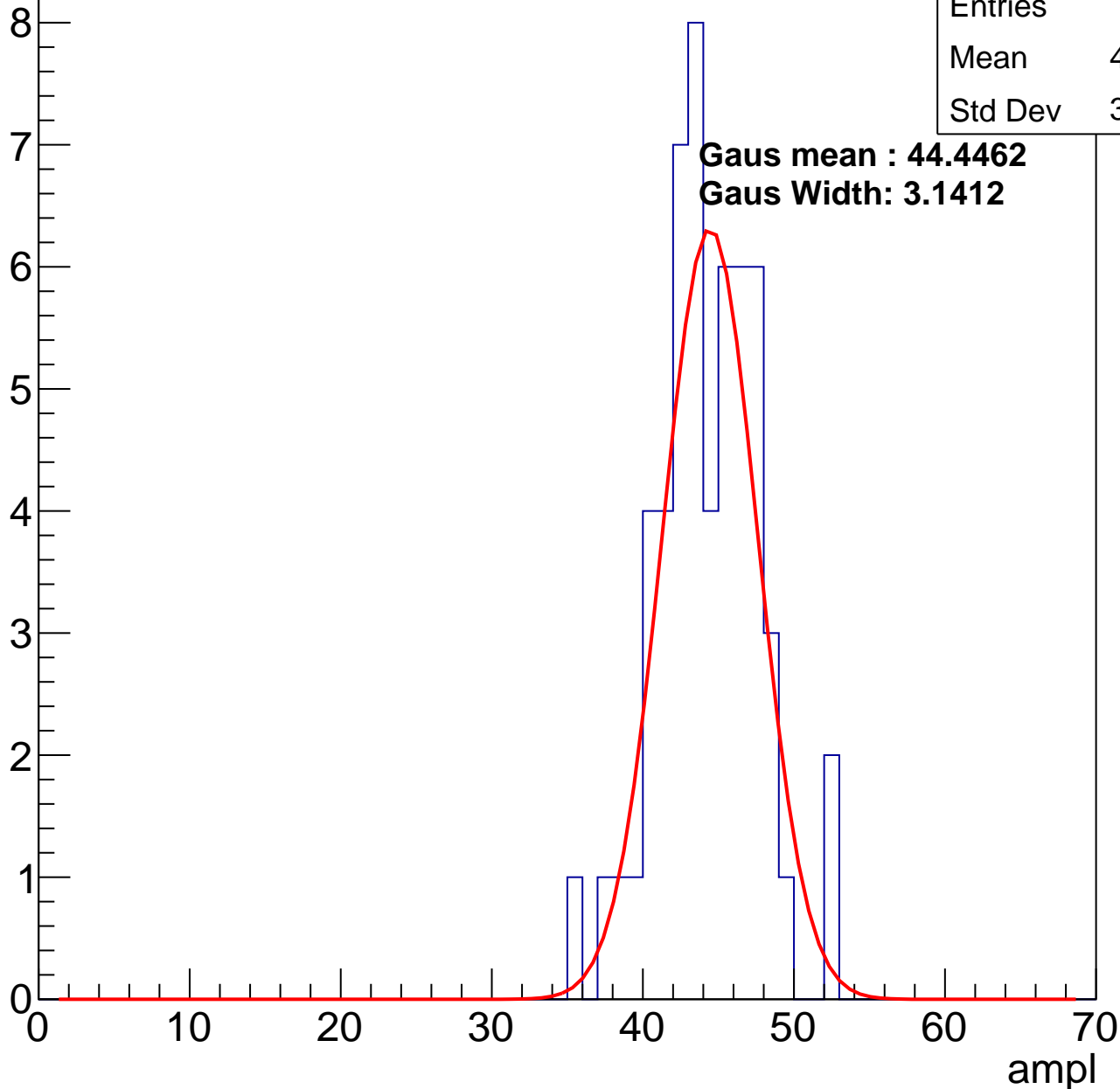
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.85
Std Dev	3.333

Gaus mean : 44.4462

Gaus Width: 3.1412

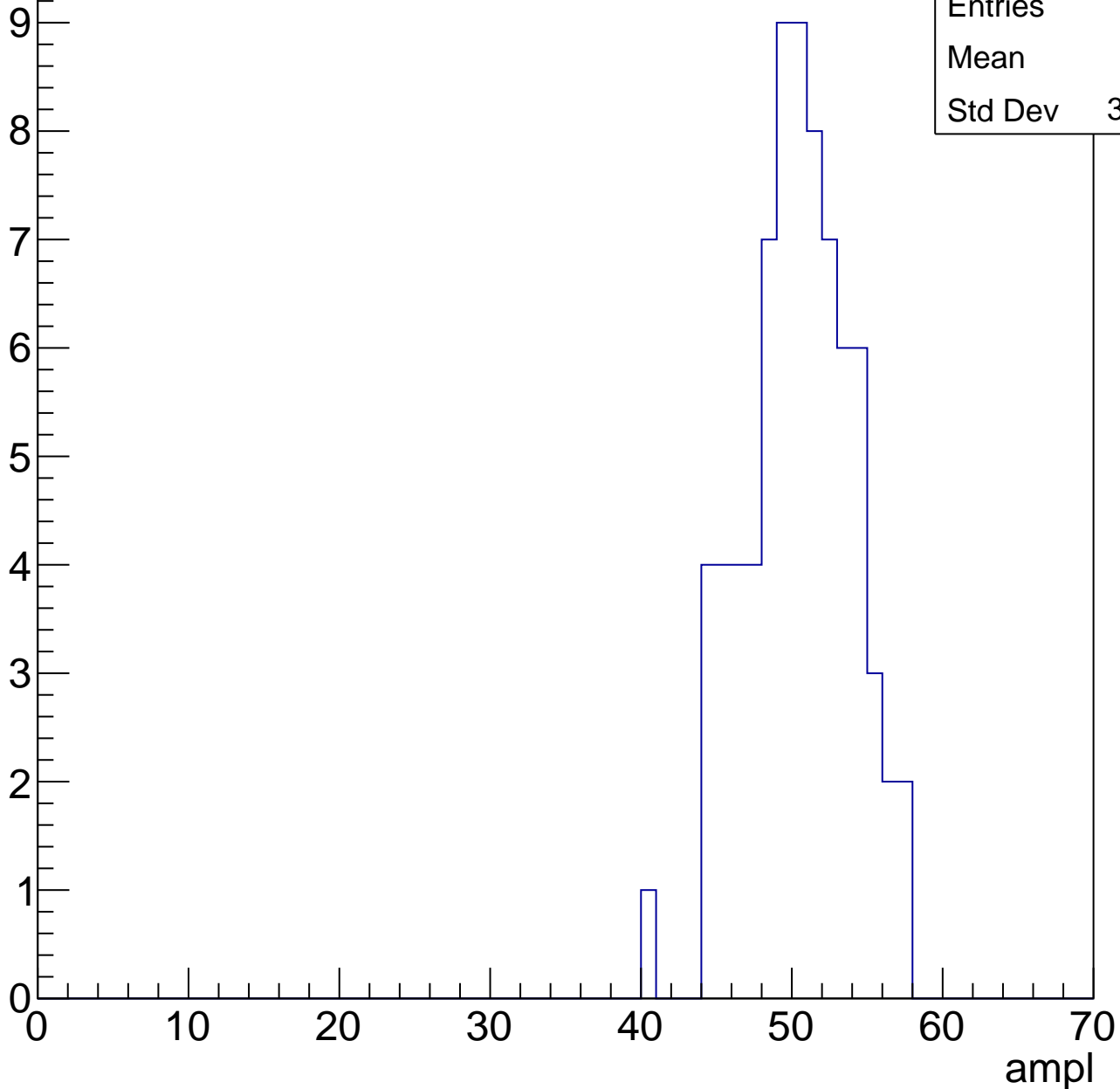


B1L102S, U11-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	50
Std Dev	3.464

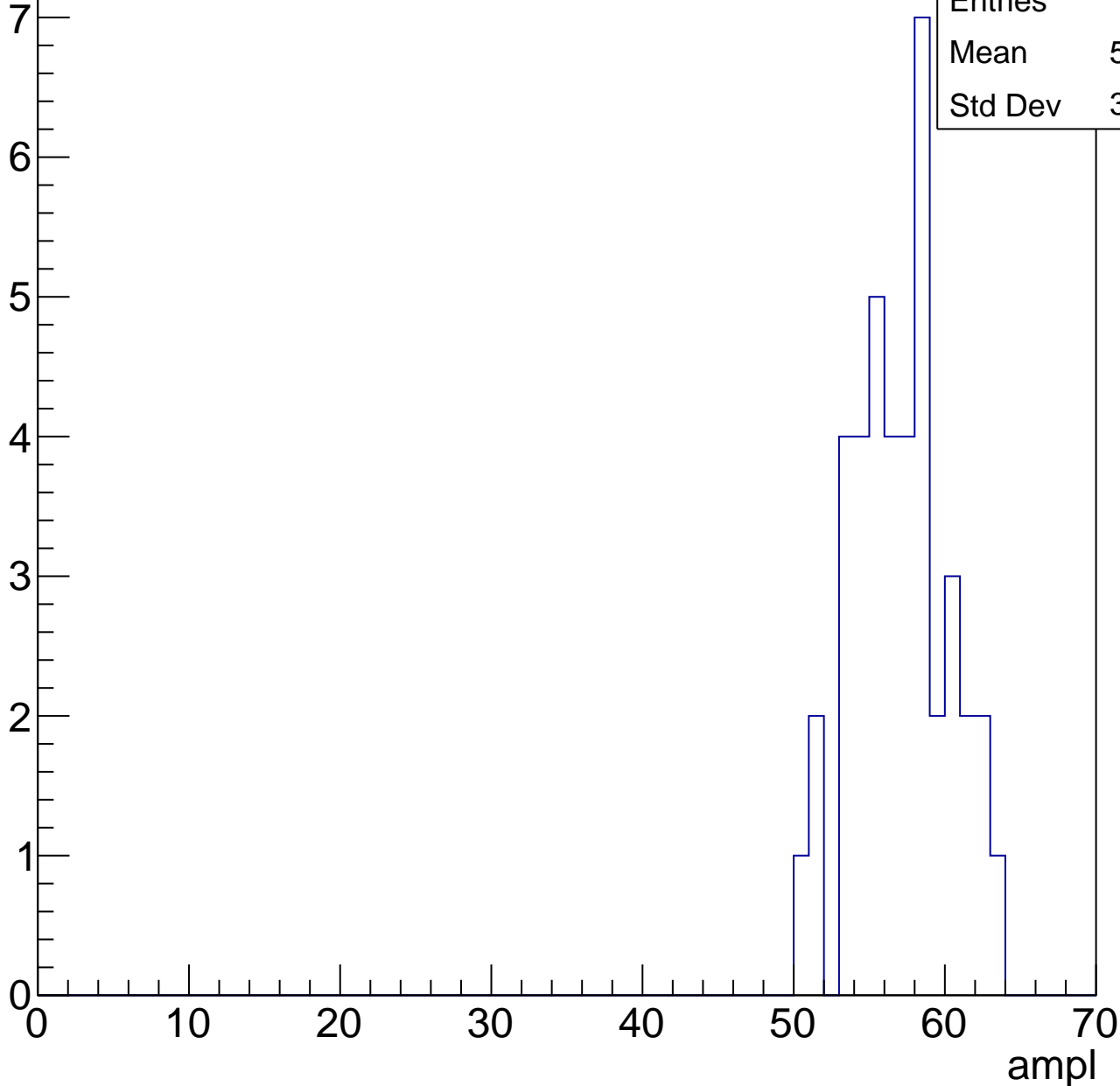


B1L102S, U11-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	56.59
Std Dev	3.115

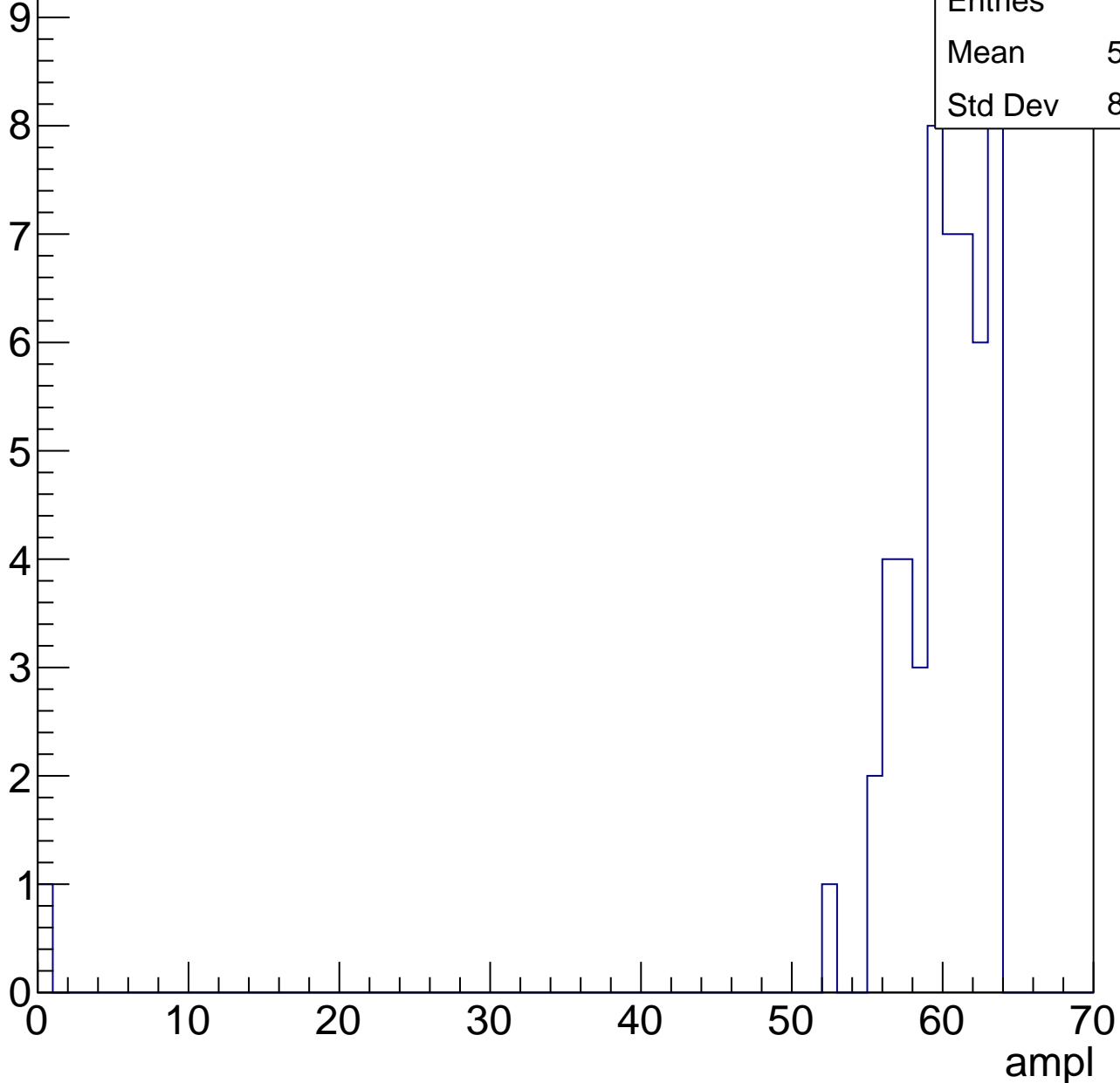


B1L102S, U11-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.58
Std Dev	8.594



B1L102S, U11-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L102S, U11-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch120, adc0

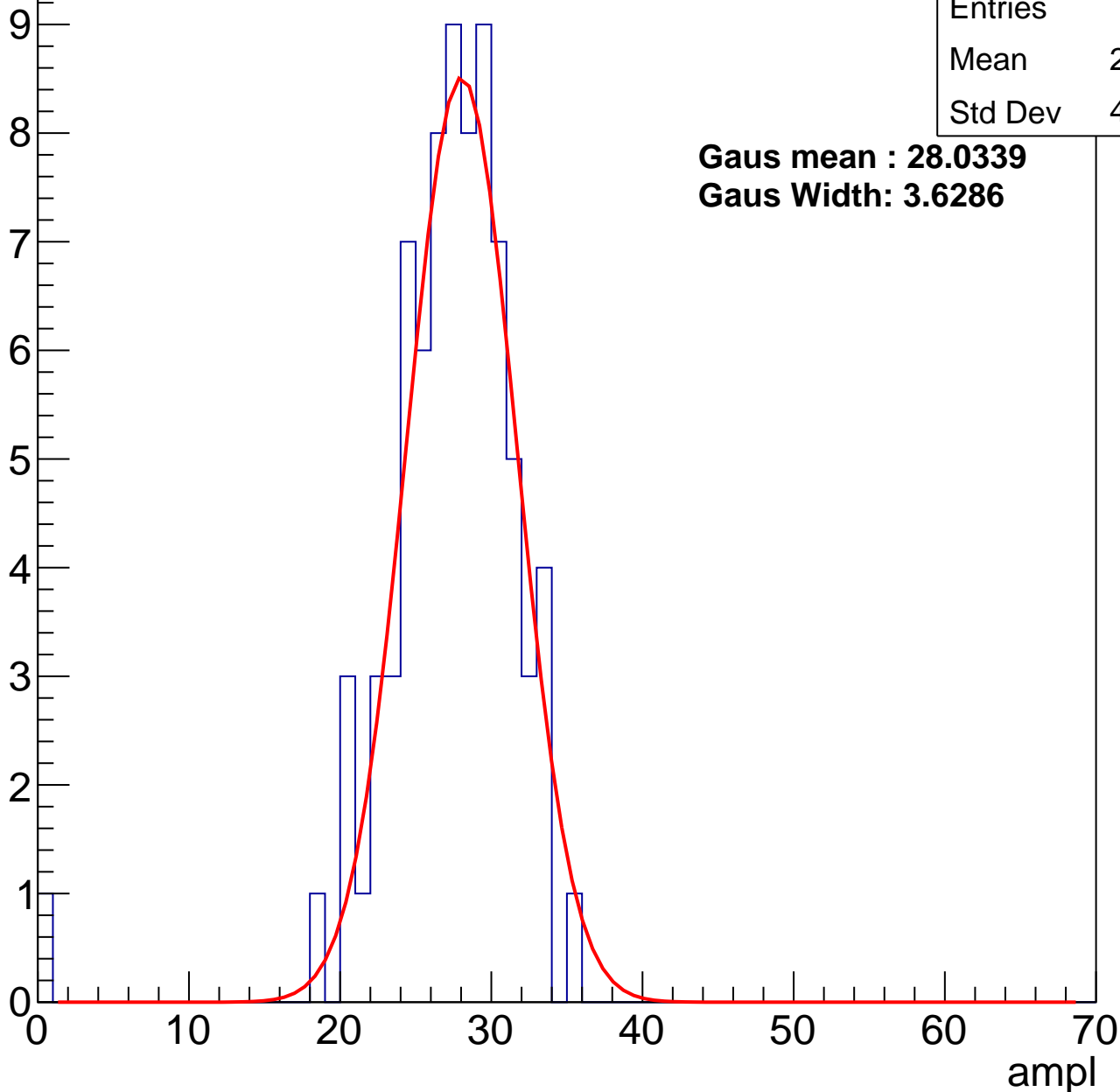
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	26.78
Std Dev	4.608

Gaus mean : 28.0339

Gaus Width: 3.6286



B1L102S, U11-ch120, adc1

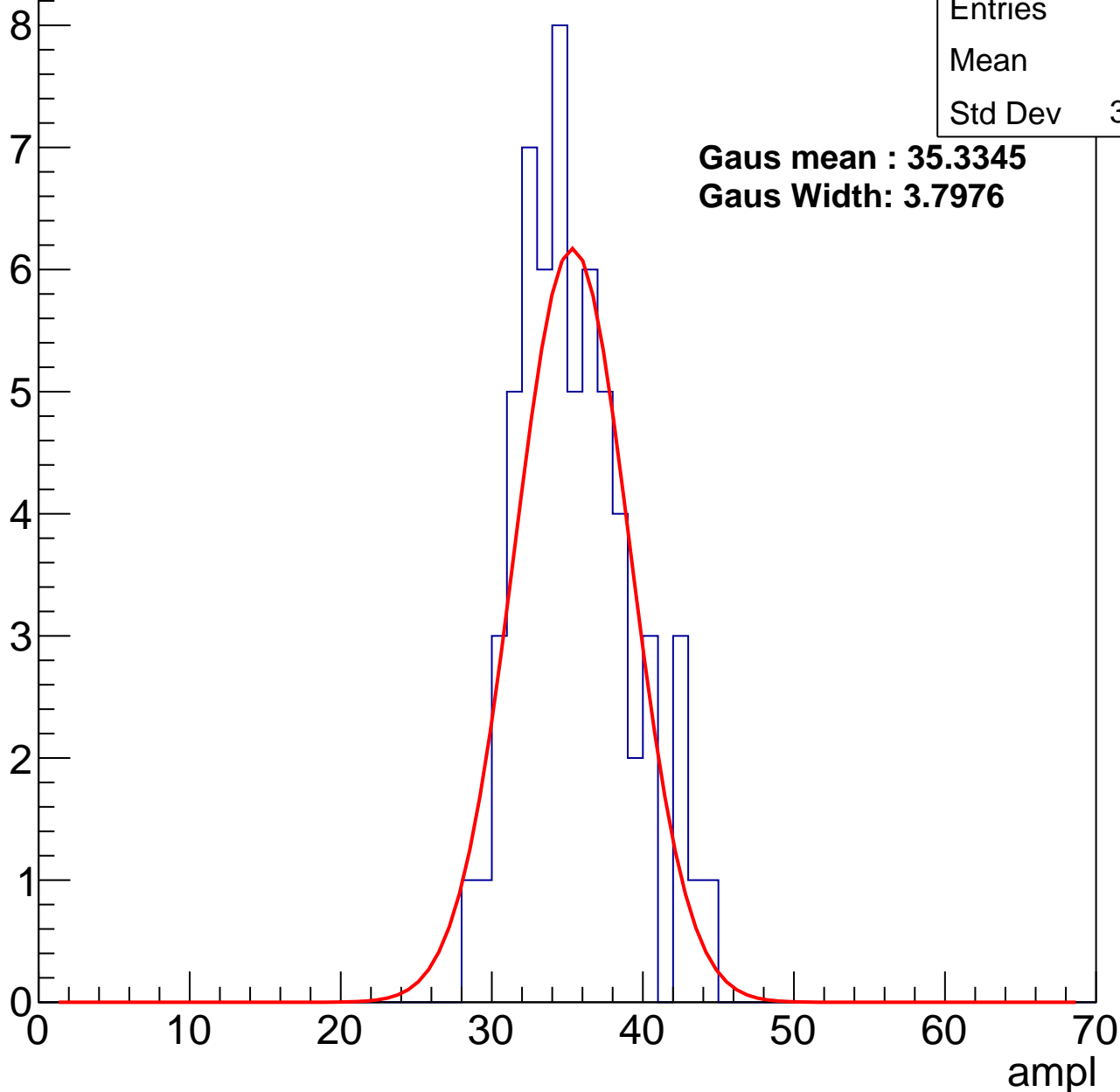
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35
Std Dev	3.612

Gaus mean : 35.3345

Gaus Width: 3.7976



B1L102S, U11-ch120, adc2

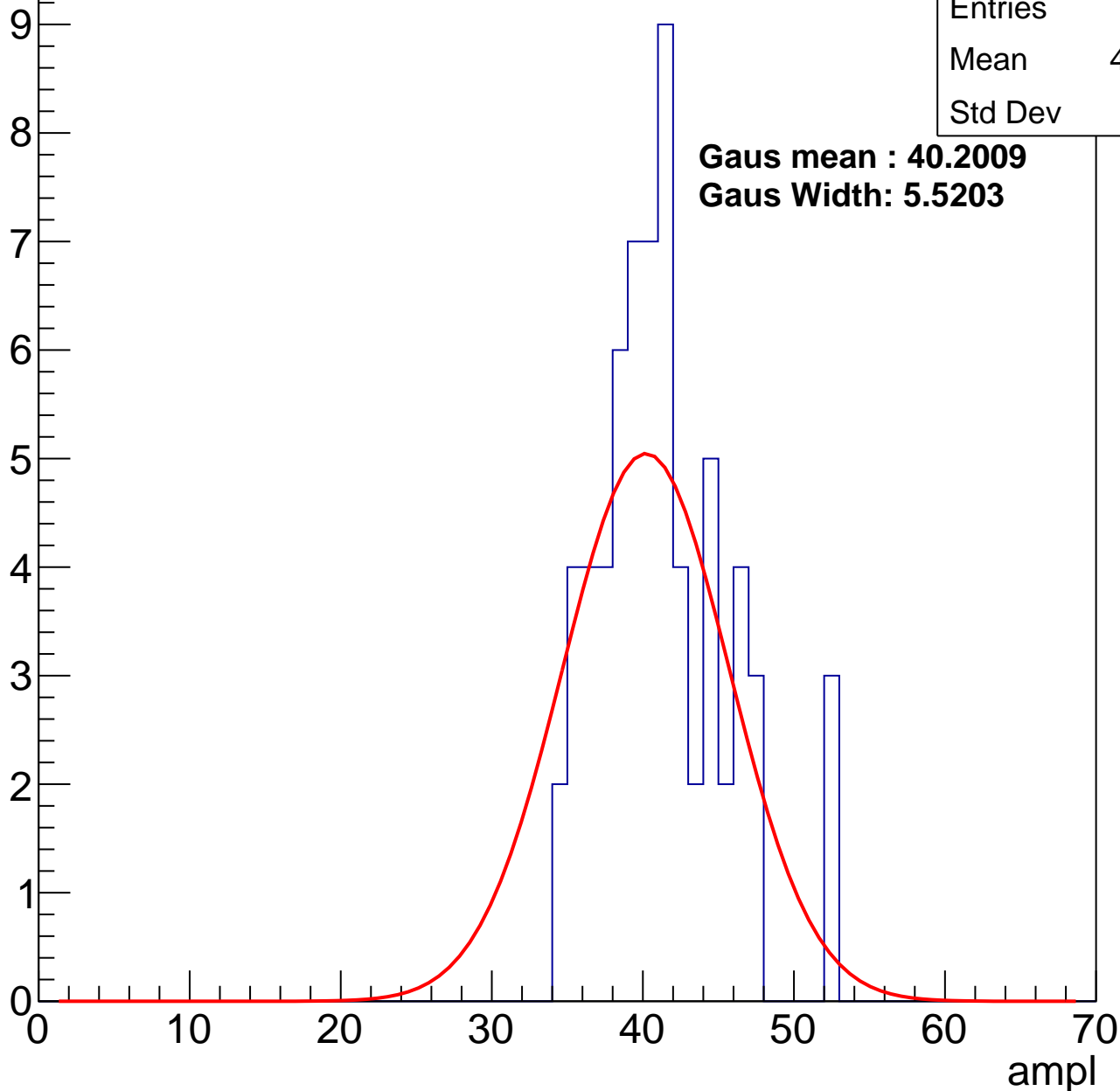
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.83
Std Dev	4.18

Gaus mean : 40.2009

Gaus Width: 5.5203

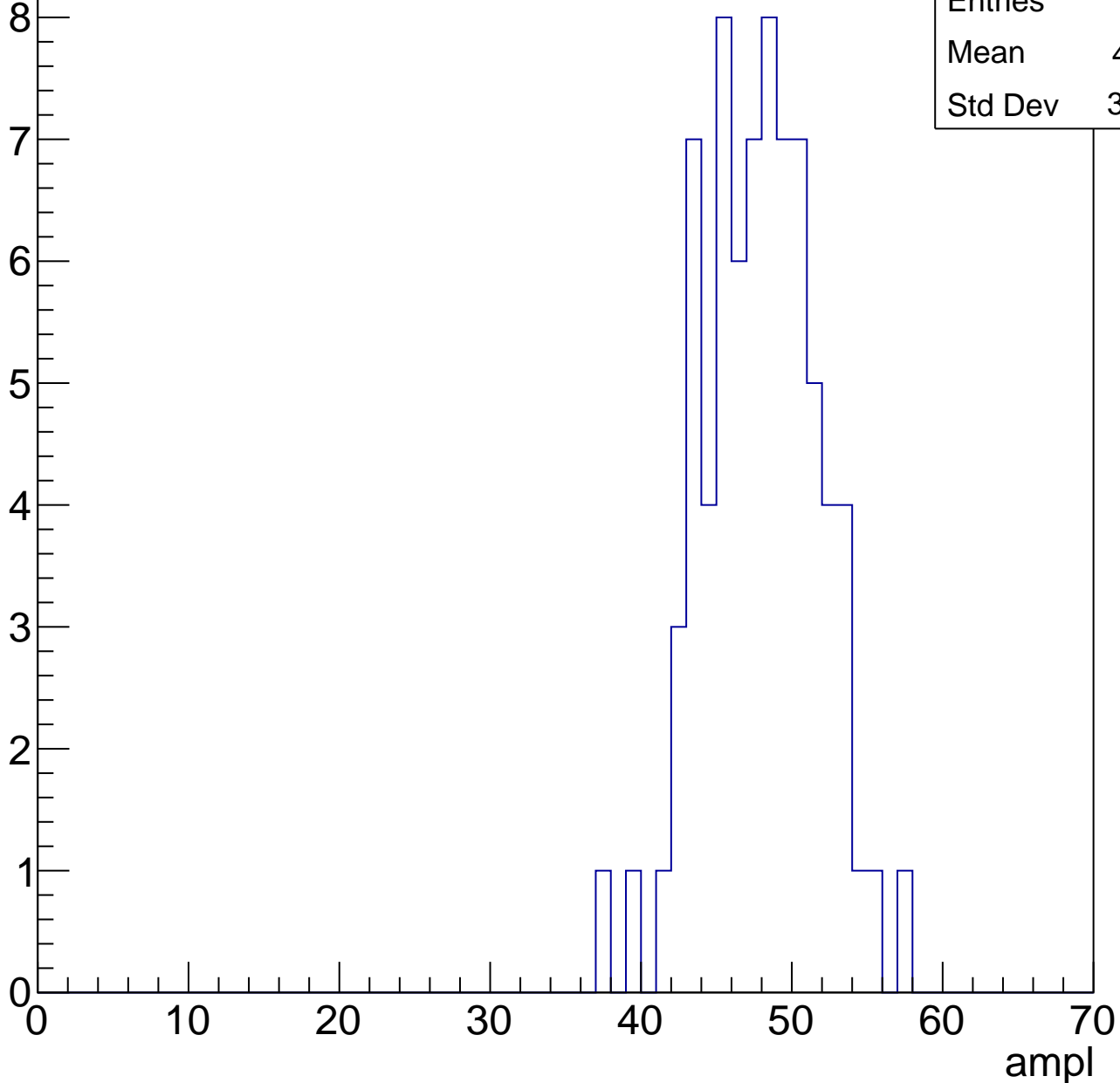


B1L102S, U11-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

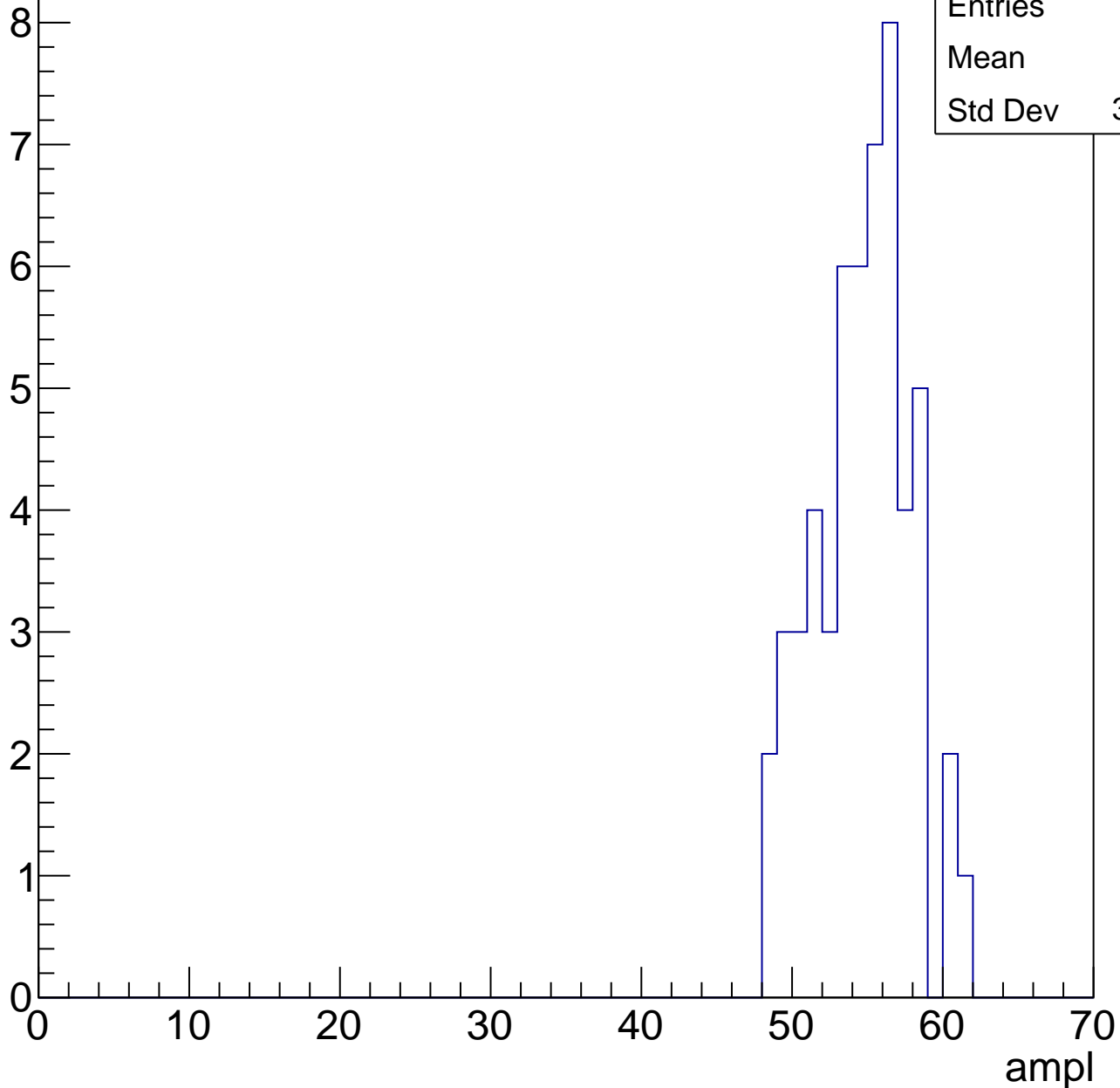
Entries	76
Mean	47.41
Std Dev	3.777



B1L102S, U11-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



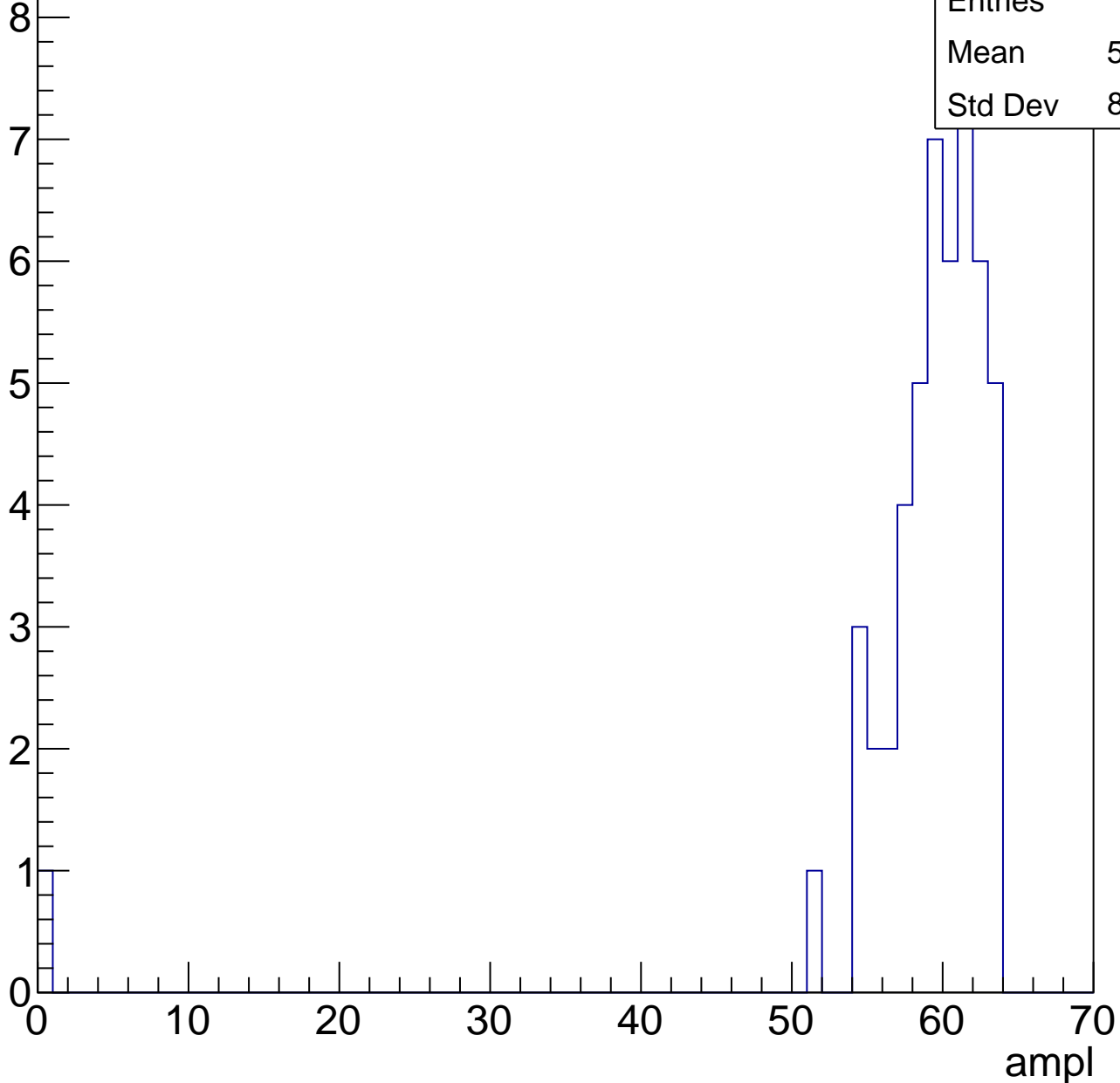
Entries	54
Mean	54.2
Std Dev	3.111

B1L102S, U11-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.02
Std Dev	8.735

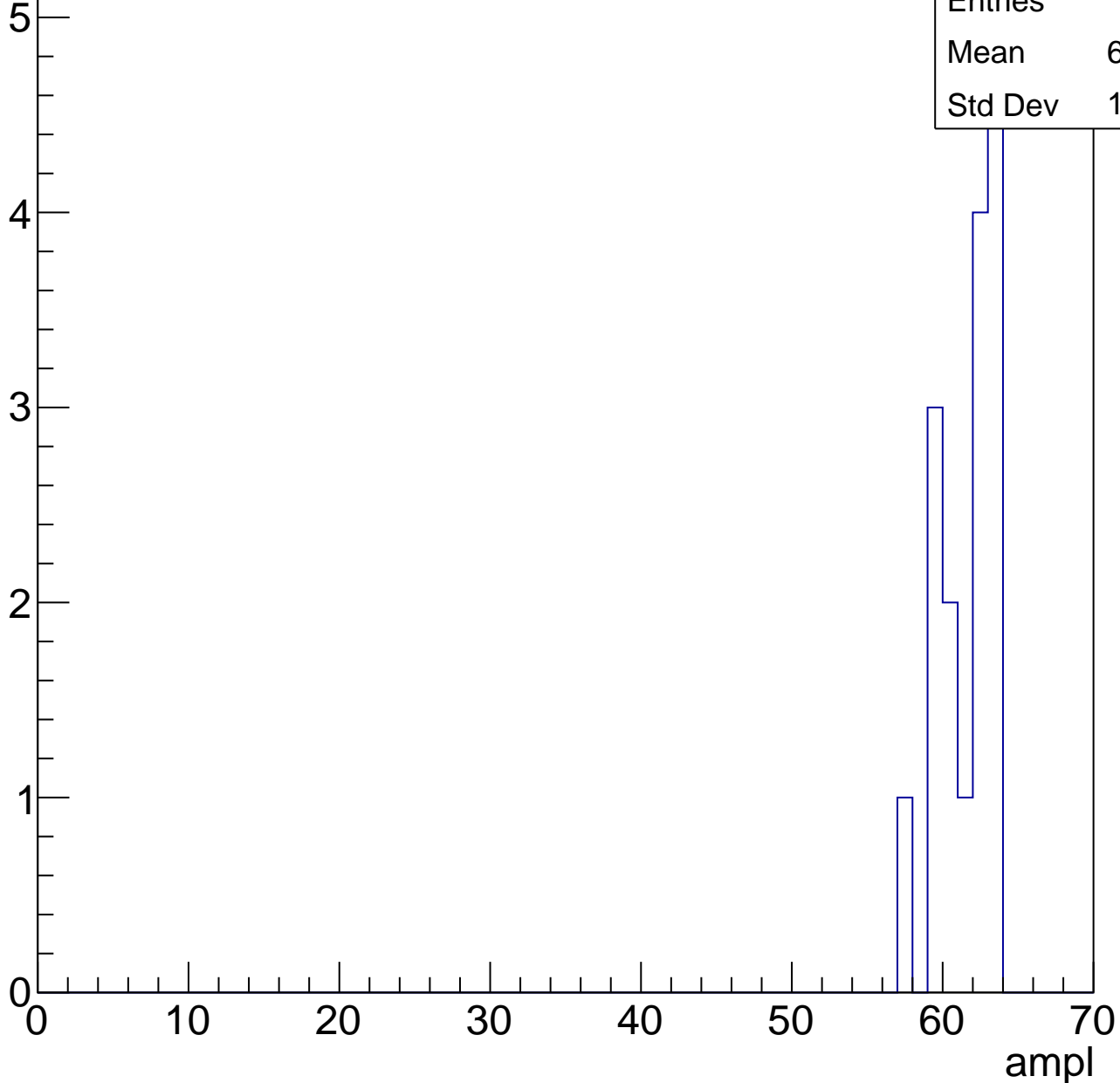


B1L102S, U11-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.12
Std Dev	1.833



B1L102S, U11-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch121, adc0

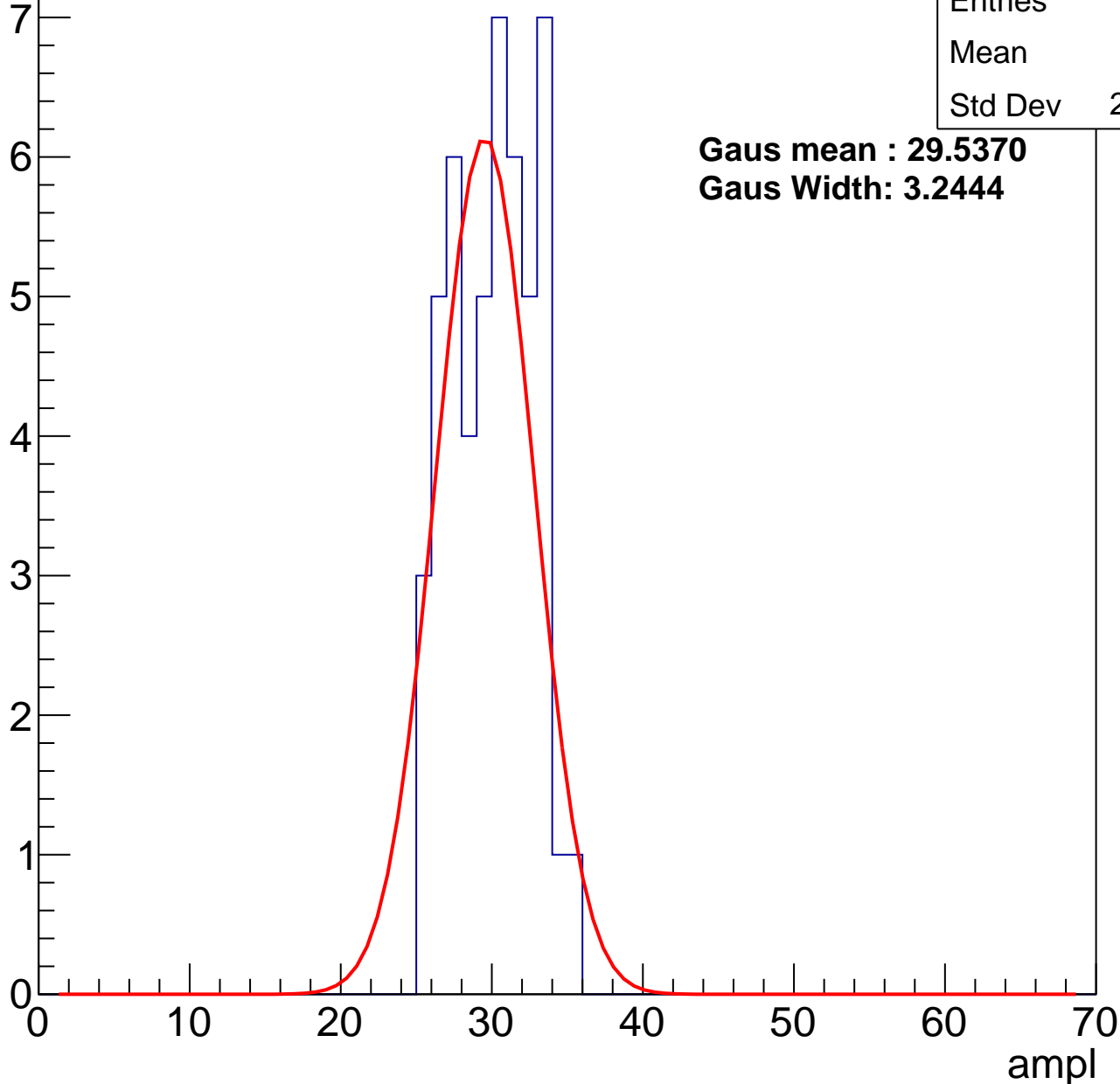
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	29.6
Std Dev	2.653

Gaus mean : 29.5370

Gaus Width: 3.2444



B1L102S, U11-ch121, adc1

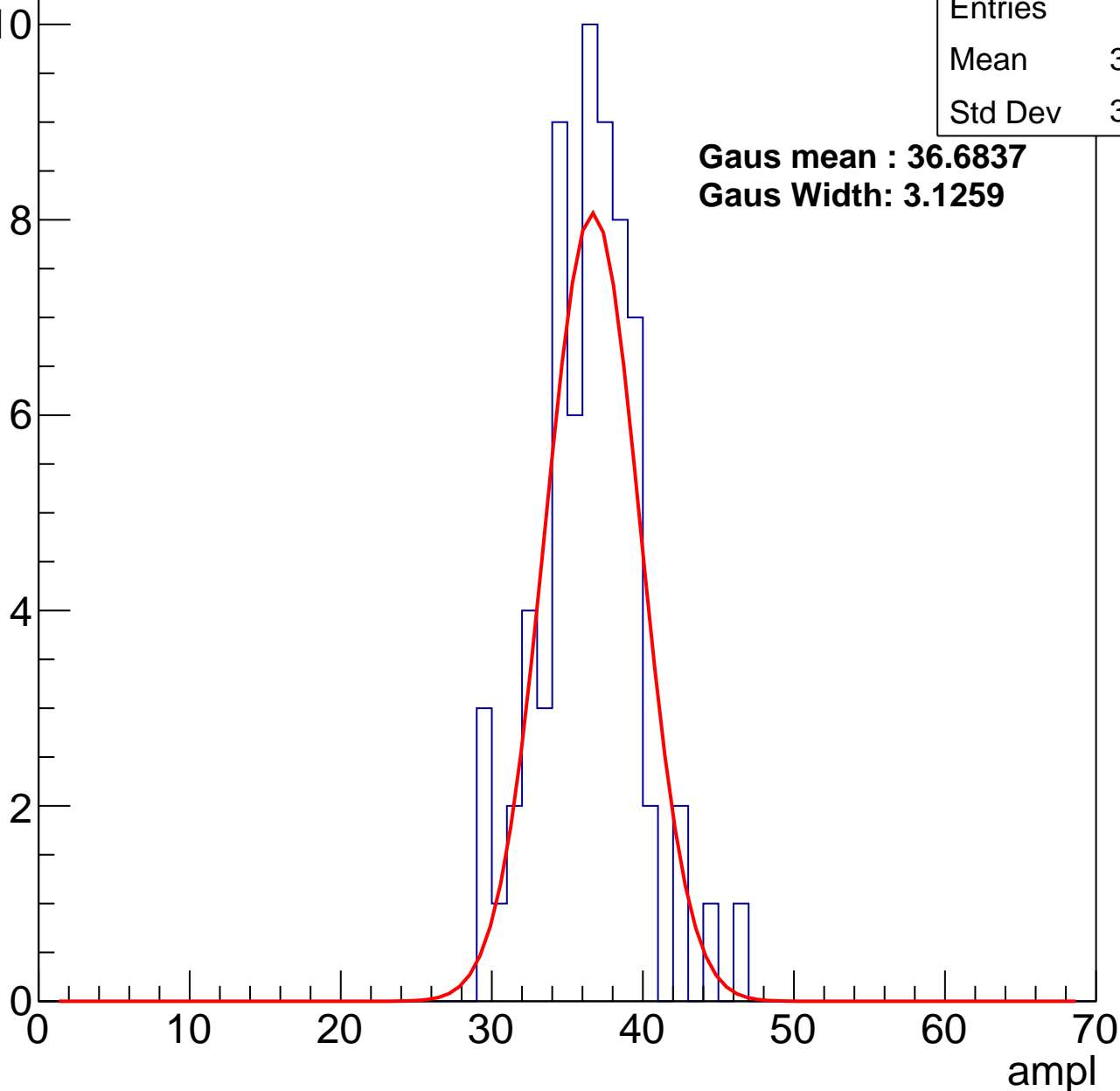
calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	35.97
Std Dev	3.294

Gaus mean : 36.6837

Gaus Width: 3.1259

Entry



B1L102S, U11-ch121, adc2

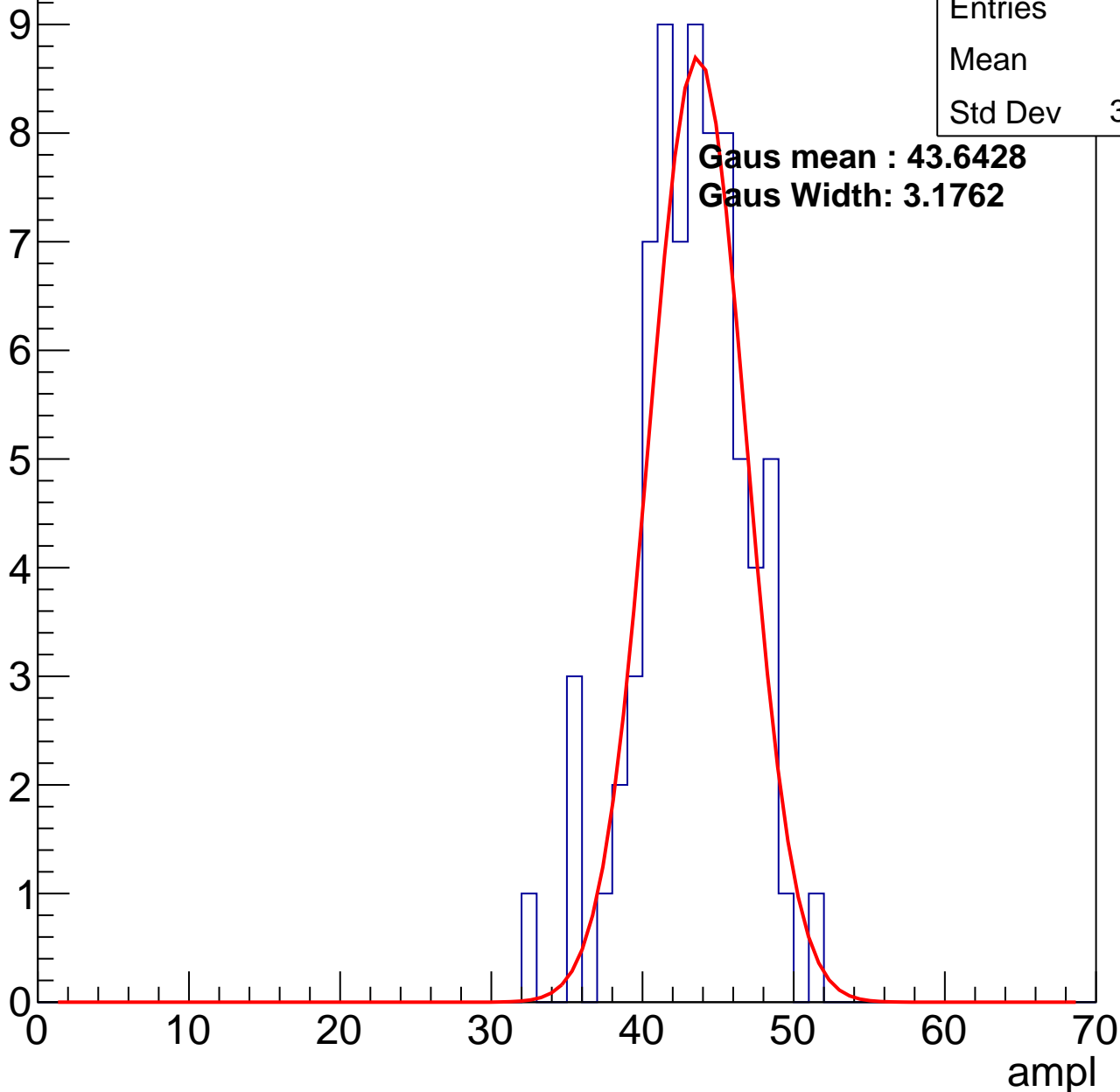
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.8
Std Dev	3.534

Gaus mean : 43.6428

Gaus Width: 3.1762

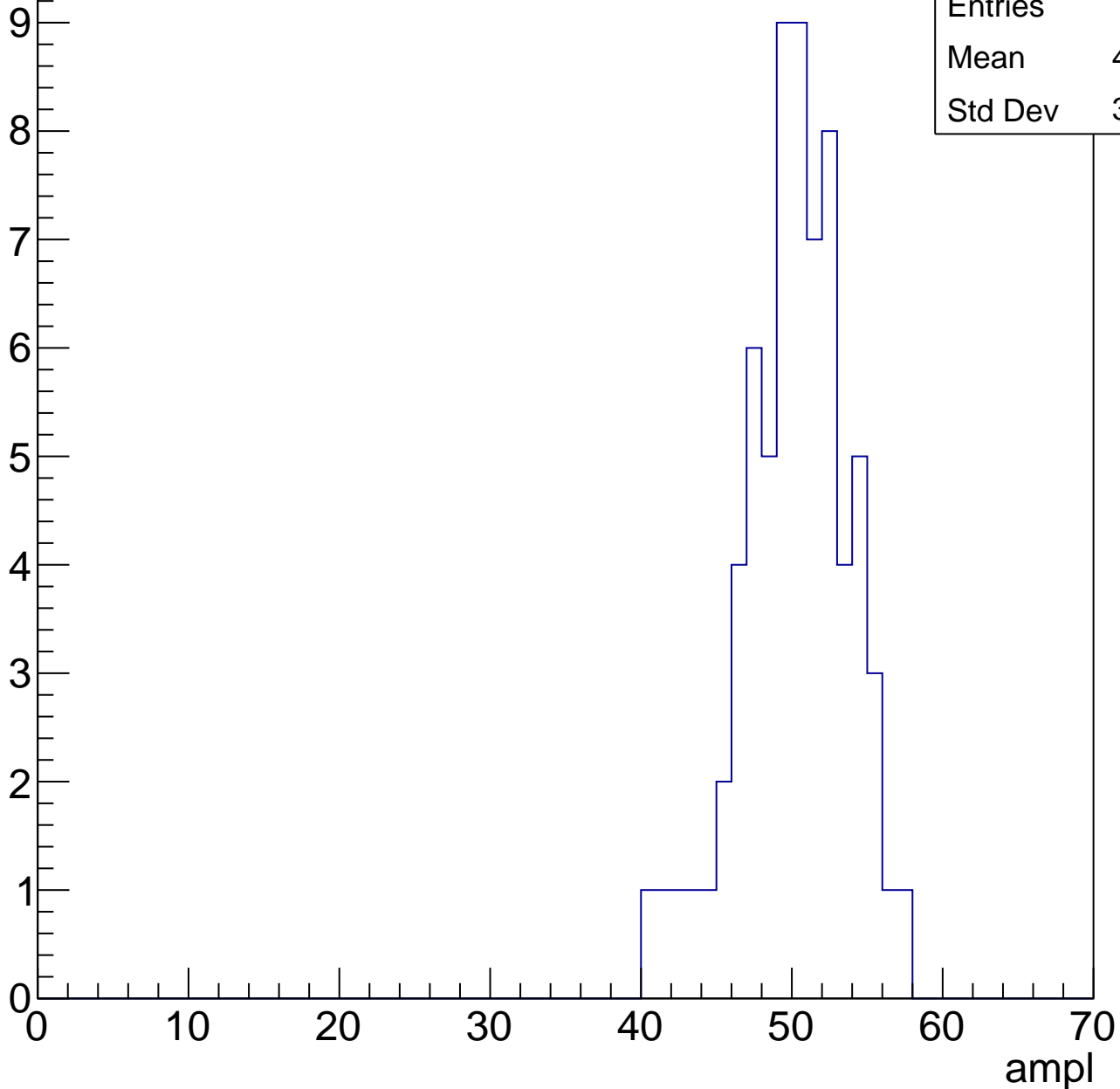


B1L102S, U11-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.71
Std Dev	3.481

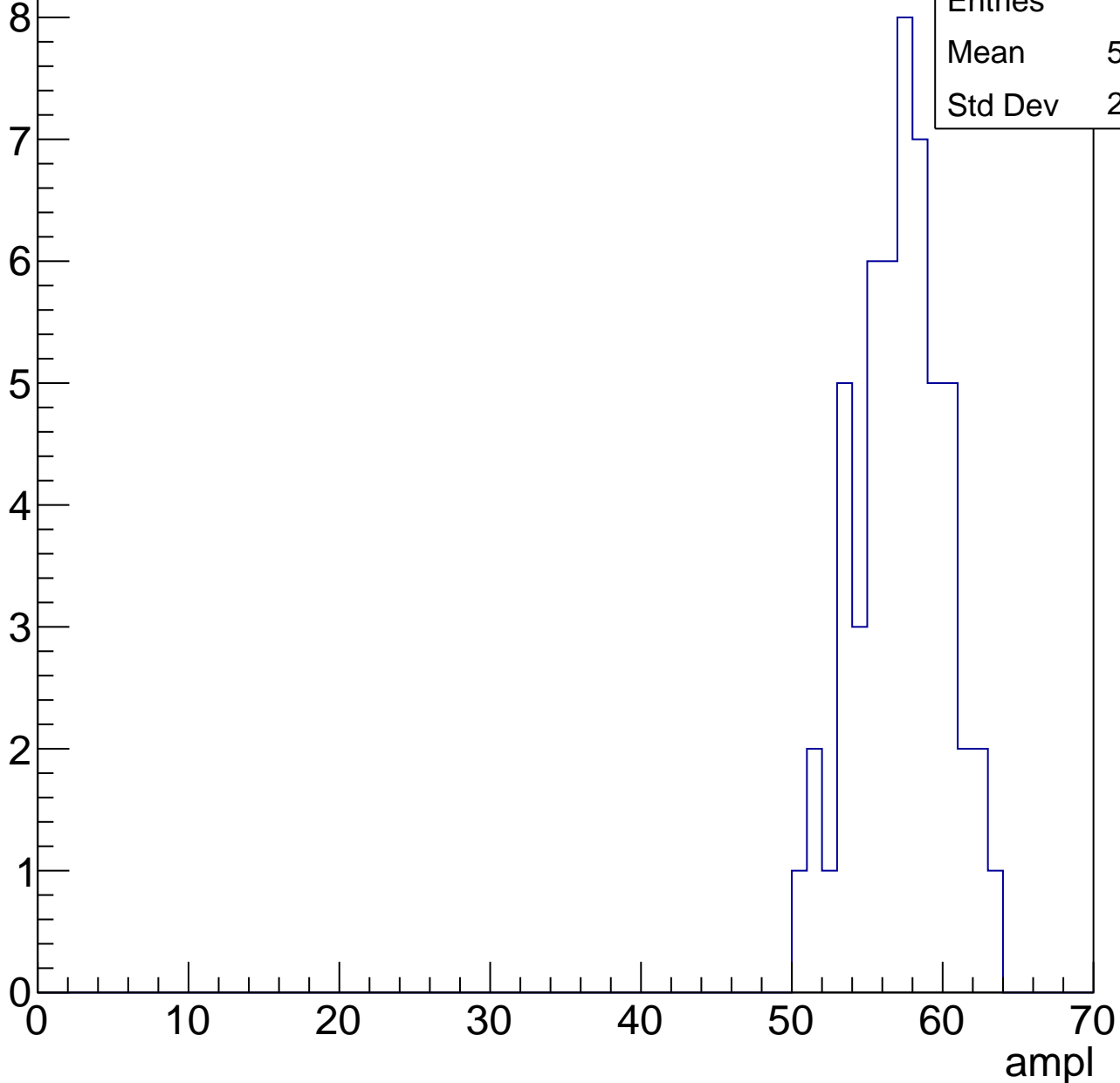


B1L102S, U11-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.72
Std Dev	2.947

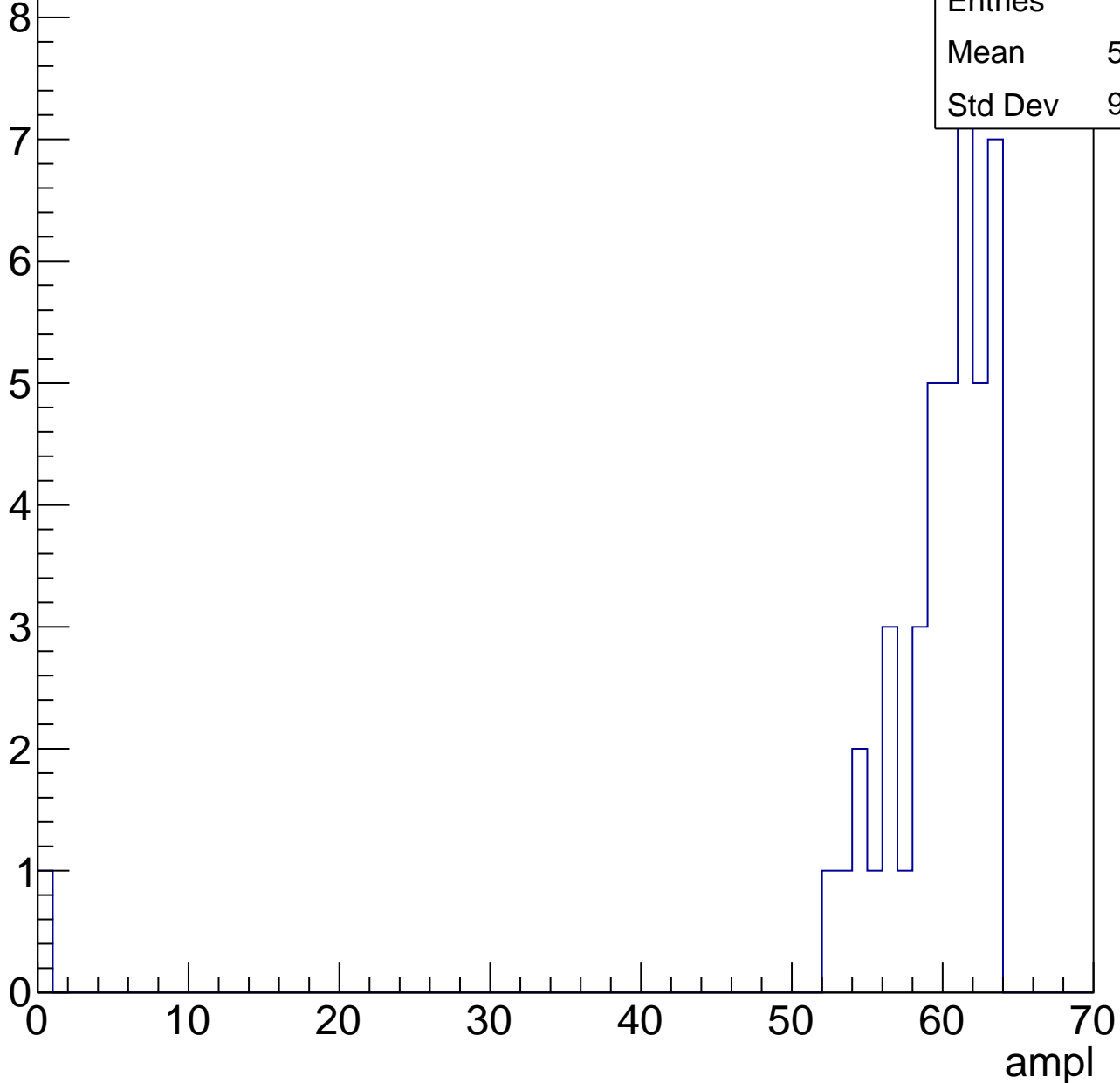


B1L102S, U11-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

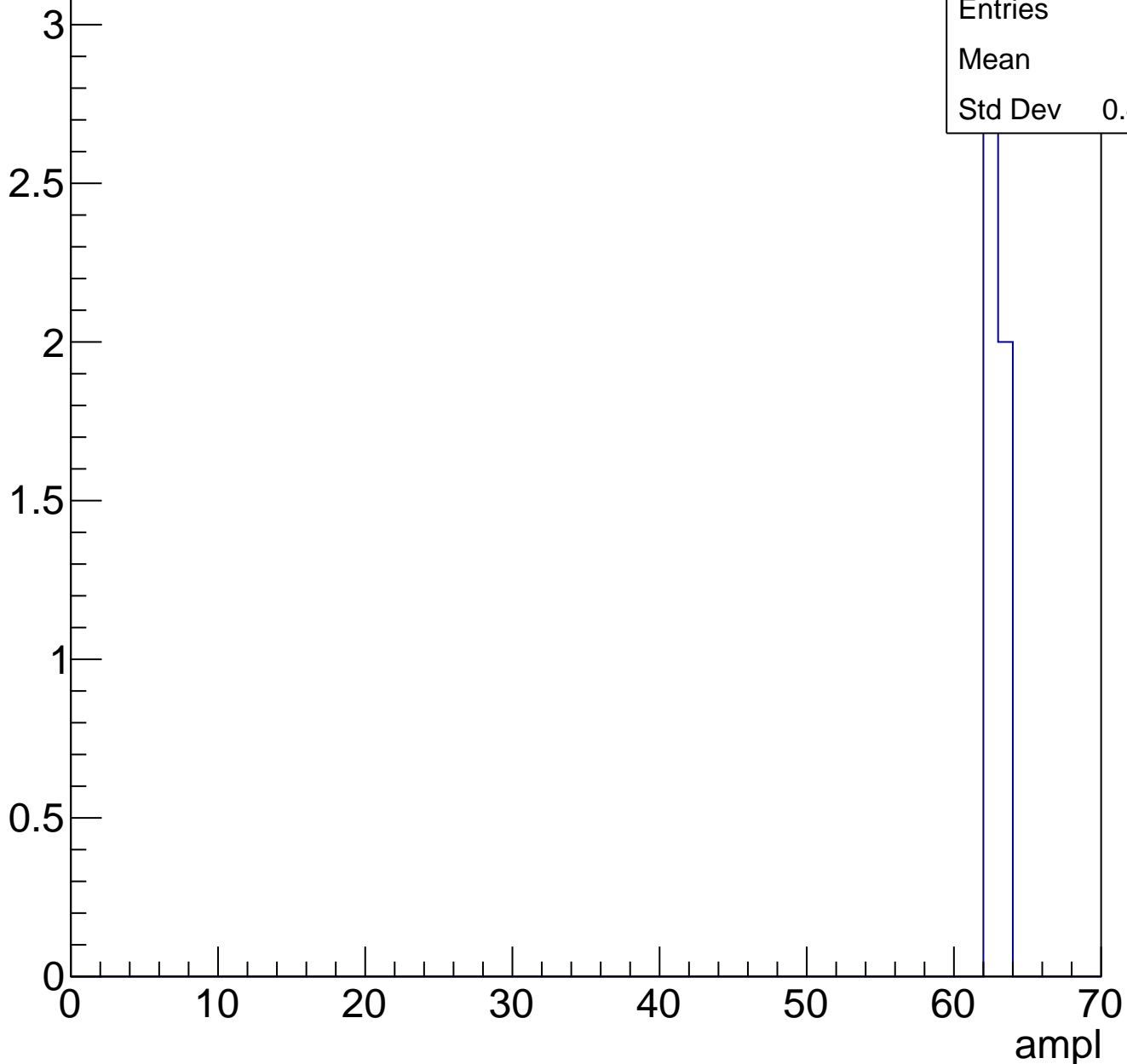
Entries	43
Mean	58.16
Std Dev	9.438



B1L102S, U11-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L102S, U11-ch122, adc0

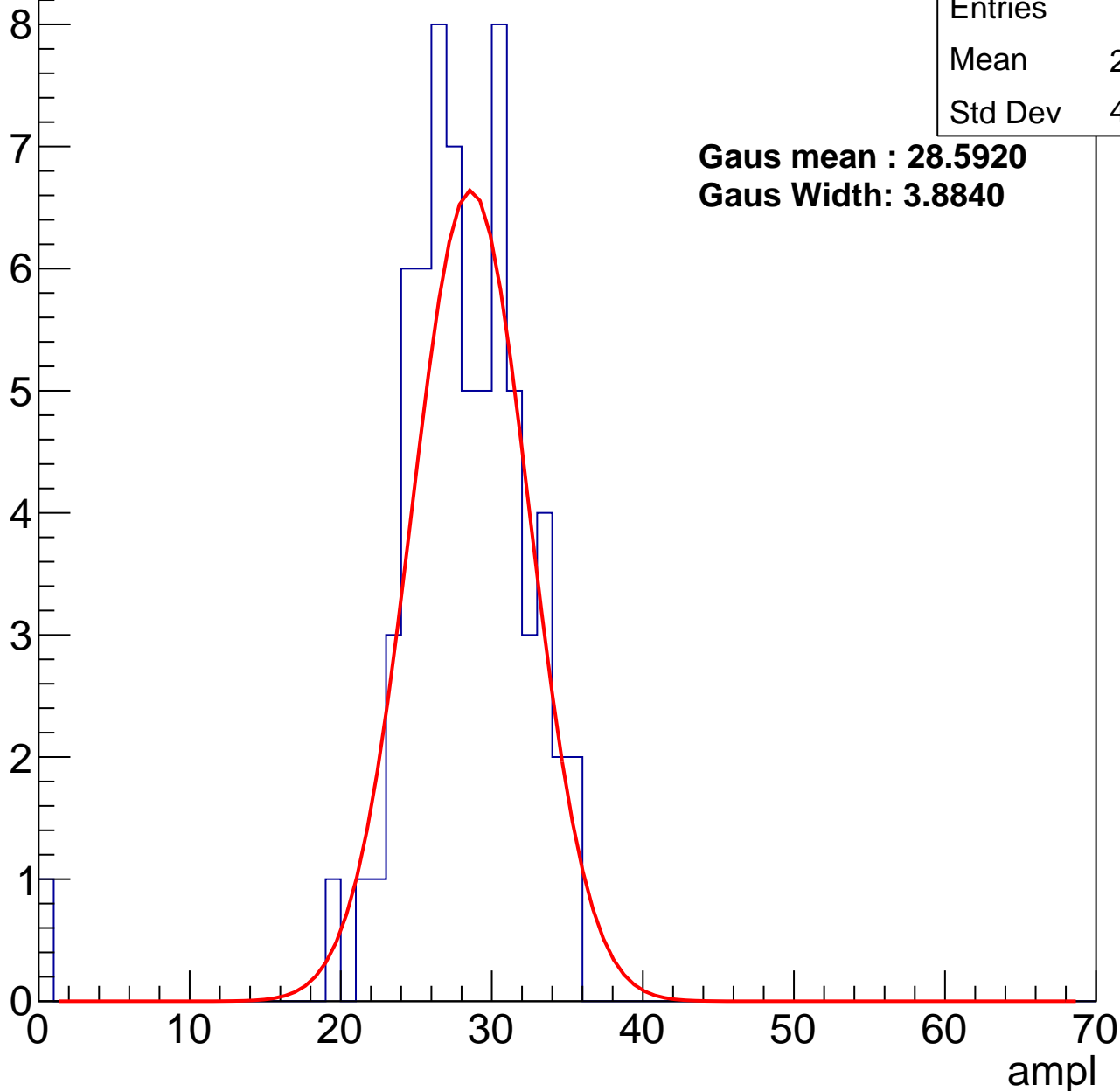
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.47
Std Dev	4.843

Gaus mean : 28.5920

Gaus Width: 3.8840



B1L102S, U11-ch122, adc1

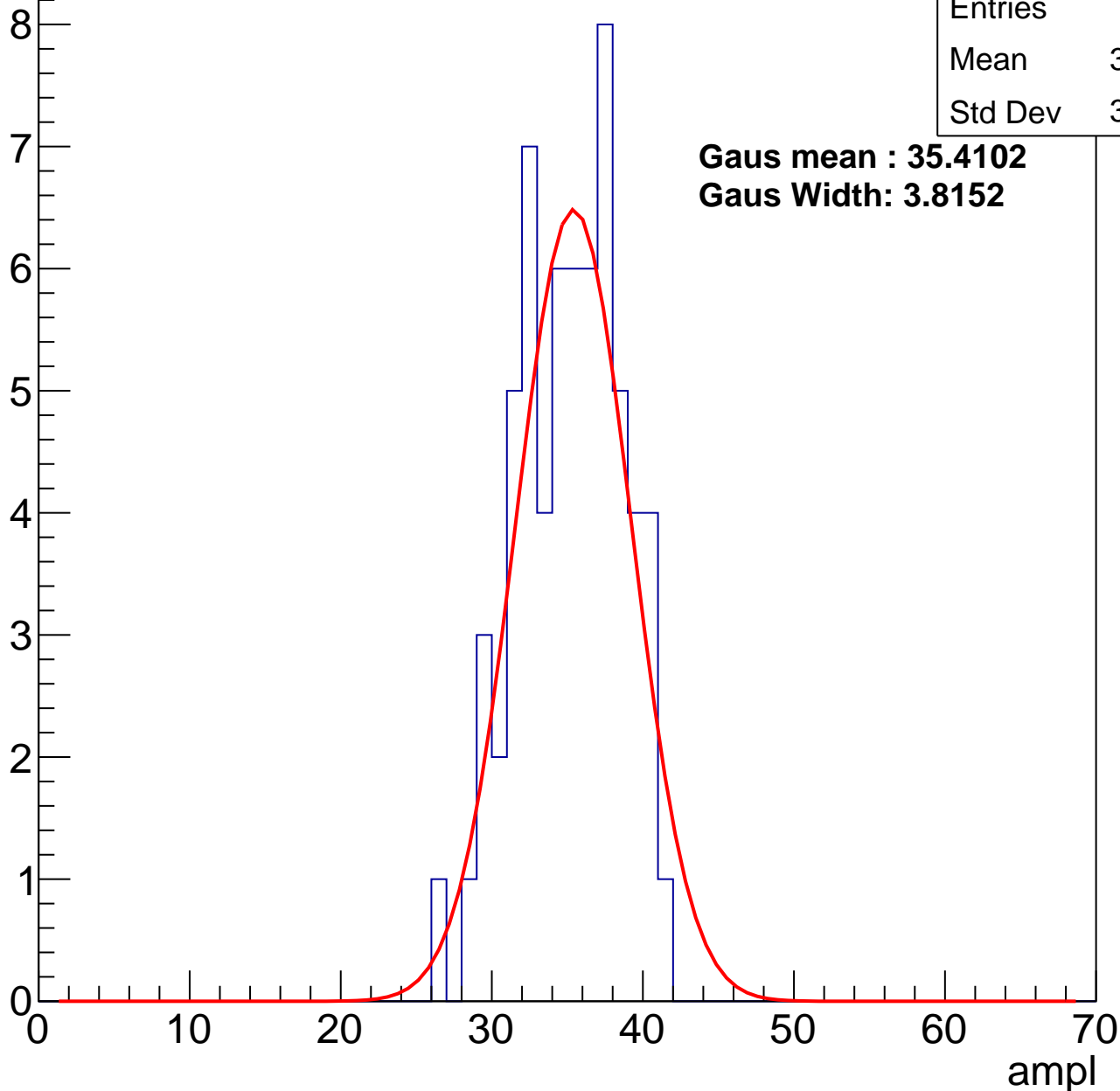
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.68
Std Dev	3.403

Gaus mean : 35.4102

Gaus Width: 3.8152



B1L102S, U11-ch122, adc2

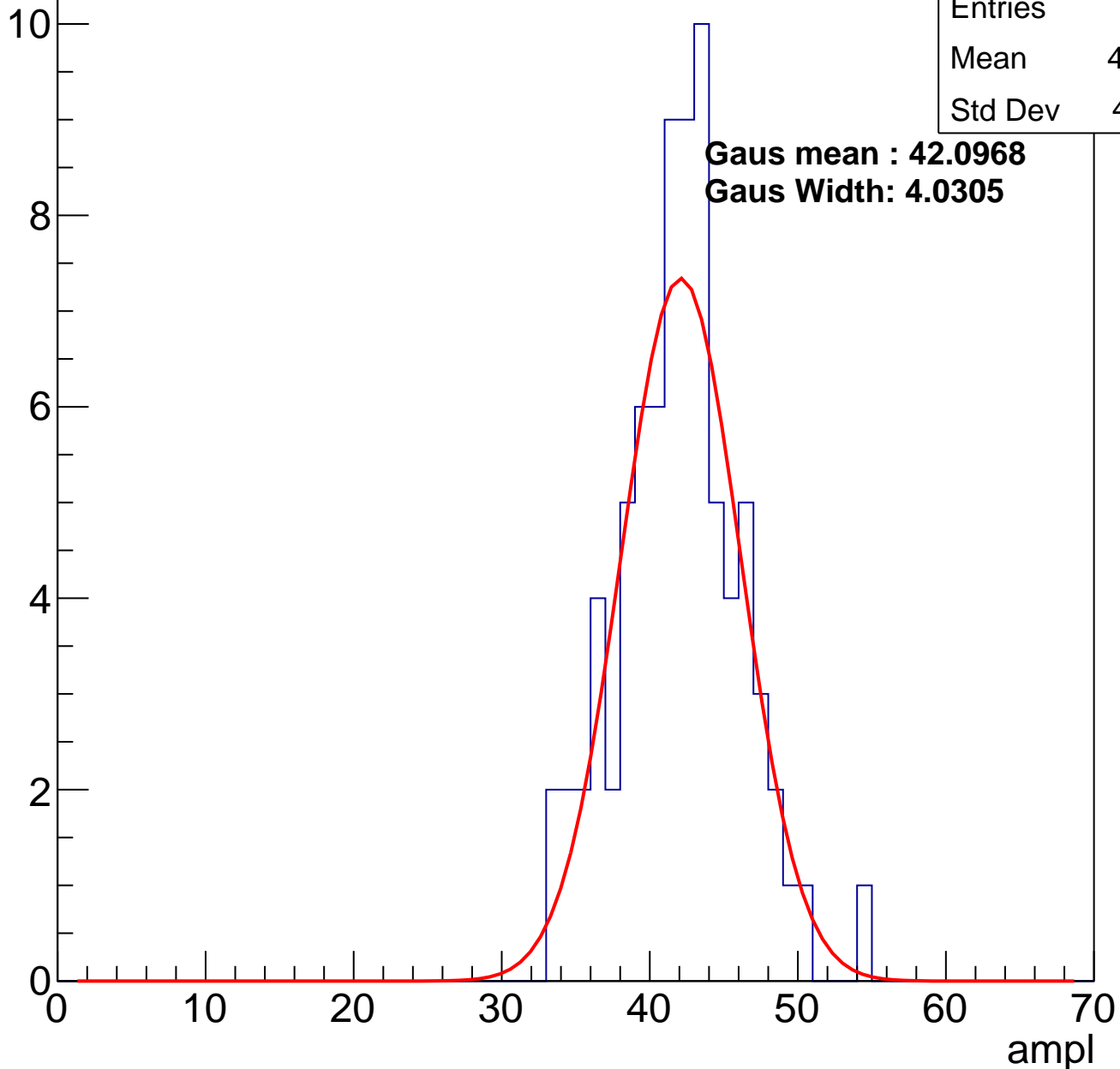
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	41.56
Std Dev	4.031

Gaus mean : 42.0968

Gaus Width: 4.0305

Entry

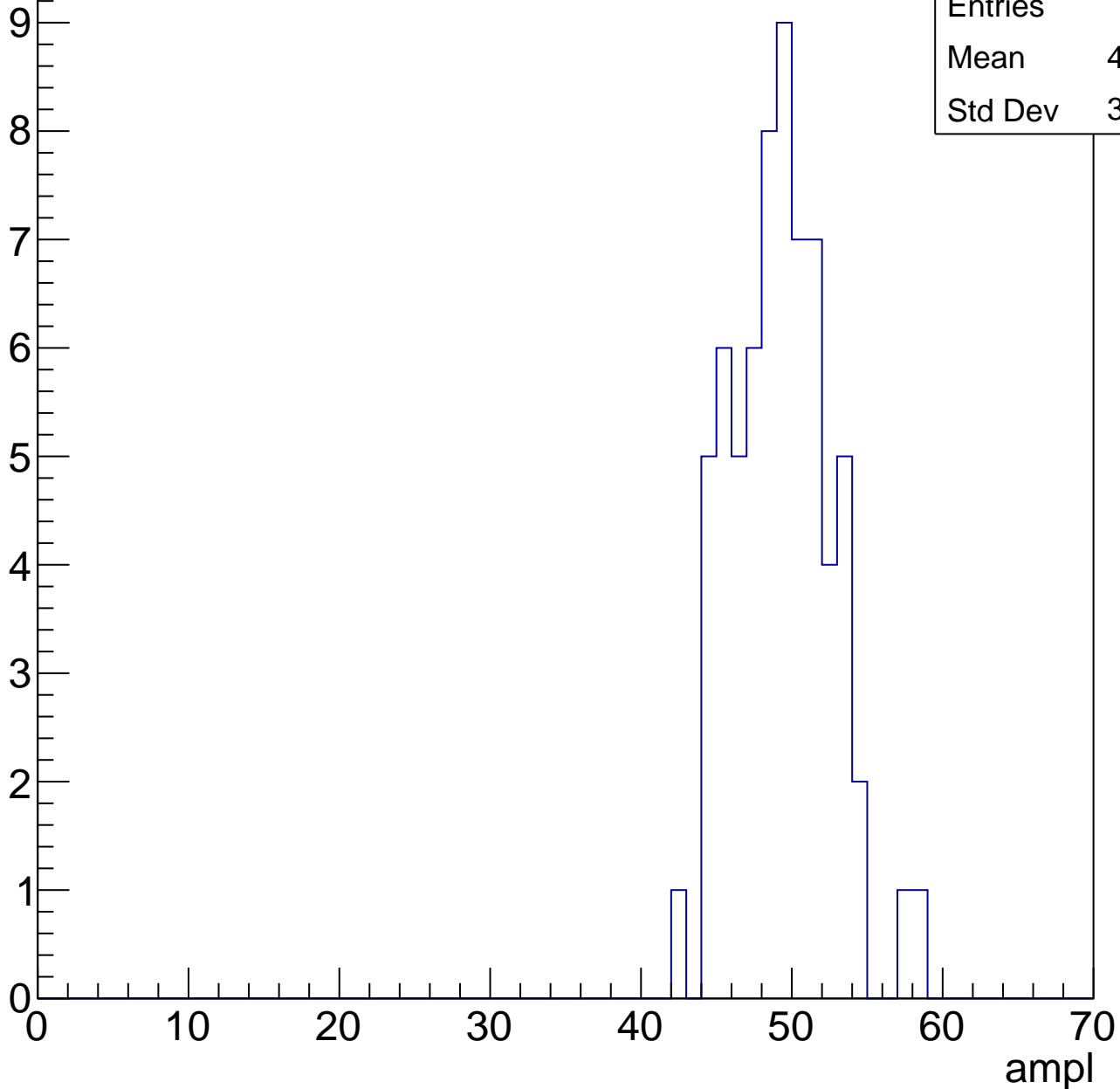


B1L102S, U11-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.84
Std Dev	3.212

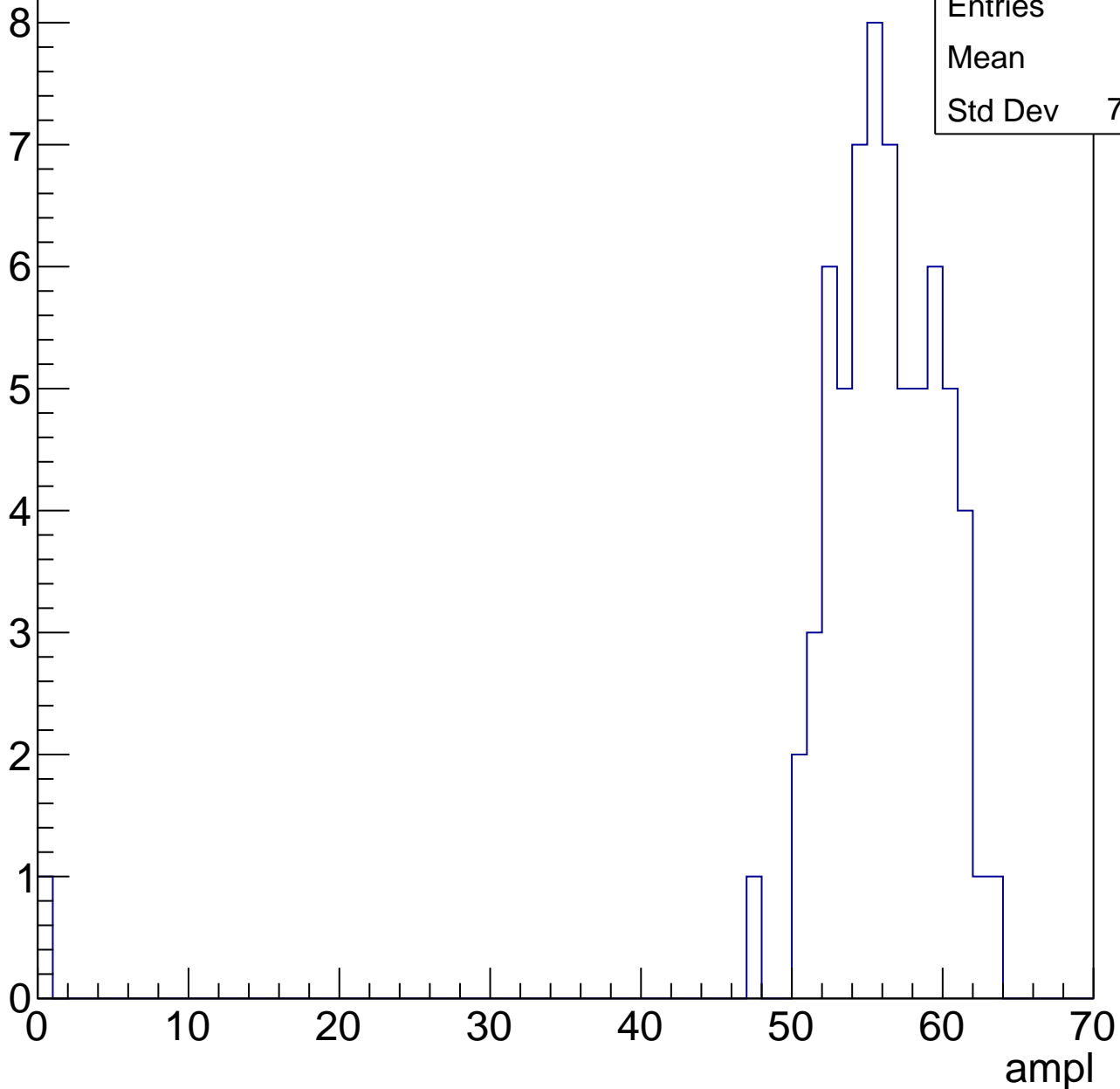


B1L102S, U11-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55
Std Dev	7.549

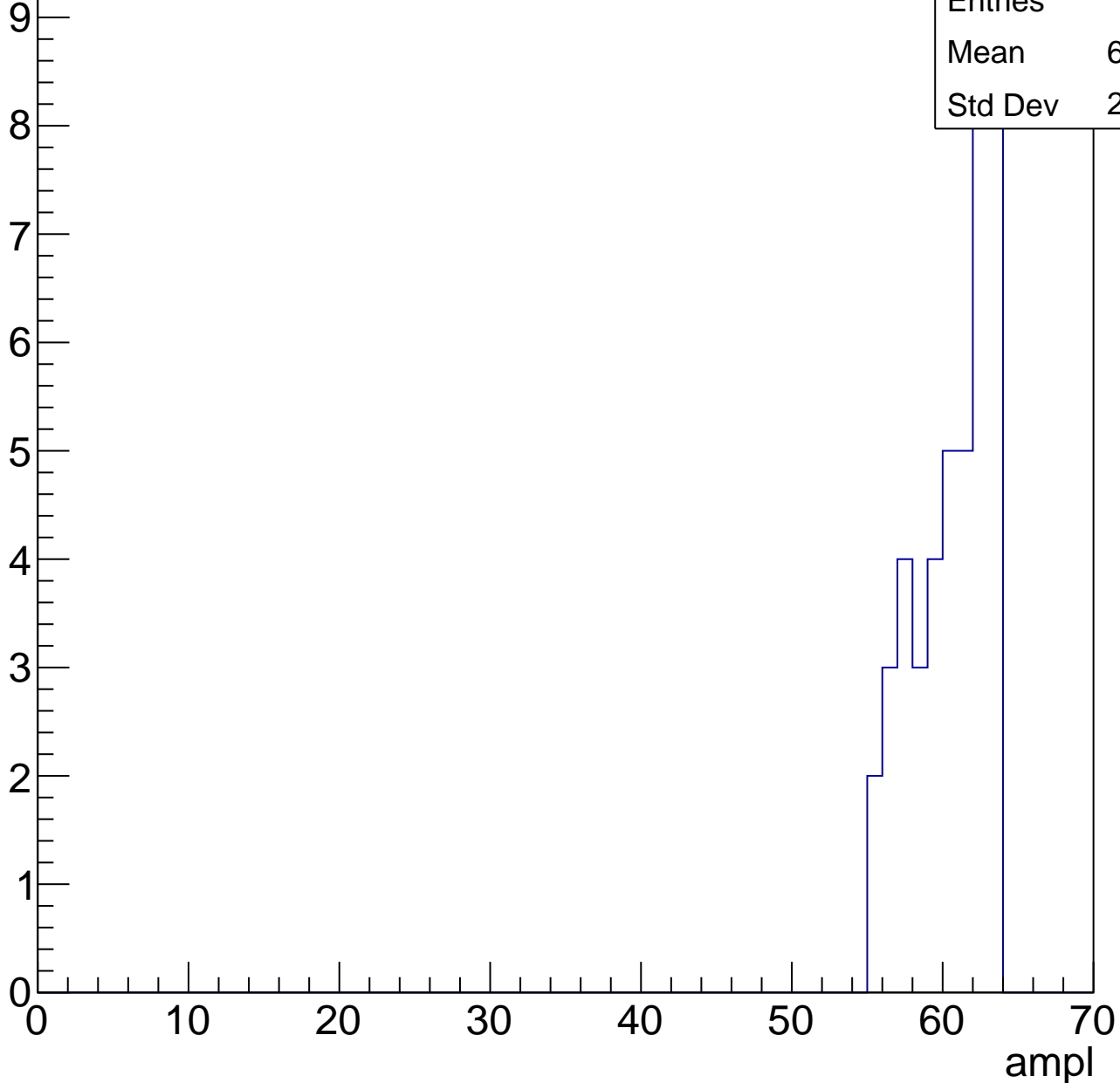


B1L102S, U11-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

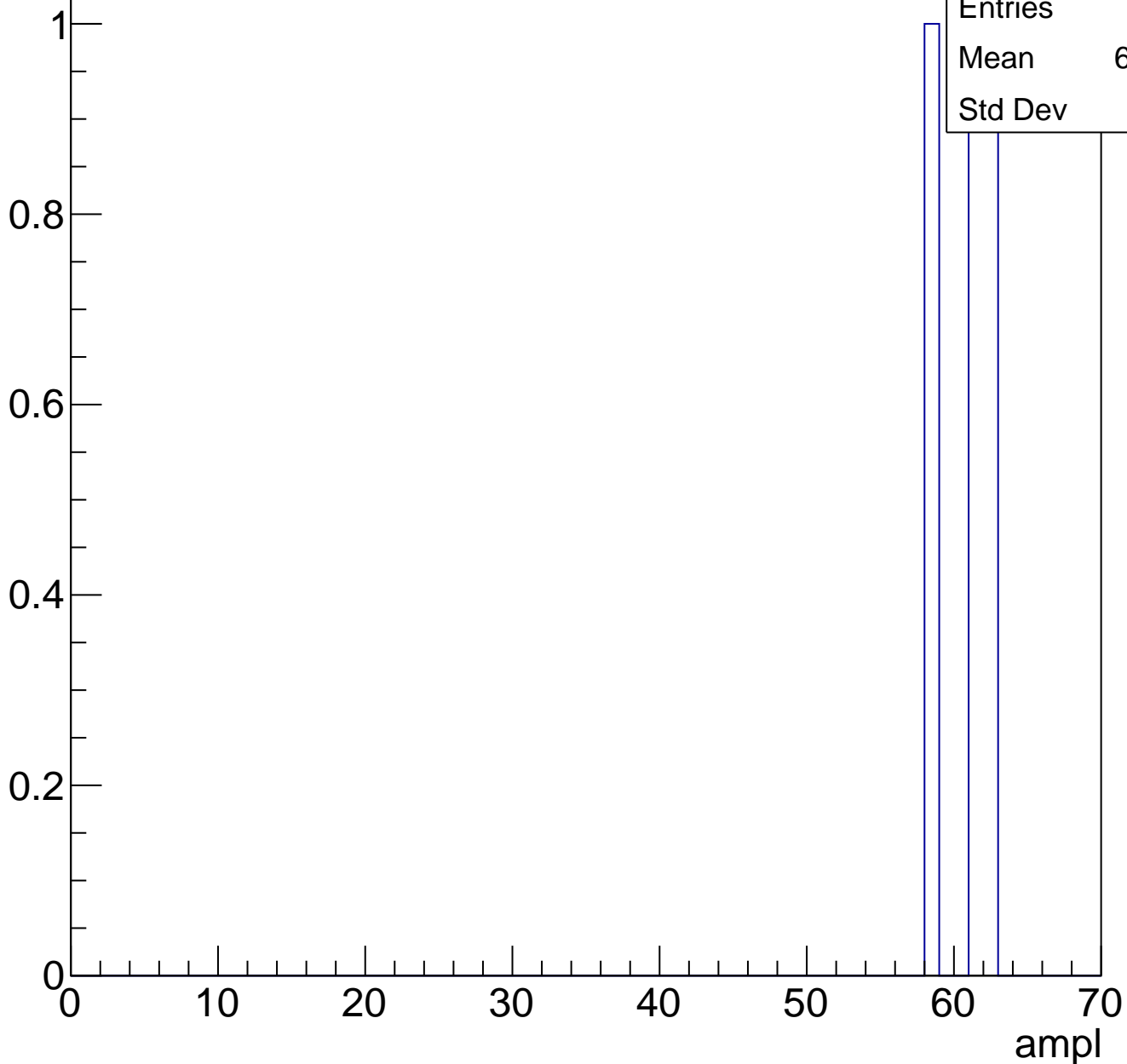
Entries	43
Mean	60.09
Std Dev	2.495



B1L102S, U11-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch123, adc0

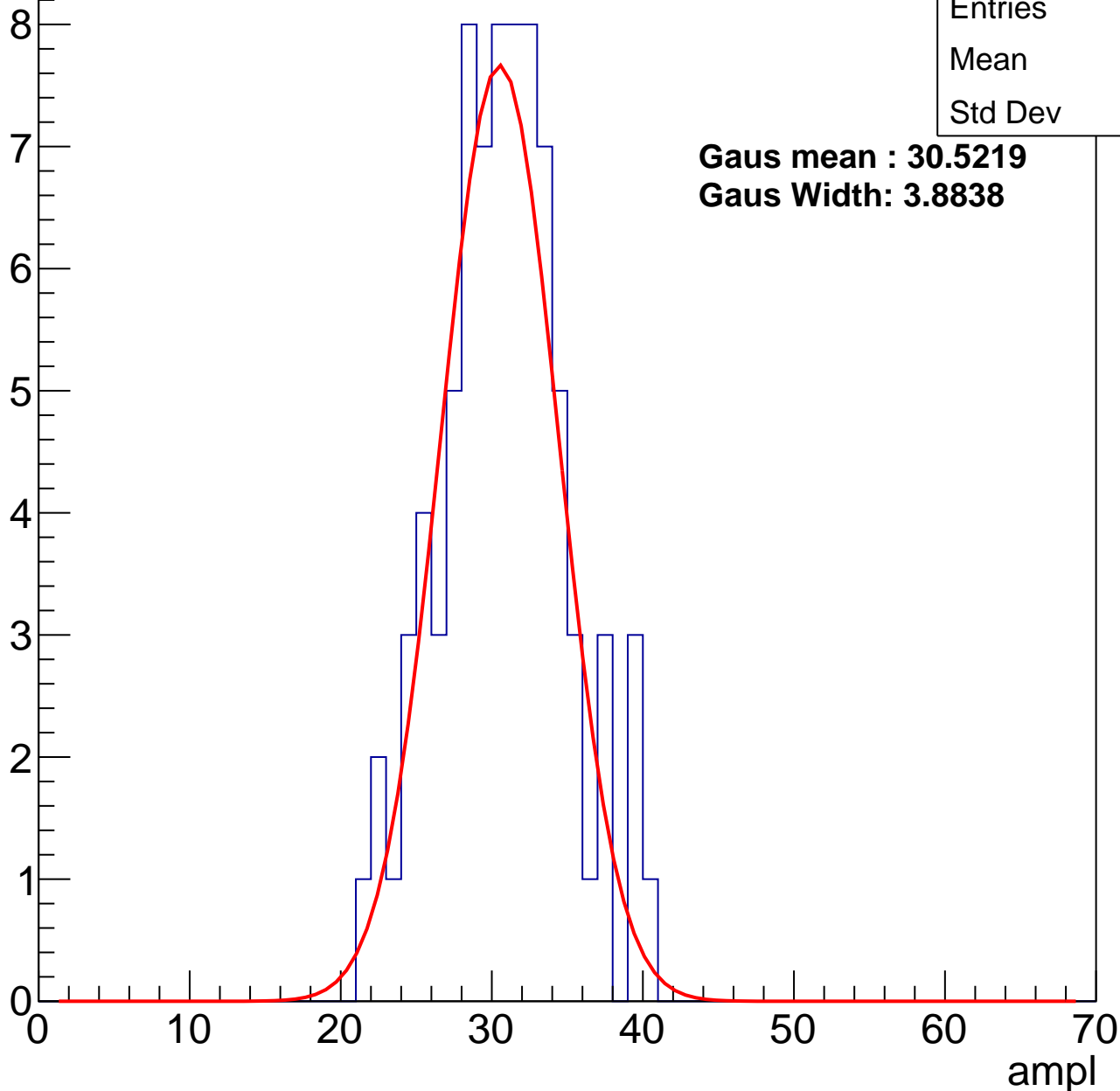
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	30.3
Std Dev	4.12

Gaus mean : 30.5219

Gaus Width: 3.8838



B1L102S, U11-ch123, adc1

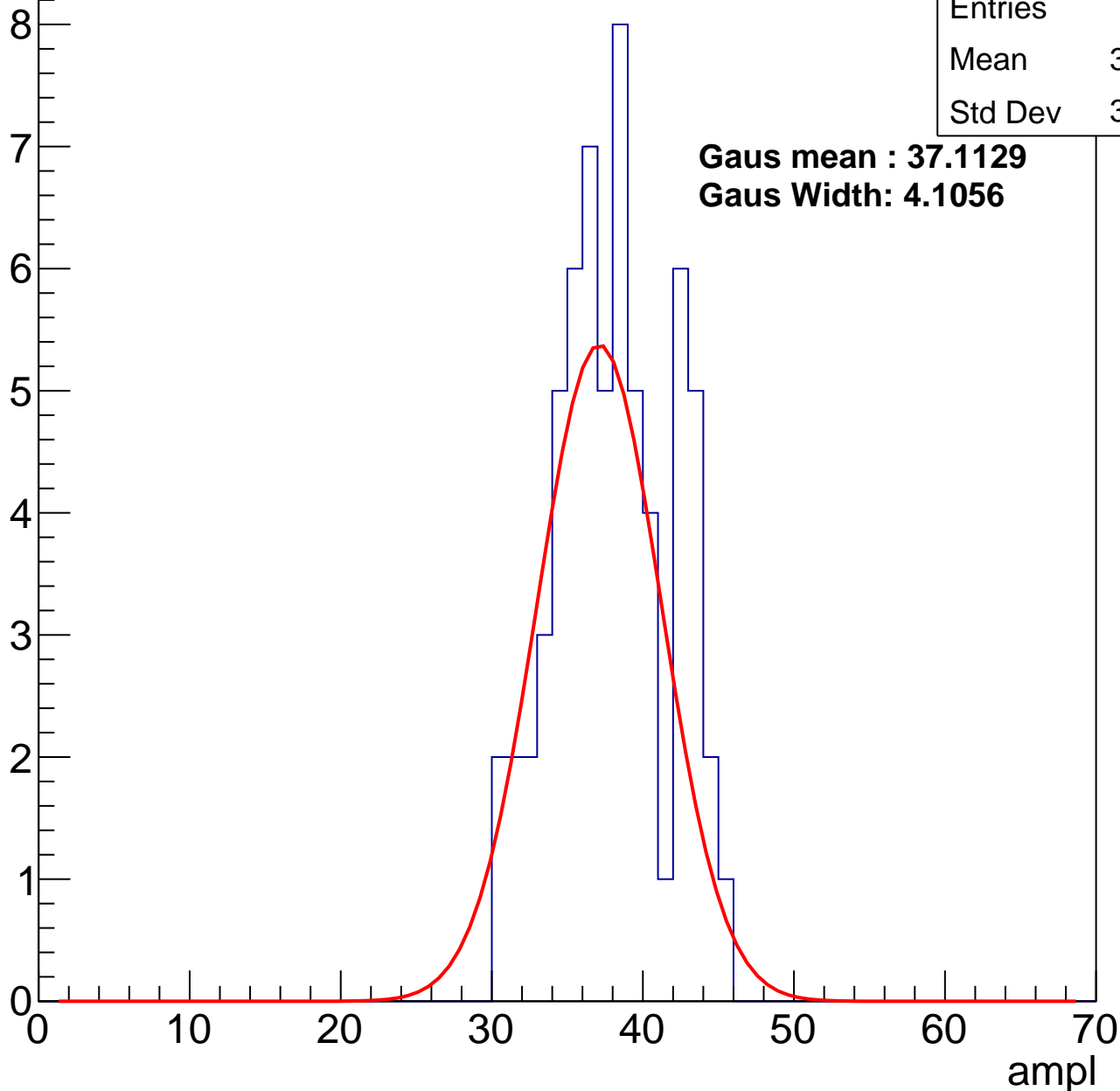
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.53
Std Dev	3.746

Gaus mean : 37.1129

Gaus Width: 4.1056



B1L102S, U11-ch123, adc2

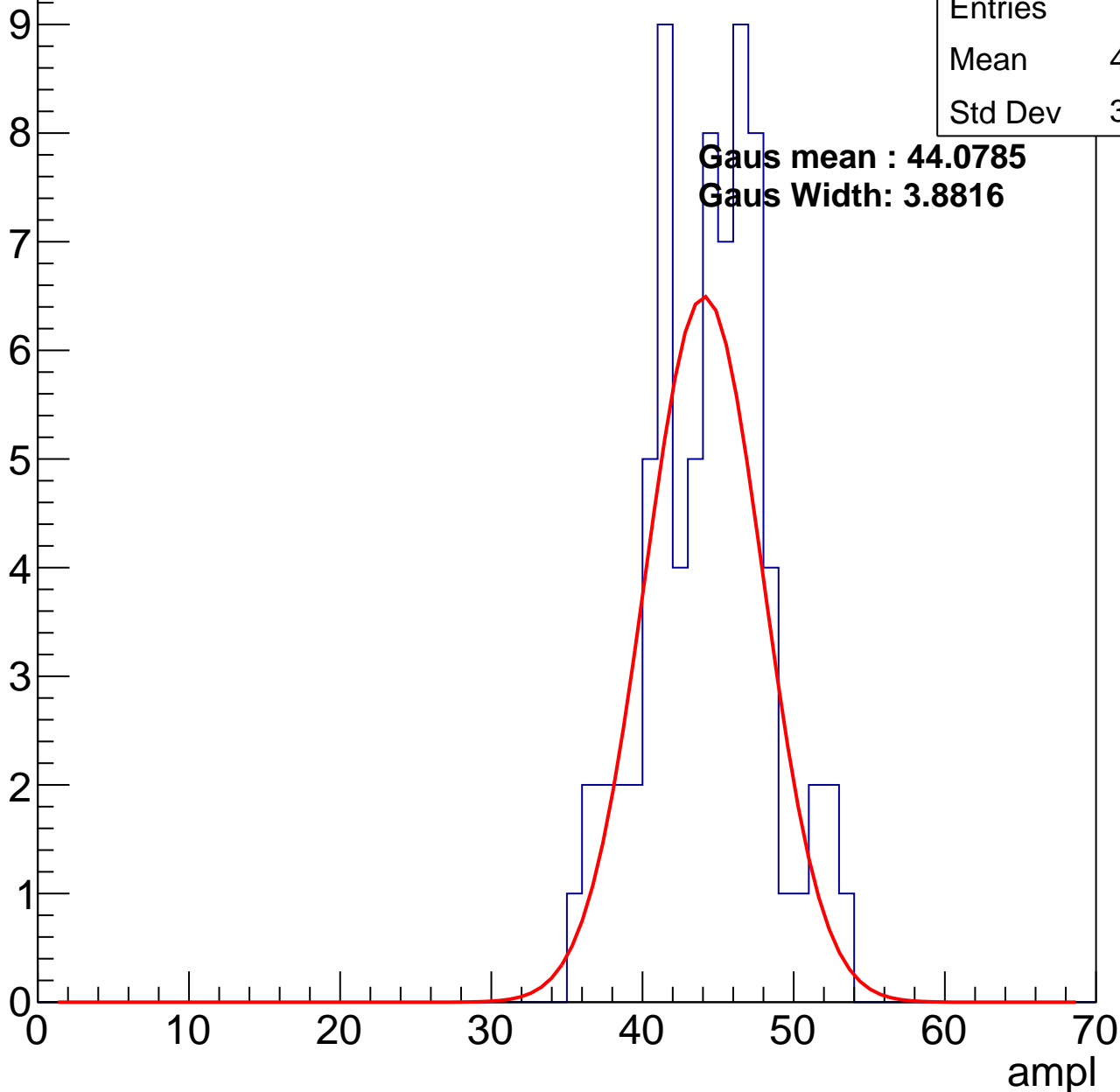
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.92
Std Dev	3.929

Gaus mean : 44.0785

Gaus Width: 3.8816

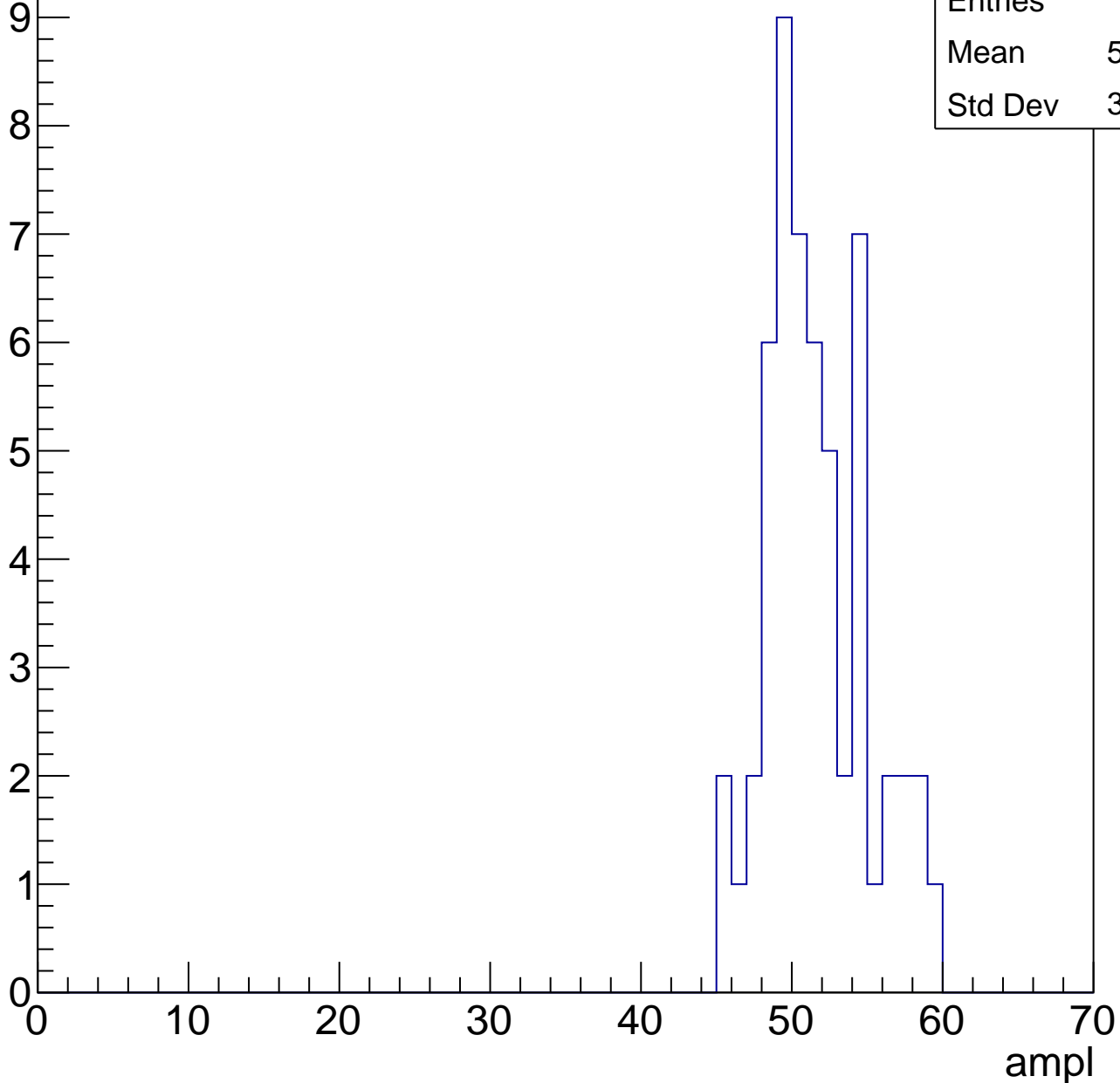


B1L102S, U11-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	51.18
Std Dev	3.303

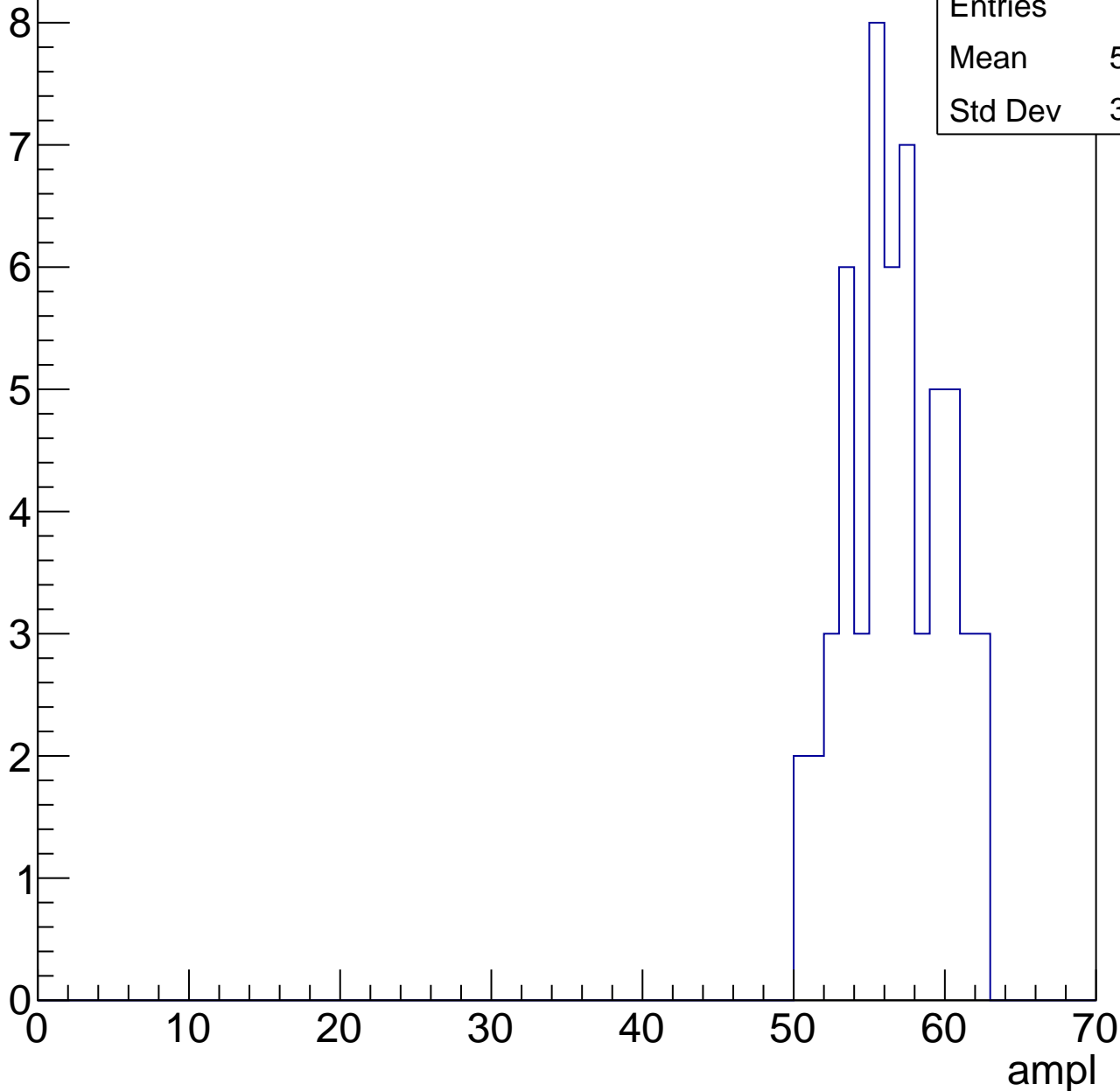


B1L102S, U11-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.27
Std Dev	3.182

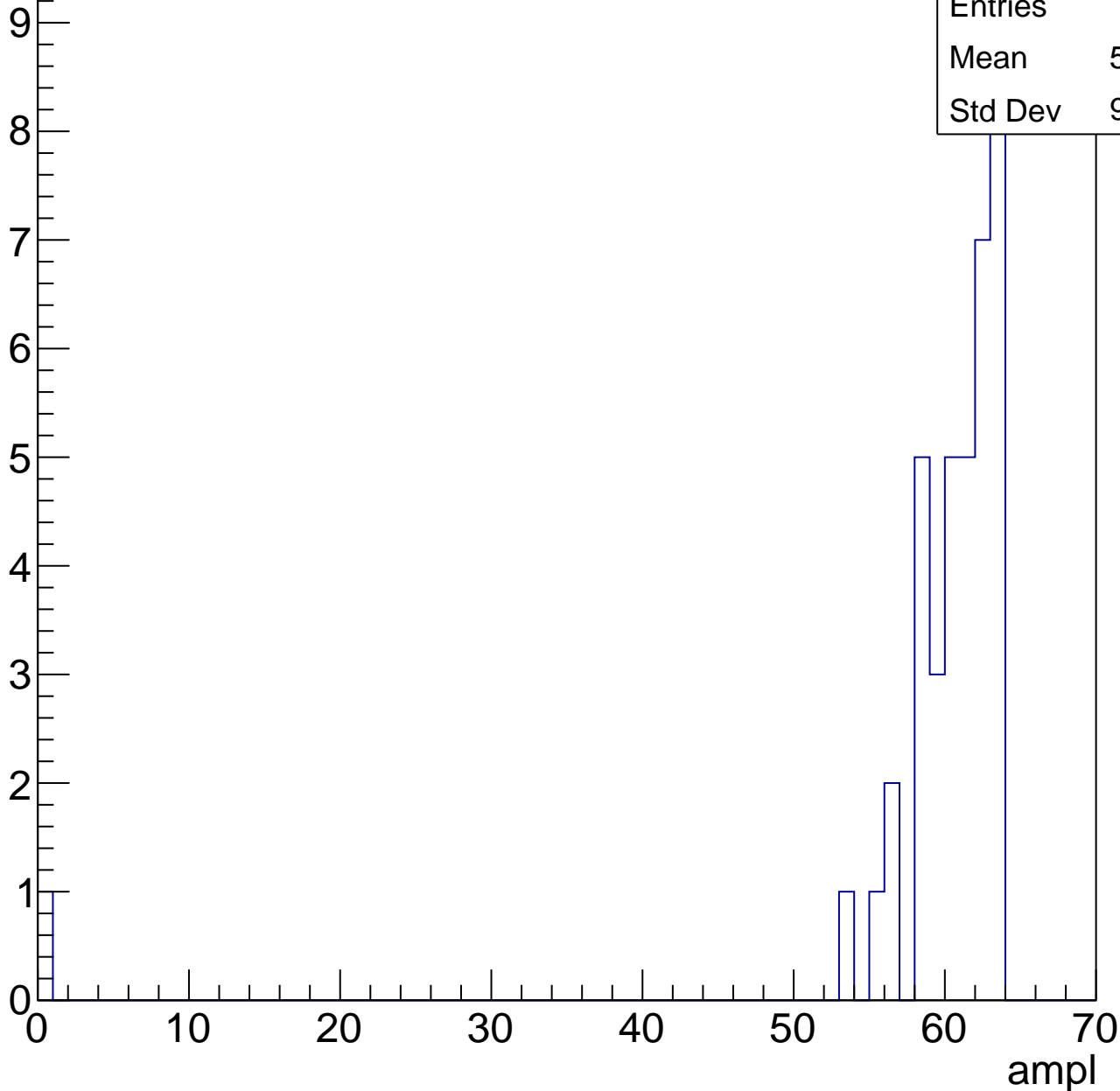


B1L102S, U11-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.79
Std Dev	9.853



B1L102S, U11-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

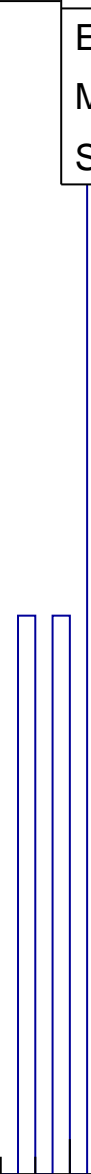
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.2
Std Dev	2.04

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch124, adc0

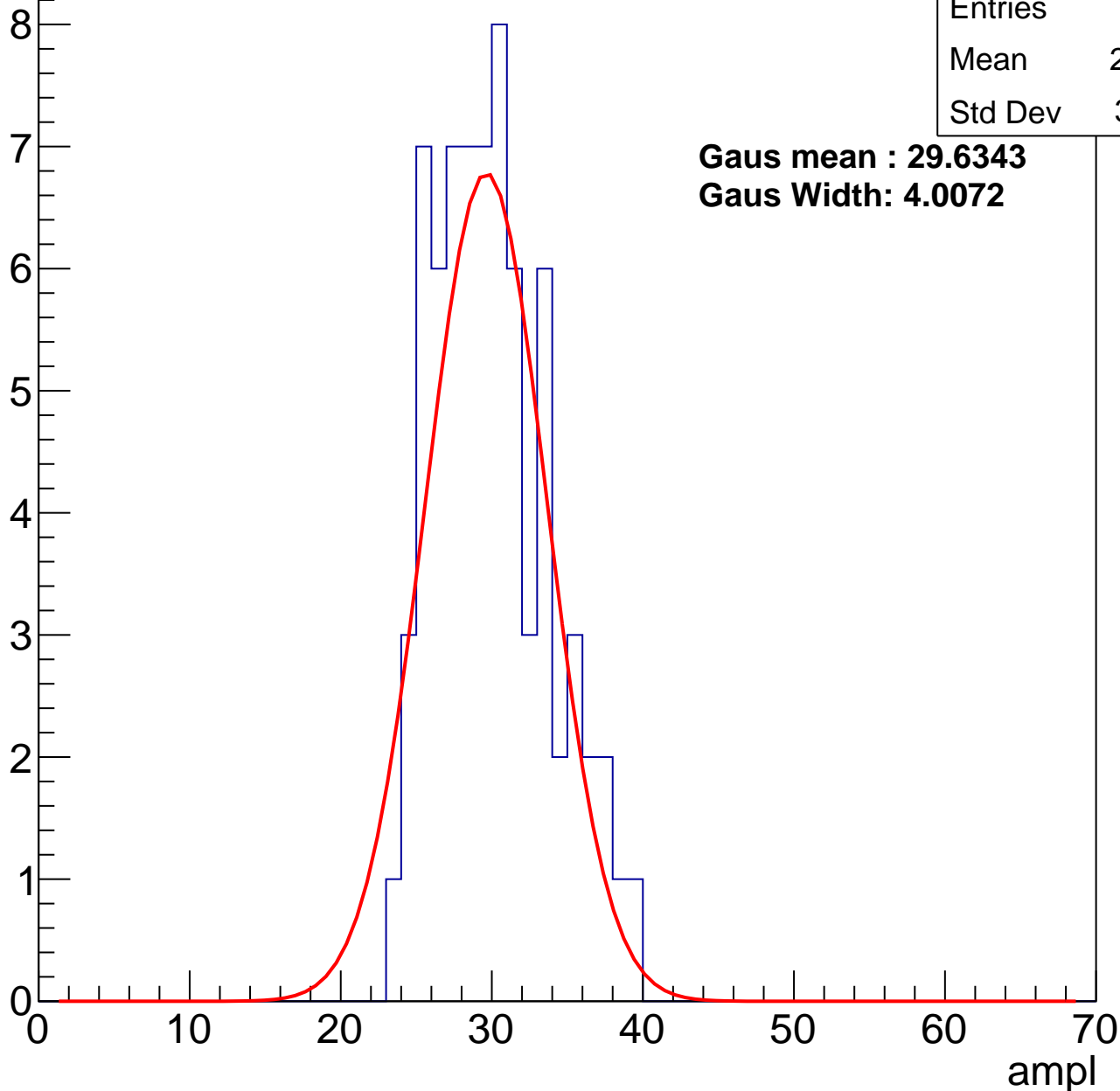
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.58
Std Dev	3.741

Gaus mean : 29.6343

Gaus Width: 4.0072



B1L102S, U11-ch124, adc1

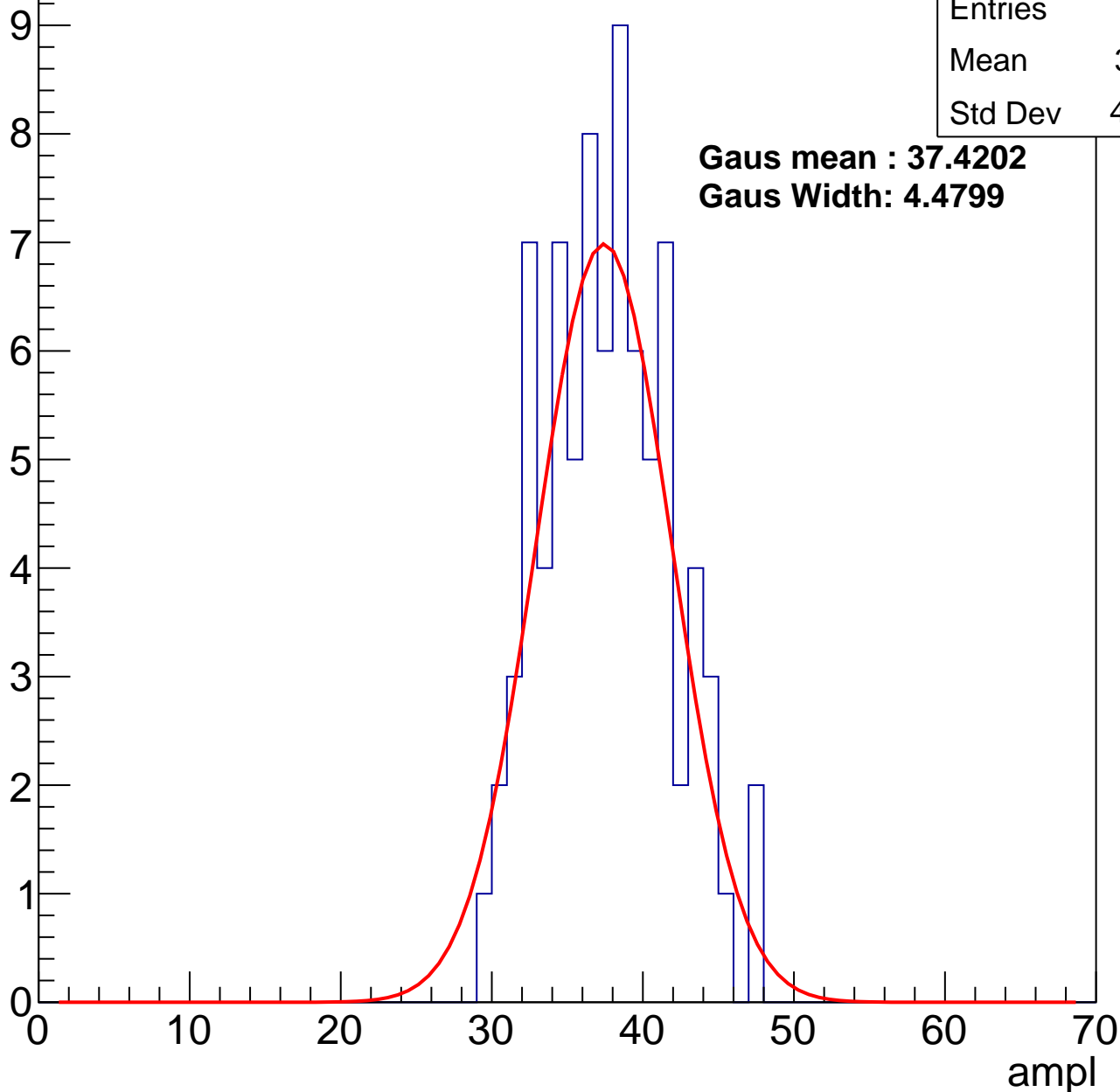
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	37.21
Std Dev	4.119

Gaus mean : 37.4202

Gaus Width: 4.4799



B1L102S, U11-ch124, adc2

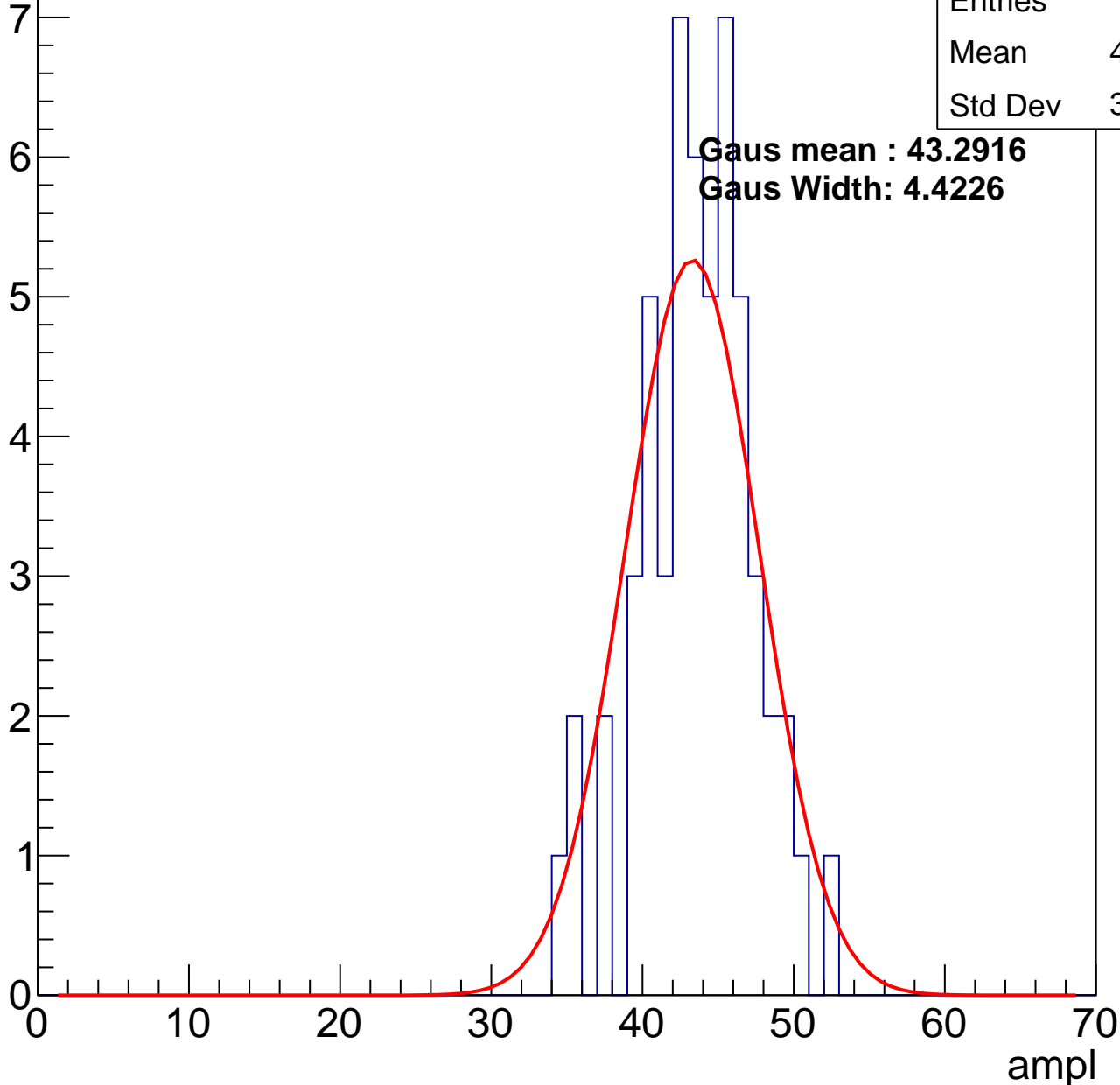
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.13
Std Dev	3.742

Gaus mean : 43.2916

Gaus Width: 4.4226

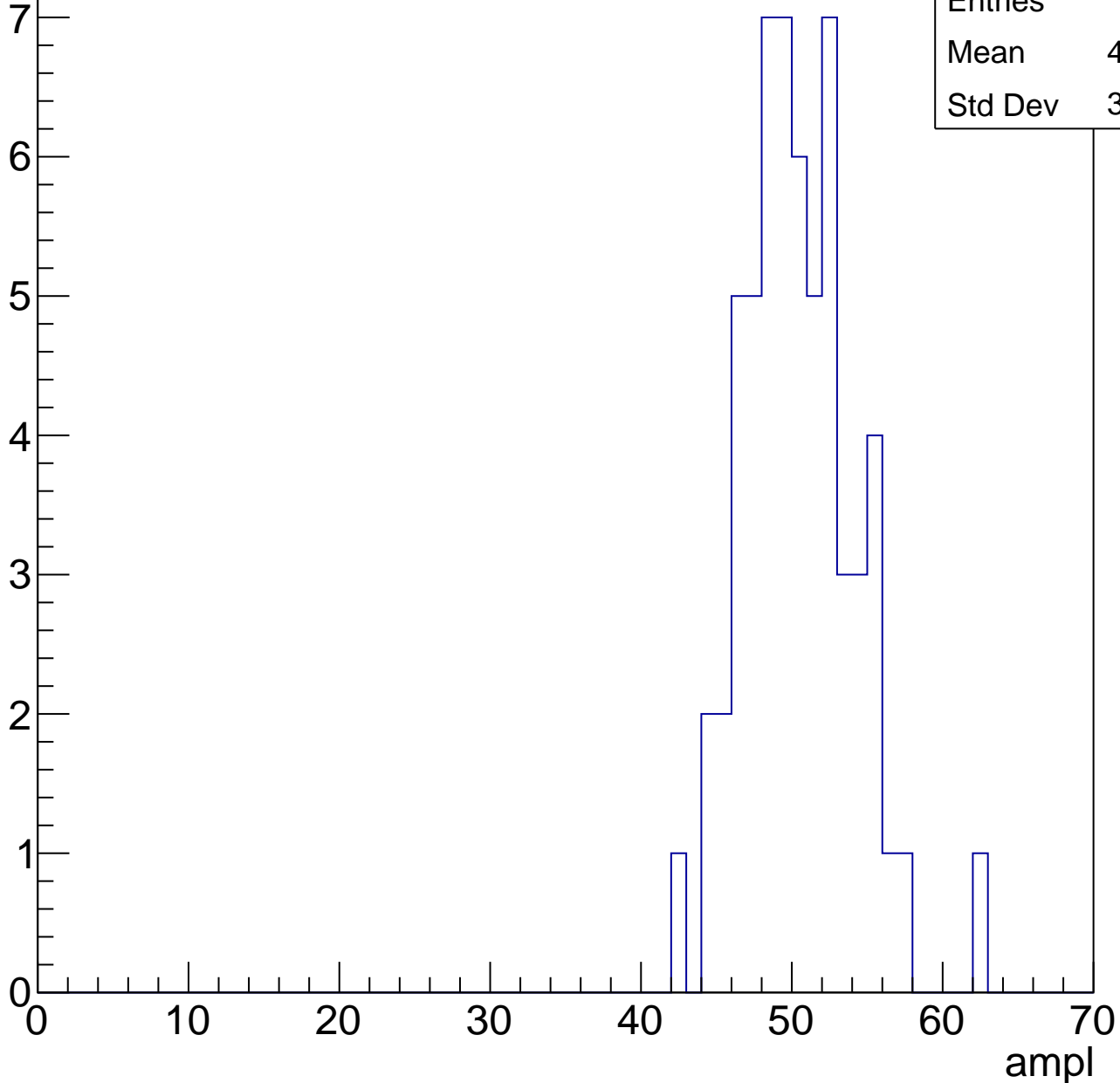


B1L102S, U11-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.98
Std Dev	3.612

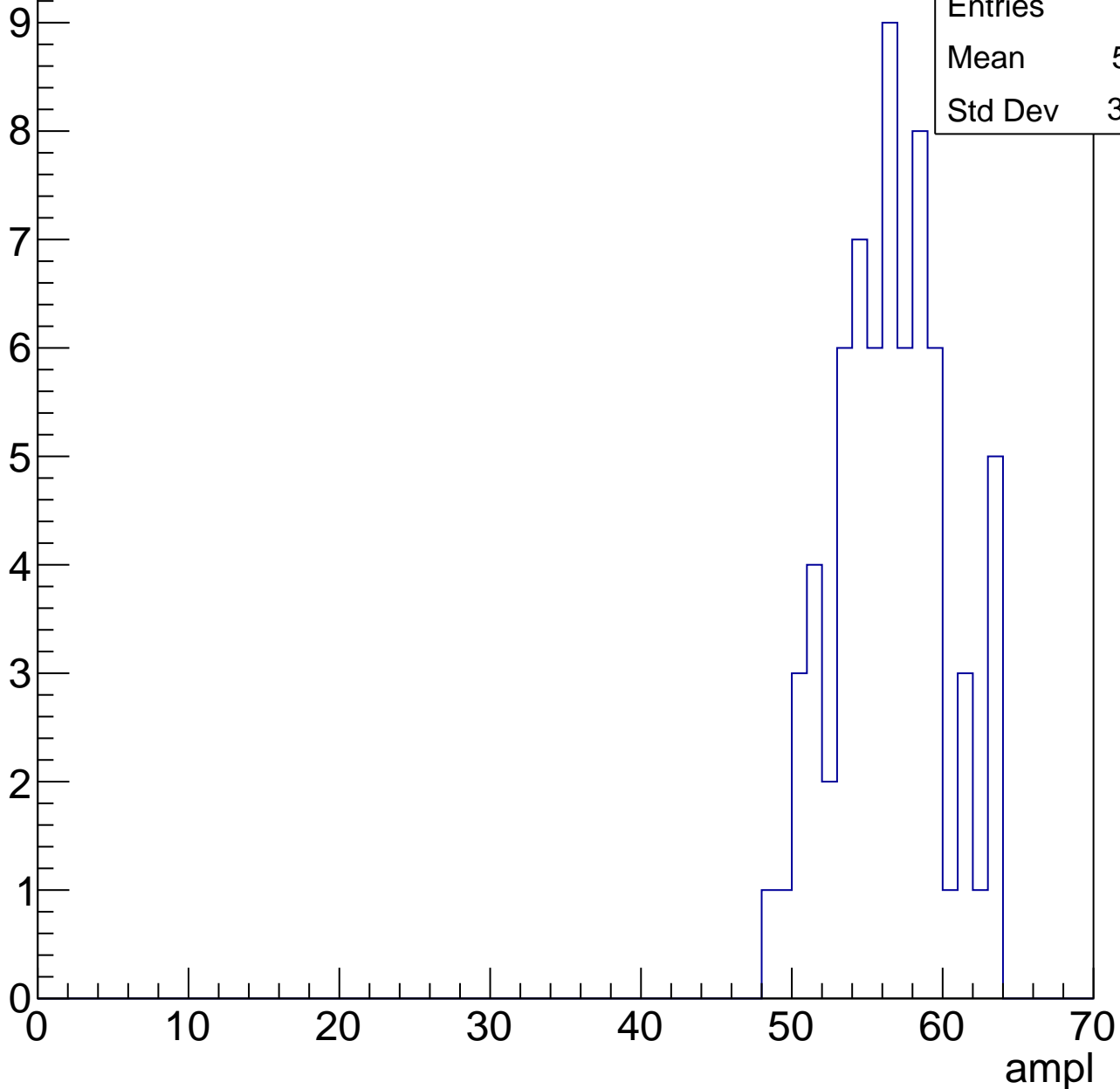


B1L102S, U11-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	56.01
Std Dev	3.622

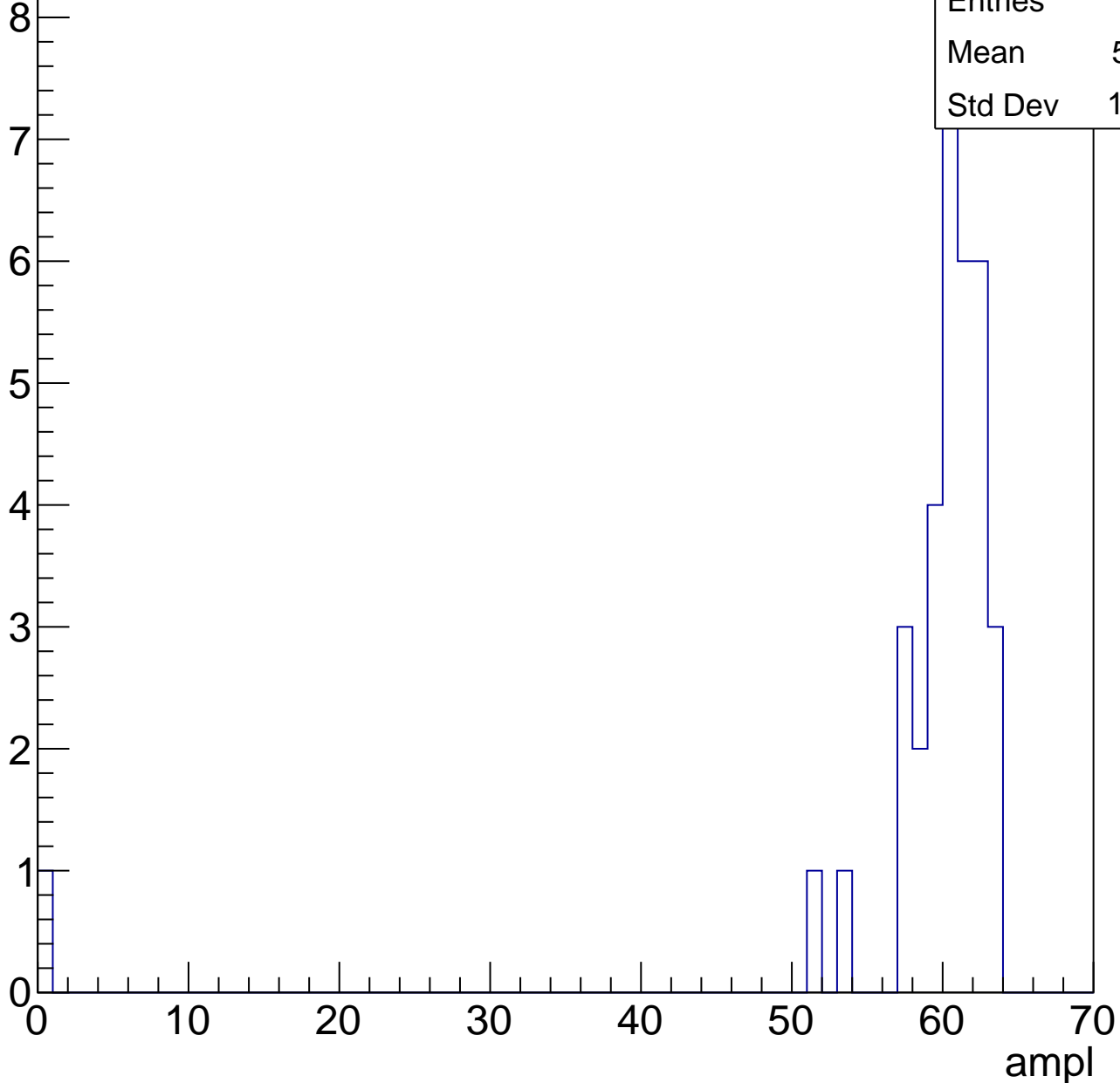


B1L102S, U11-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.11
Std Dev	10.28



B1L102S, U11-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

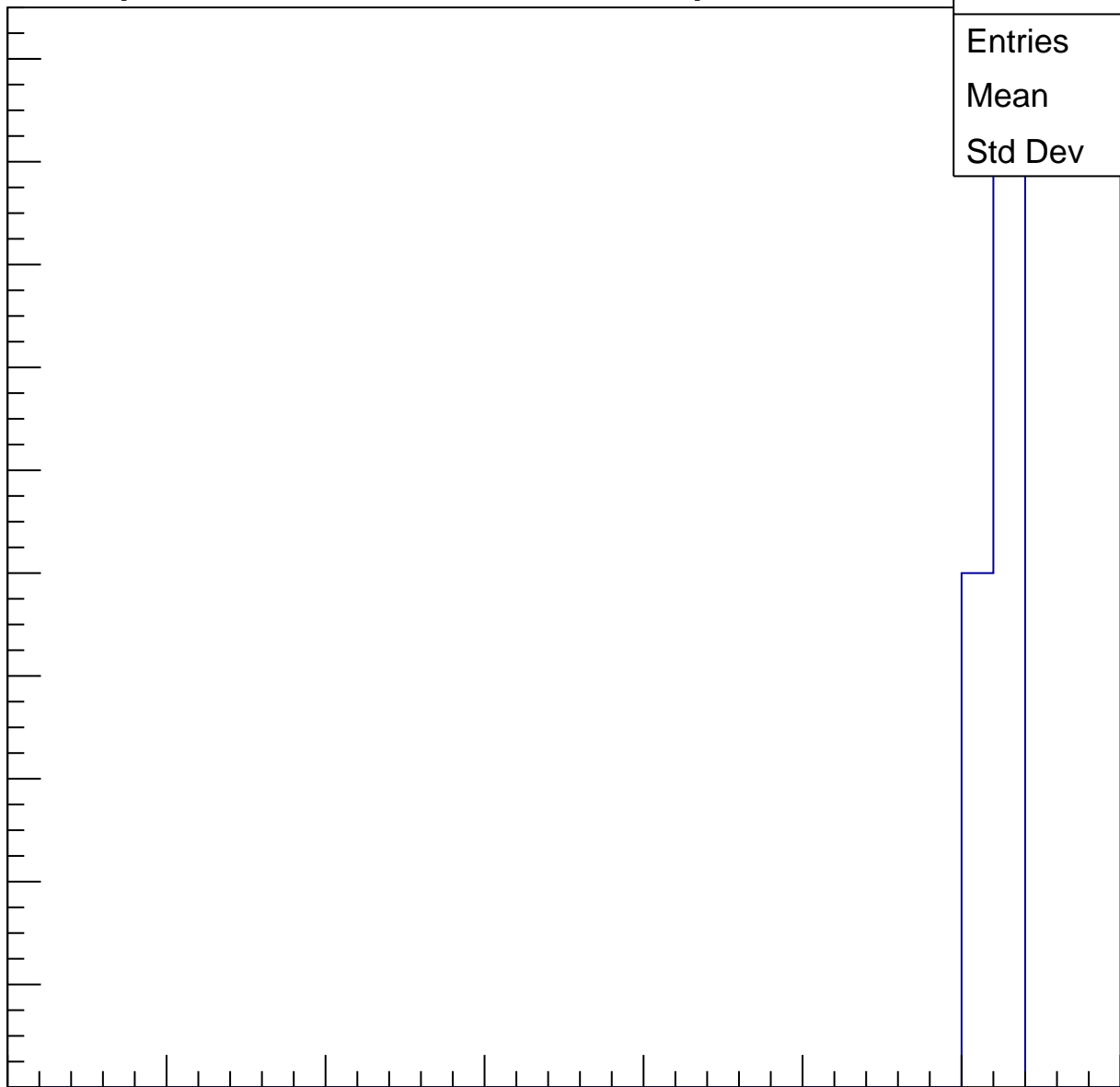
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

0 10 20 30 40 50 60 70

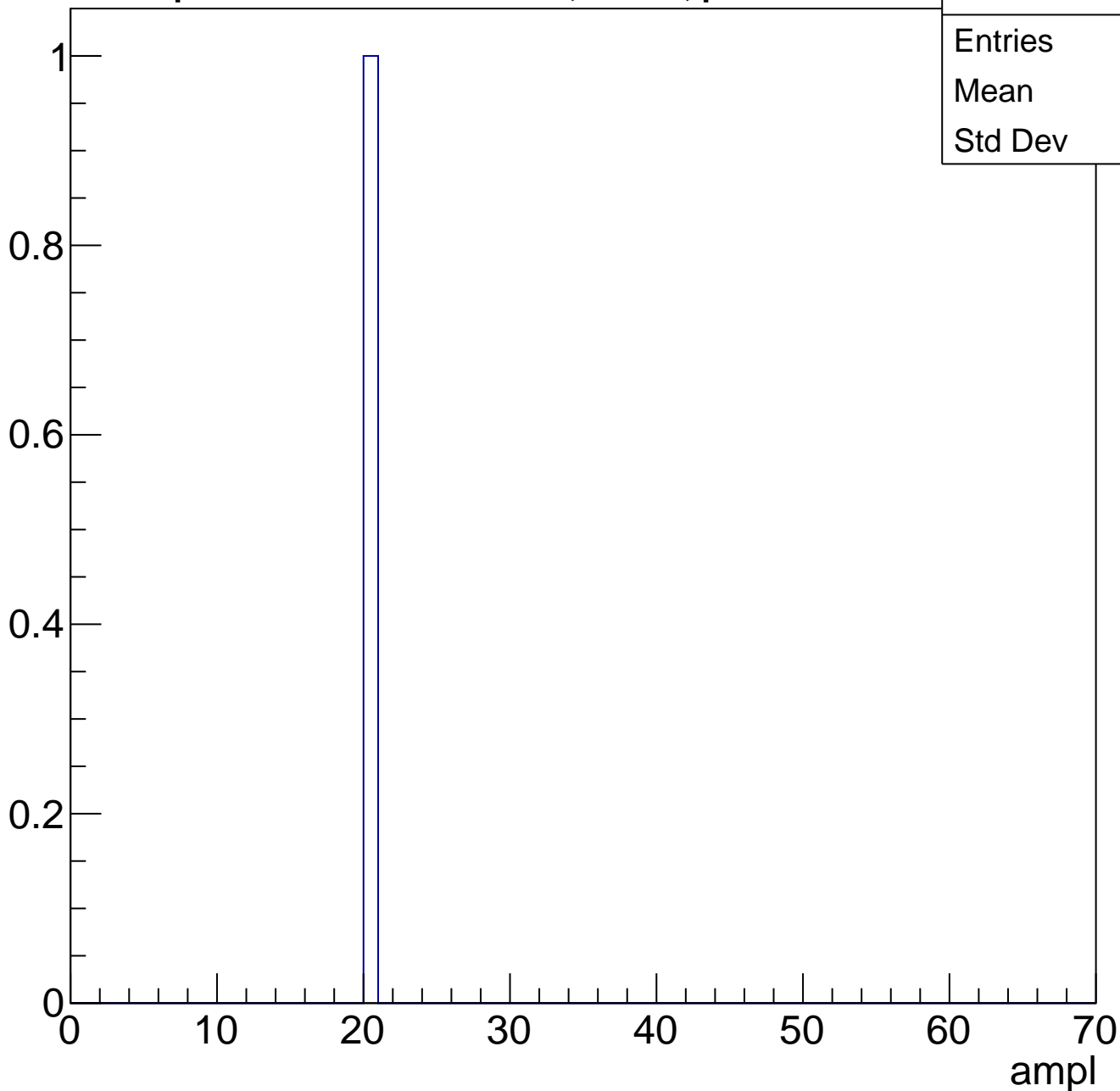
ampl



B1L102S, U11-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U11-ch125, adc0

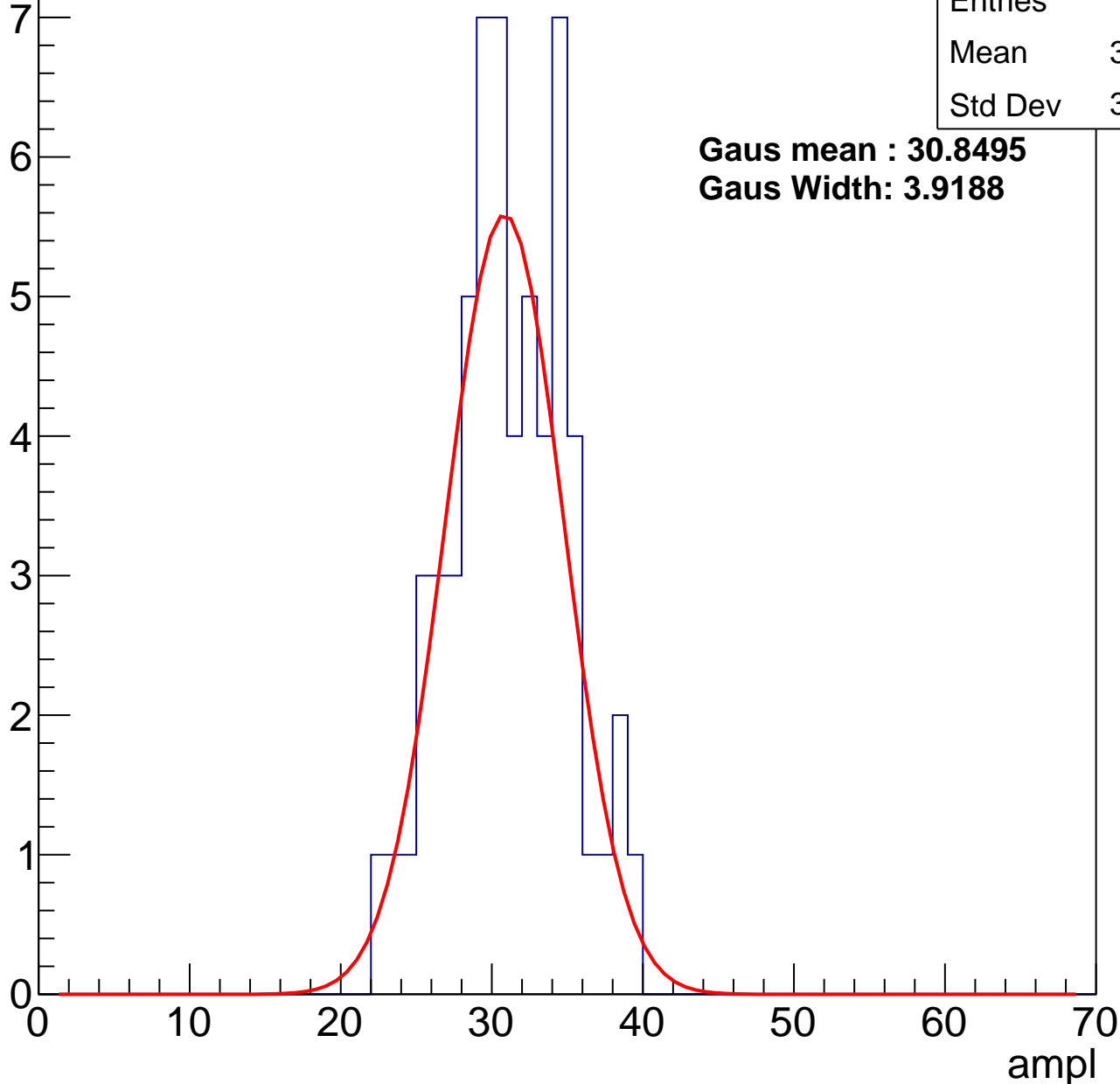
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	30.63
Std Dev	3.803

Gaus mean : 30.8495

Gaus Width: 3.9188



B1L102S, U11-ch125, adc1

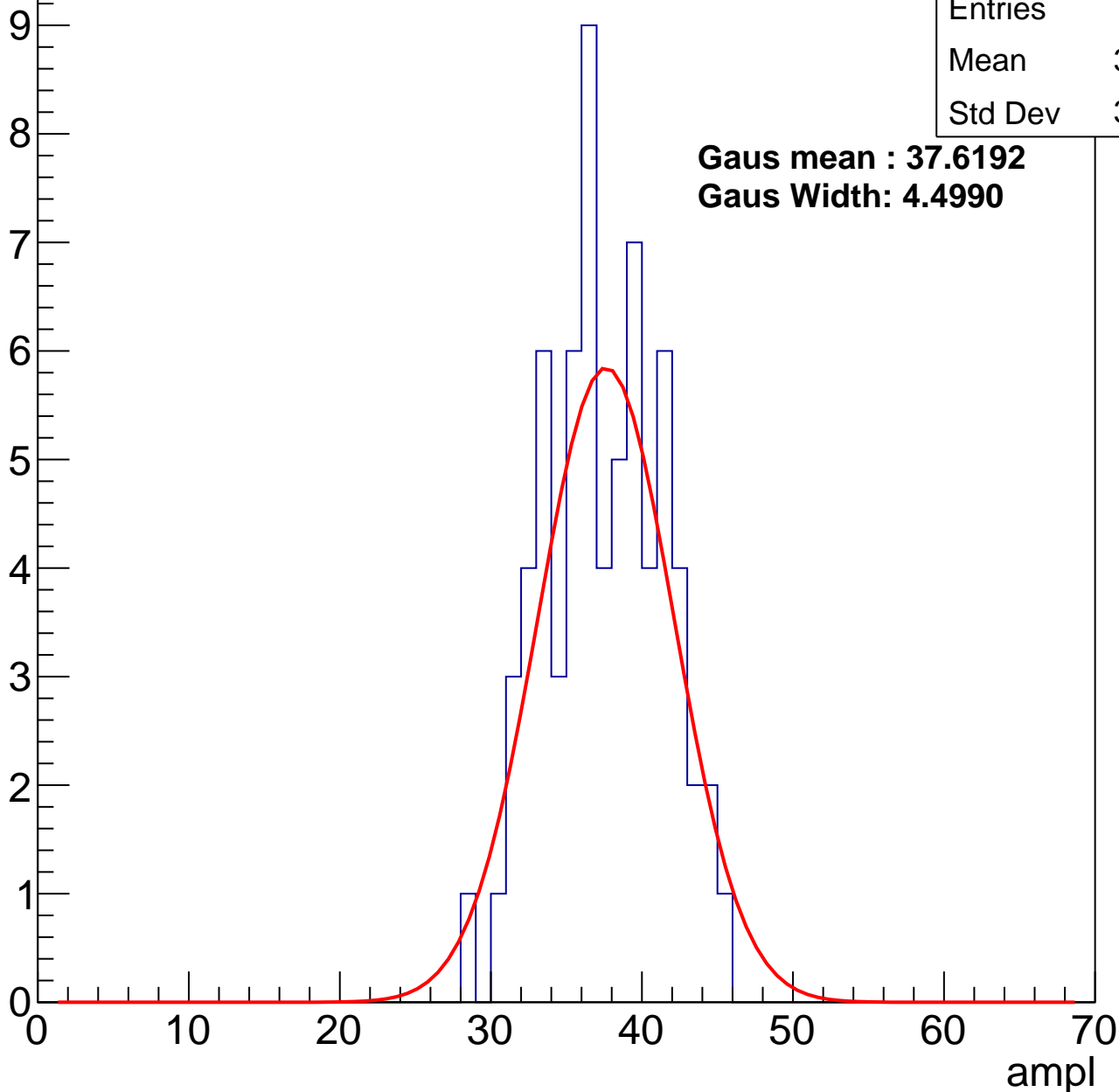
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.01
Std Dev	3.821

Gaus mean : 37.6192

Gaus Width: 4.4990



B1L102S, U11-ch125, adc2

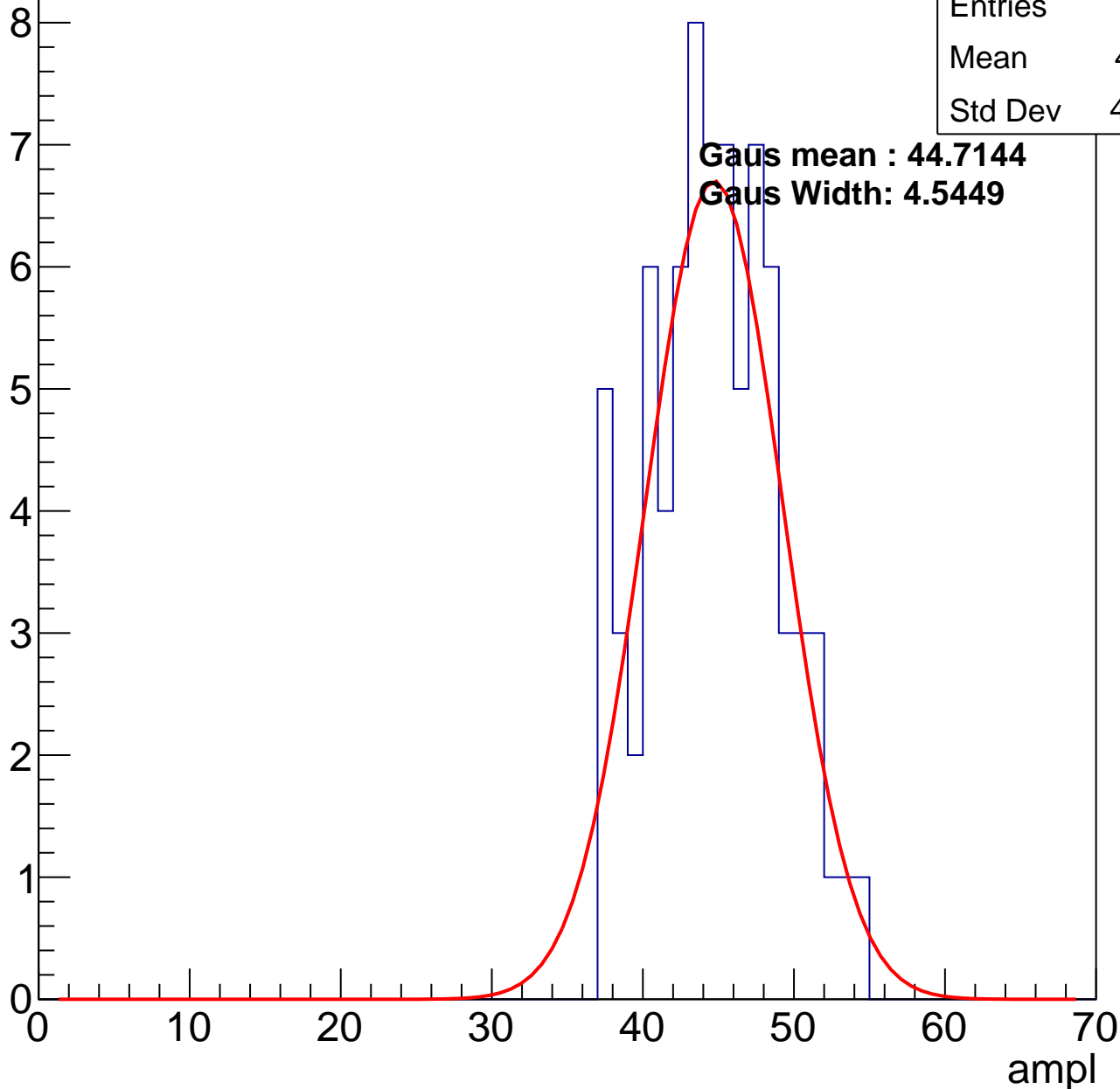
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	44.31
Std Dev	4.115

Gaus mean : 44.7144

Gaus Width: 4.5449

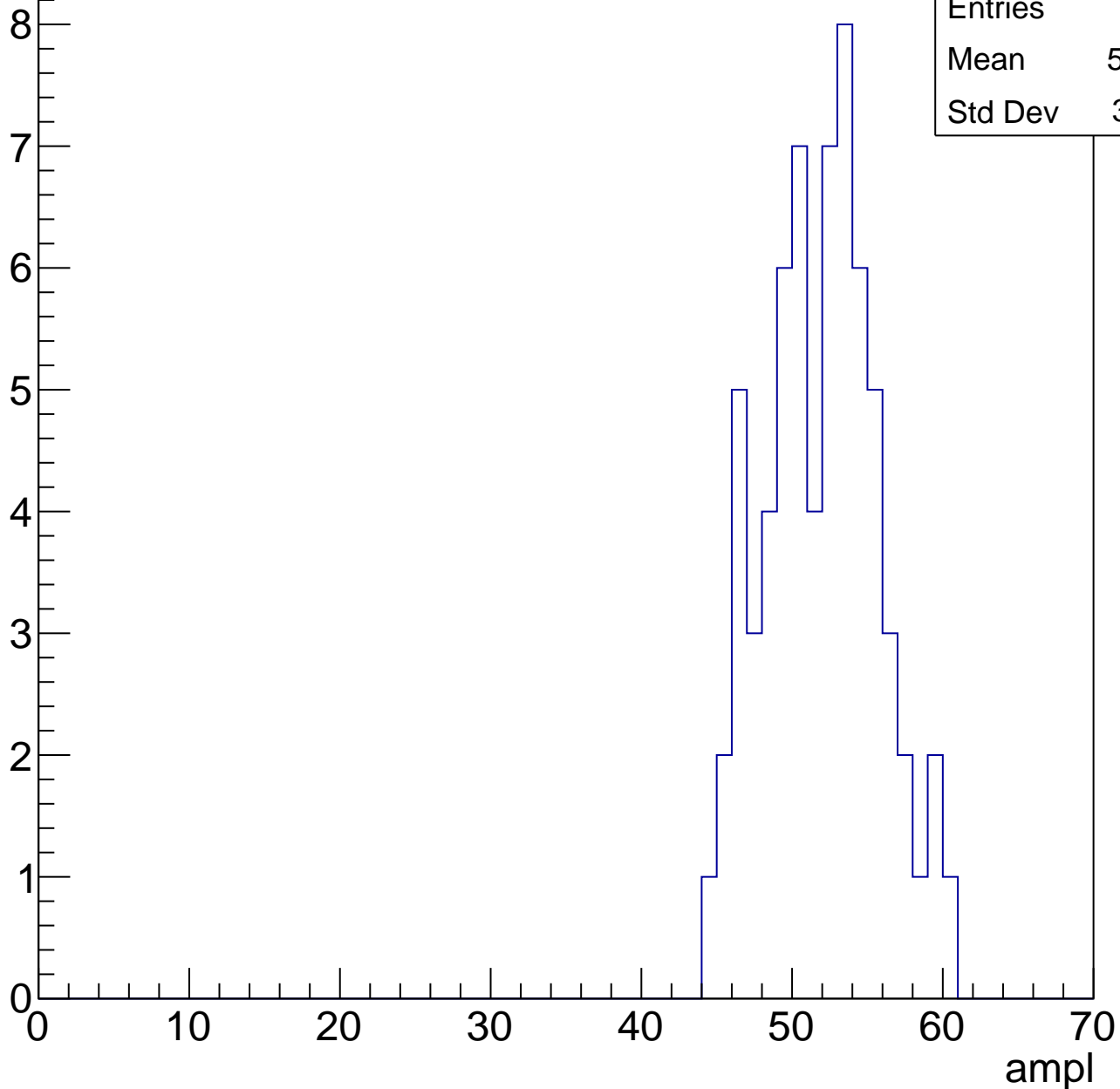


B1L102S, U11-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	51.49
Std Dev	3.711

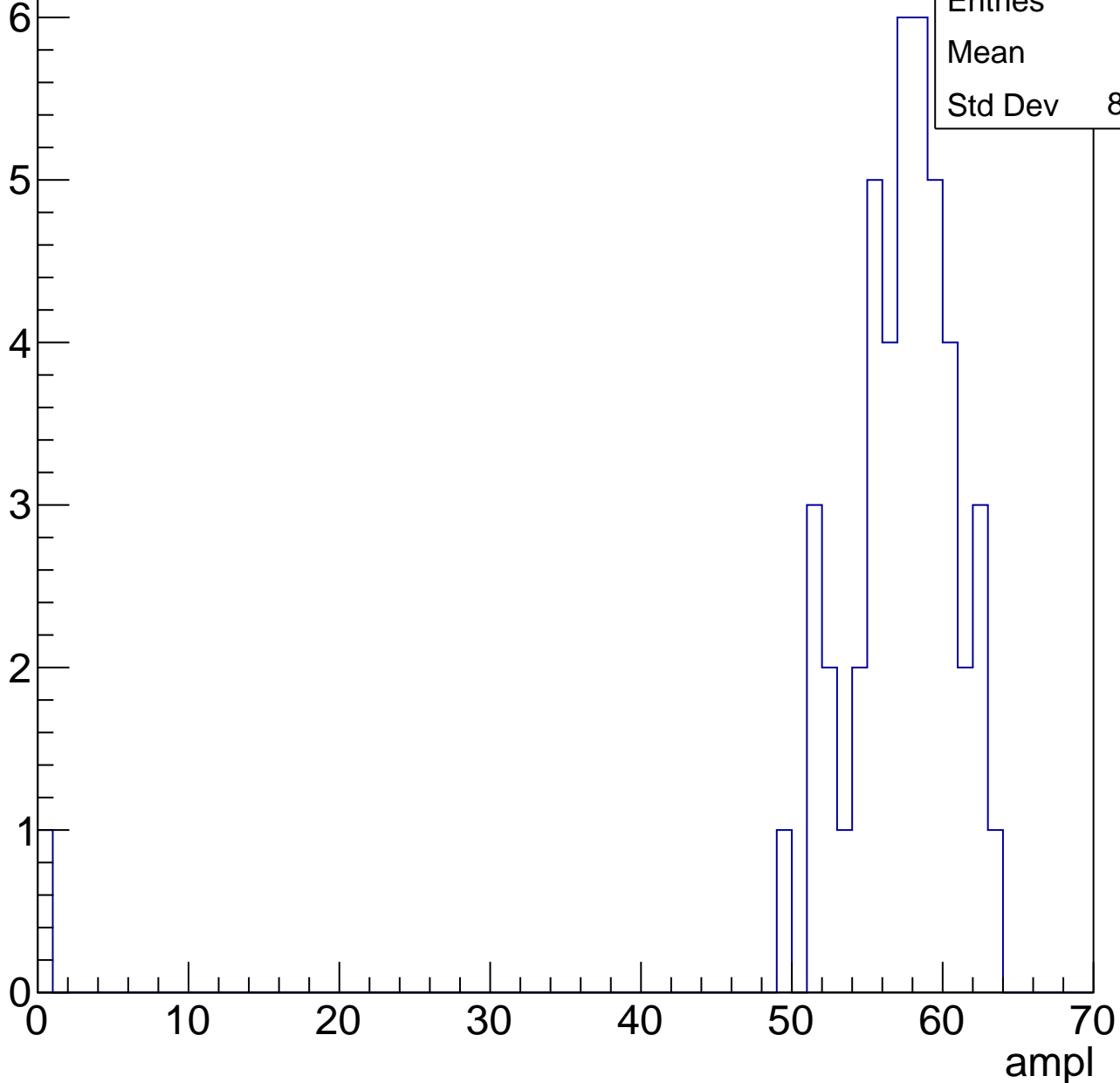


B1L102S, U11-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.7
Std Dev	8.915

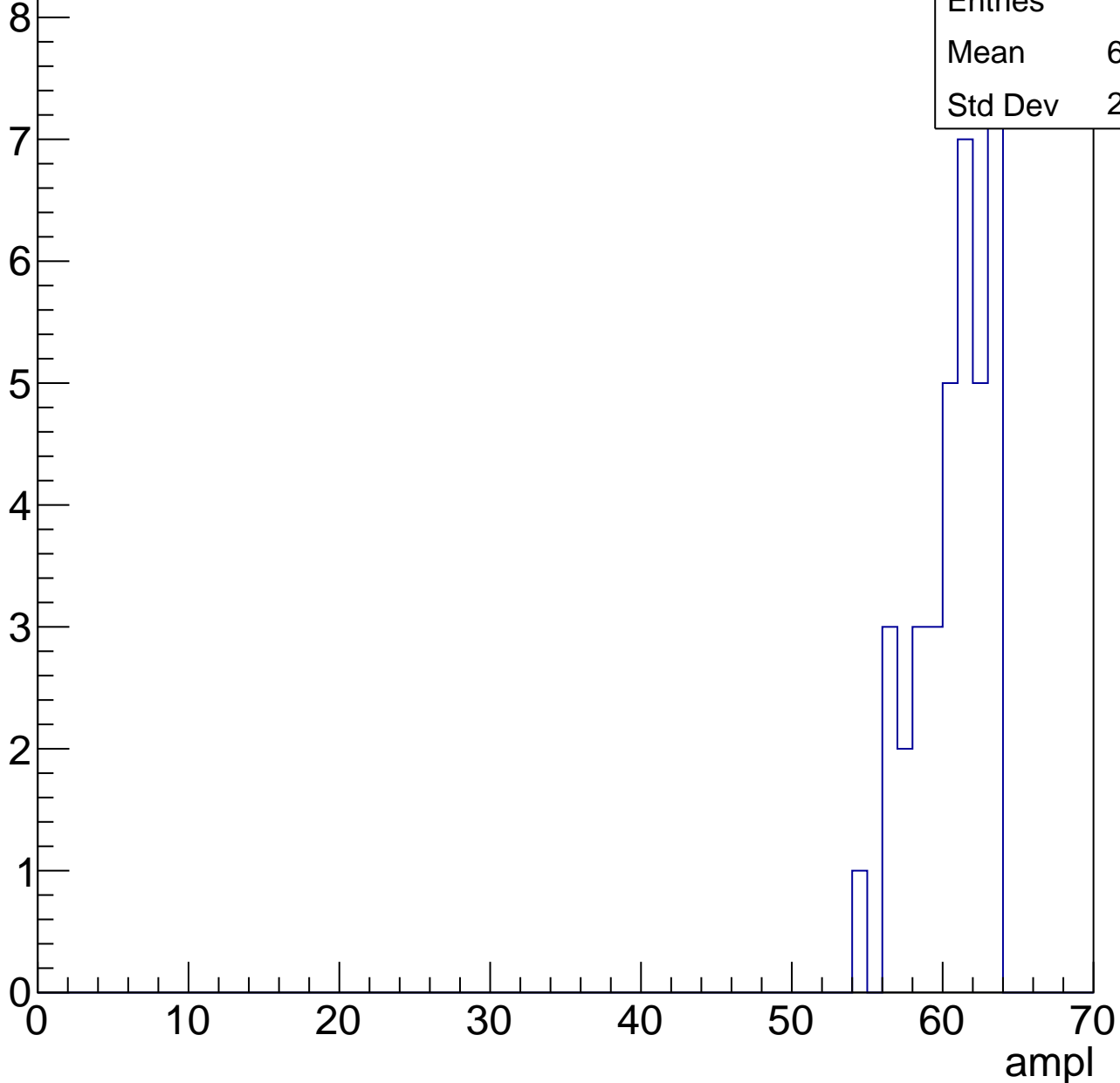


B1L102S, U11-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

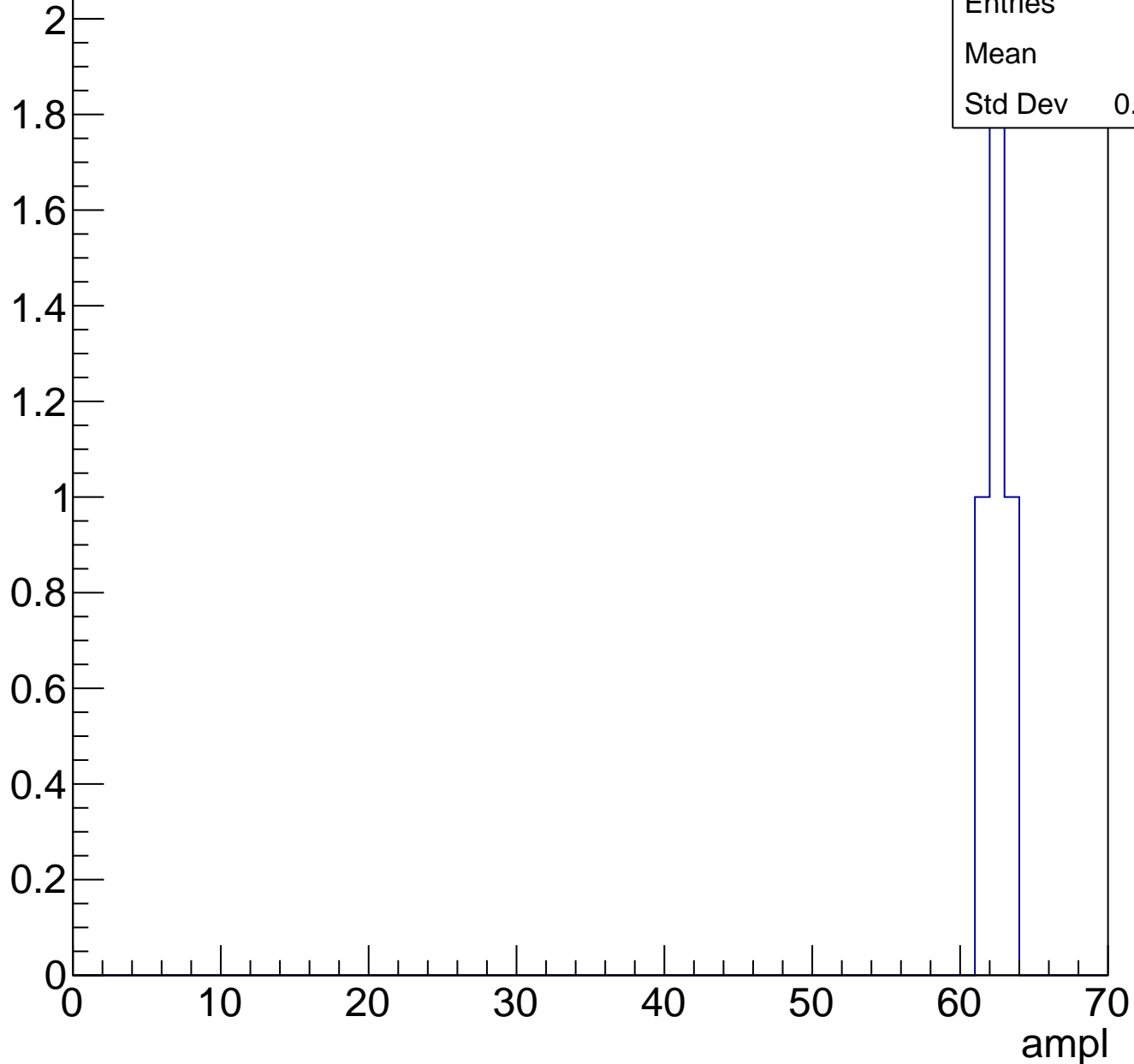
Entries	37
Mean	60.22
Std Dev	2.406



B1L102S, U11-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U11-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch126, adc0

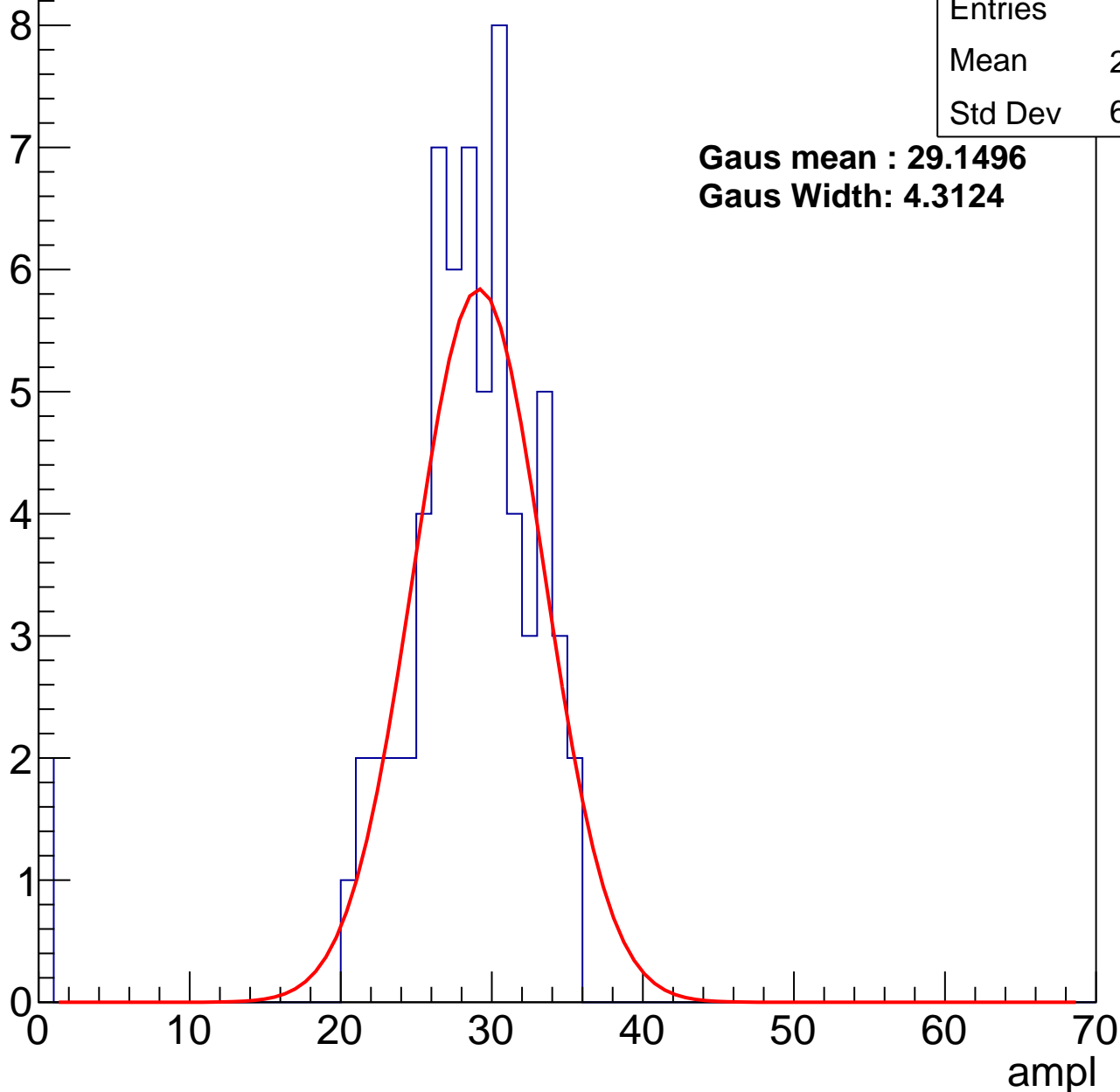
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.42
Std Dev	6.056

Gaus mean : 29.1496

Gaus Width: 4.3124



B1L102S, U11-ch126, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	36.45
Std Dev	5.728

Gaus mean : 37.1578

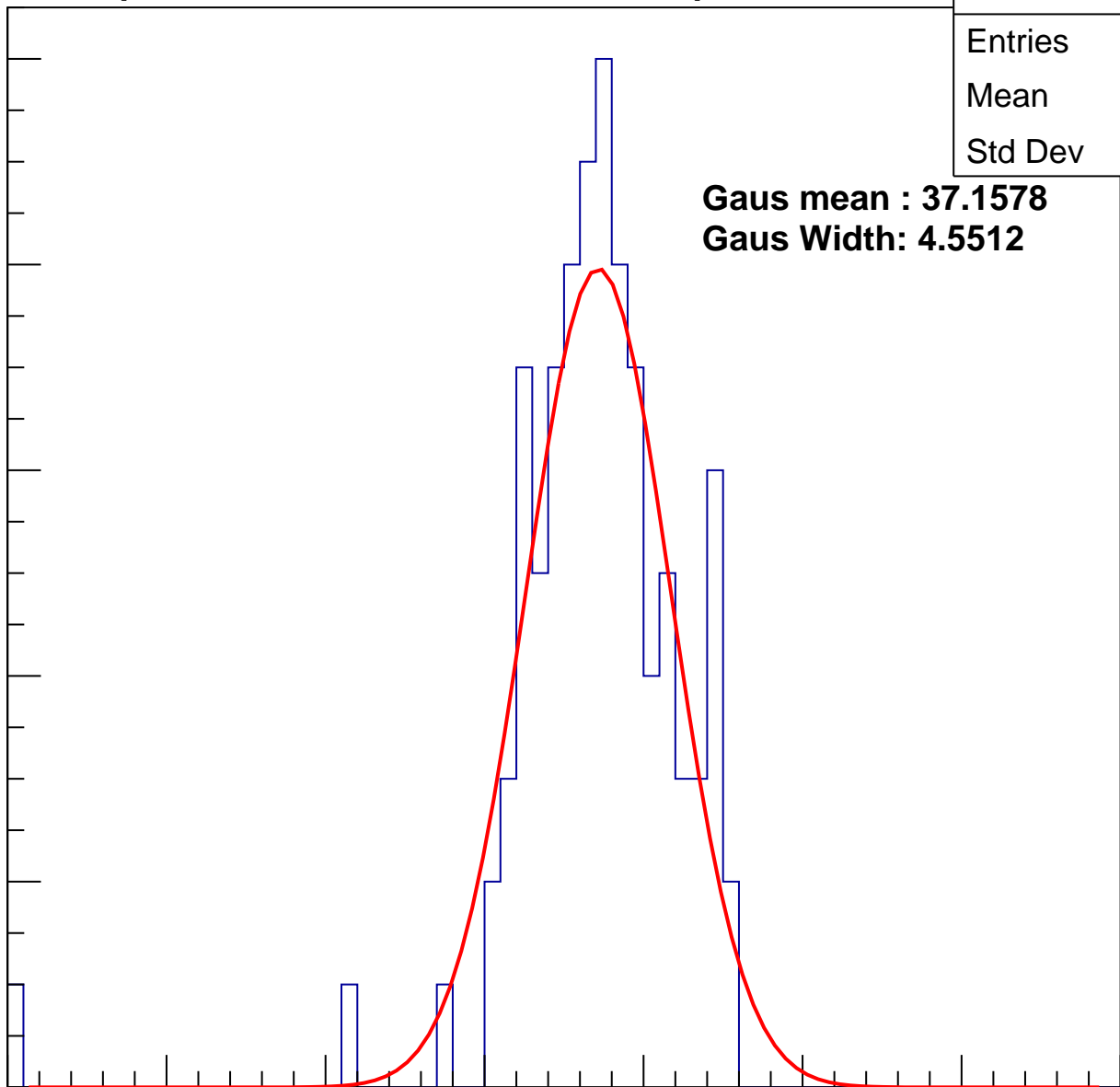
Gaus Width: 4.5512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

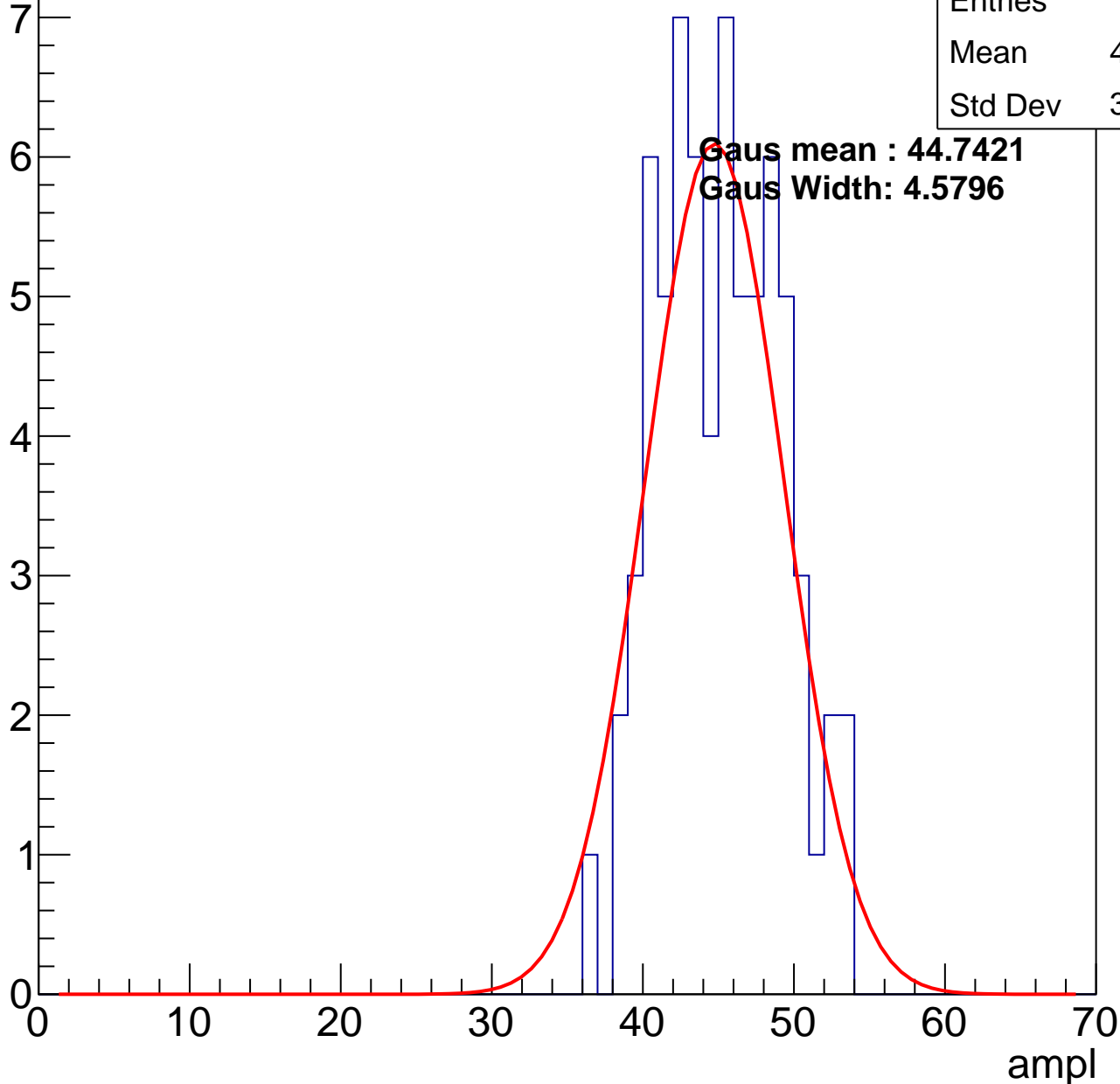


B1L102S, U11-ch126, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	44.66
Std Dev	3.956

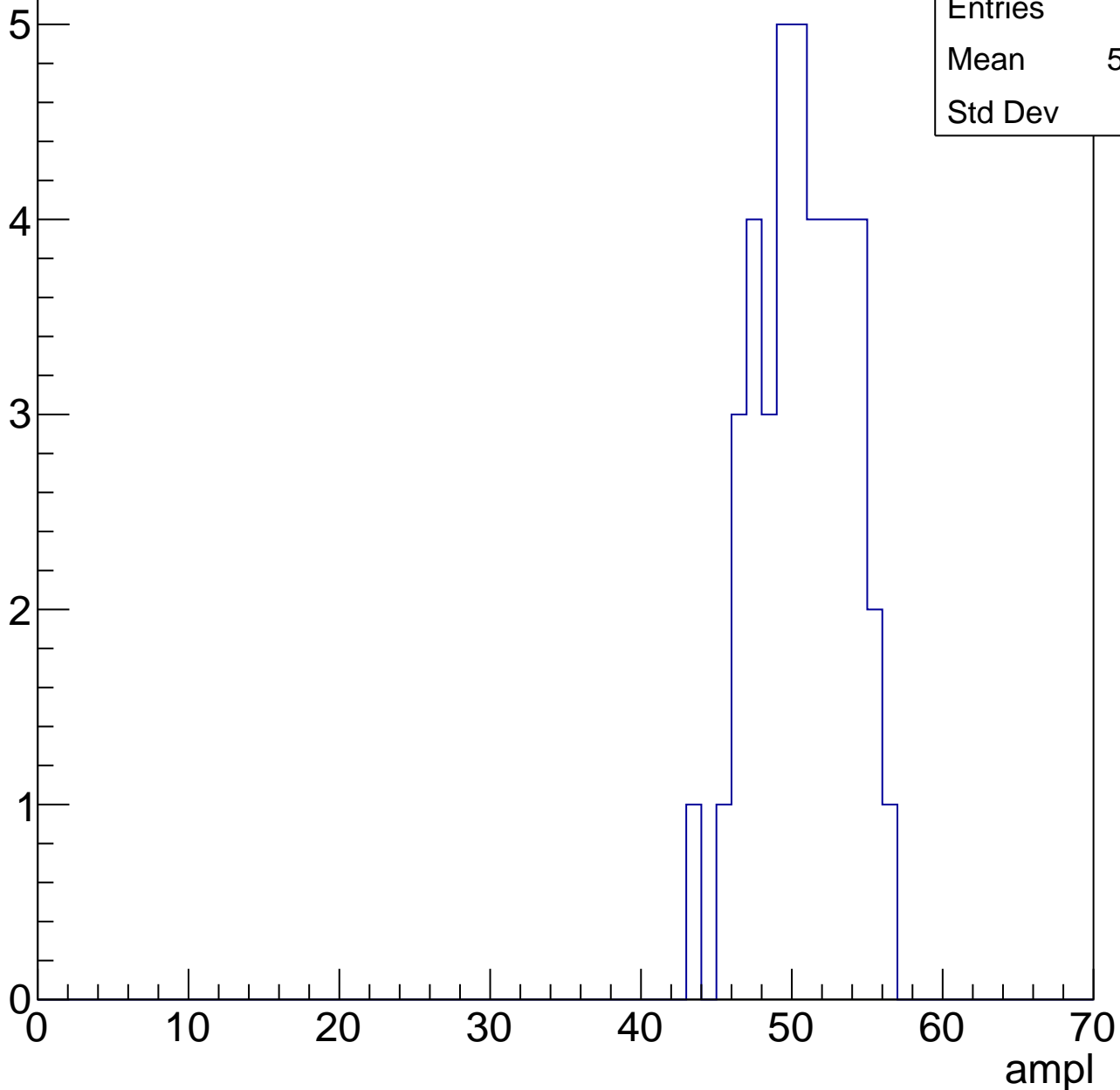


B1L102S, U11-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	50.22
Std Dev	3.04

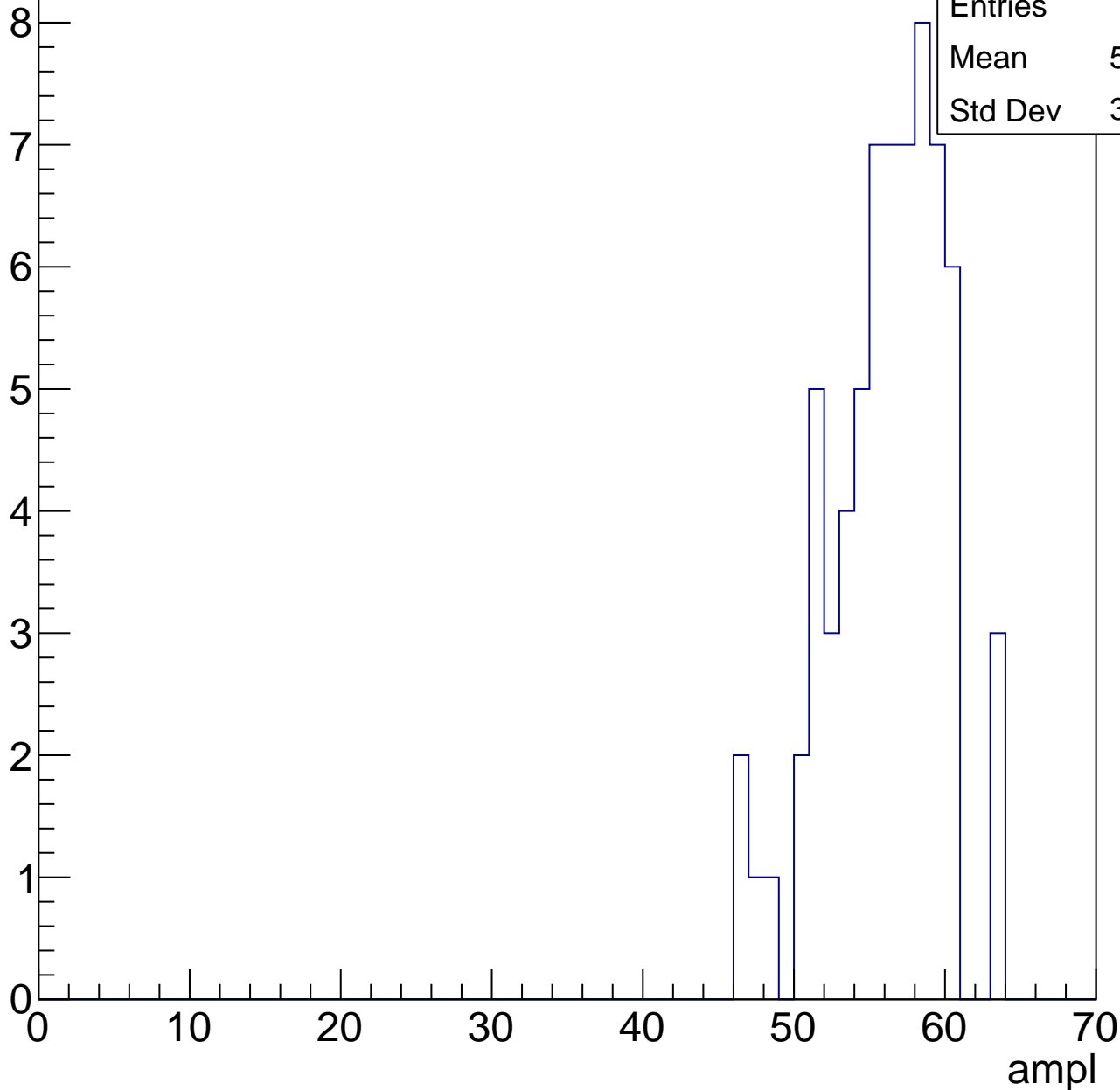


B1L102S, U11-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.62
Std Dev	3.812

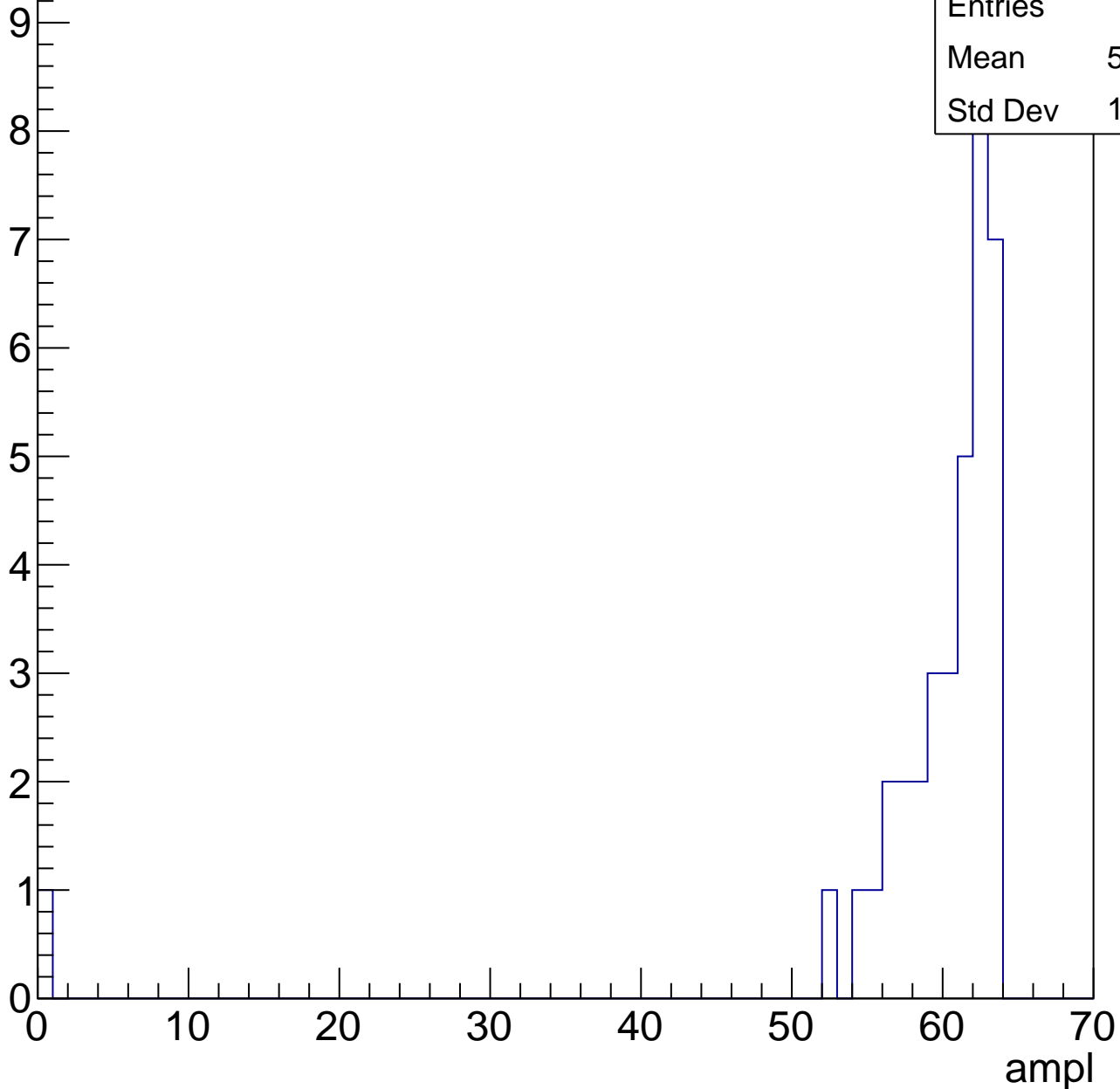


B1L102S, U11-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.49
Std Dev	10.14

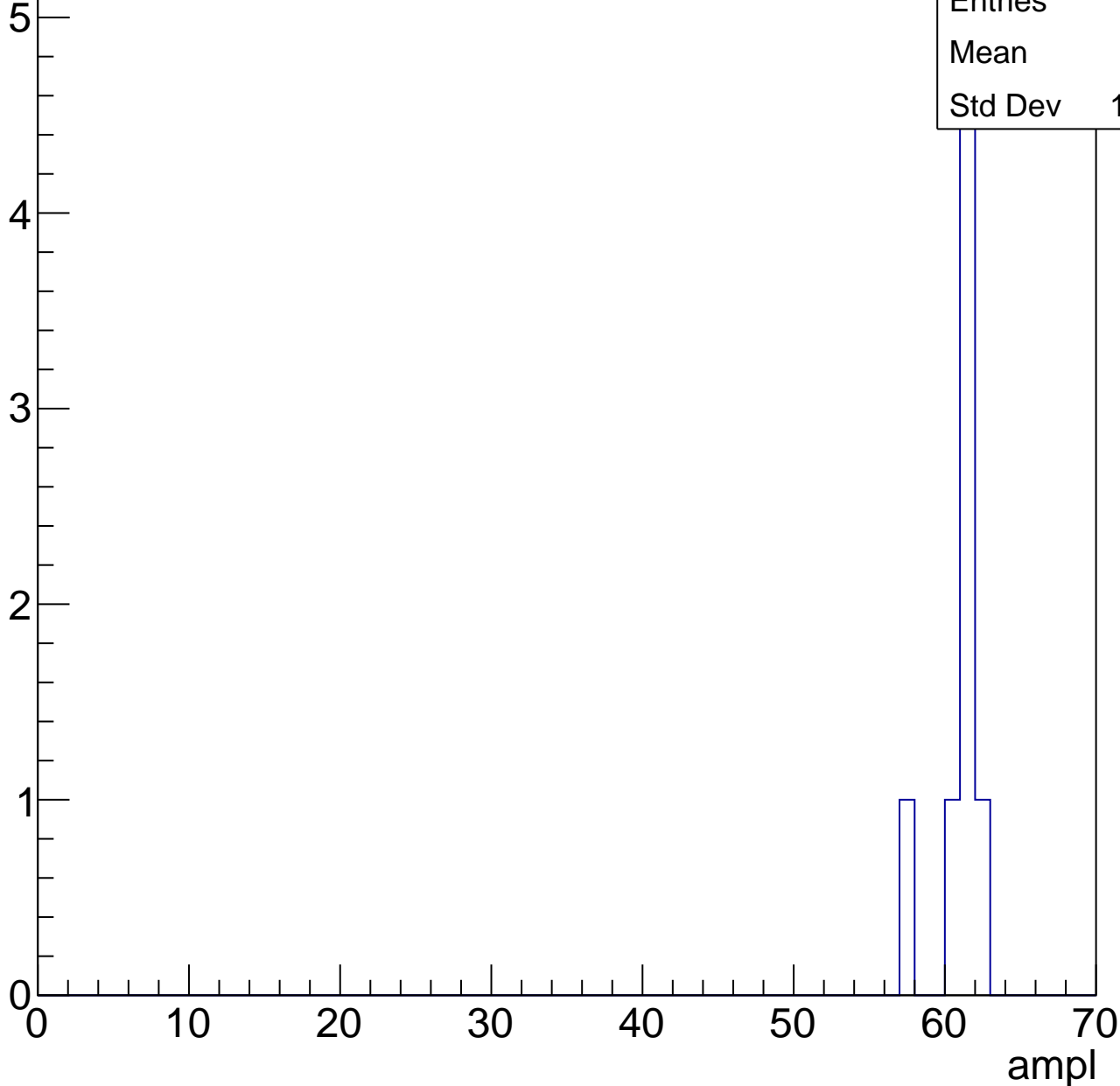


B1L102S, U11-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	60.5
Std Dev	1.414



B1L102S, U11-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U11-ch127, adc0

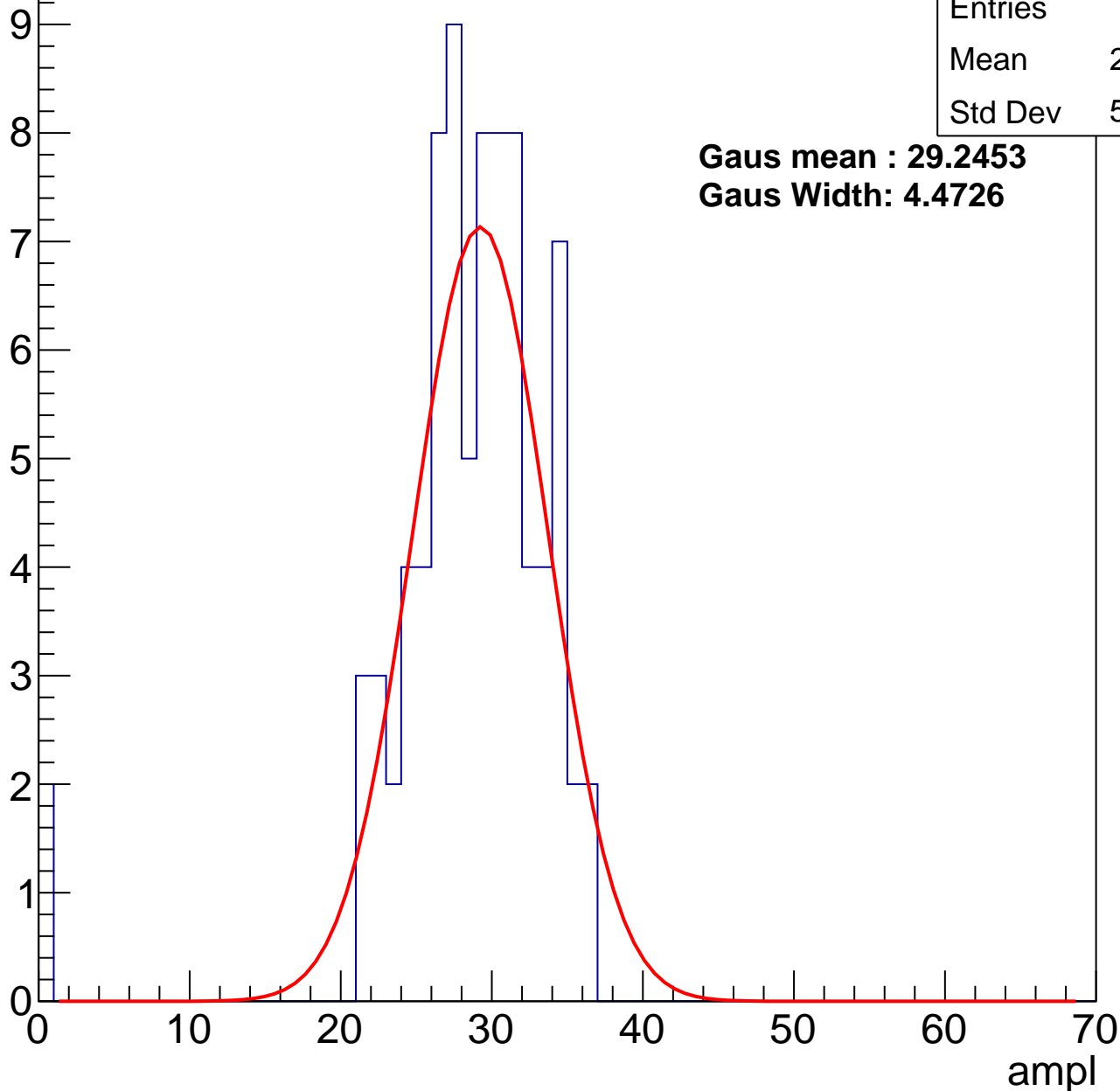
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.98
Std Dev	5.775

Gaus mean : 29.2453

Gaus Width: 4.4726



B1L102S, U11-ch127, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	37.33
Std Dev	3.8

Gaus mean : 37.6838

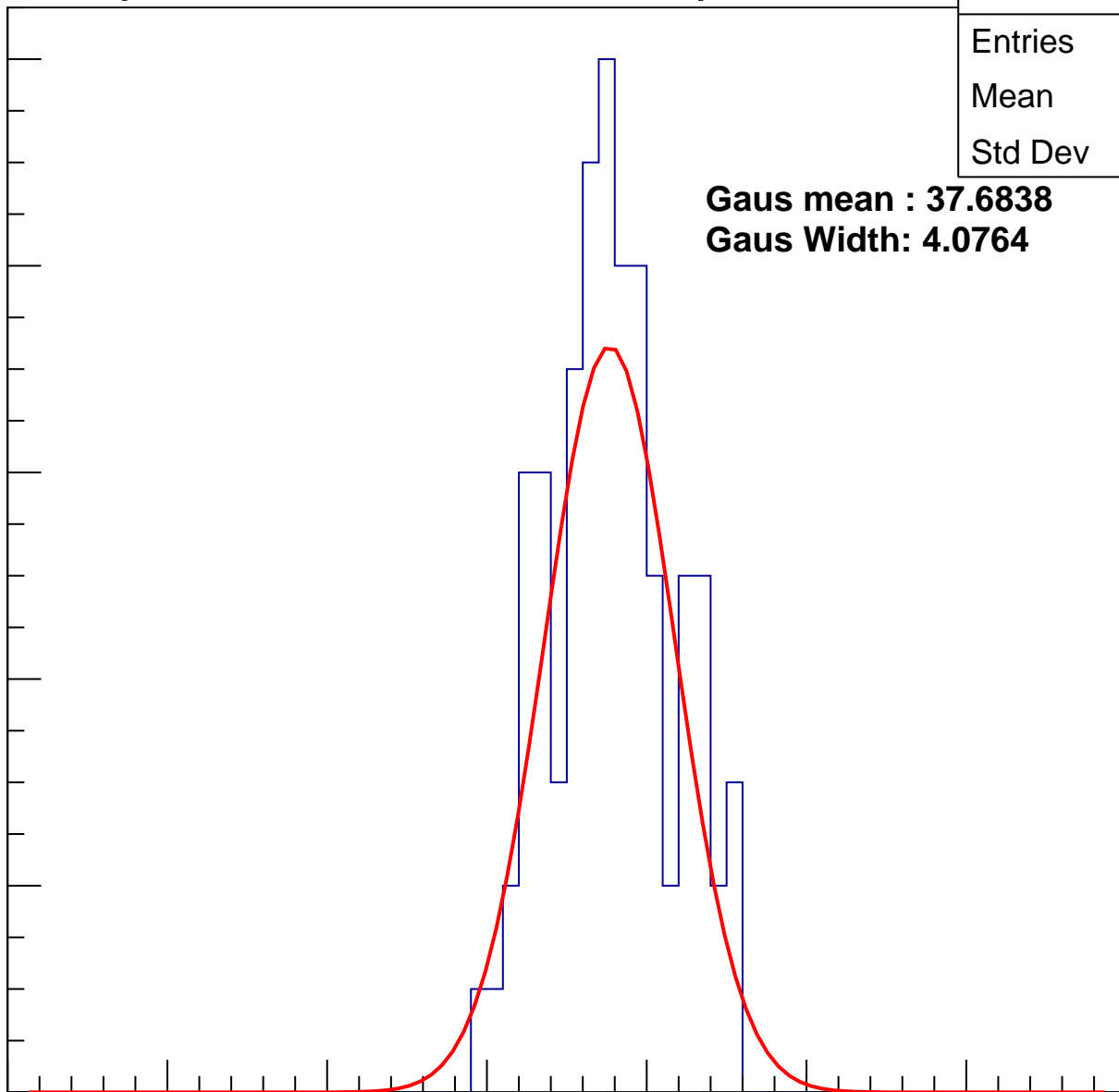
Gaus Width: 4.0764

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U11-ch127, adc2

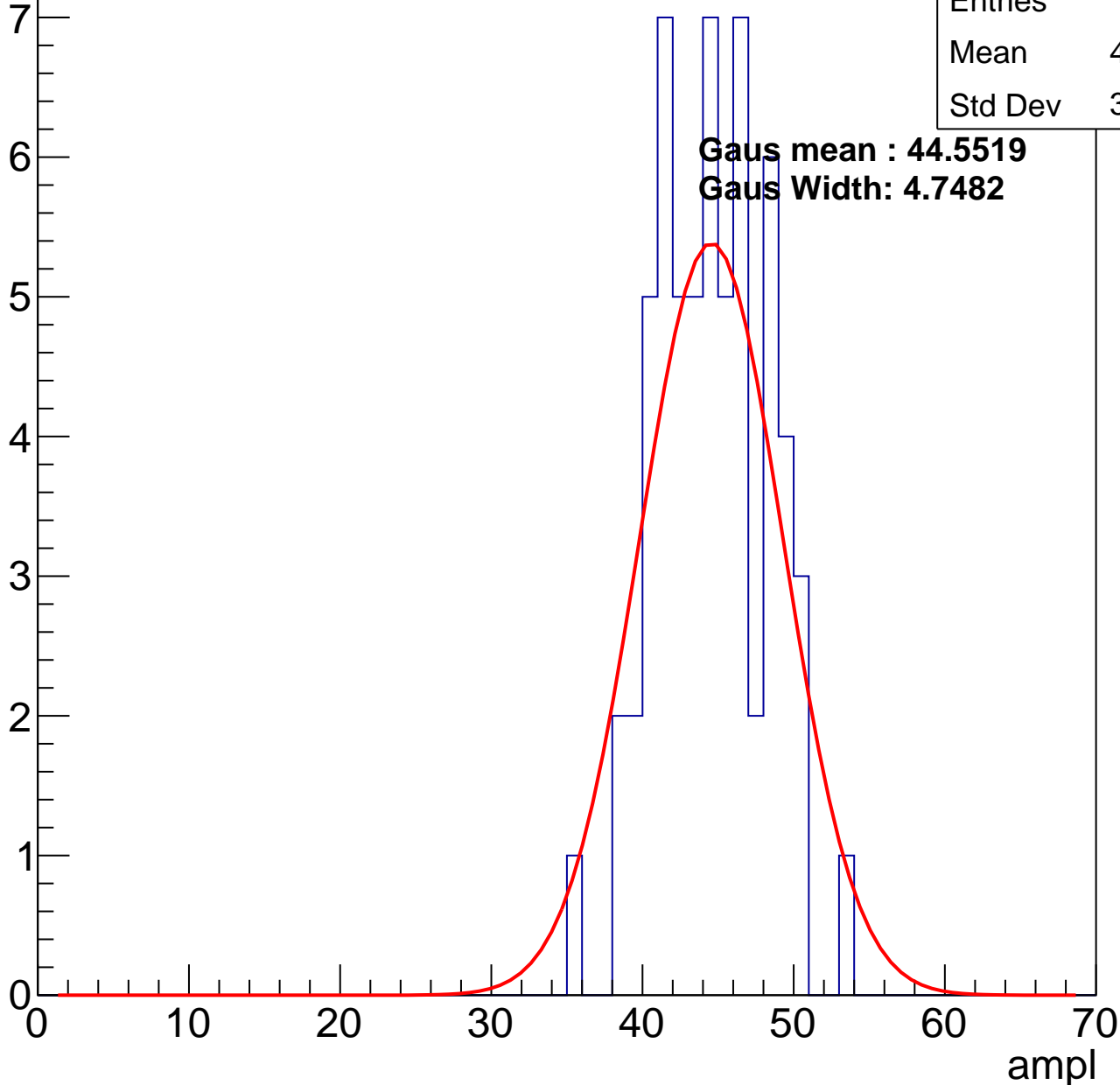
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.15
Std Dev	3.605

Gaus mean : 44.5519

Gaus Width: 4.7482

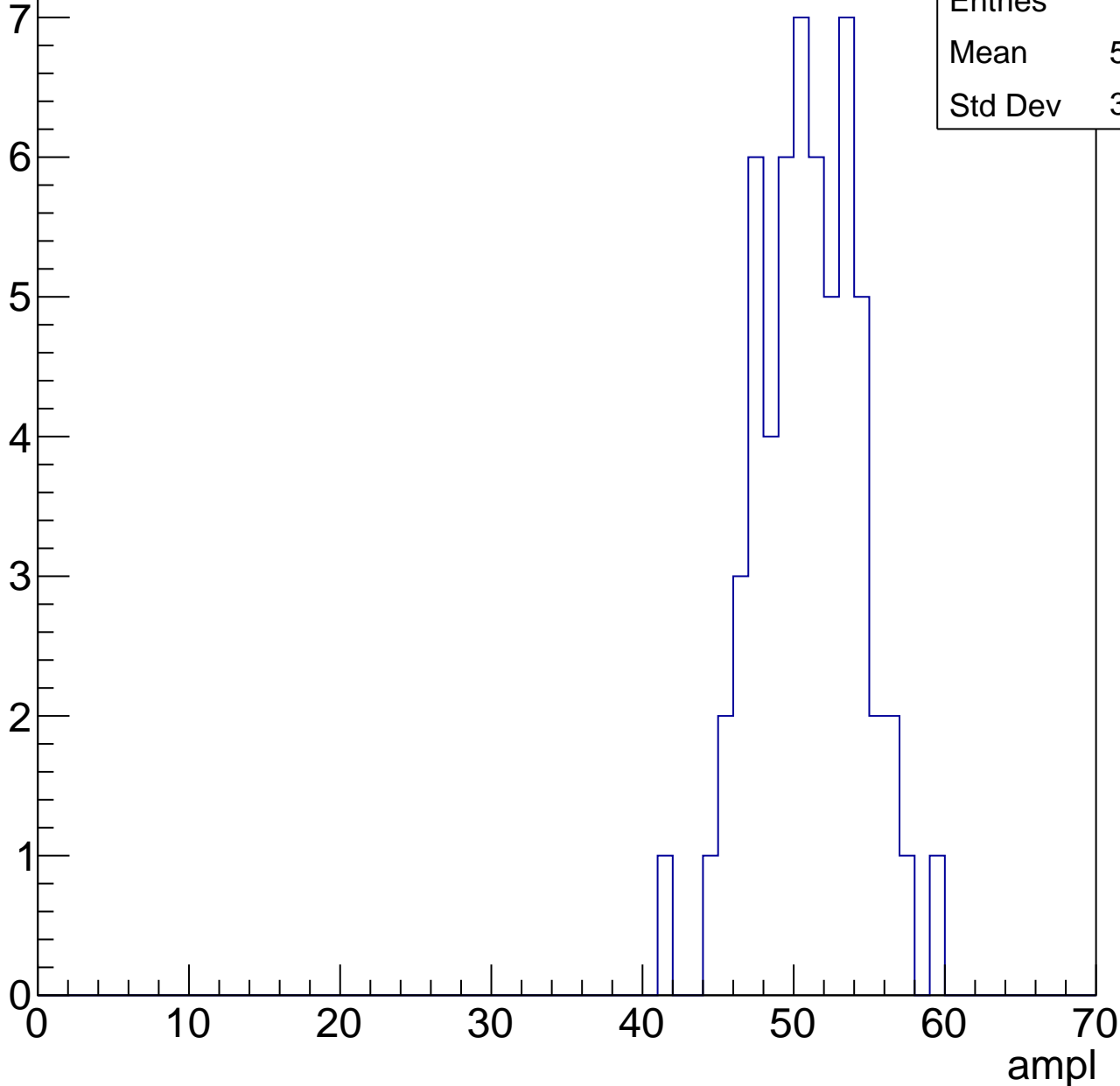


B1L102S, U11-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.44
Std Dev	3.446

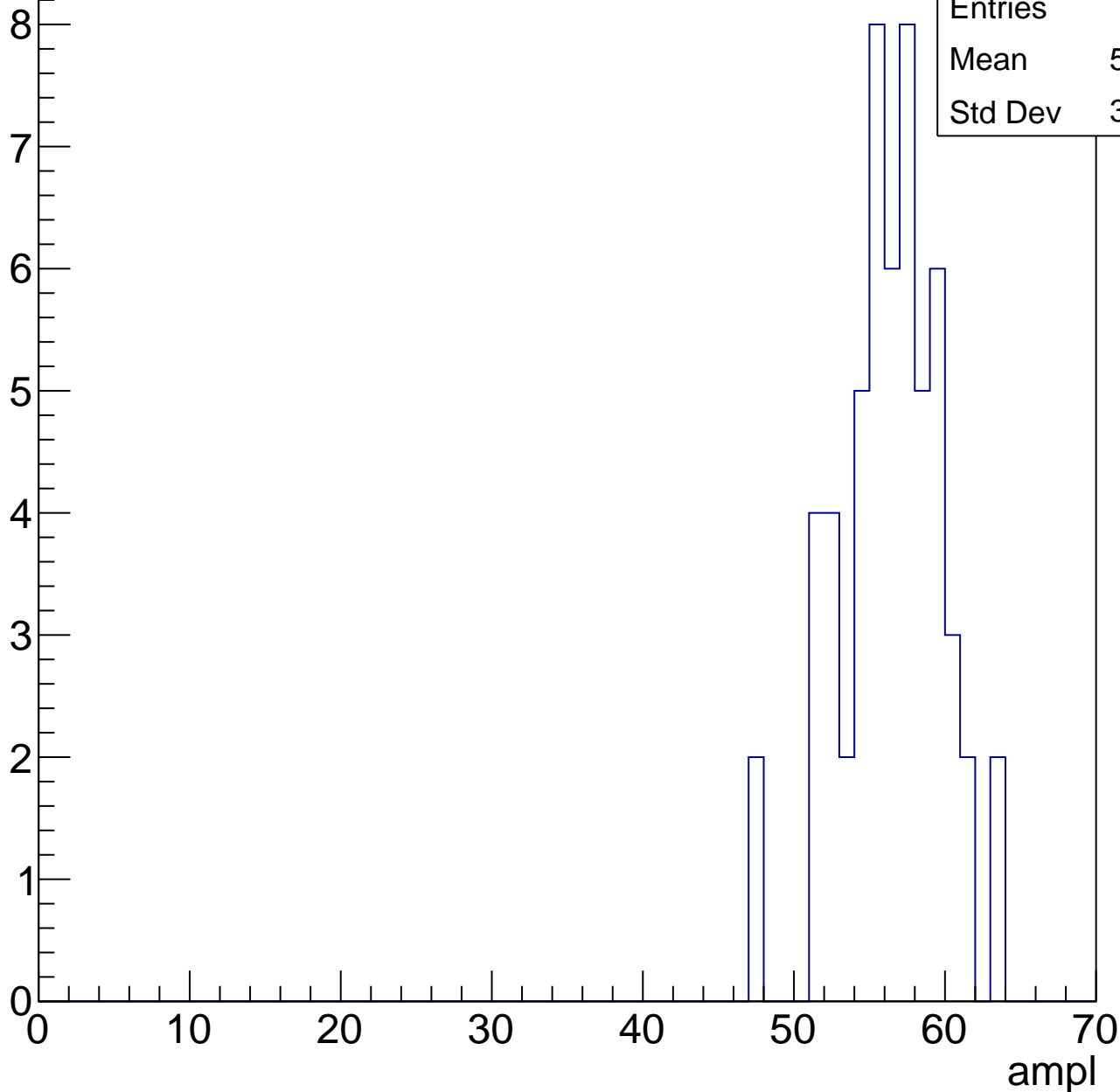


B1L102S, U11-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.89
Std Dev	3.375

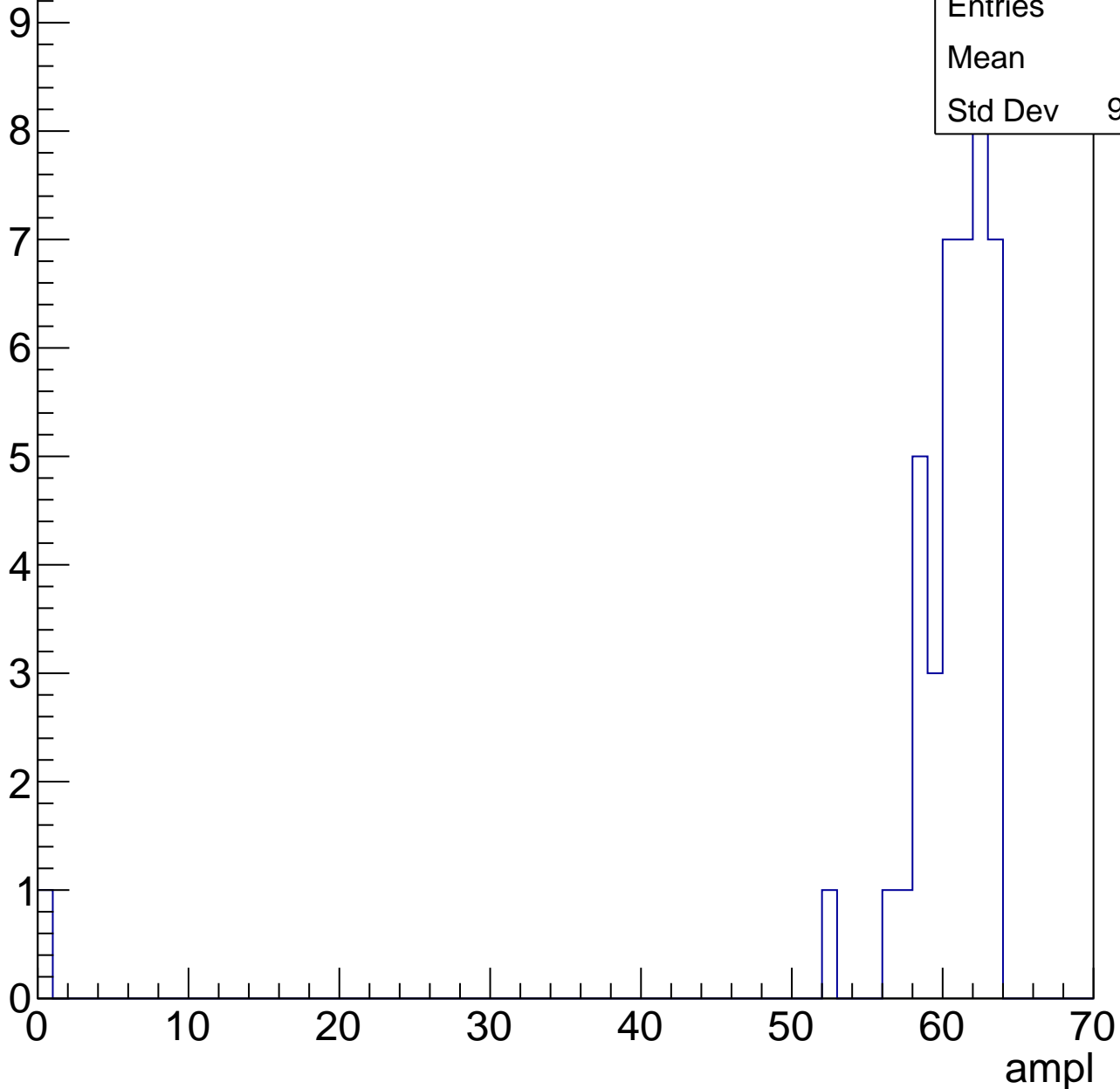


B1L102S, U11-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	59
Std Dev	9.482



B1L102S, U11-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries	4
Mean	60.5
Std Dev	2.693

B1L102S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

