

B1L002S, U7-ch0, adc0

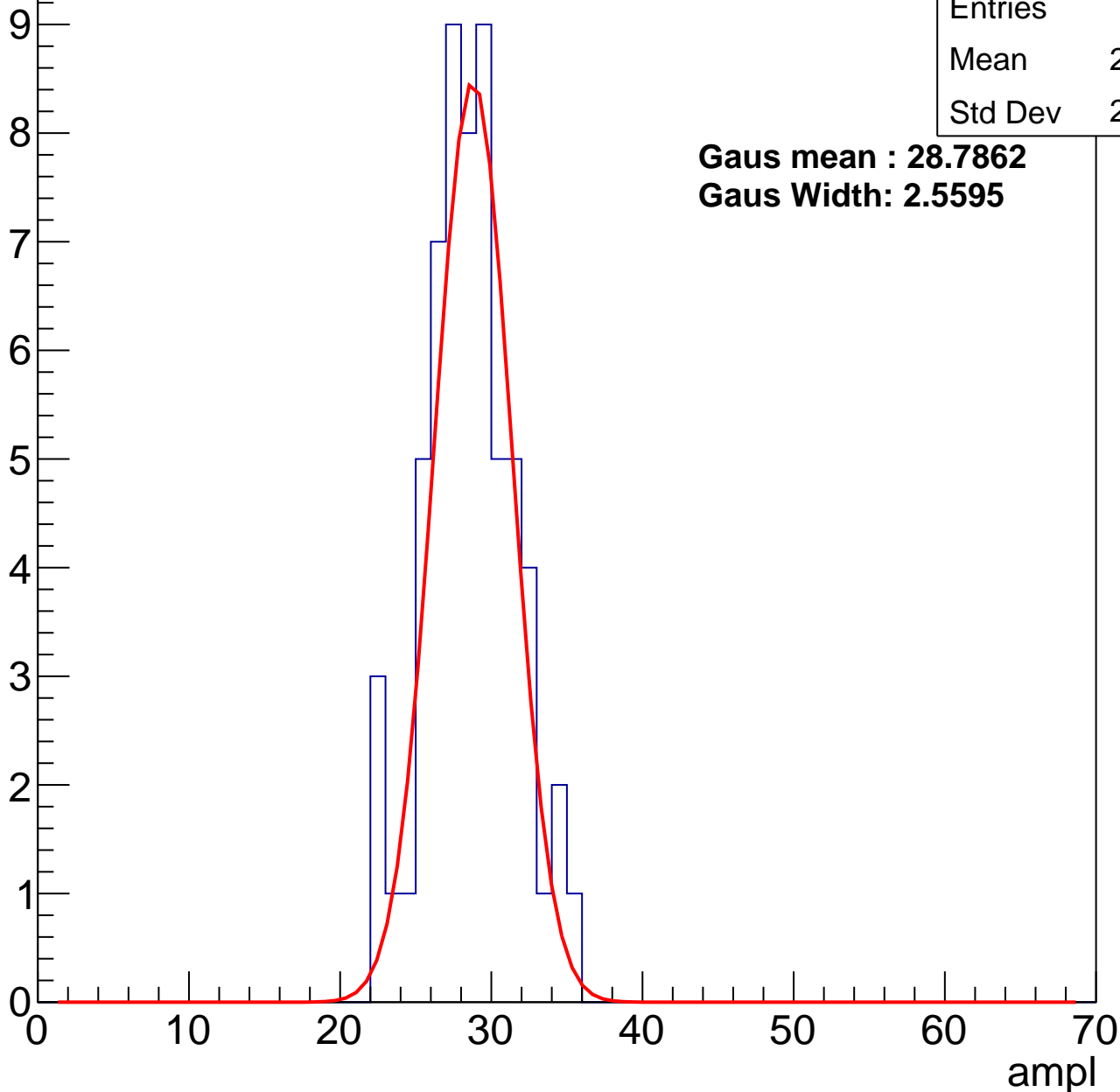
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.15
Std Dev	2.902

Gaus mean : 28.7862

Gaus Width: 2.5595



B1L002S, U7-ch0, adc1

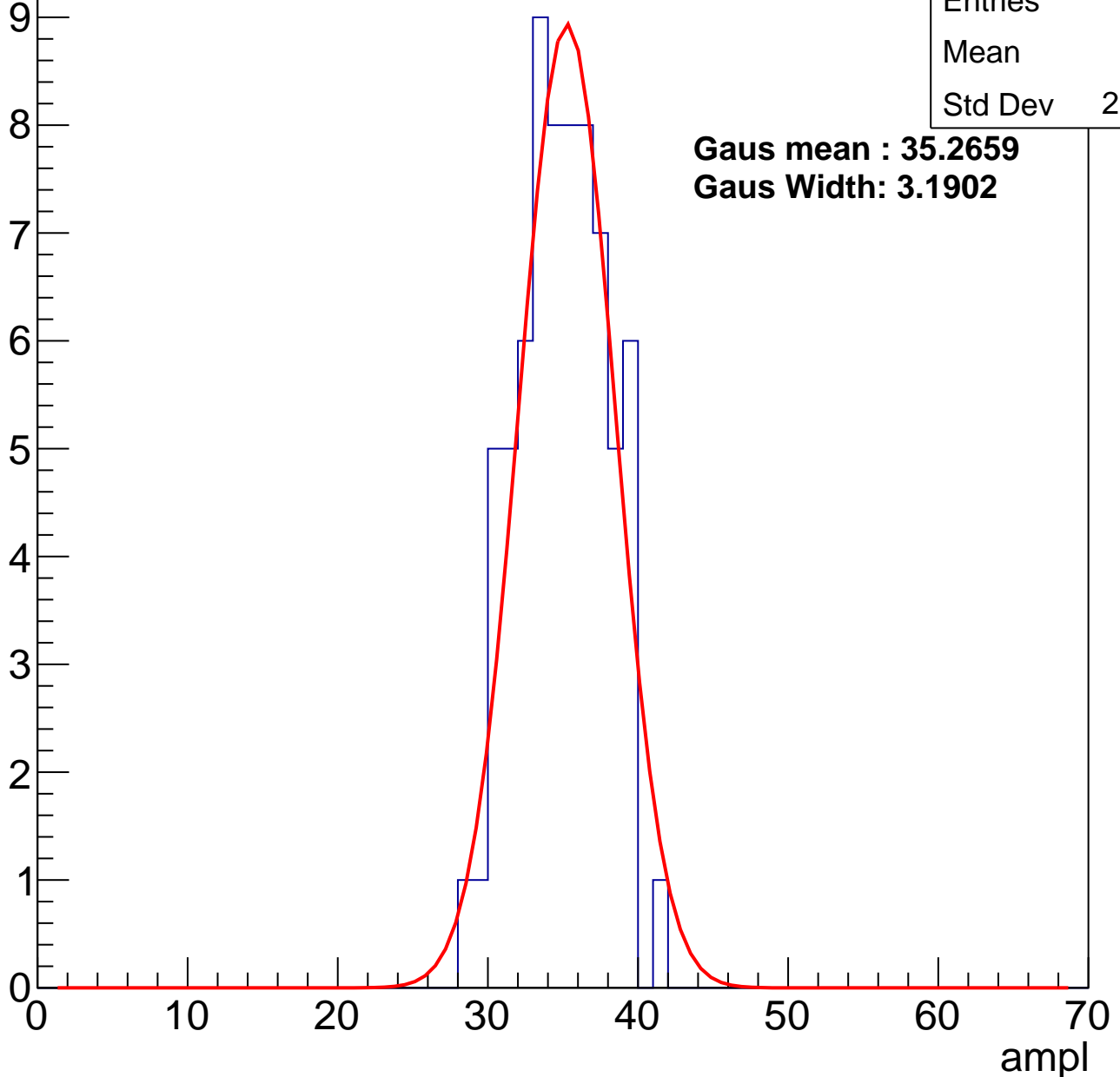
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	34.5
Std Dev	2.887

Gaus mean : 35.2659

Gaus Width: 3.1902



B1L002S, U7-ch0, adc2

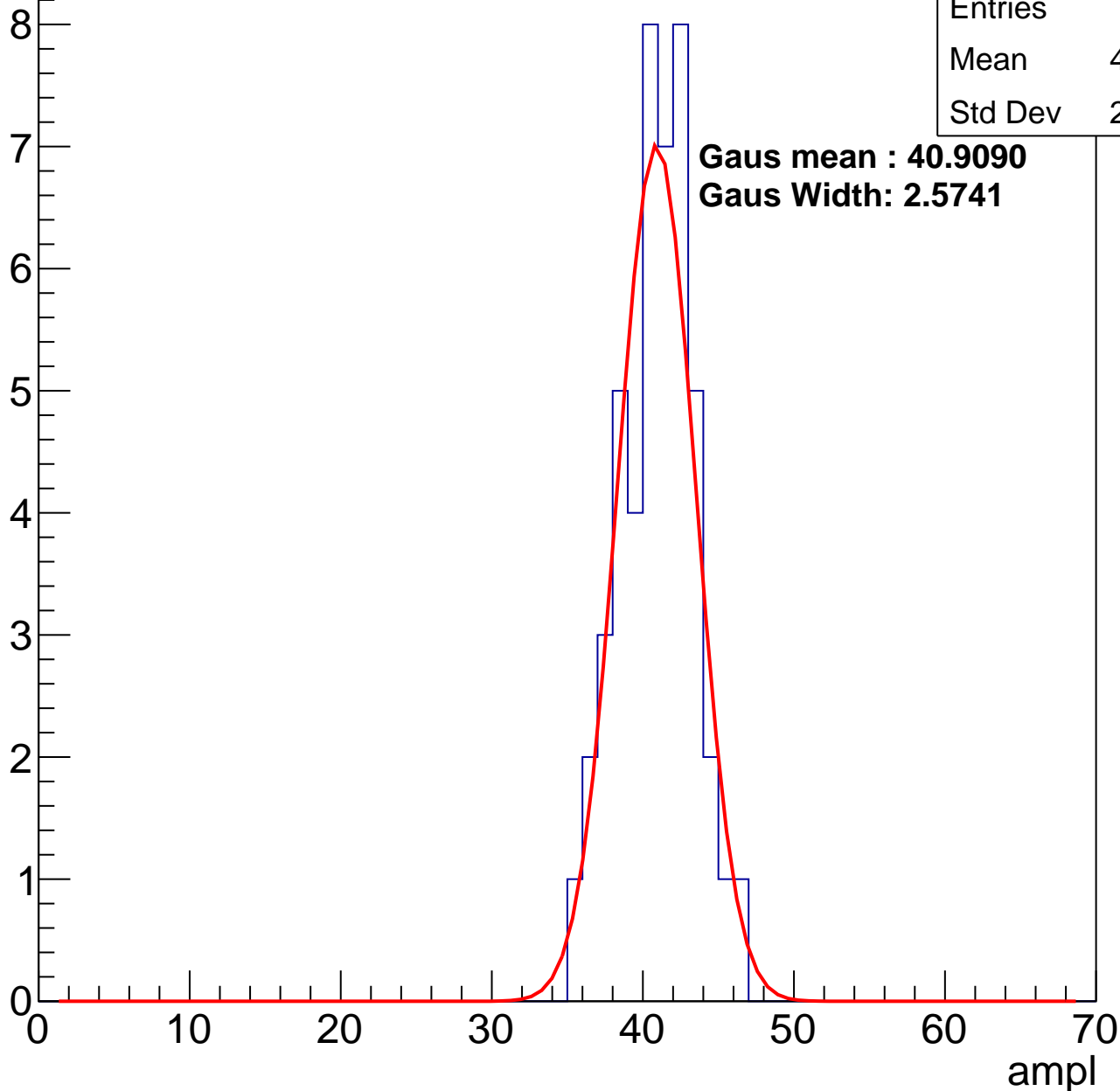
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	40.45
Std Dev	2.422

Gaus mean : 40.9090

Gaus Width: 2.5741

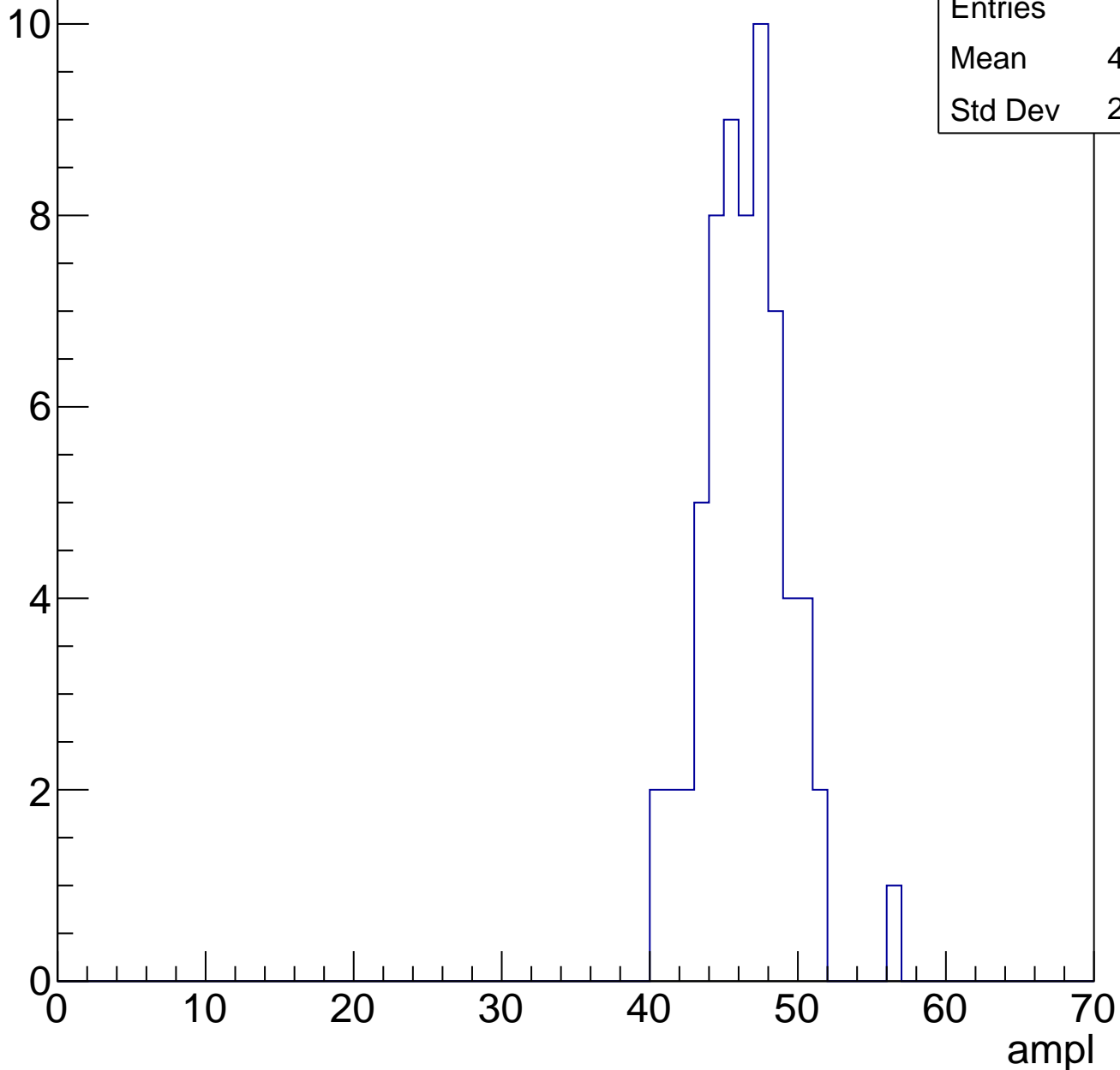


B1L002S, U7-ch0, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	64
Mean	46.03
Std Dev	2.872

Entry

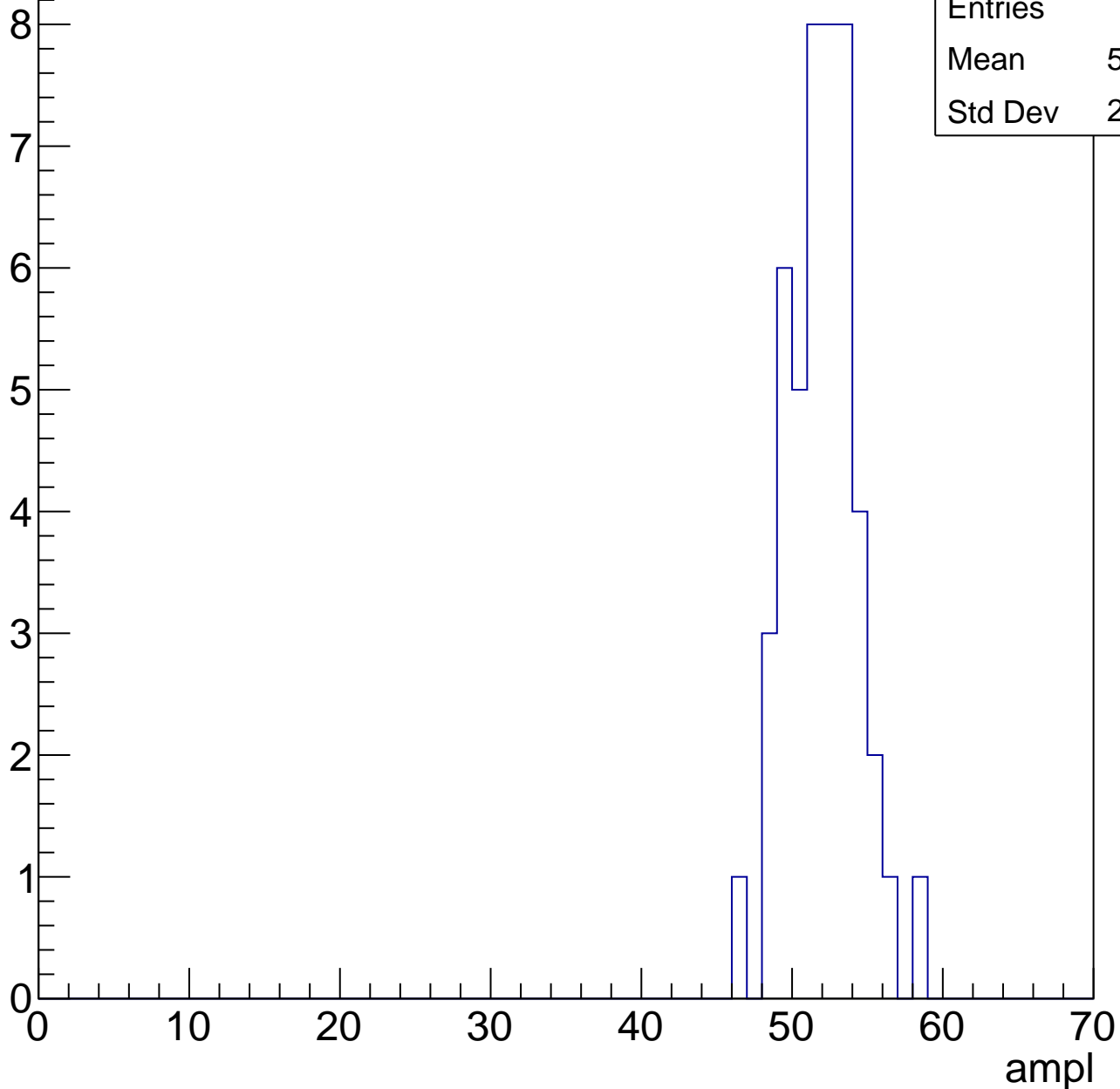


B1L002S, U7-ch0, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

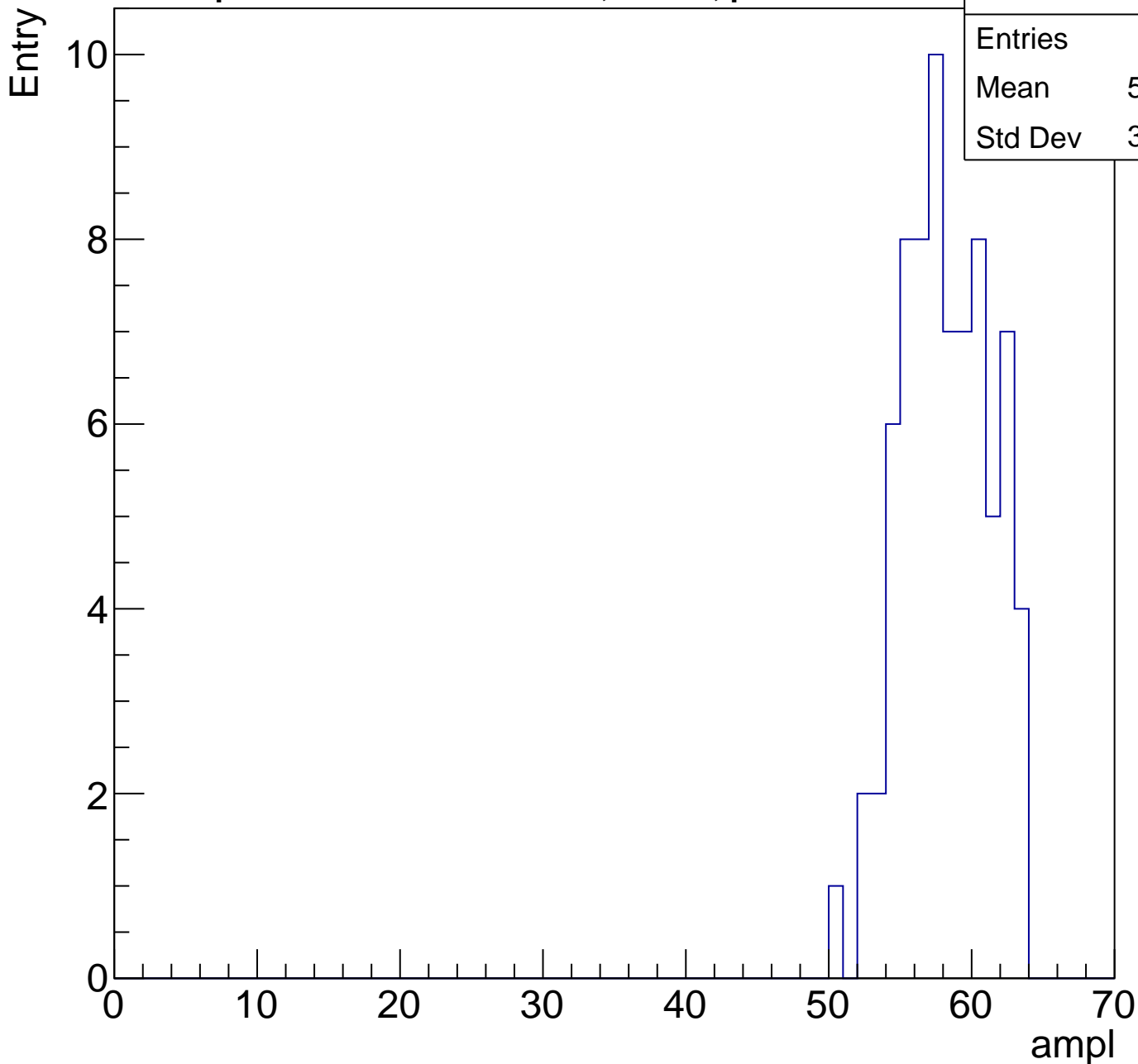
Entries	47
Mean	51.53
Std Dev	2.305



B1L002S, U7-ch0, adc5

calib_packv5_042523_0143.root, FC#10, port B3

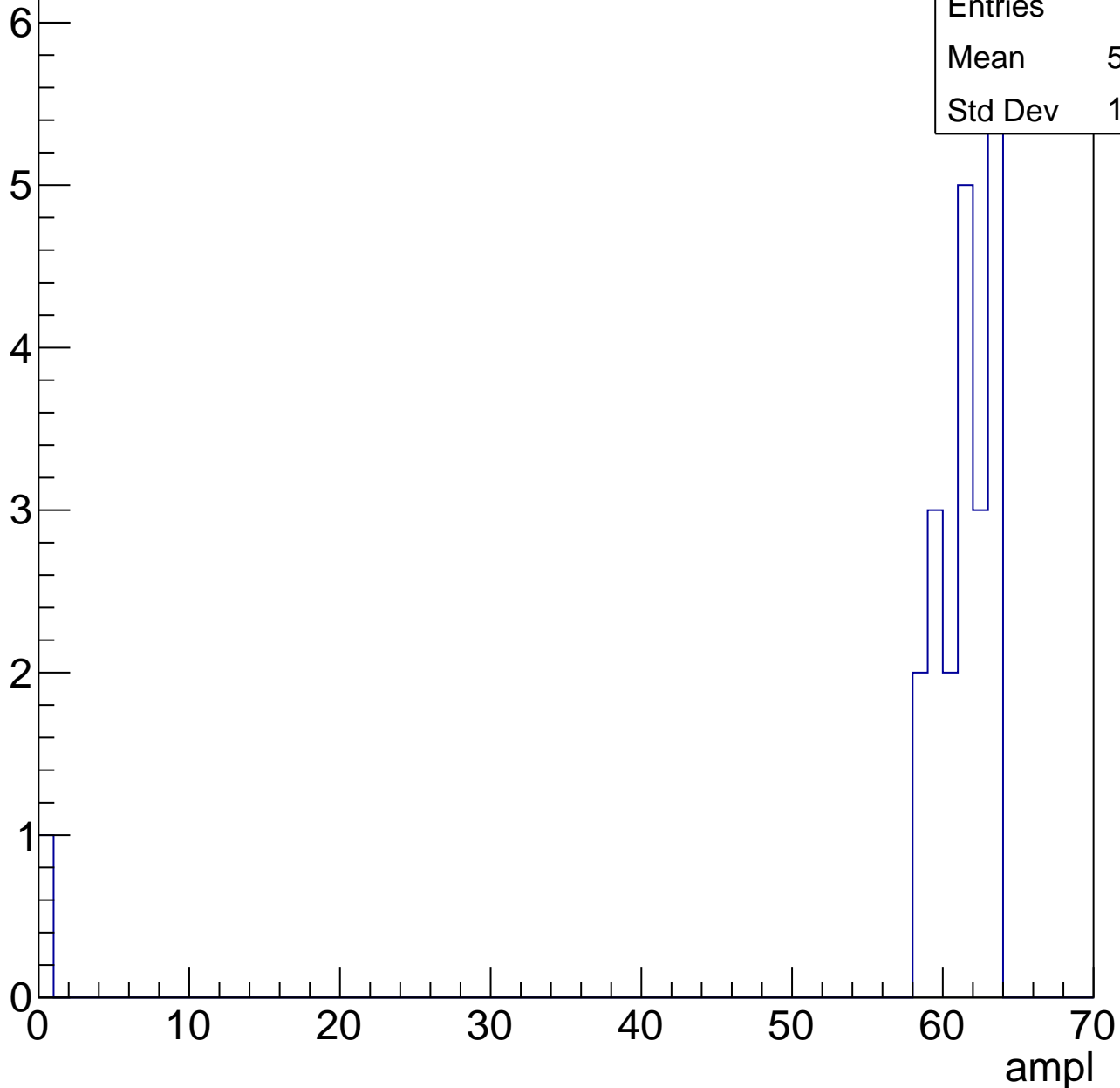
Entries	75
Mean	57.76
Std Dev	3.028



B1L002S, U7-ch0, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch0, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch1, adc0

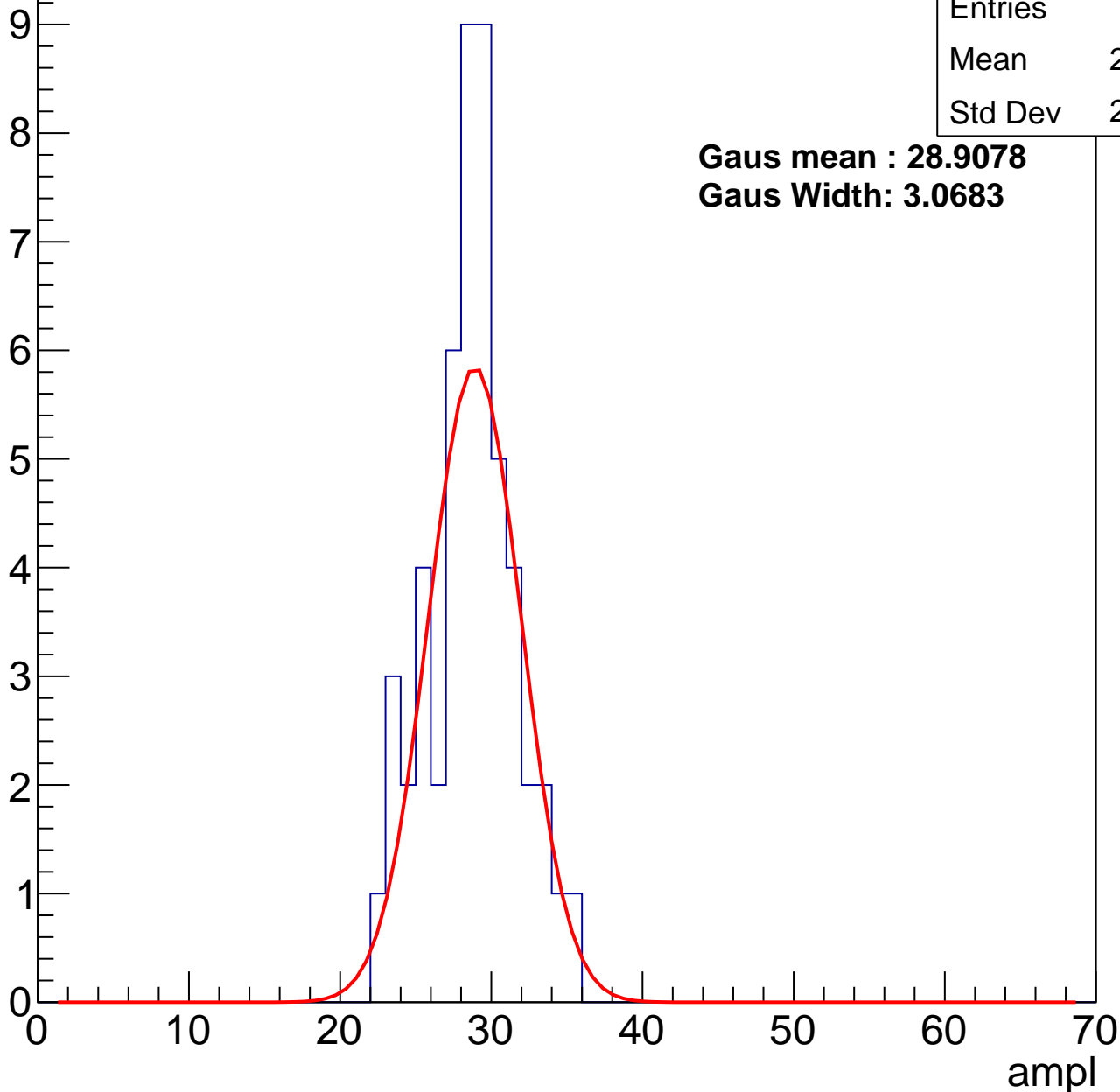
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	28.22
Std Dev	2.879

Gaus mean : 28.9078

Gaus Width: 3.0683



B1L002S, U7-ch1, adc1

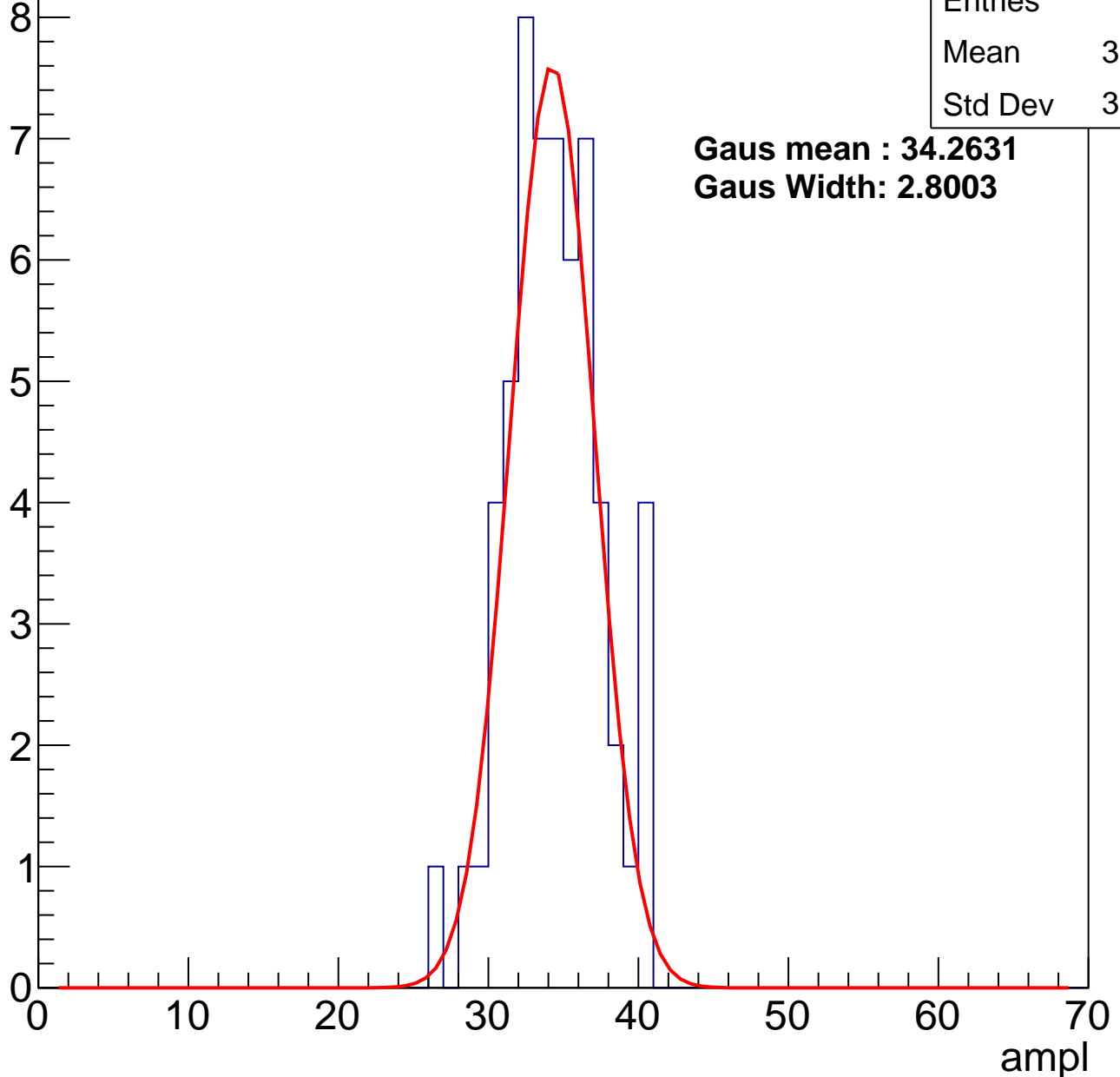
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	33.93
Std Dev	3.062

Gaus mean : 34.2631

Gaus Width: 2.8003



B1L002S, U7-ch1, adc2

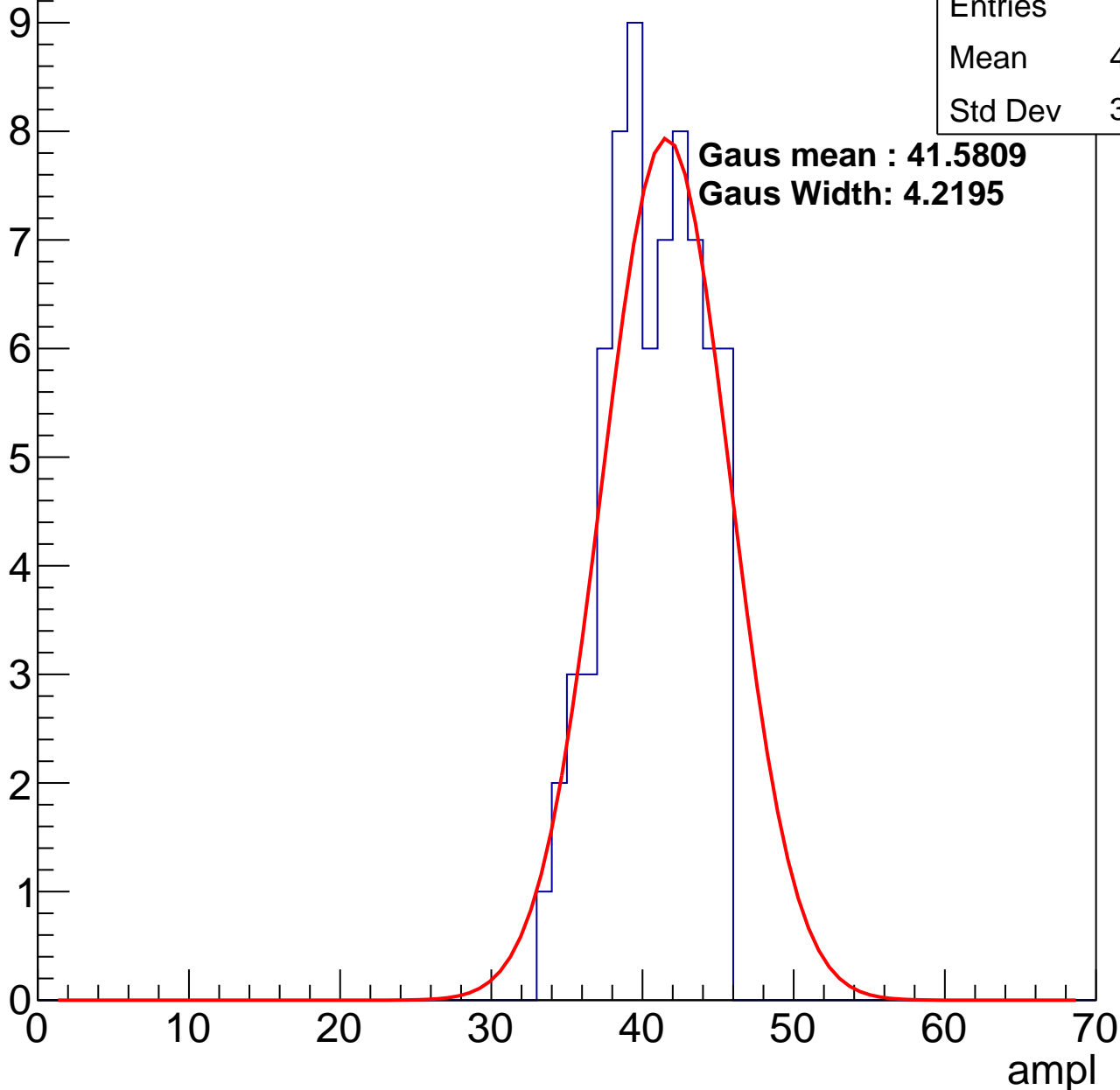
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	40.12
Std Dev	3.086

Gaus mean : 41.5809

Gaus Width: 4.2195

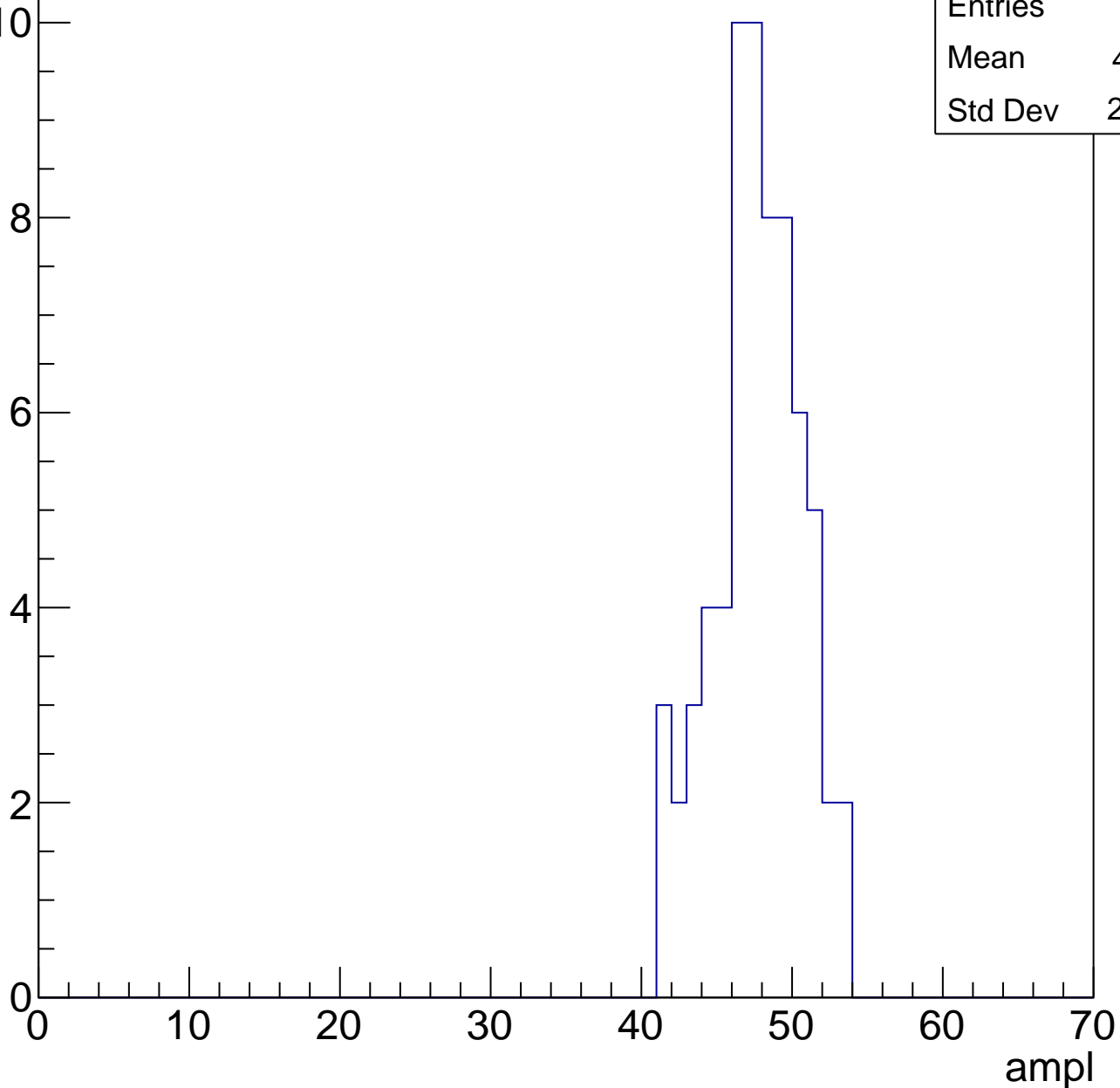


B1L002S, U7-ch1, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	47.21
Std Dev	2.894

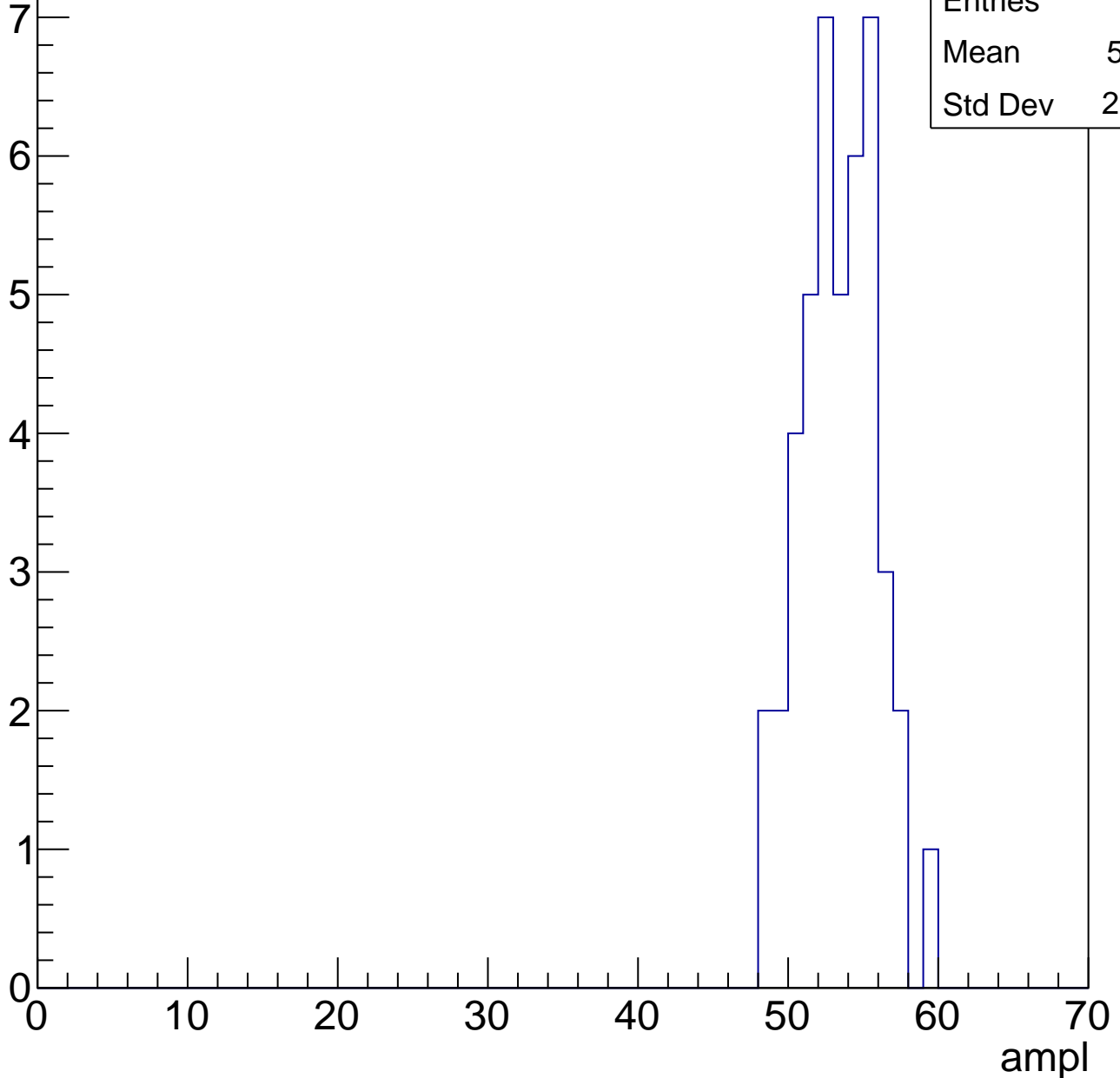


B1L002S, U7-ch1, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	44
Mean	52.91
Std Dev	2.494



B1L002S, U7-ch1, adc5

calib_packv5_042523_0143.root, FC#10, port B3

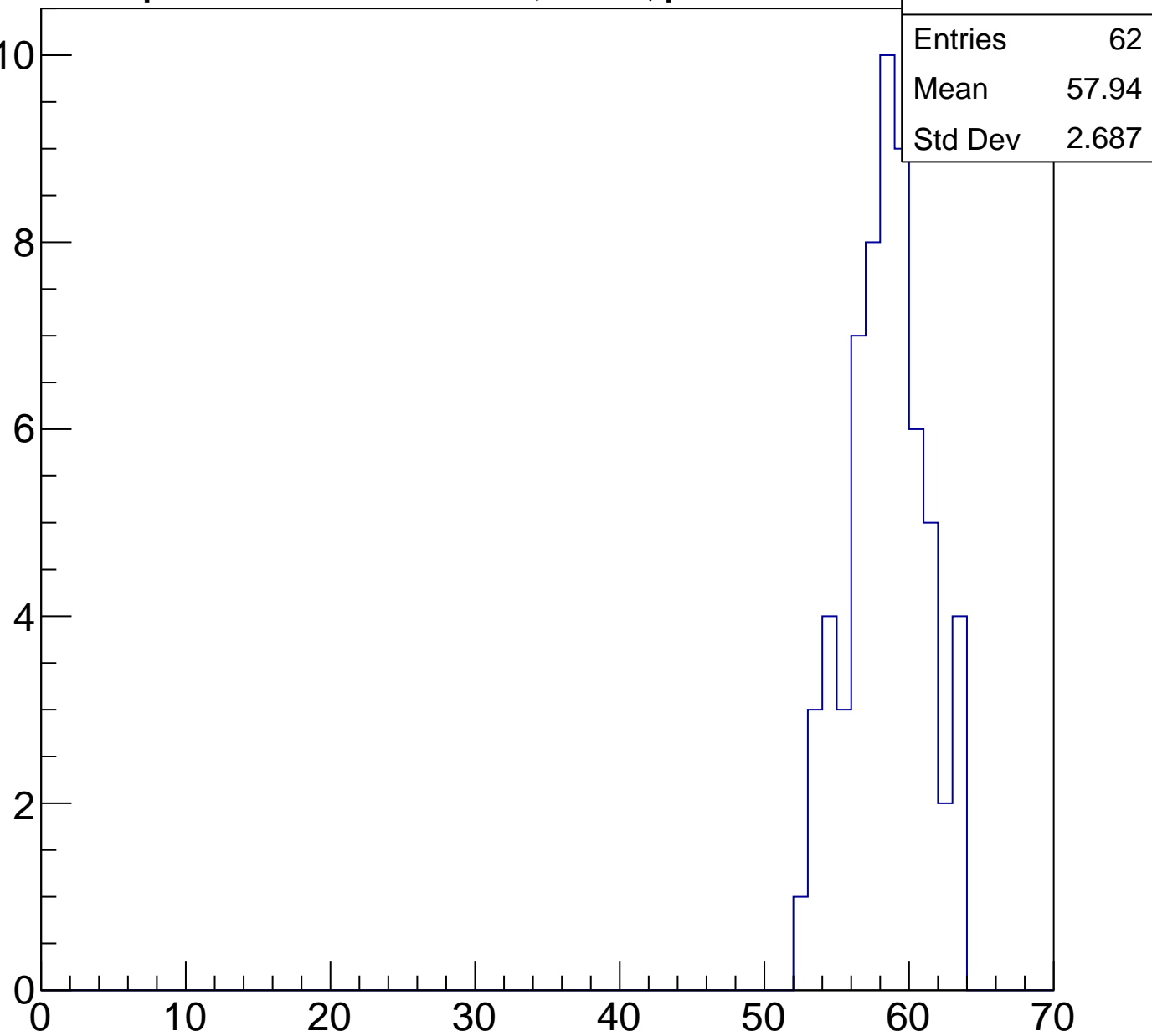
Entries	62
Mean	57.94
Std Dev	2.687

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

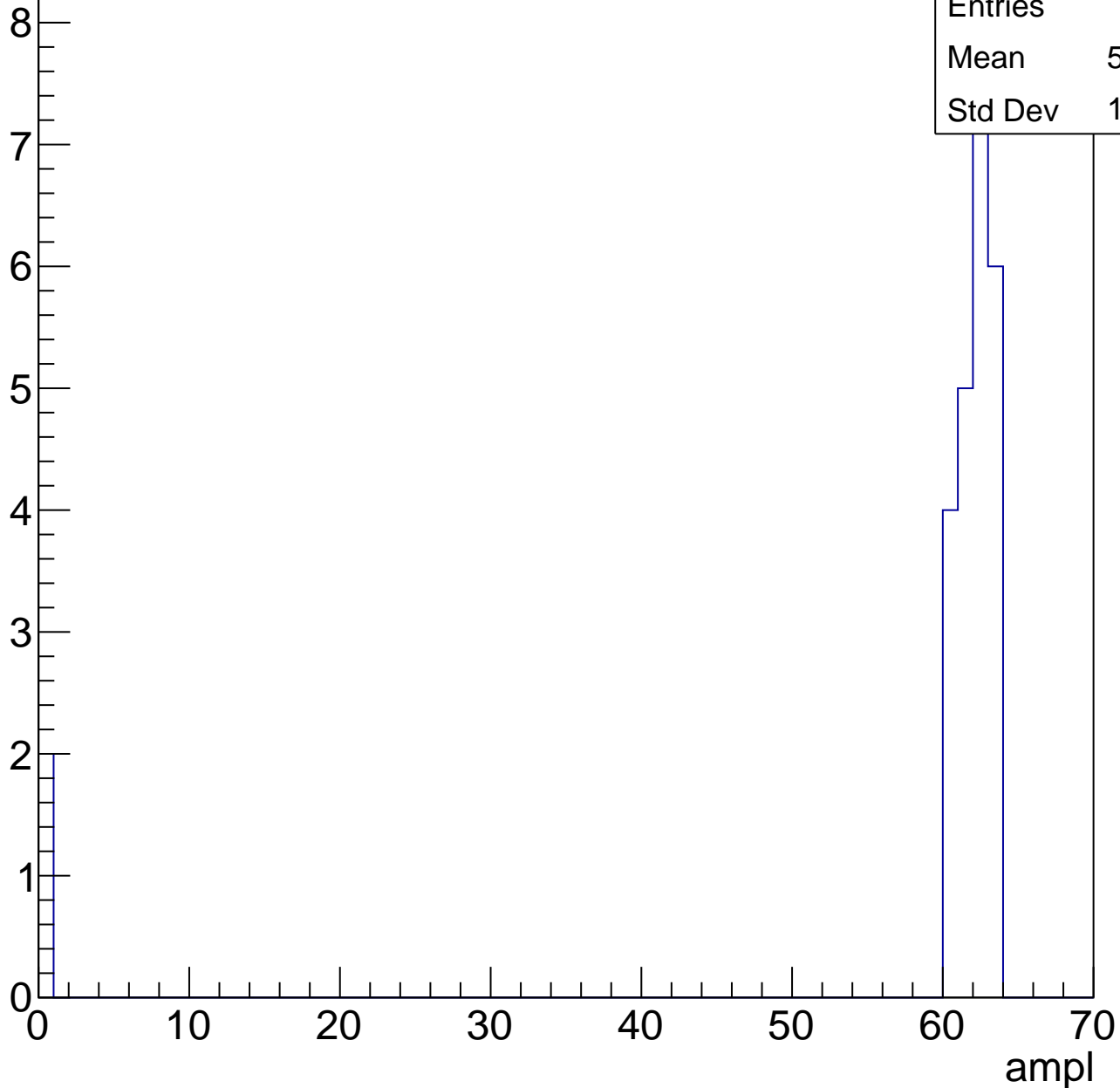


B1L002S, U7-ch1, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	25
Mean	56.76
Std Dev	16.77



B1L002S, U7-ch1, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L002S, U7-ch2, adc0

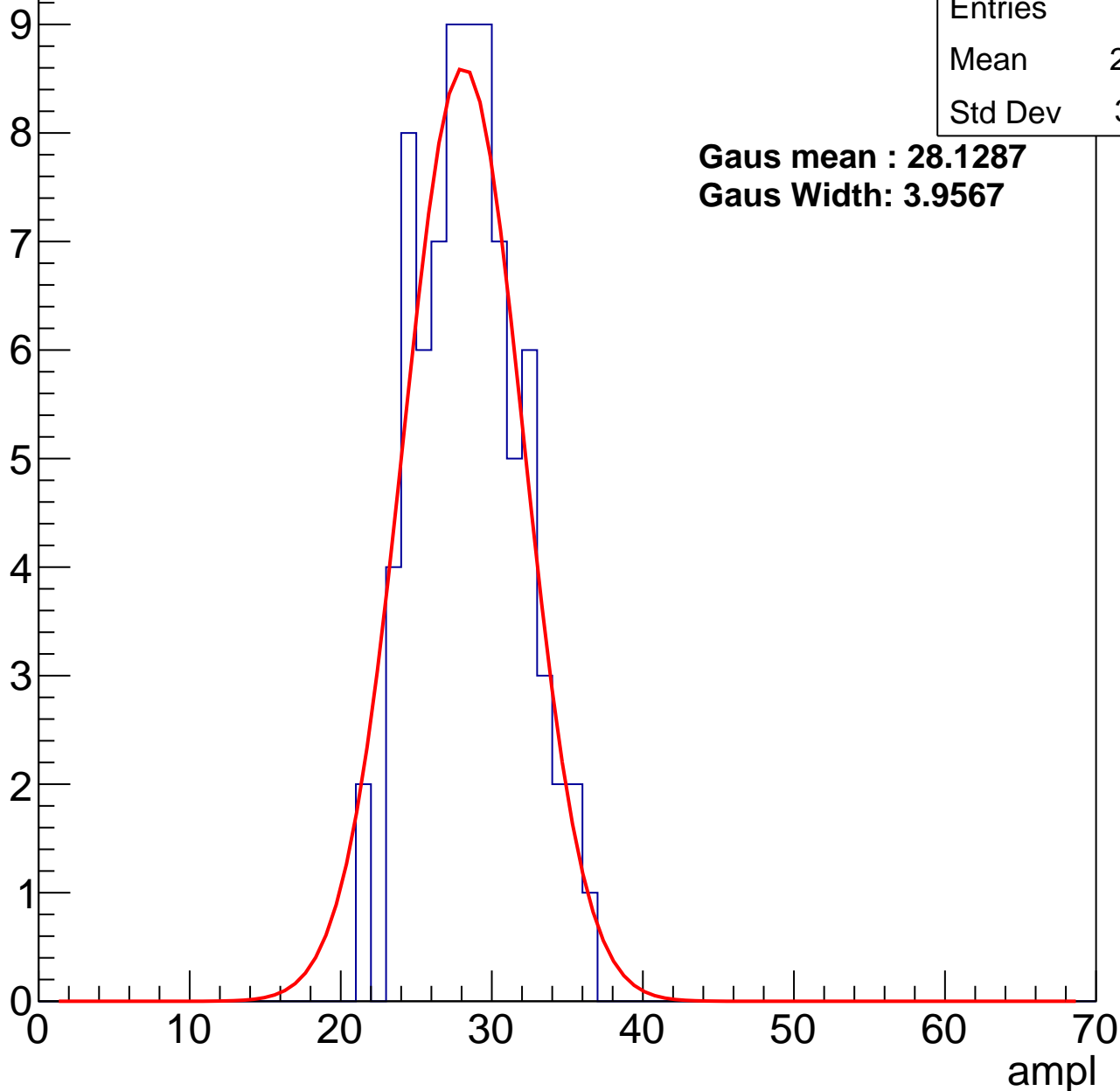
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	80
Mean	28.05
Std Dev	3.361

Gaus mean : 28.1287

Gaus Width: 3.9567



B1L002S, U7-ch2, adc1

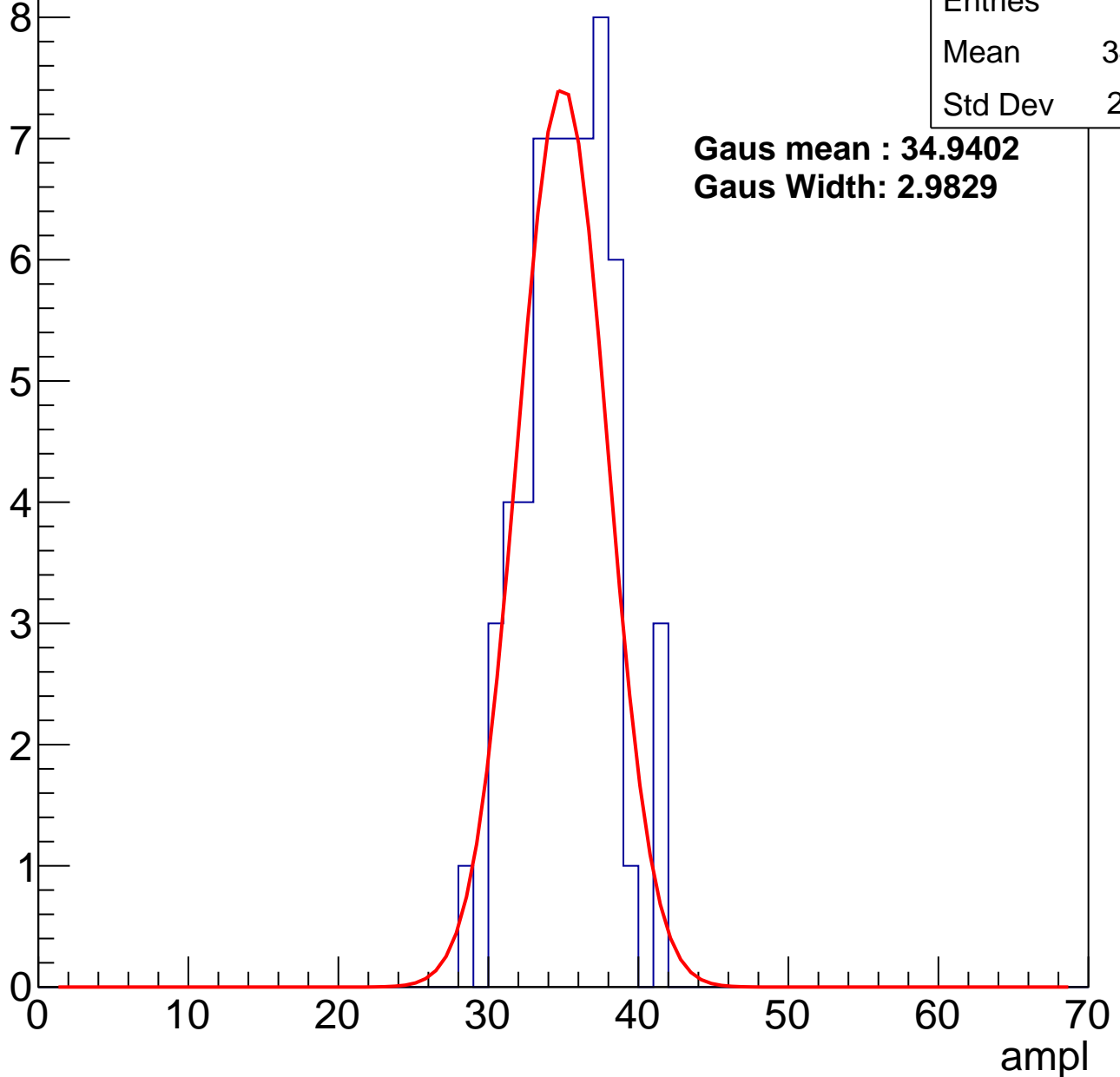
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	34.86
Std Dev	2.861

Gaus mean : 34.9402

Gaus Width: 2.9829



B1L002S, U7-ch2, adc2

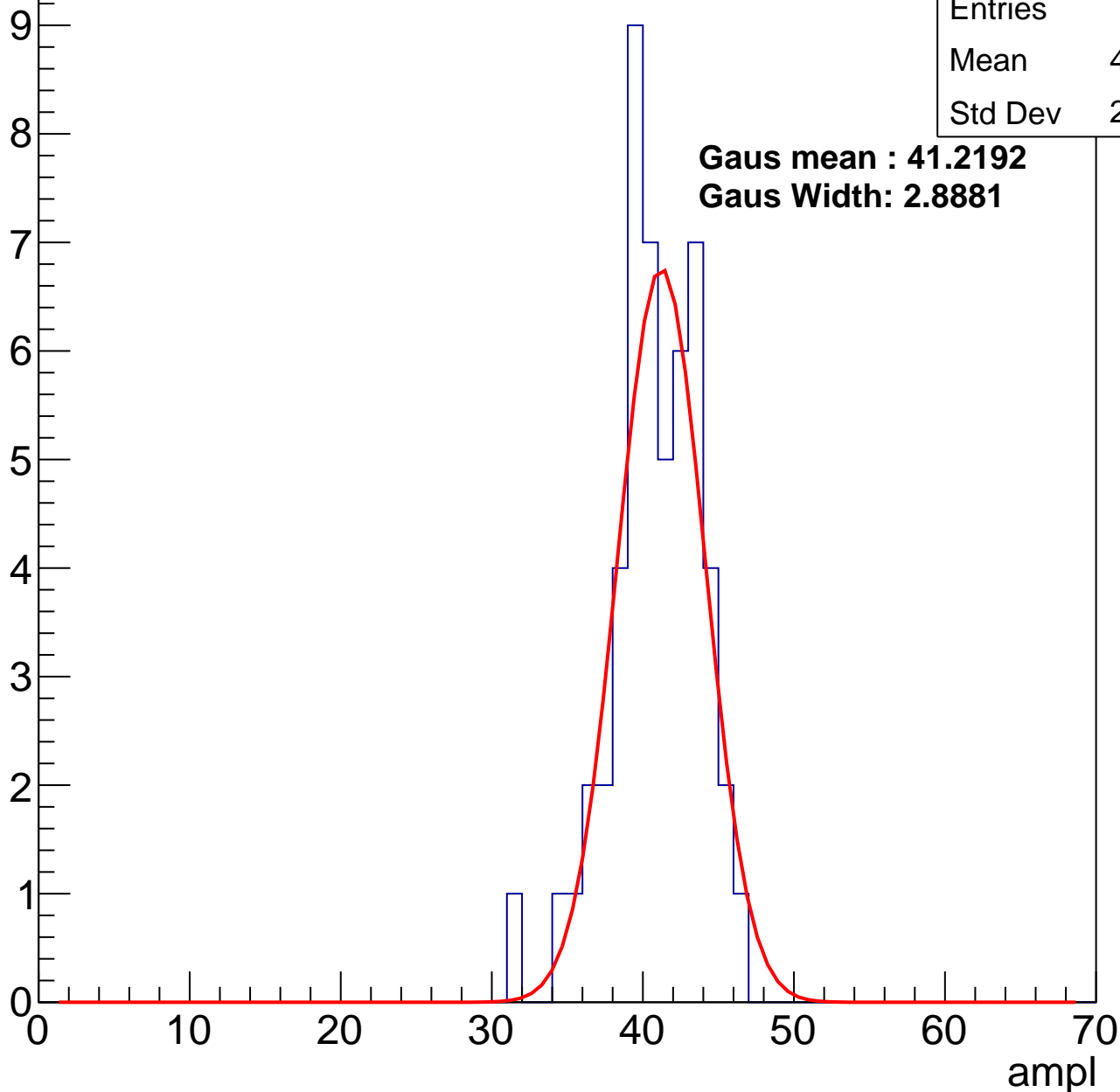
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.37
Std Dev	2.948

Gaus mean : 41.2192

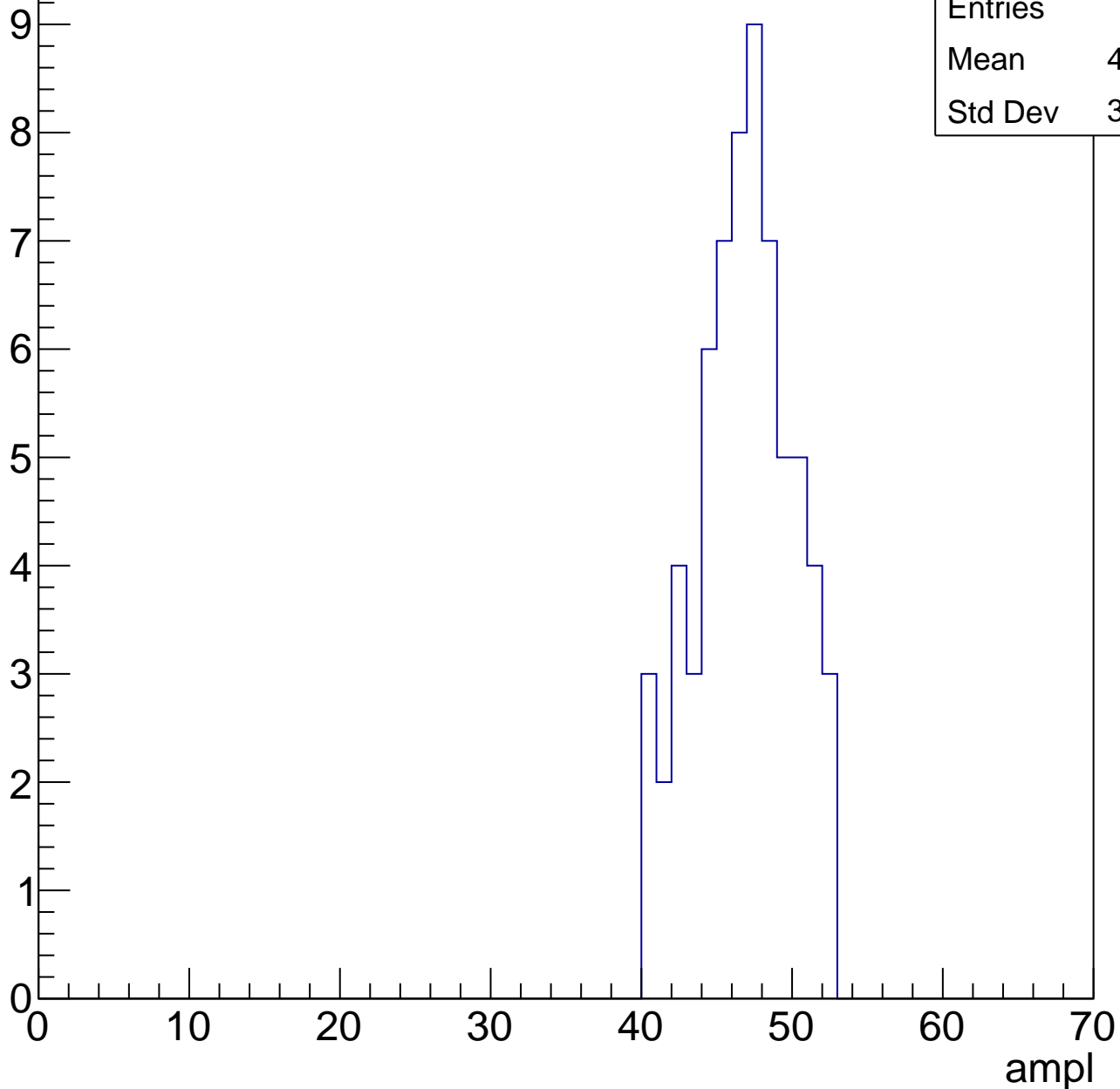
Gaus Width: 2.8881



B1L002S, U7-ch2, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



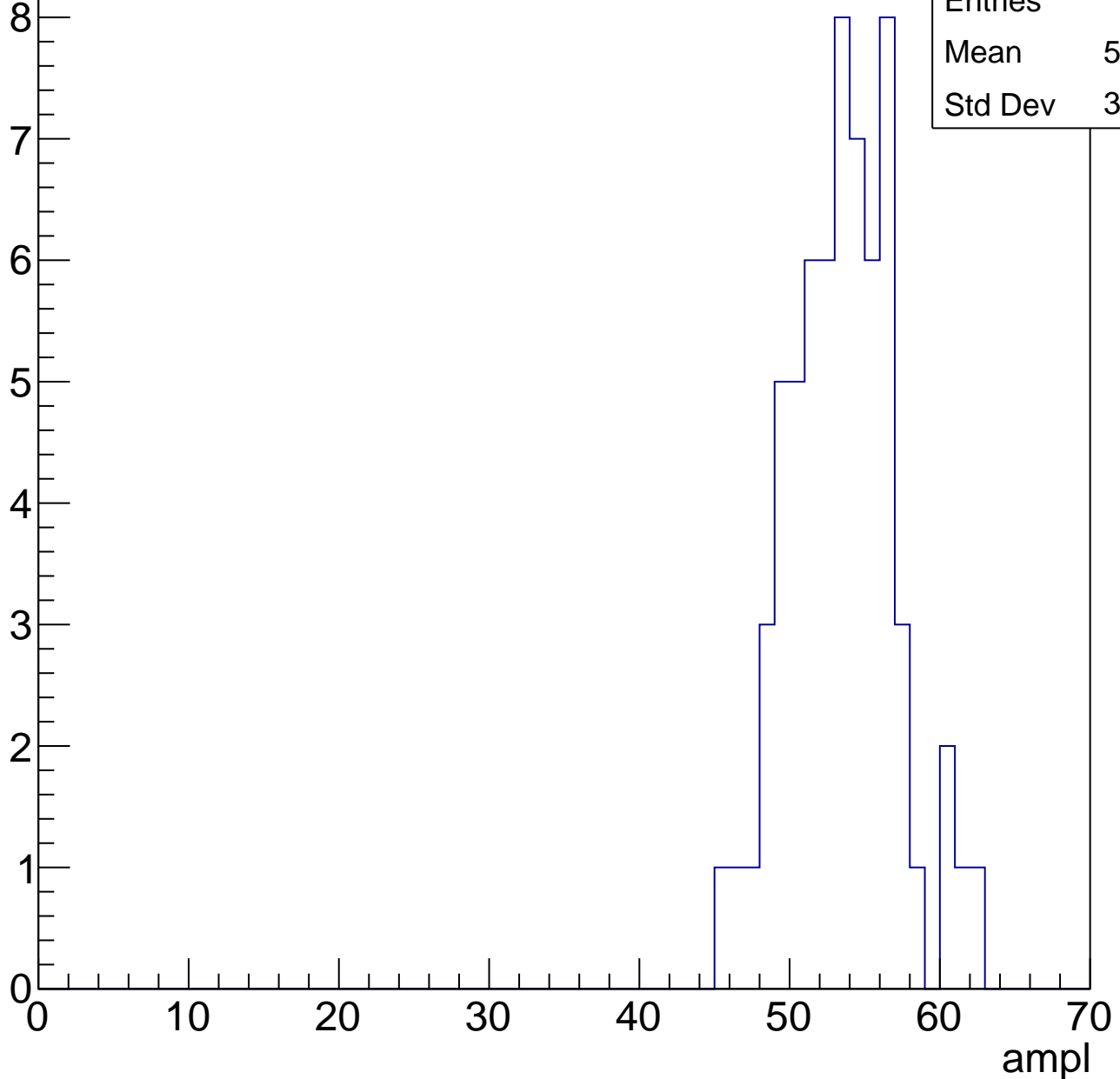
Entries	66
Mean	46.36
Std Dev	3.117

B1L002S, U7-ch2, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	53.03
Std Dev	3.504



B1L002S, U7-ch2, adc5

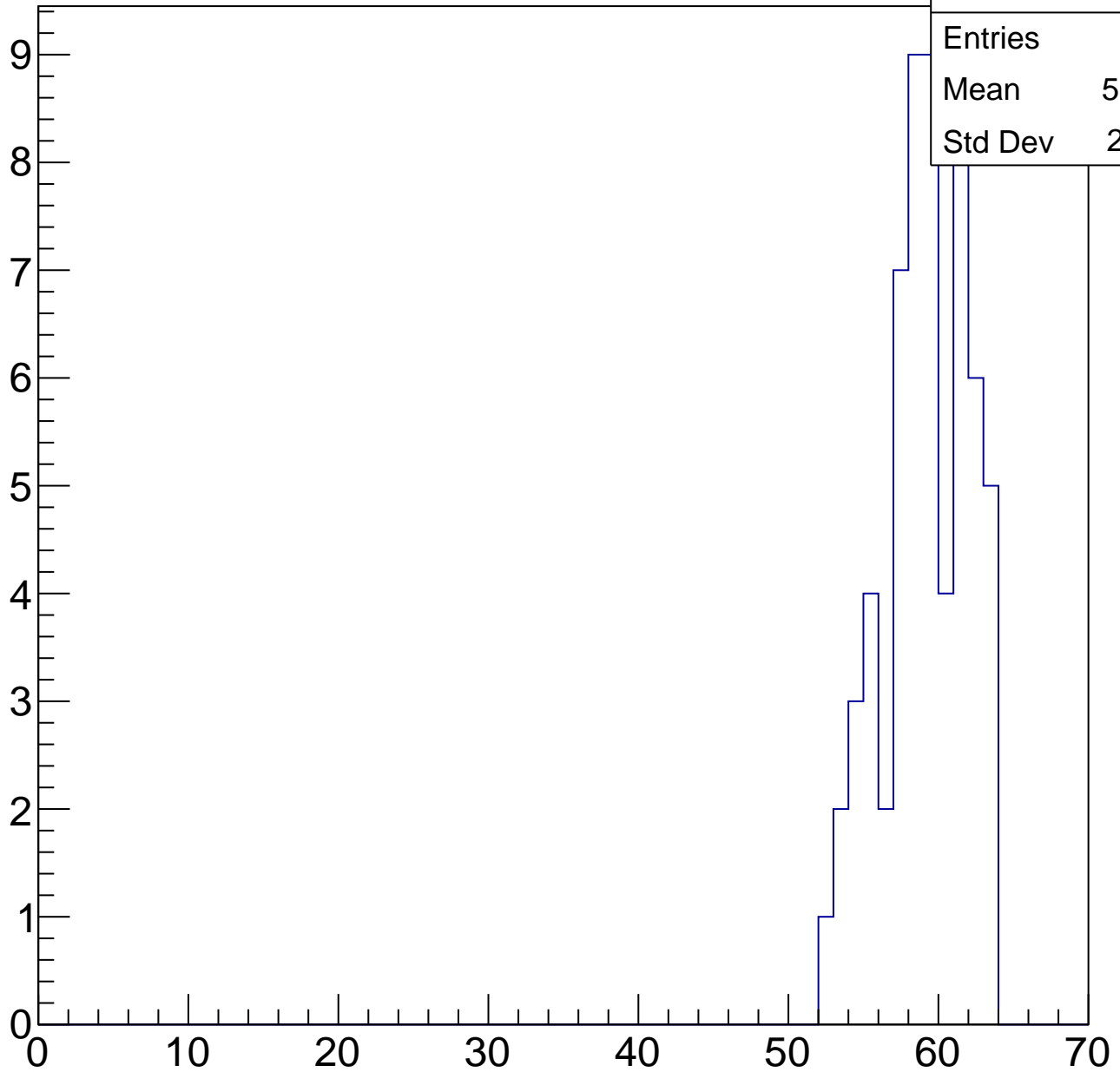
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.65
Std Dev	2.821

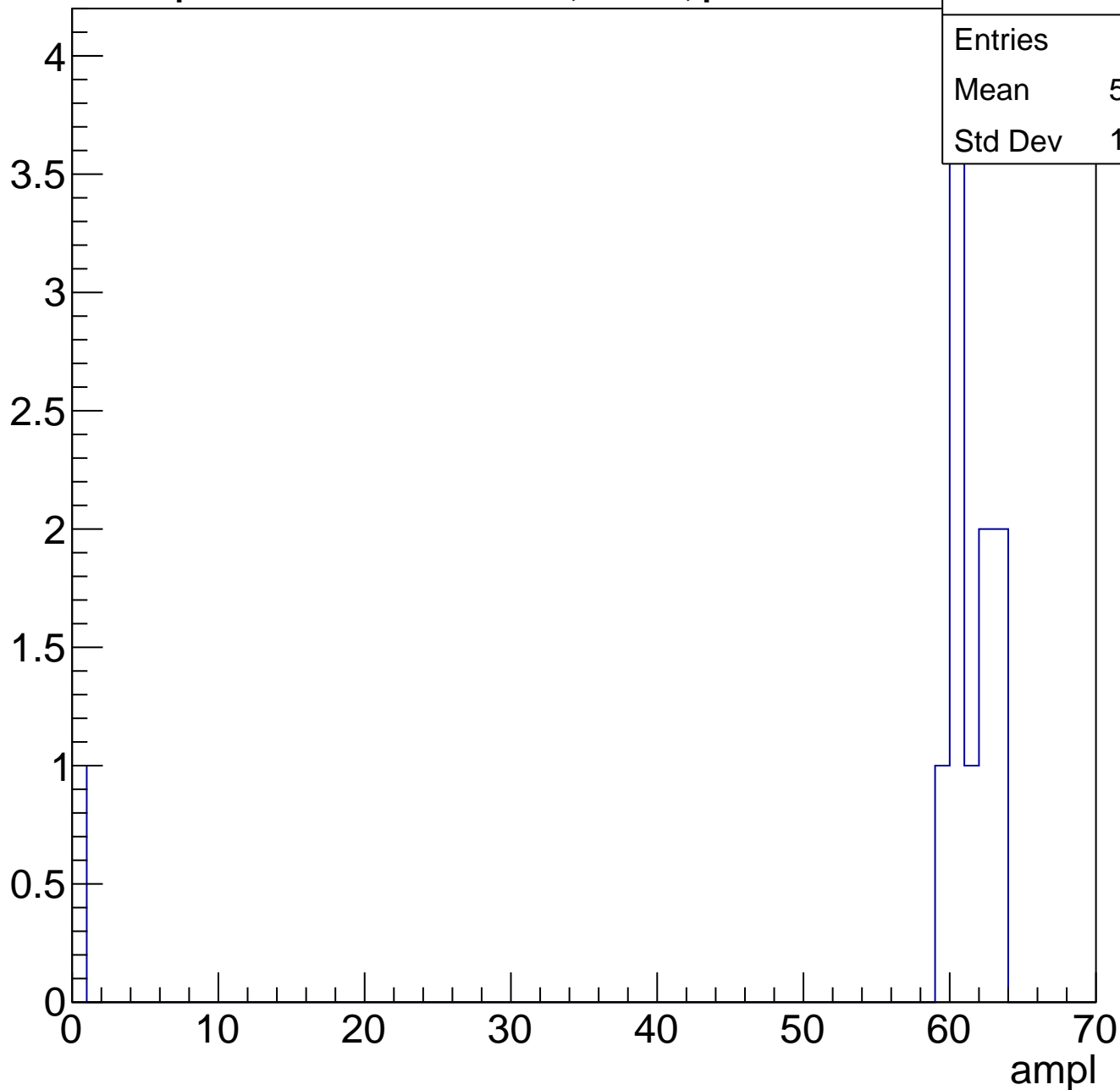
ampl



B1L002S, U7-ch2, adc6

calib_packv5_042523_0143.root, FC#10, port B3

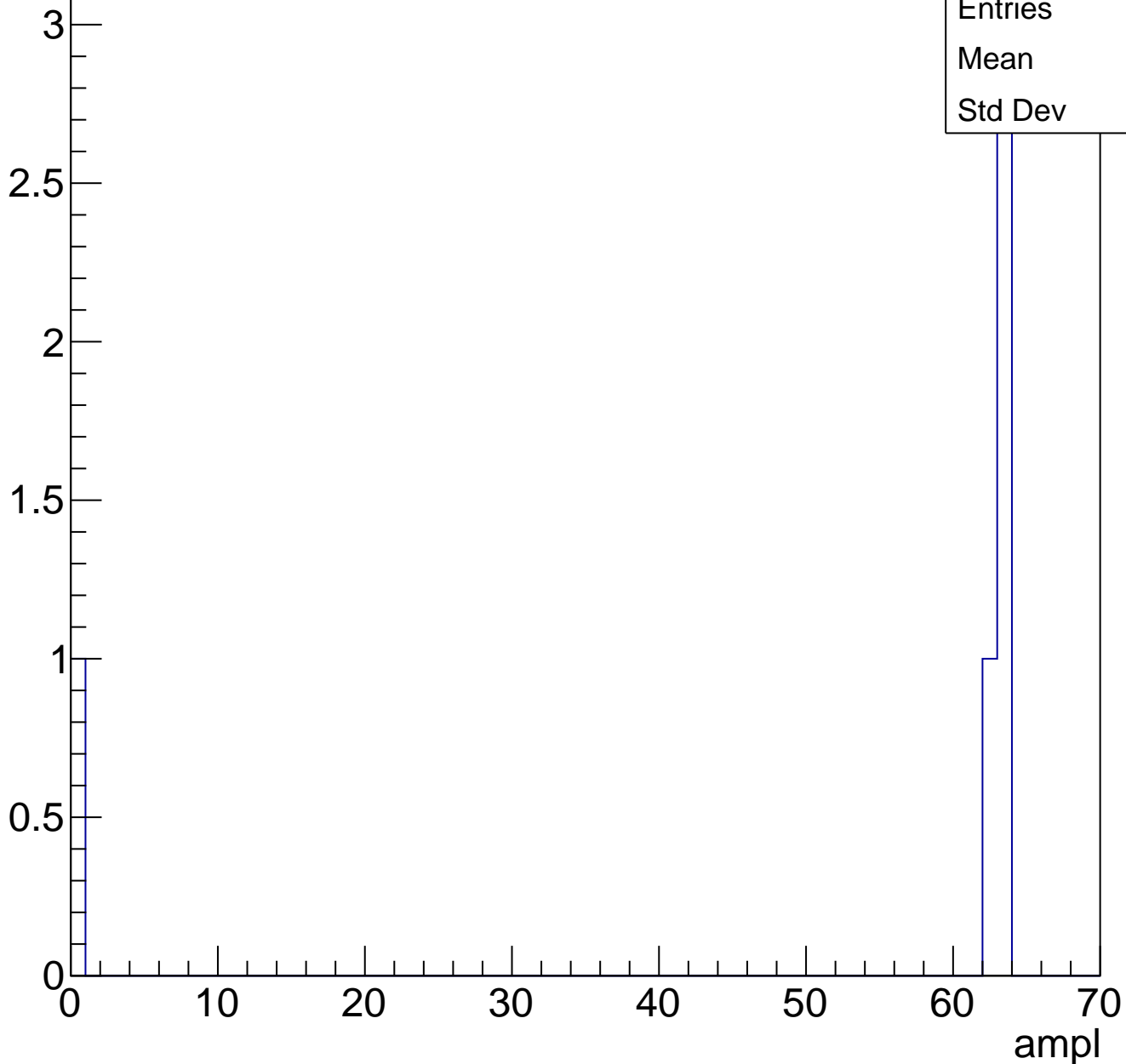
Entry



B1L002S, U7-ch2, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch3, adc0

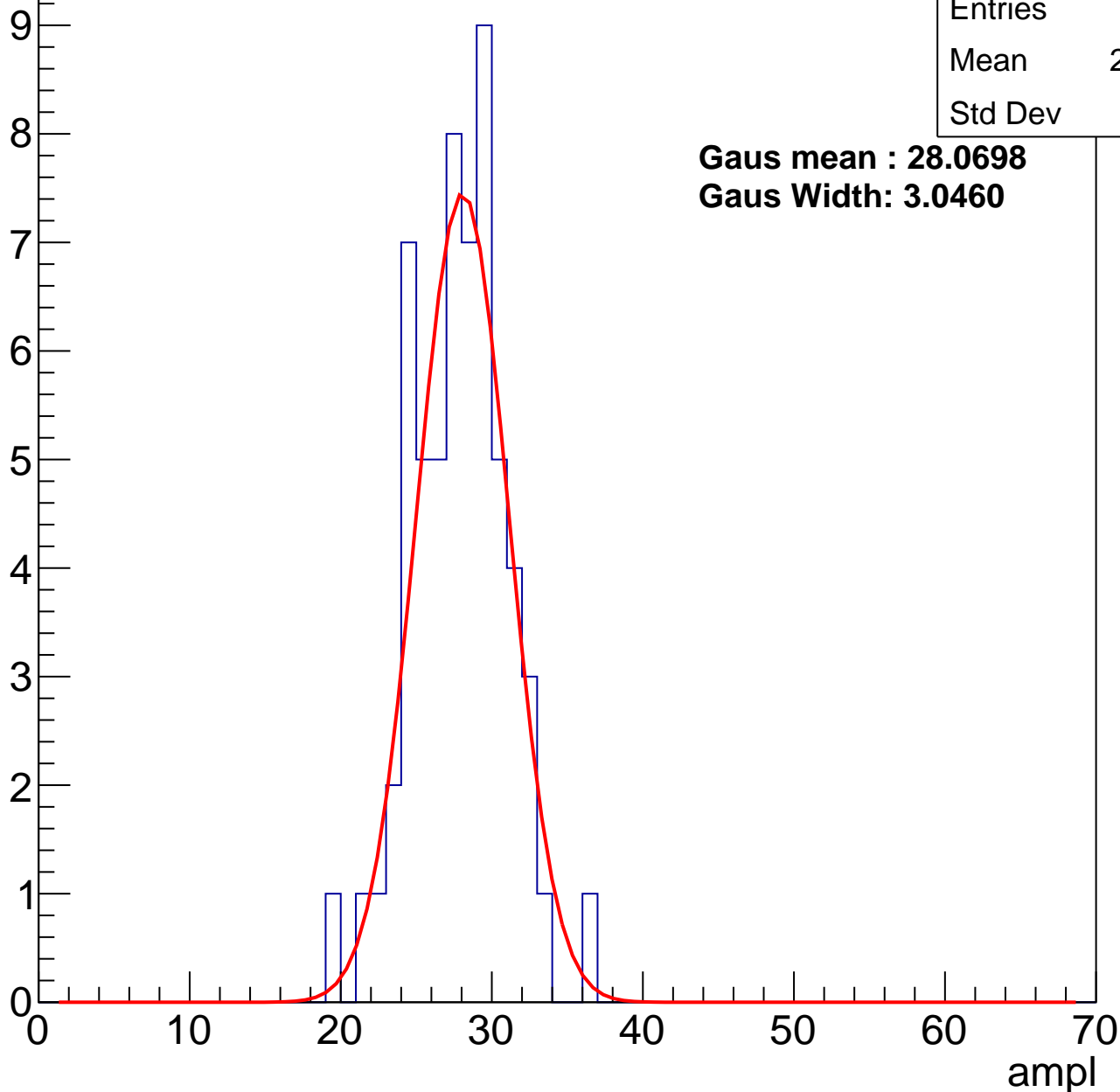
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.38
Std Dev	3.11

Gaus mean : 28.0698

Gaus Width: 3.0460



B1L002S, U7-ch3, adc1

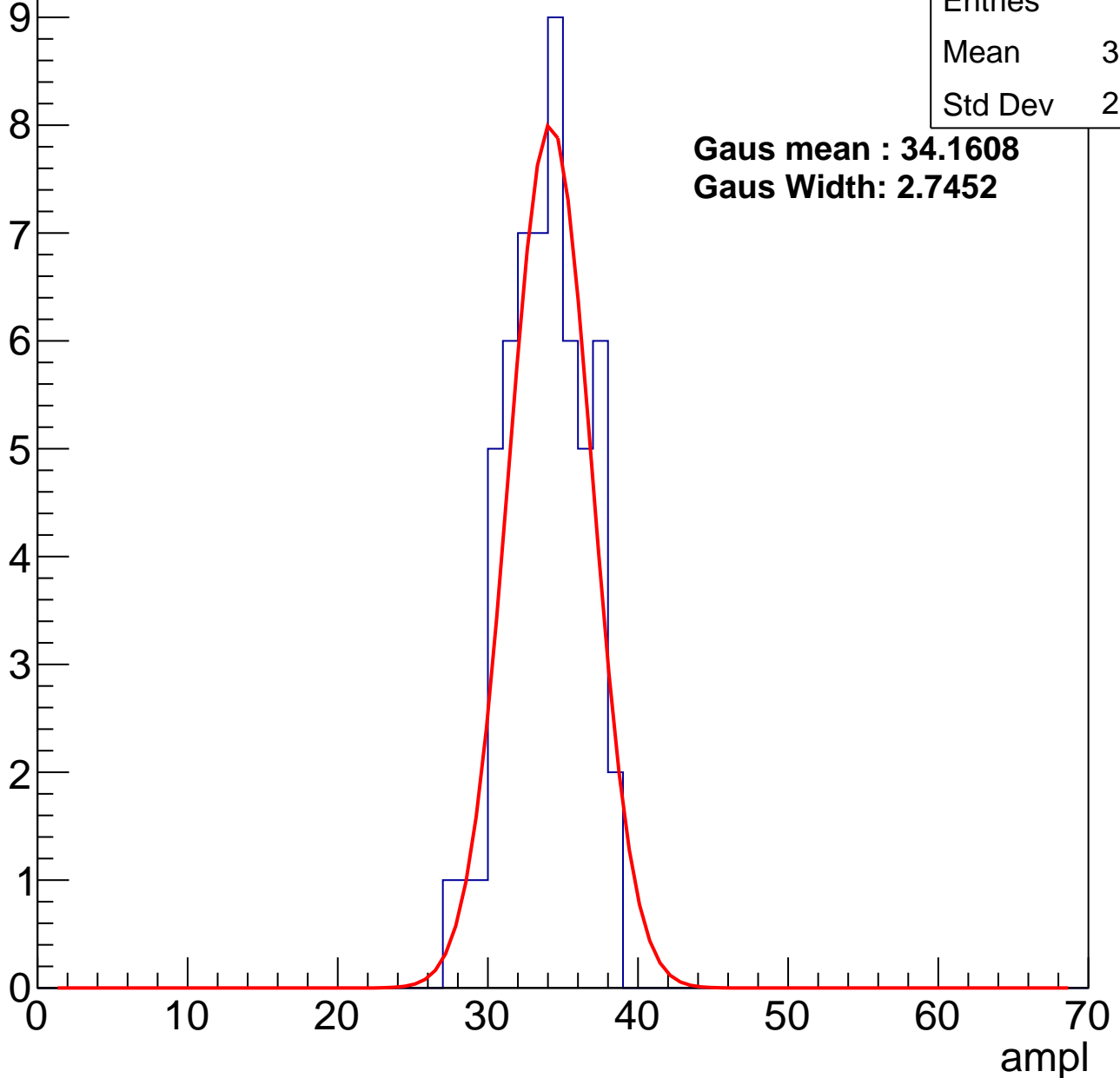
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.38
Std Dev	2.567

Gaus mean : 34.1608

Gaus Width: 2.7452



B1L002S, U7-ch3, adc2

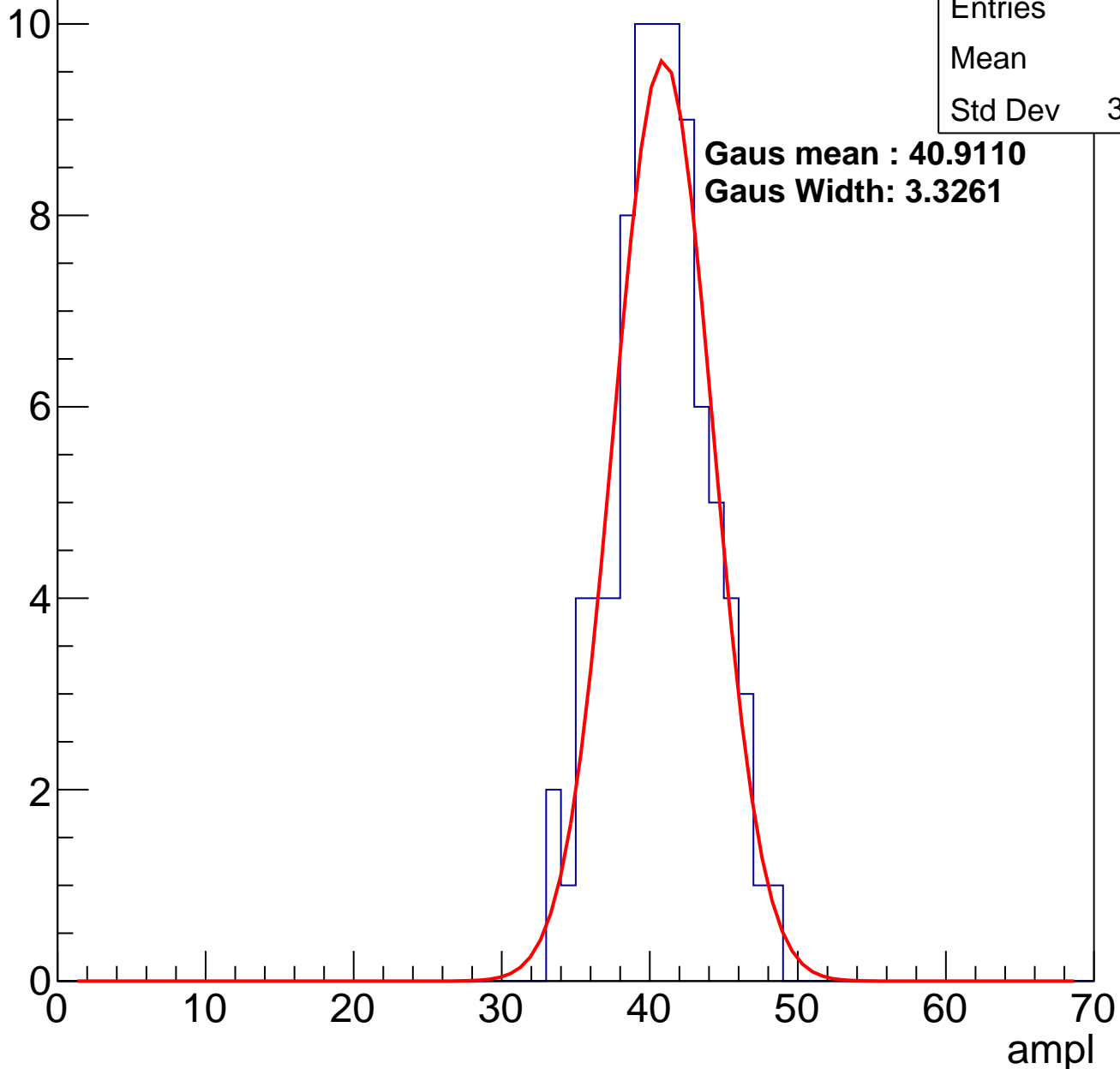
calib_packv5_042523_0143.root, FC#10, port B3

Entries	82
Mean	40.3
Std Dev	3.256

Gaus mean : 40.9110

Gaus Width: 3.3261

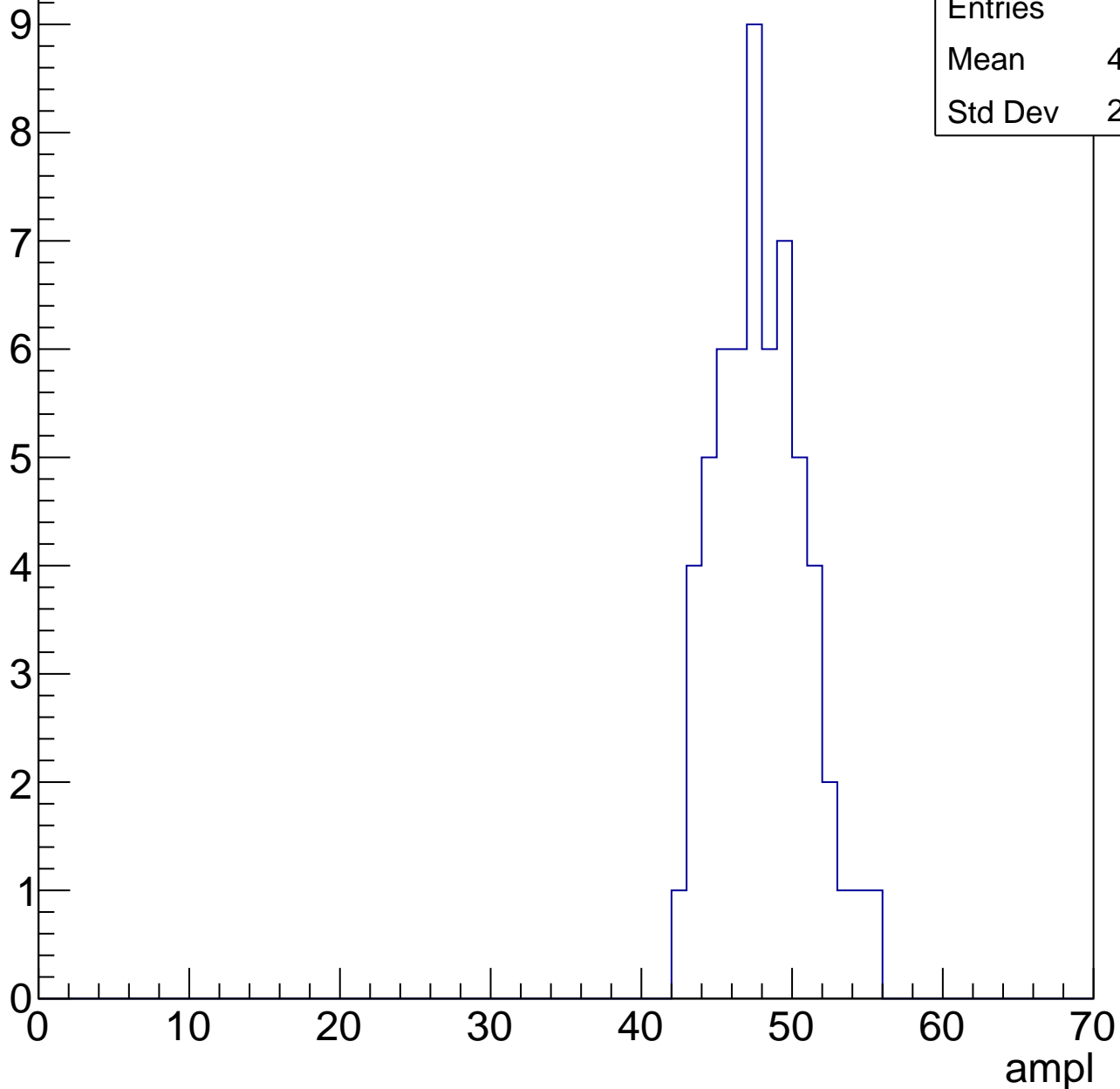
Entry



B1L002S, U7-ch3, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



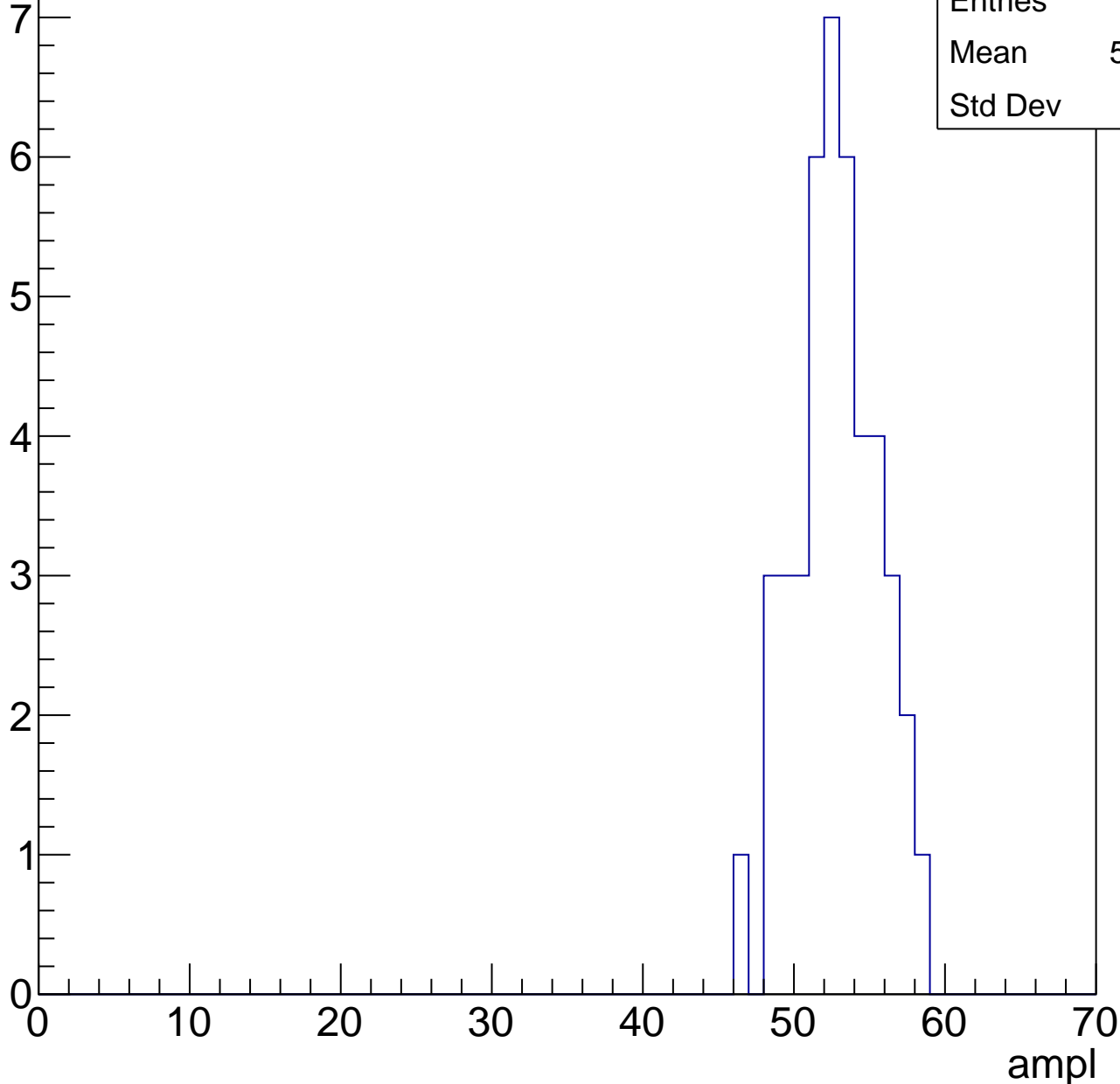
Entries	58
Mean	47.48
Std Dev	2.914

B1L002S, U7-ch3, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	43
Mean	52.35
Std Dev	2.71



B1L002S, U7-ch3, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	73
Mean	57.93
Std Dev	3.054

Entry

10

8

6

4

2

0

0

10

20

30

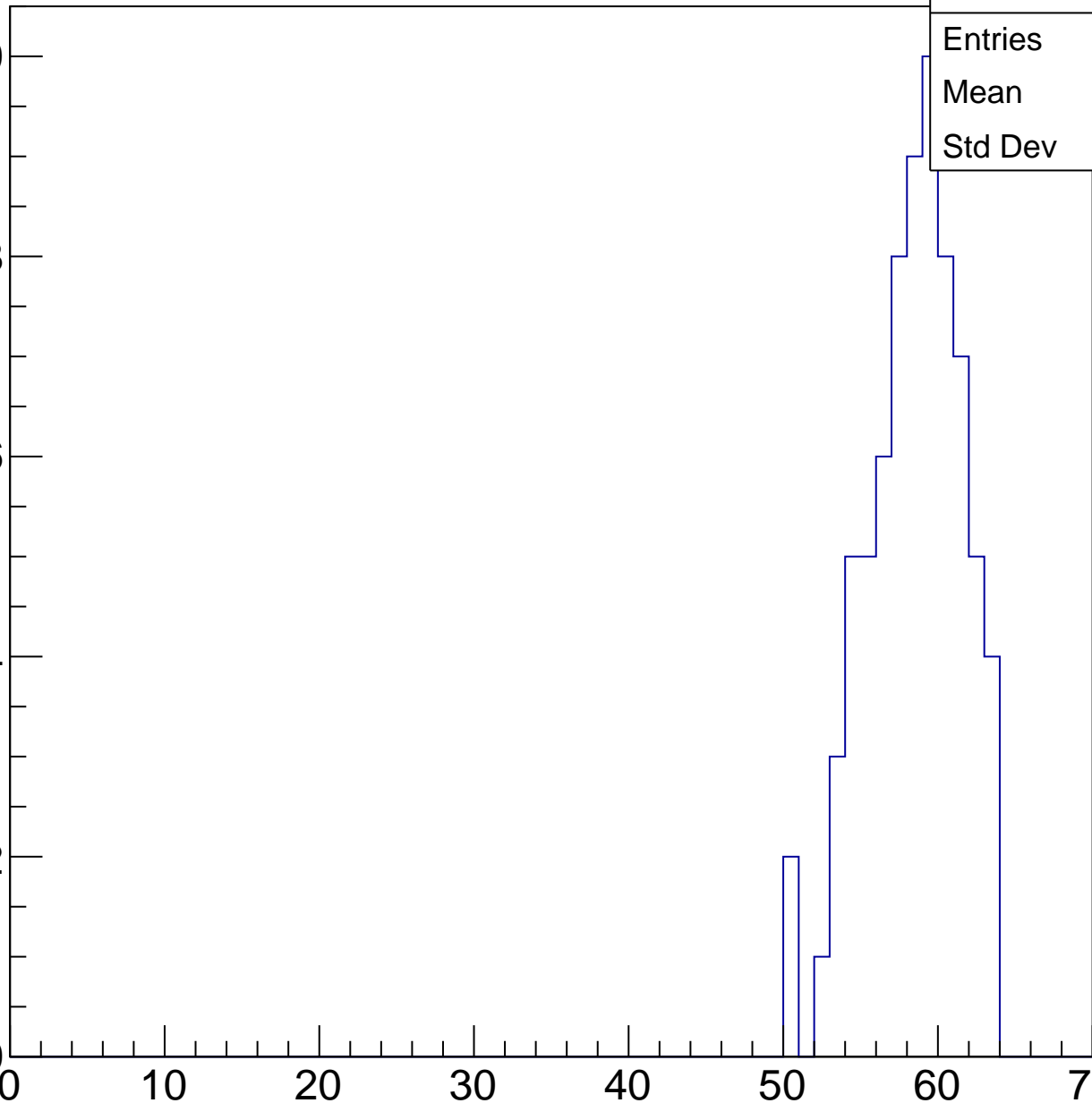
40

50

60

70

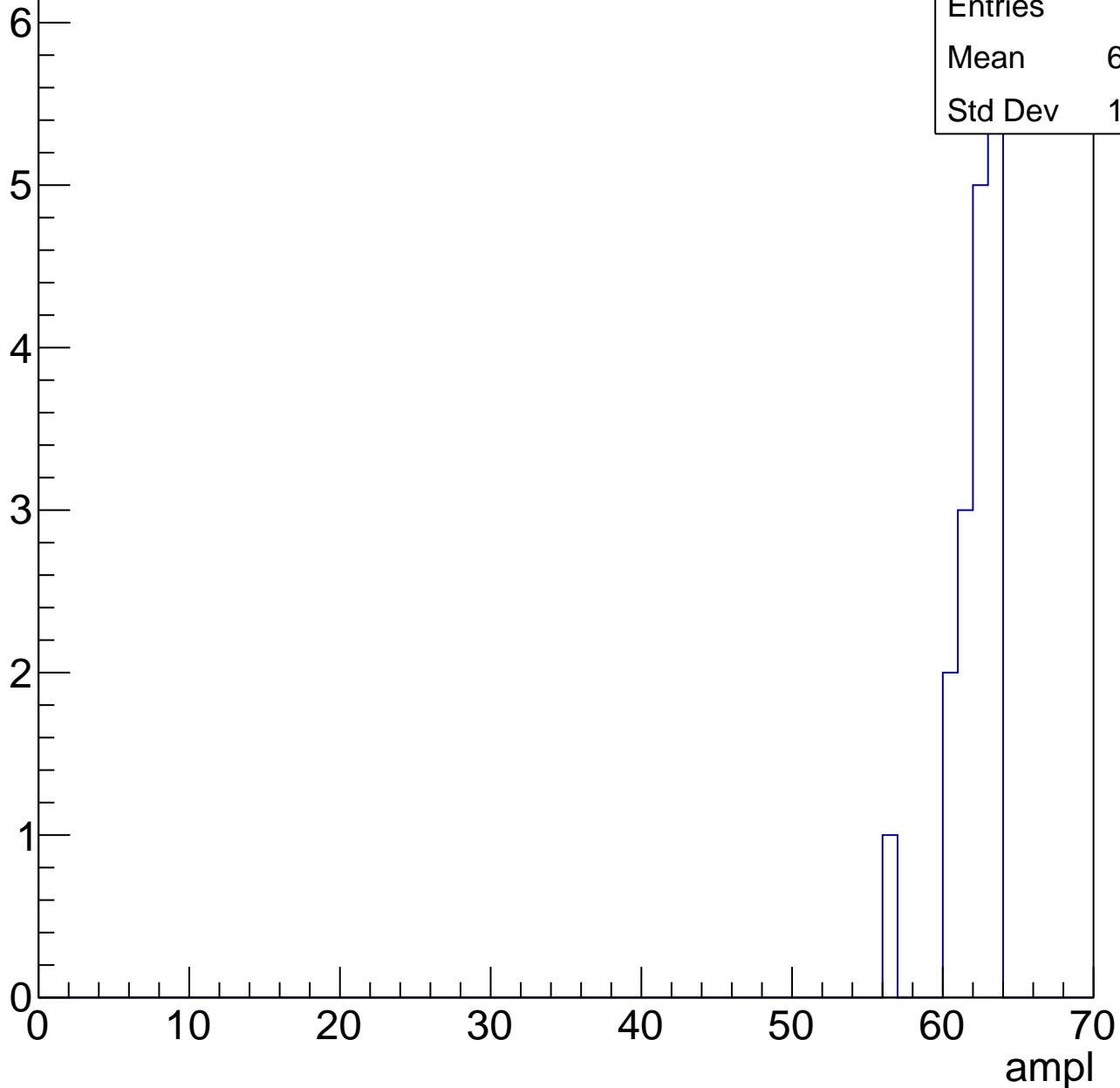
ampl



B1L002S, U7-ch3, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch3, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch4, adc0

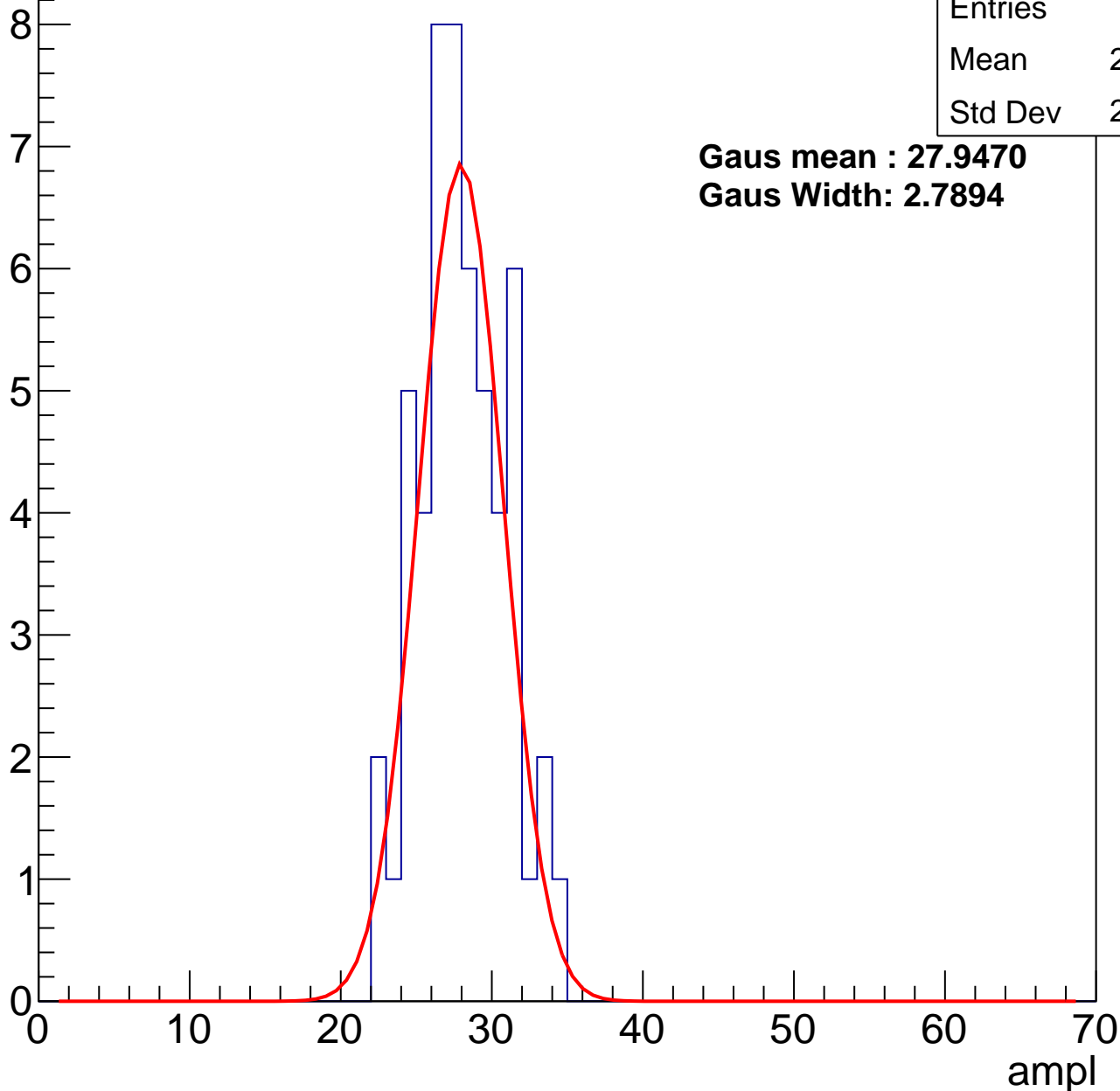
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	27.58
Std Dev	2.818

Gaus mean : 27.9470

Gaus Width: 2.7894



B1L002S, U7-ch4, adc1

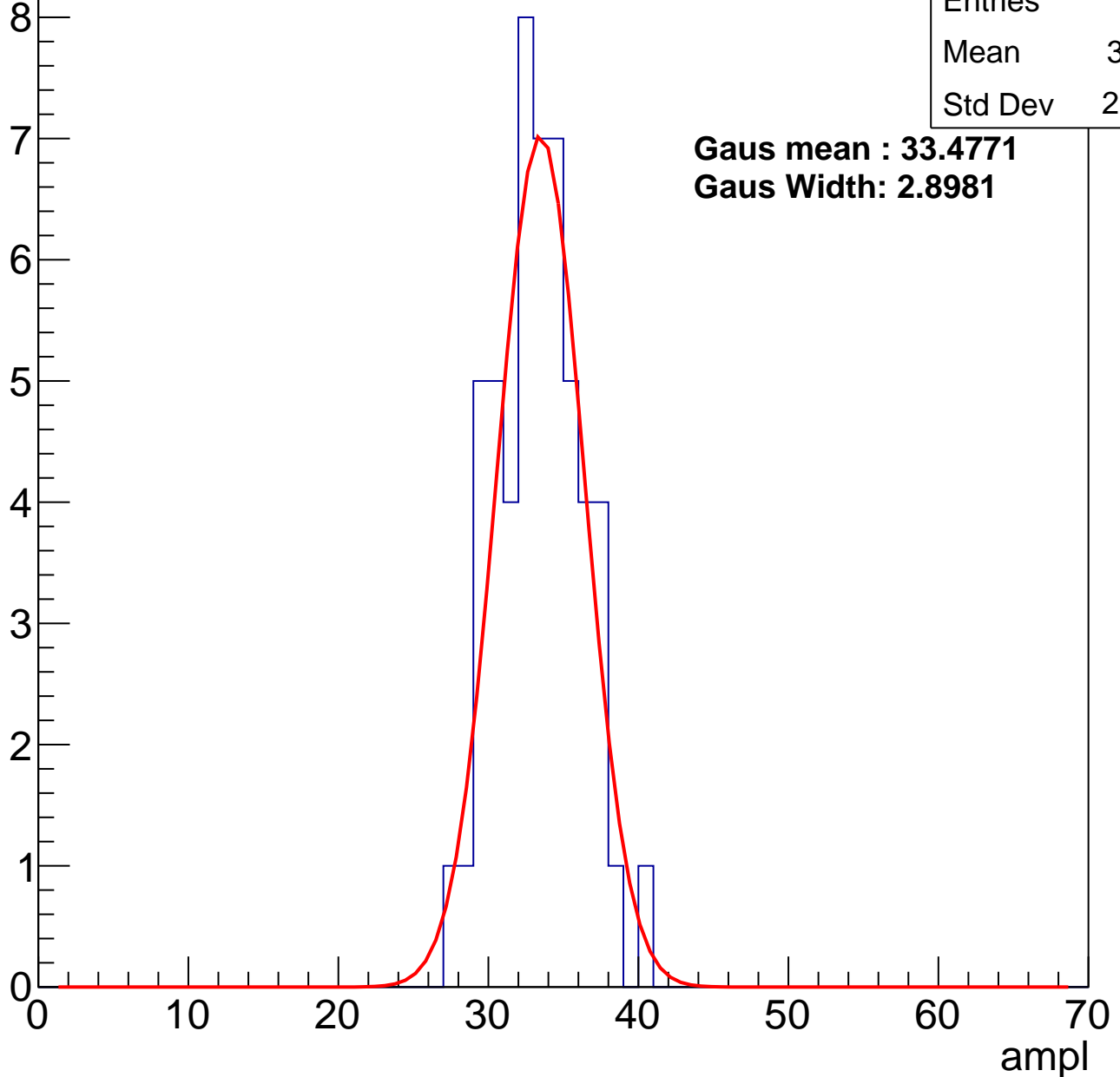
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	32.91
Std Dev	2.783

Gaus mean : 33.4771

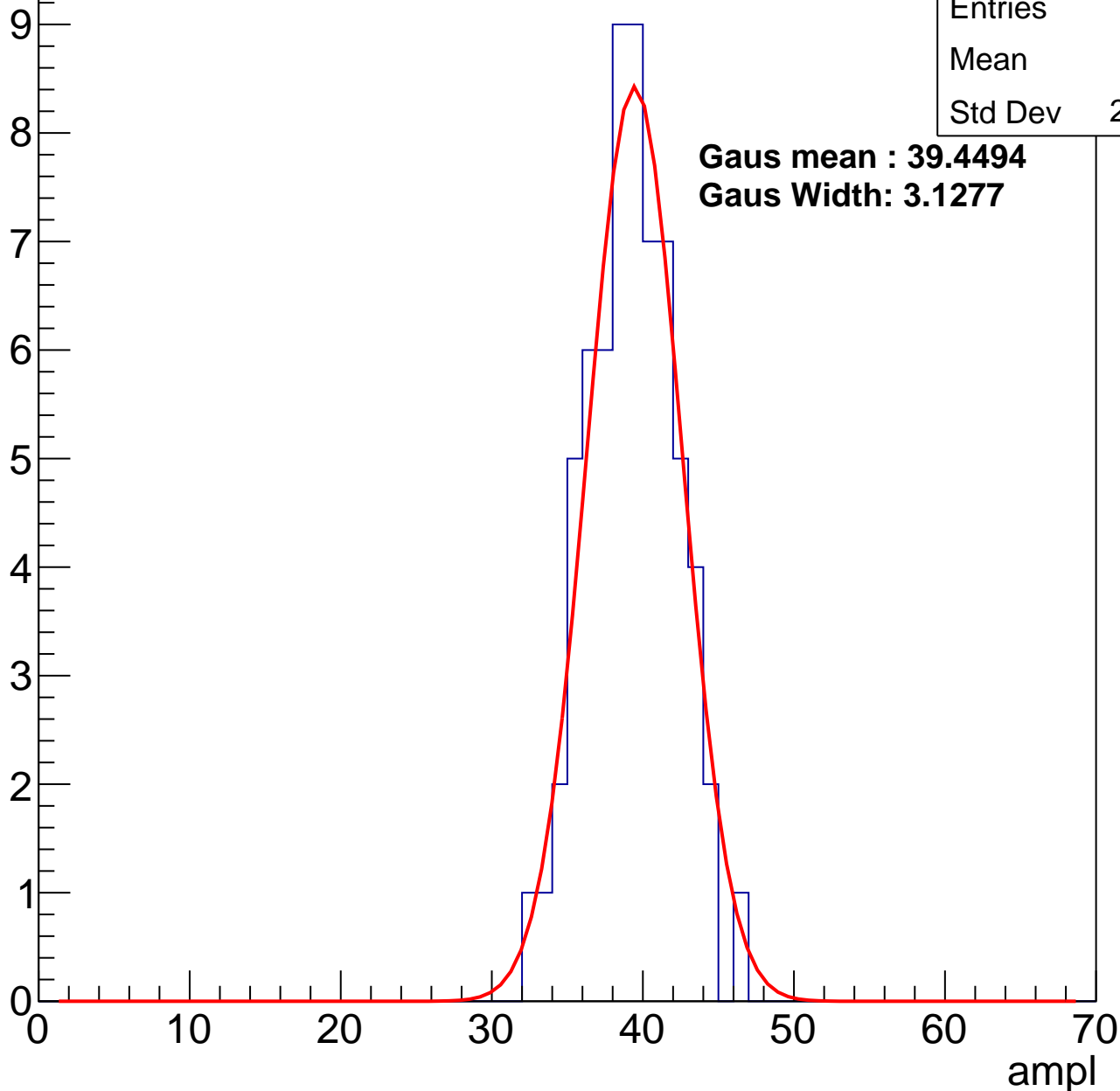
Gaus Width: 2.8981



B1L002S, U7-ch4, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

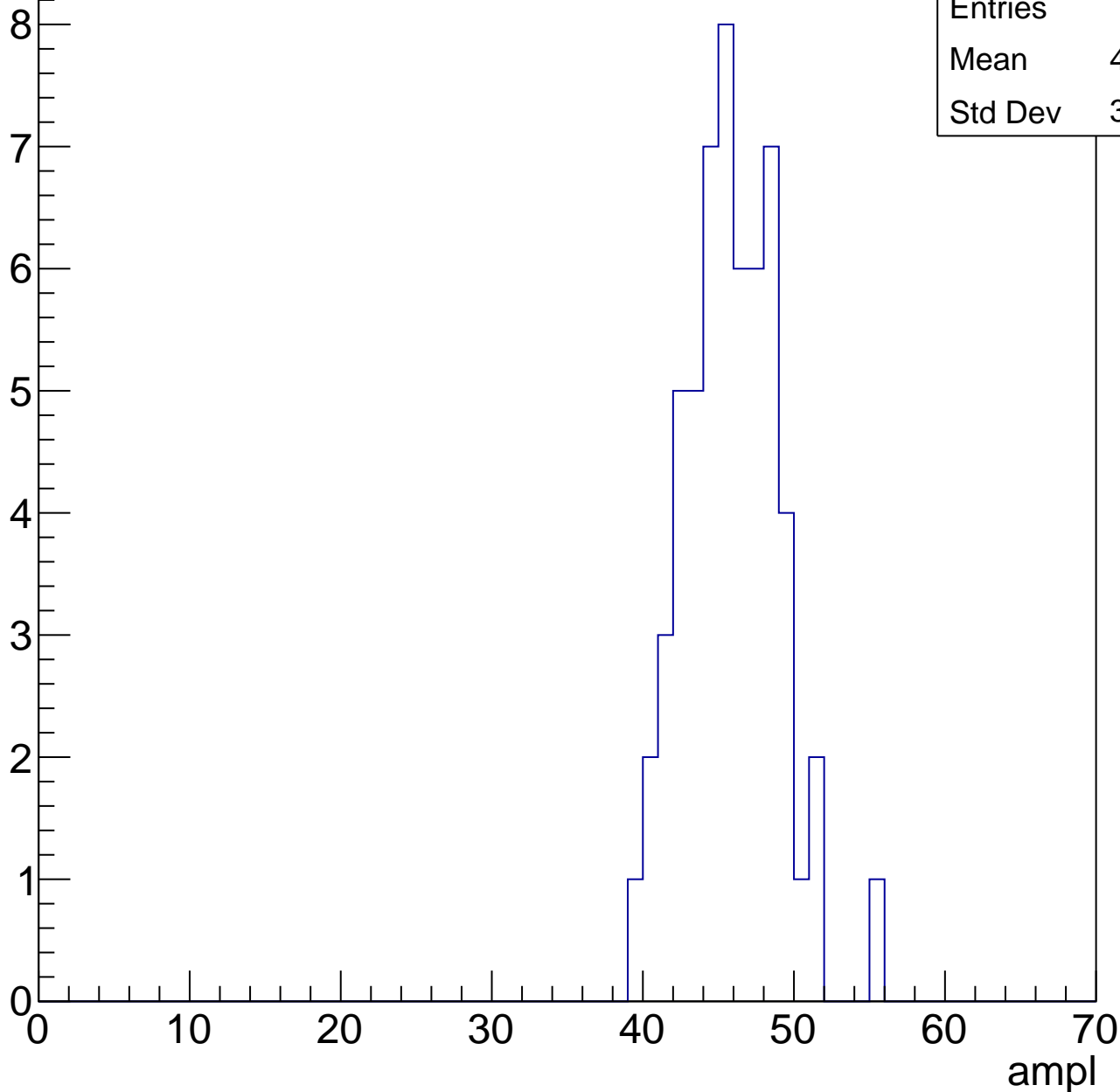


B1L002S, U7-ch4, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.38
Std Dev	3.084

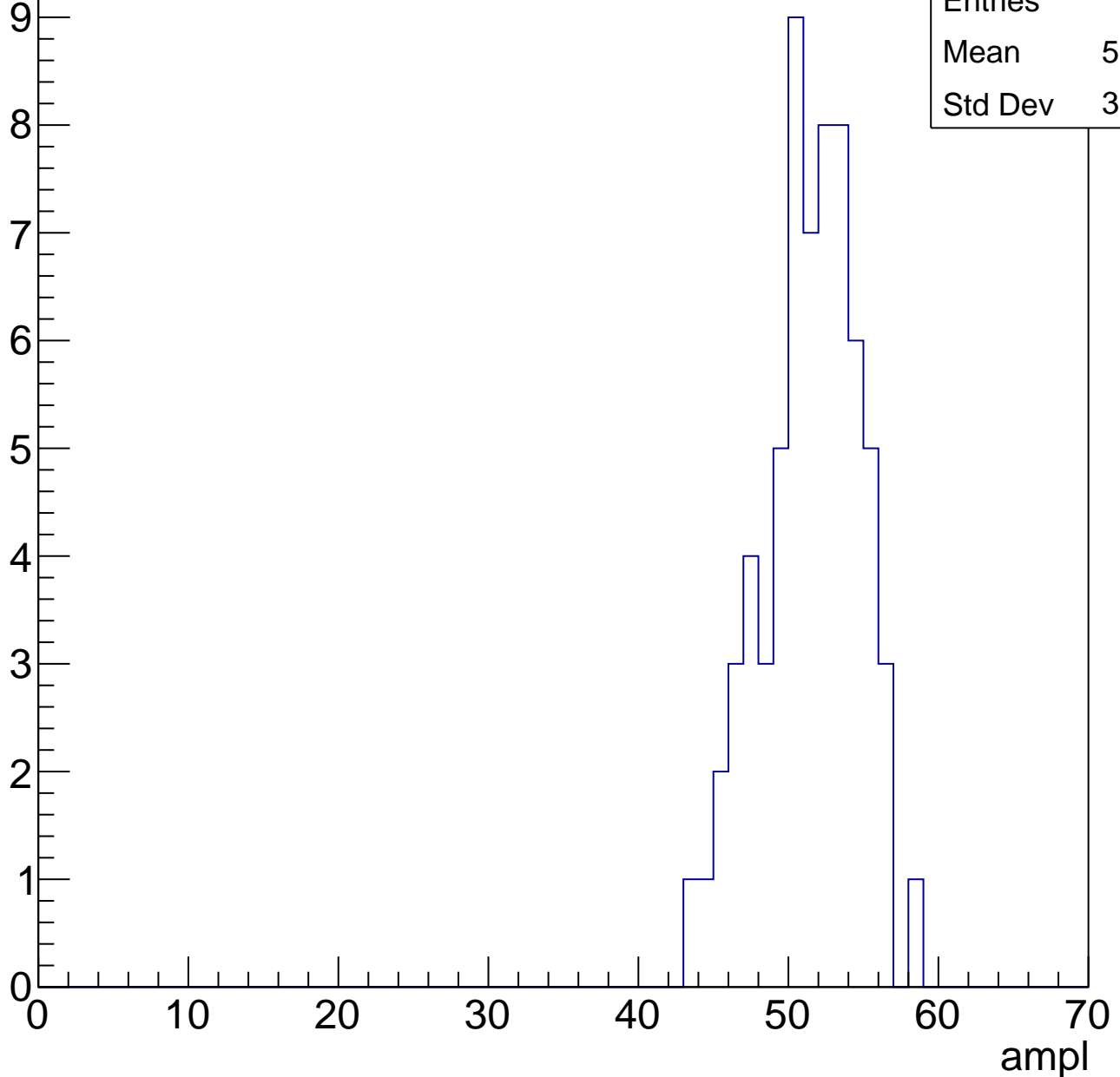


B1L002S, U7-ch4, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

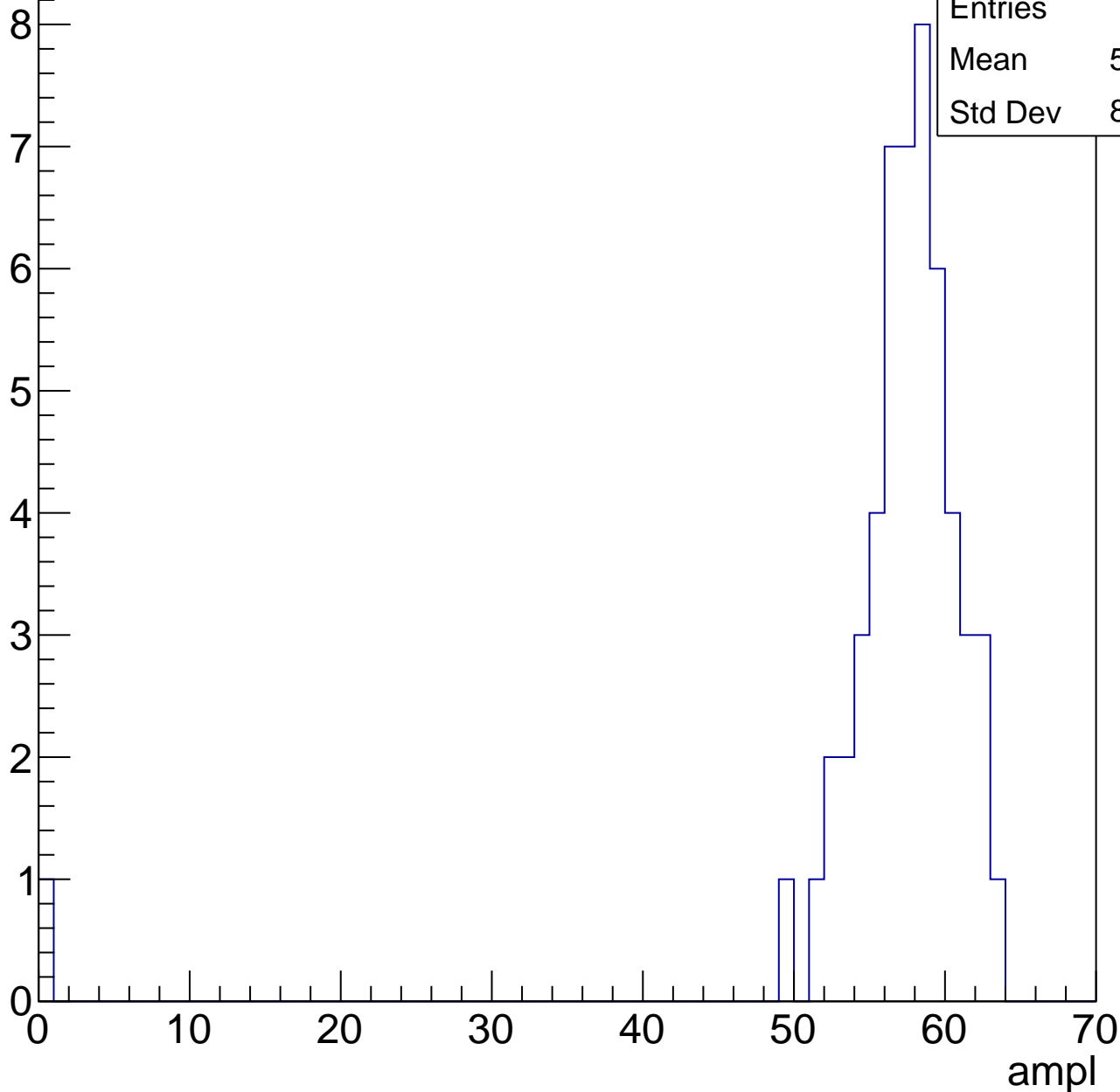
Entries	66
Mean	50.97
Std Dev	3.205



B1L002S, U7-ch4, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

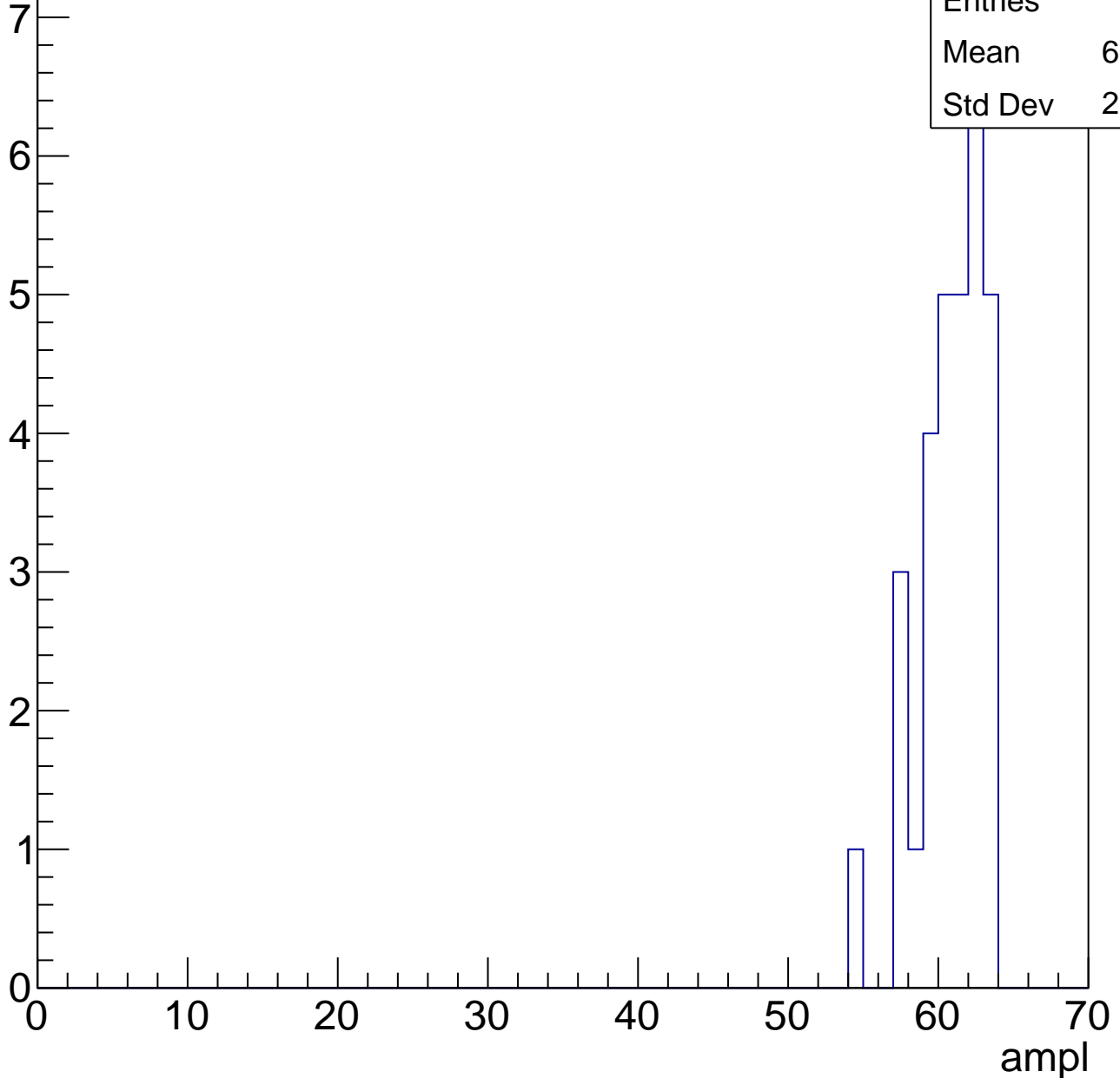


B1L002S, U7-ch4, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

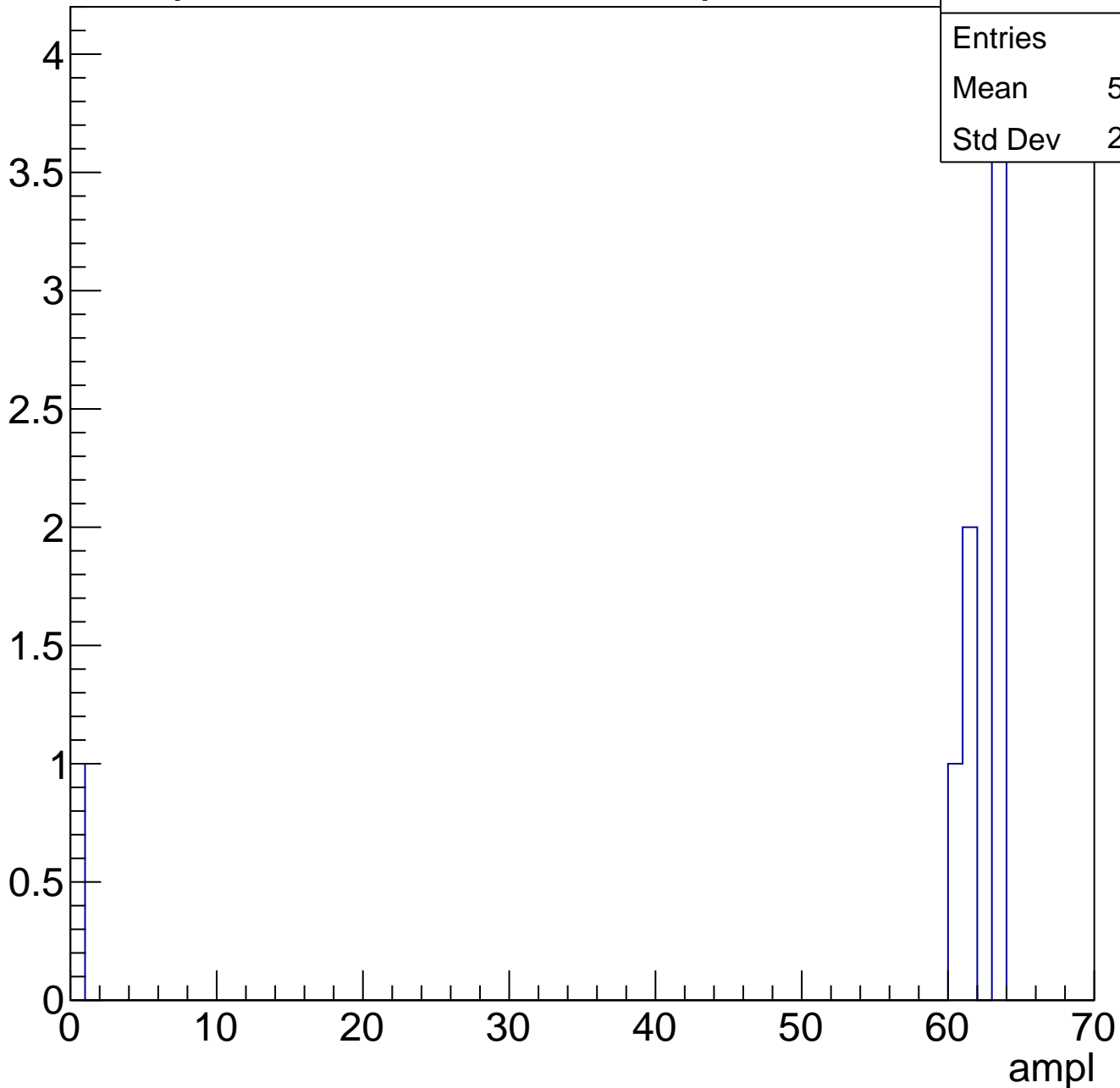
Entries	31
Mean	60.42
Std Dev	2.152



B1L002S, U7-ch4, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	8
Mean	54.25
Std Dev	20.54

B1L002S, U7-ch5, adc0

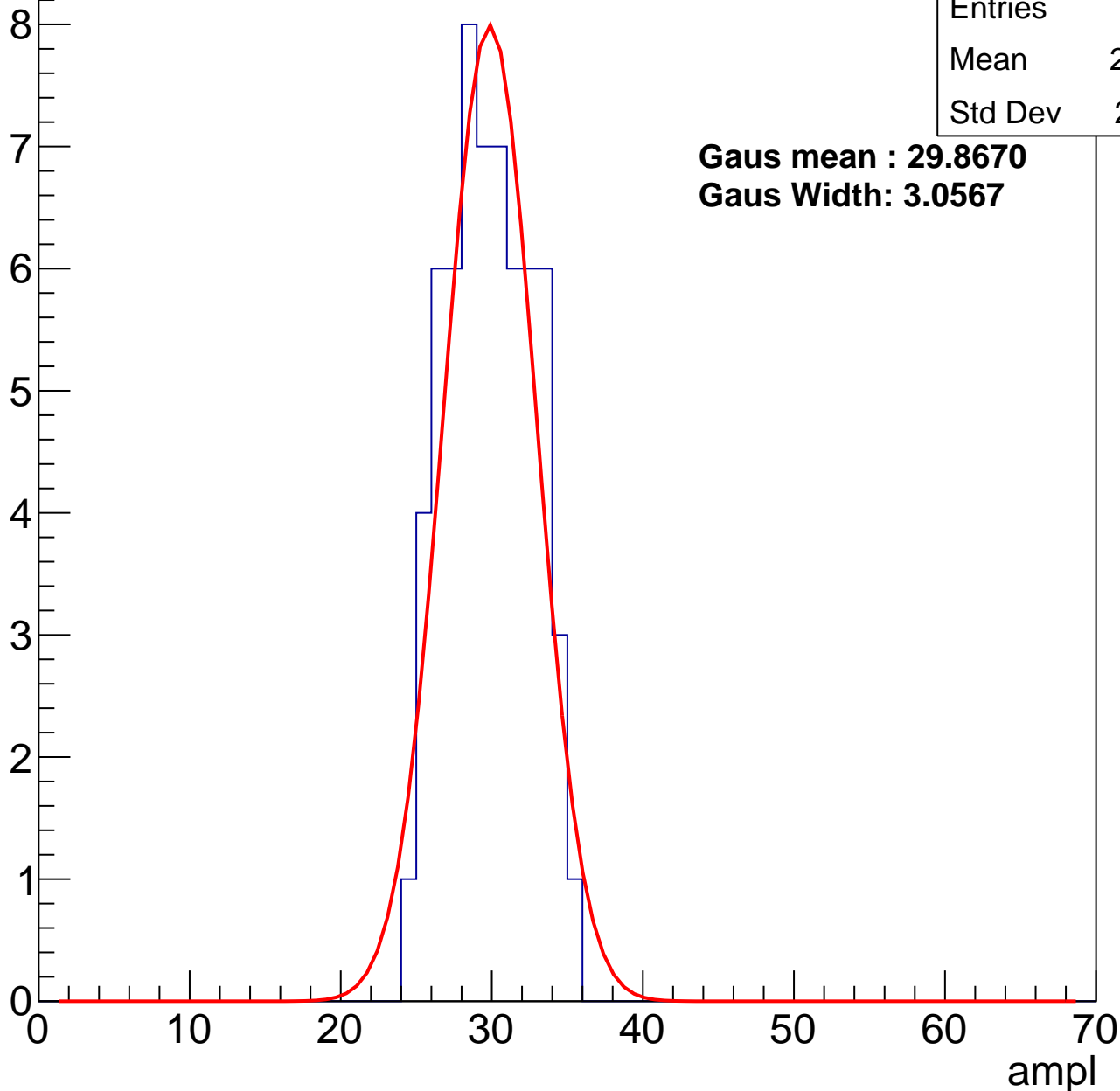
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	29.38
Std Dev	2.741

Gaus mean : 29.8670

Gaus Width: 3.0567



B1L002S, U7-ch5, adc1

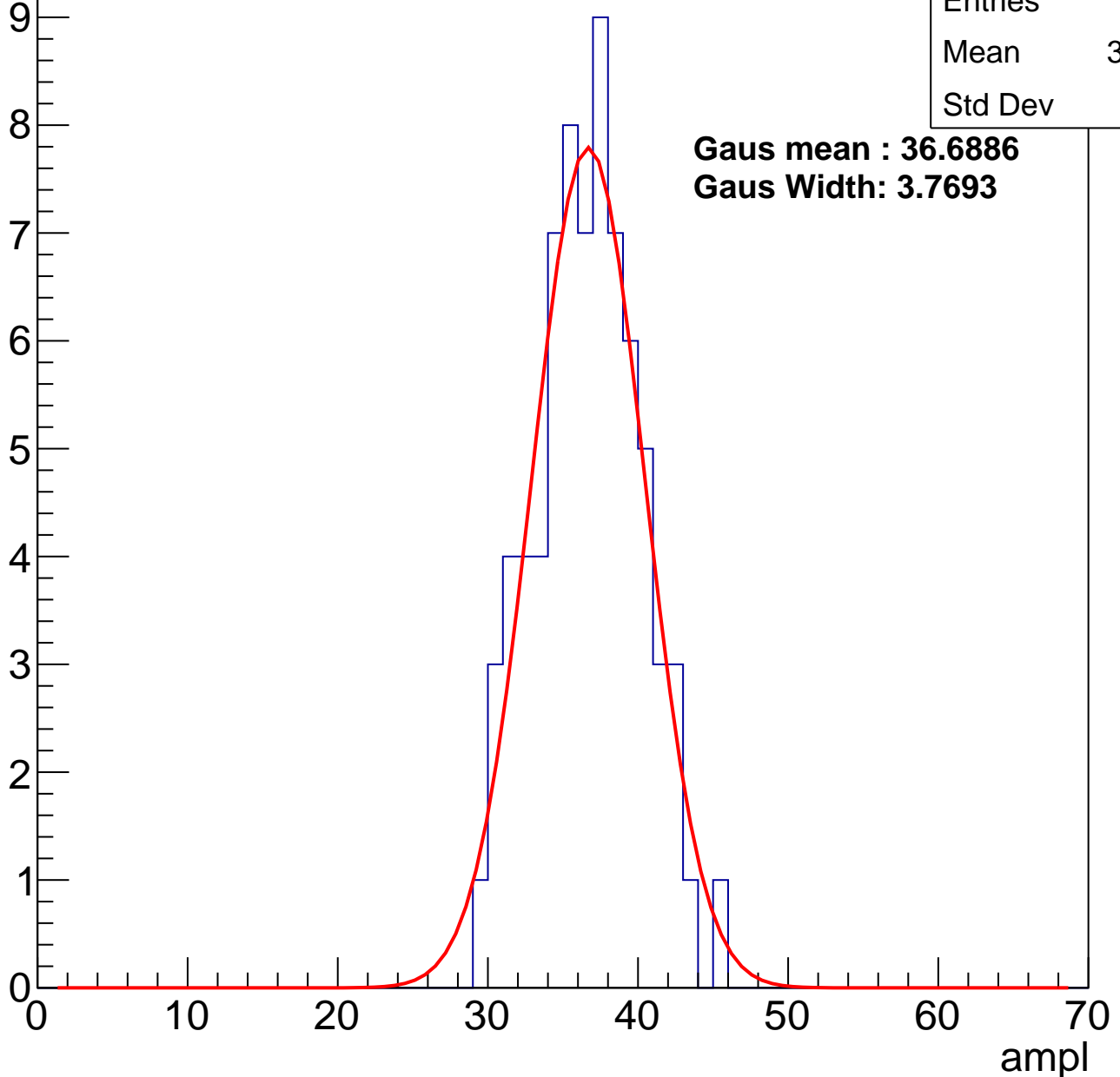
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.21
Std Dev	3.46

Gaus mean : 36.6886

Gaus Width: 3.7693



B1L002S, U7-ch5, adc2

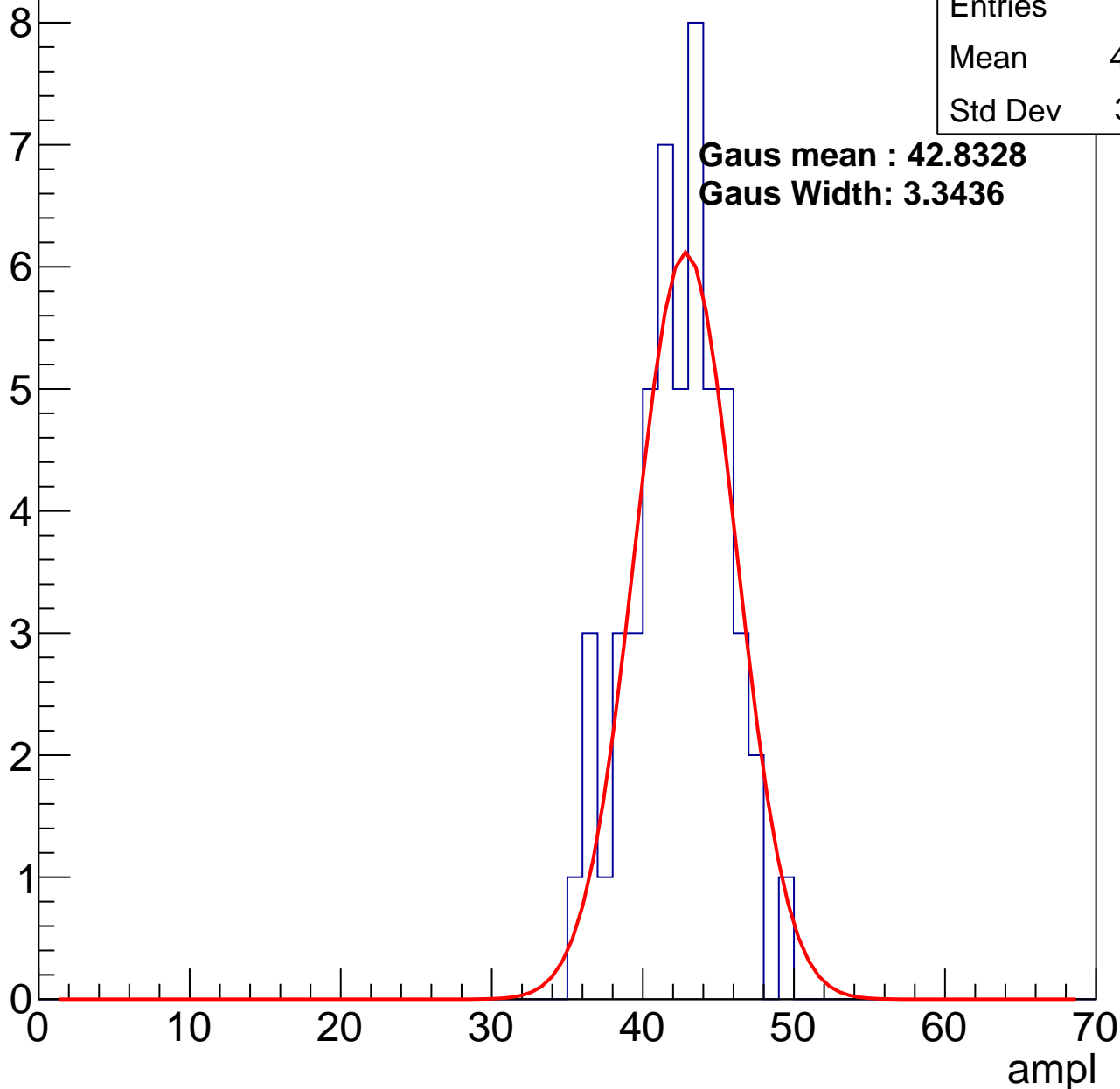
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	41.88
Std Dev	3.111

Gaus mean : 42.8328

Gaus Width: 3.3436

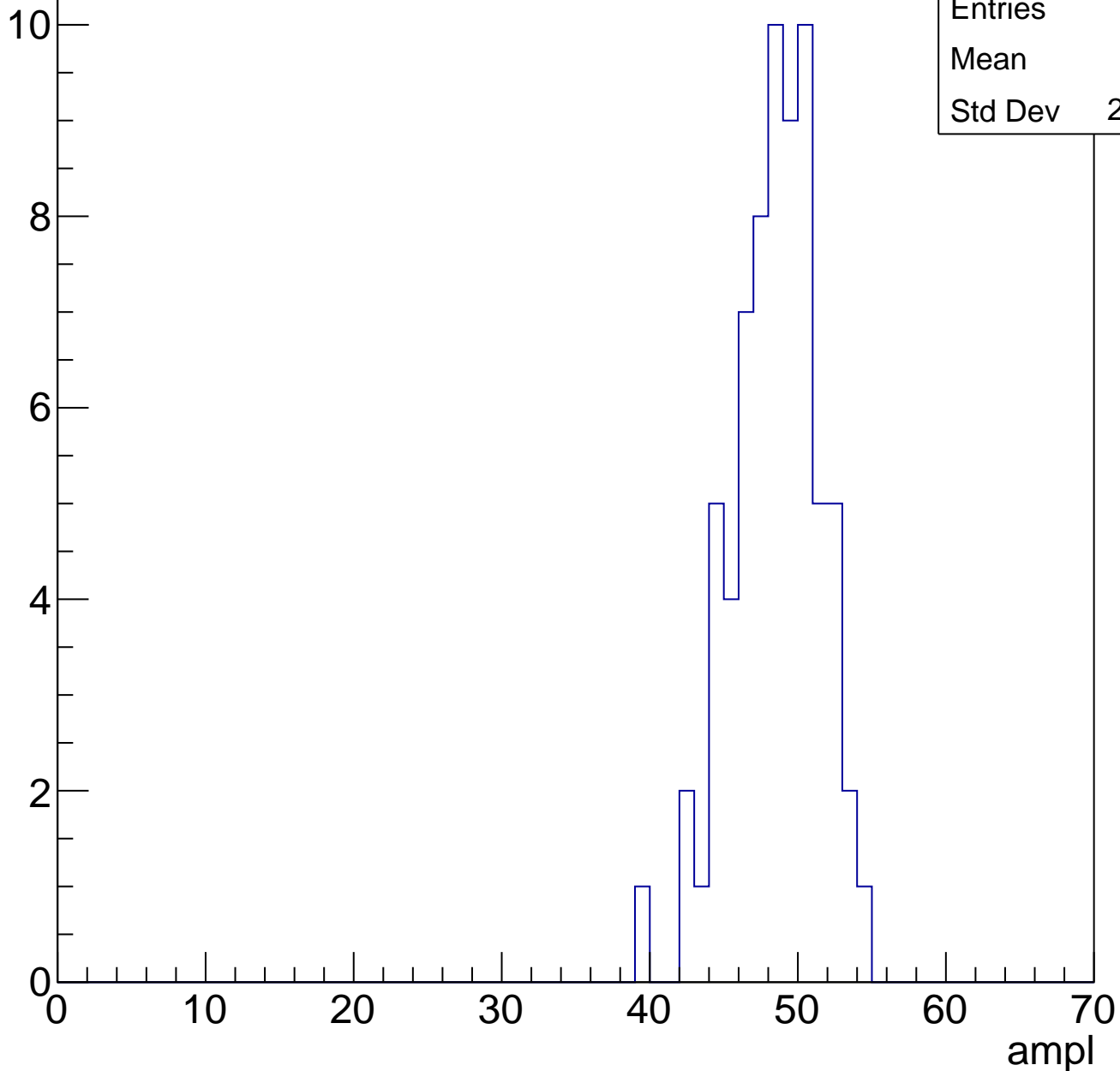


B1L002S, U7-ch5, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	48
Std Dev	2.903

Entry

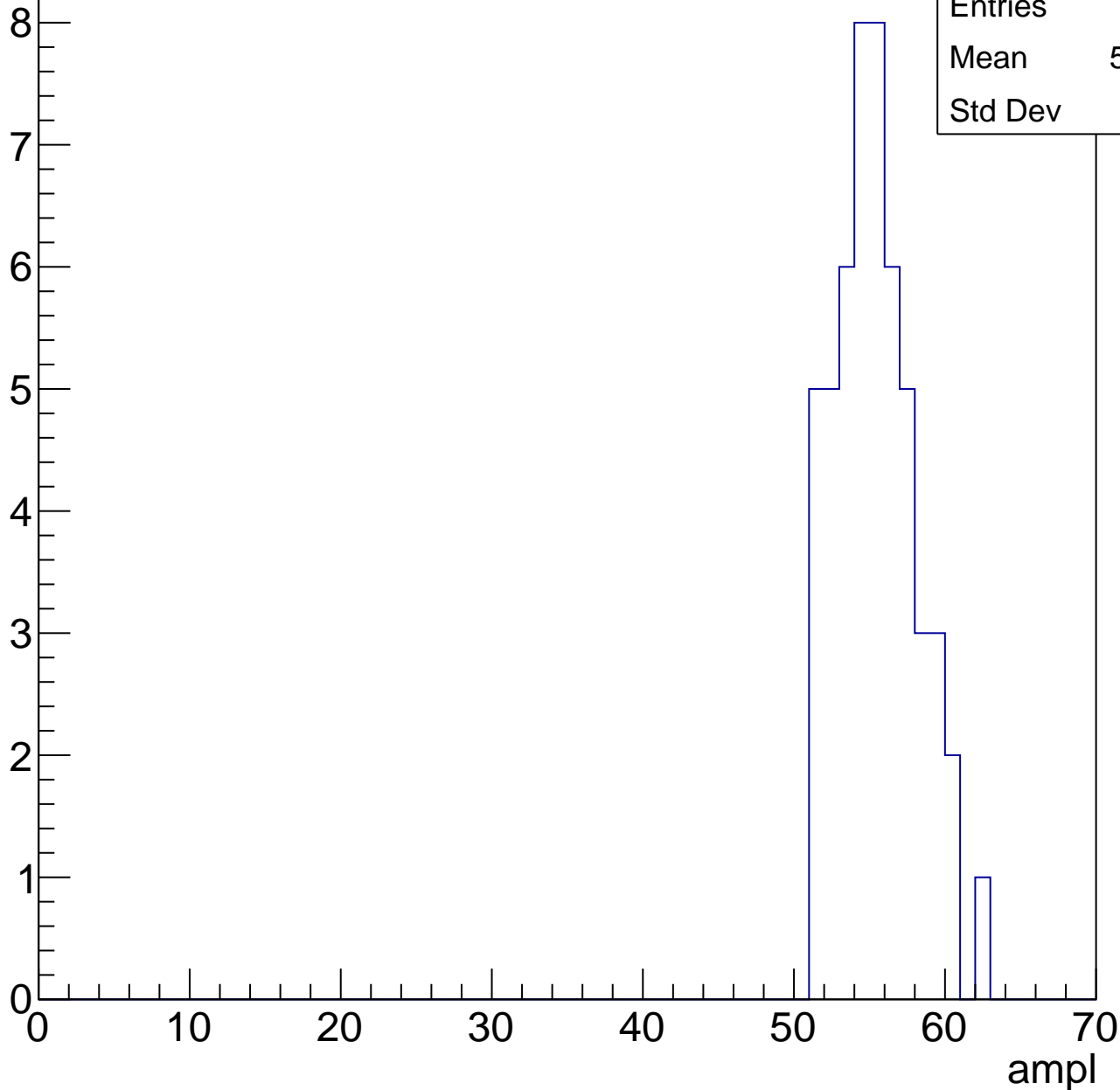


B1L002S, U7-ch5, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	54.98
Std Dev	2.62



B1L002S, U7-ch5, adc5

calib_packv5_042523_0143.root, FC#10, port B3

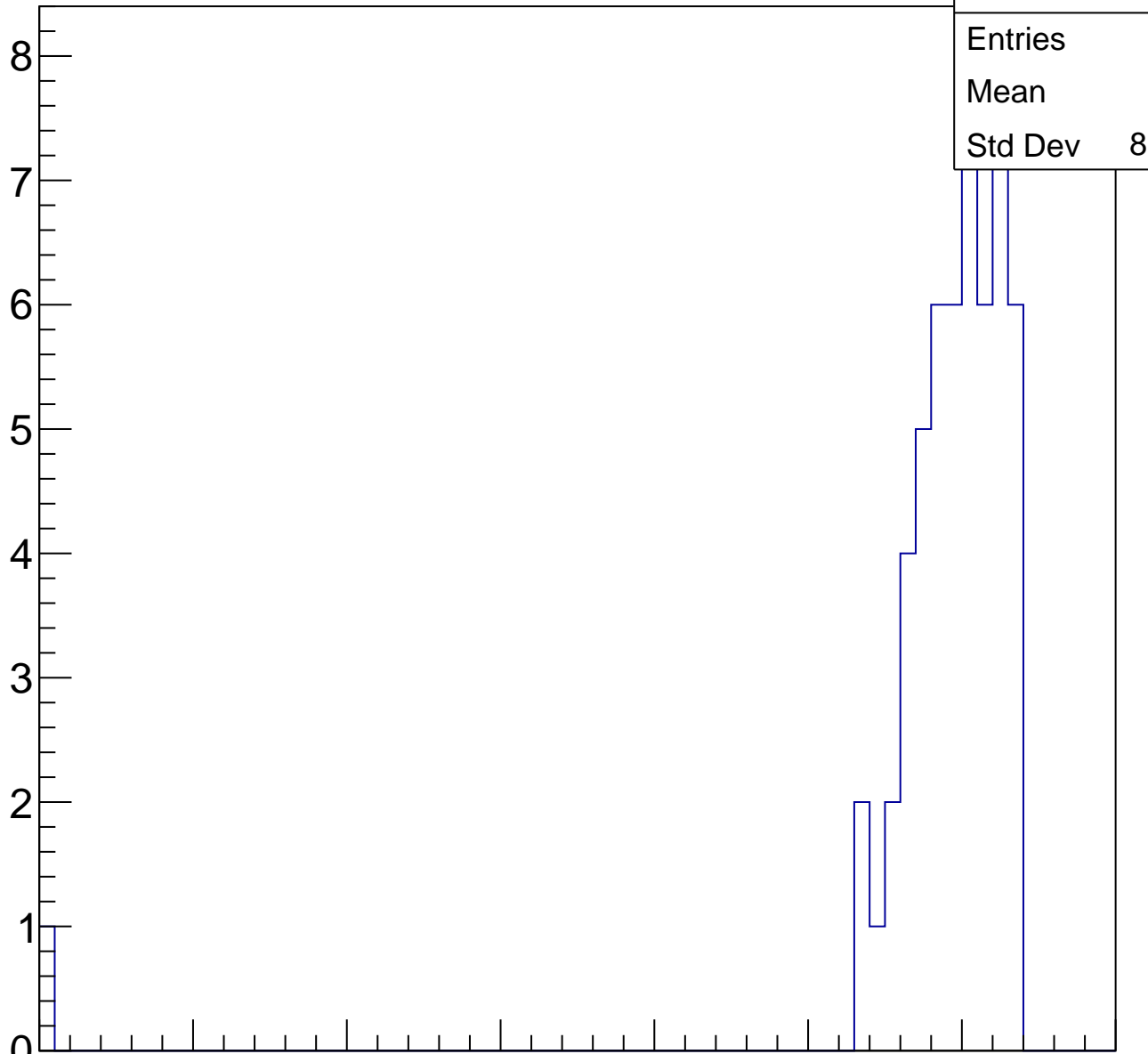
Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.2
Std Dev	8.352

ampl

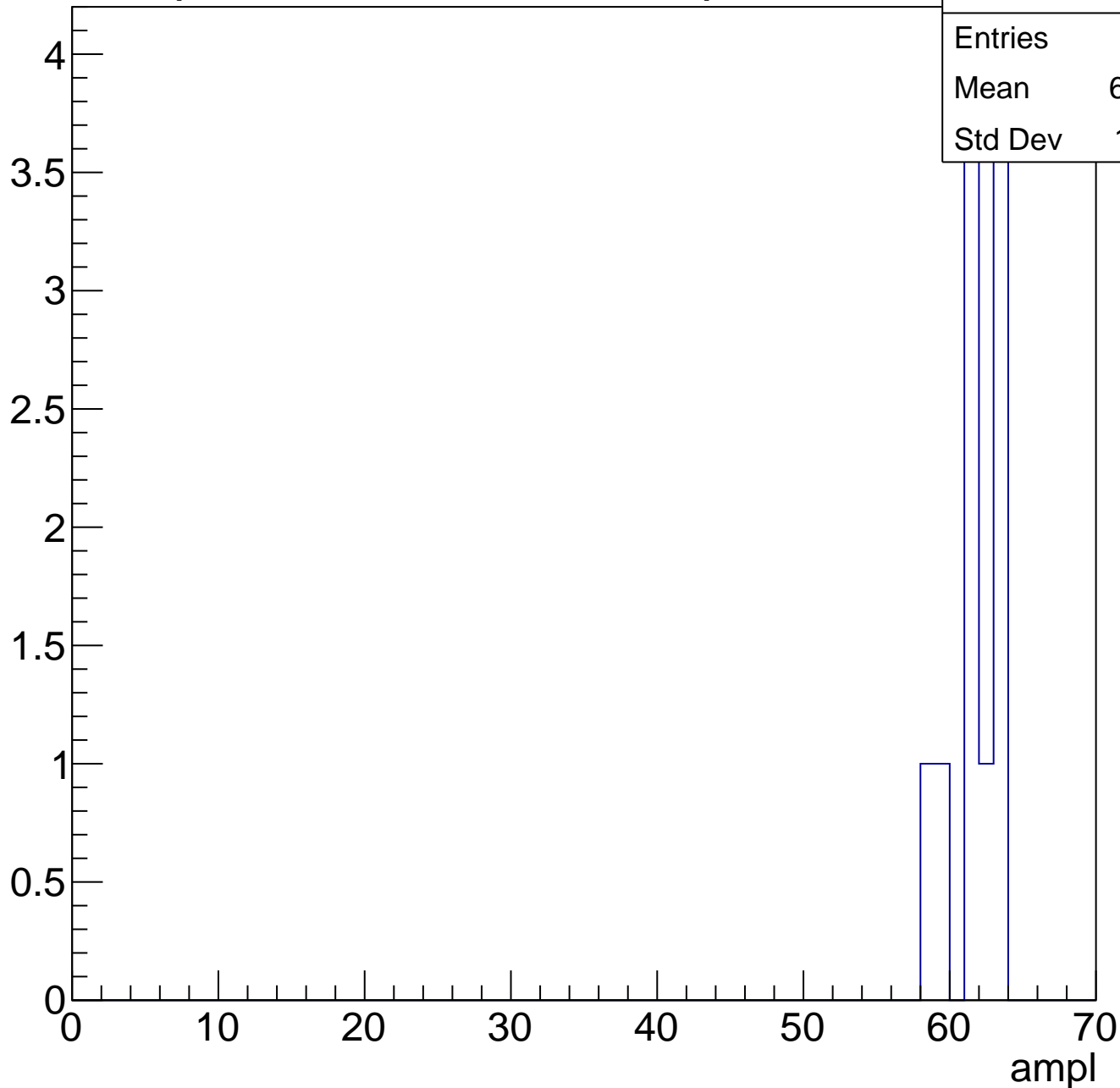
0 10 20 30 40 50 60 70



B1L002S, U7-ch5, adc6

calib_packv5_042523_0143.root, FC#10, port B3

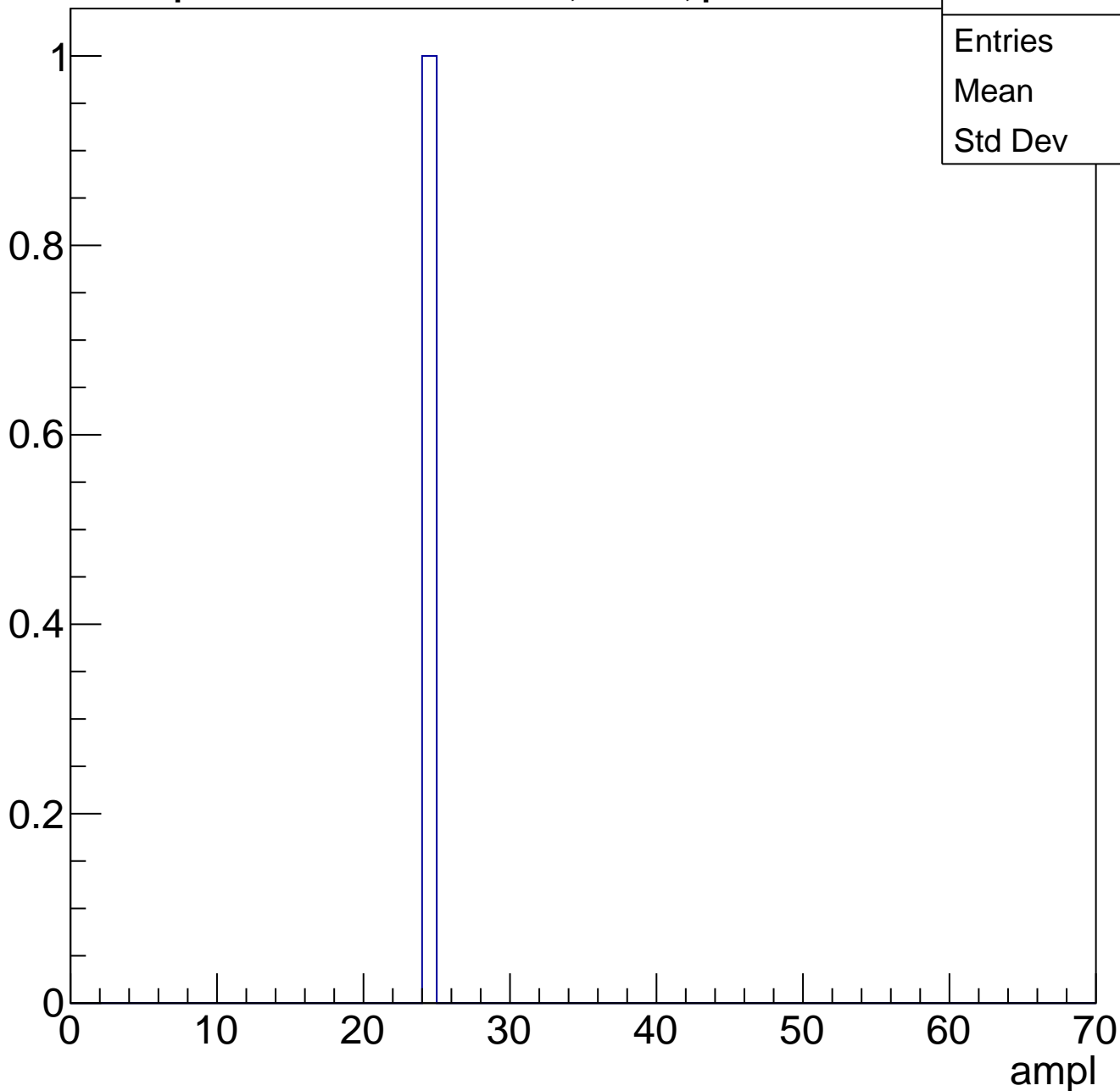
Entry



B1L002S, U7-ch5, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	24
Std Dev	0

B1L002S, U7-ch6, adc0

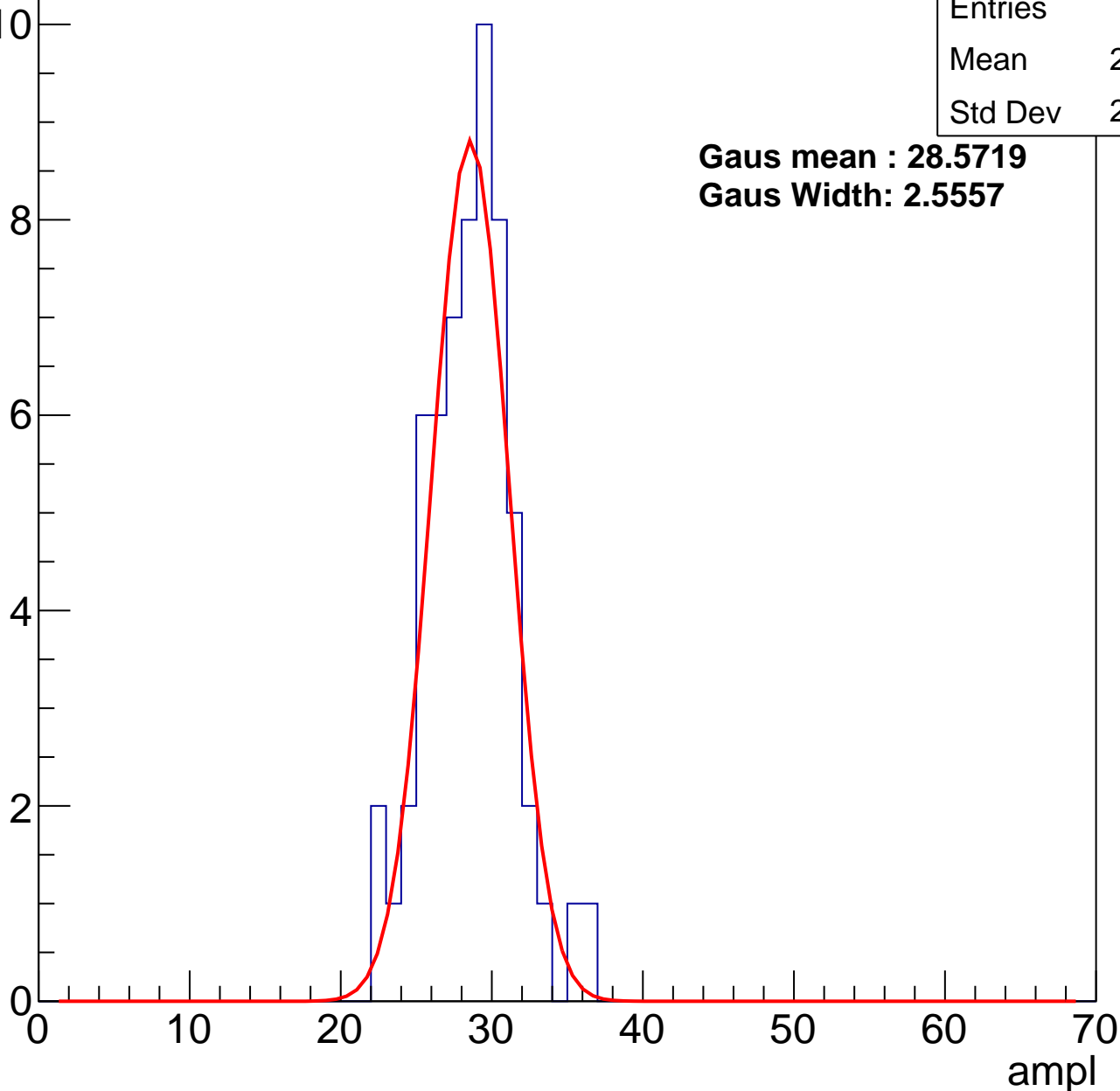
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.12
Std Dev	2.799

Gaus mean : 28.5719

Gaus Width: 2.5557



B1L002S, U7-ch6, adc1

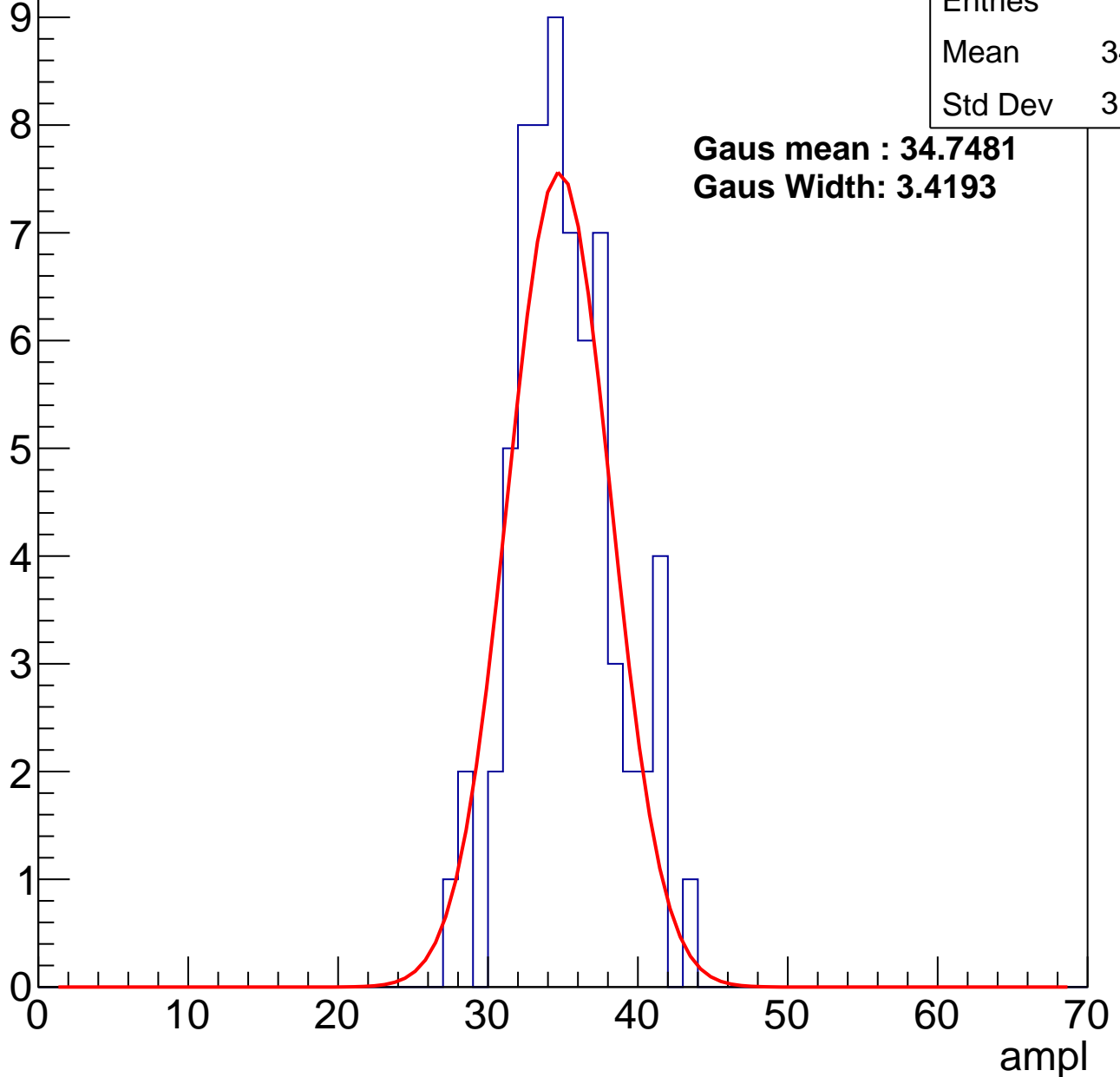
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.67
Std Dev	3.343

Gaus mean : 34.7481

Gaus Width: 3.4193



B1L002S, U7-ch6, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	41
Std Dev	3.591

Gaus mean : 41.0577

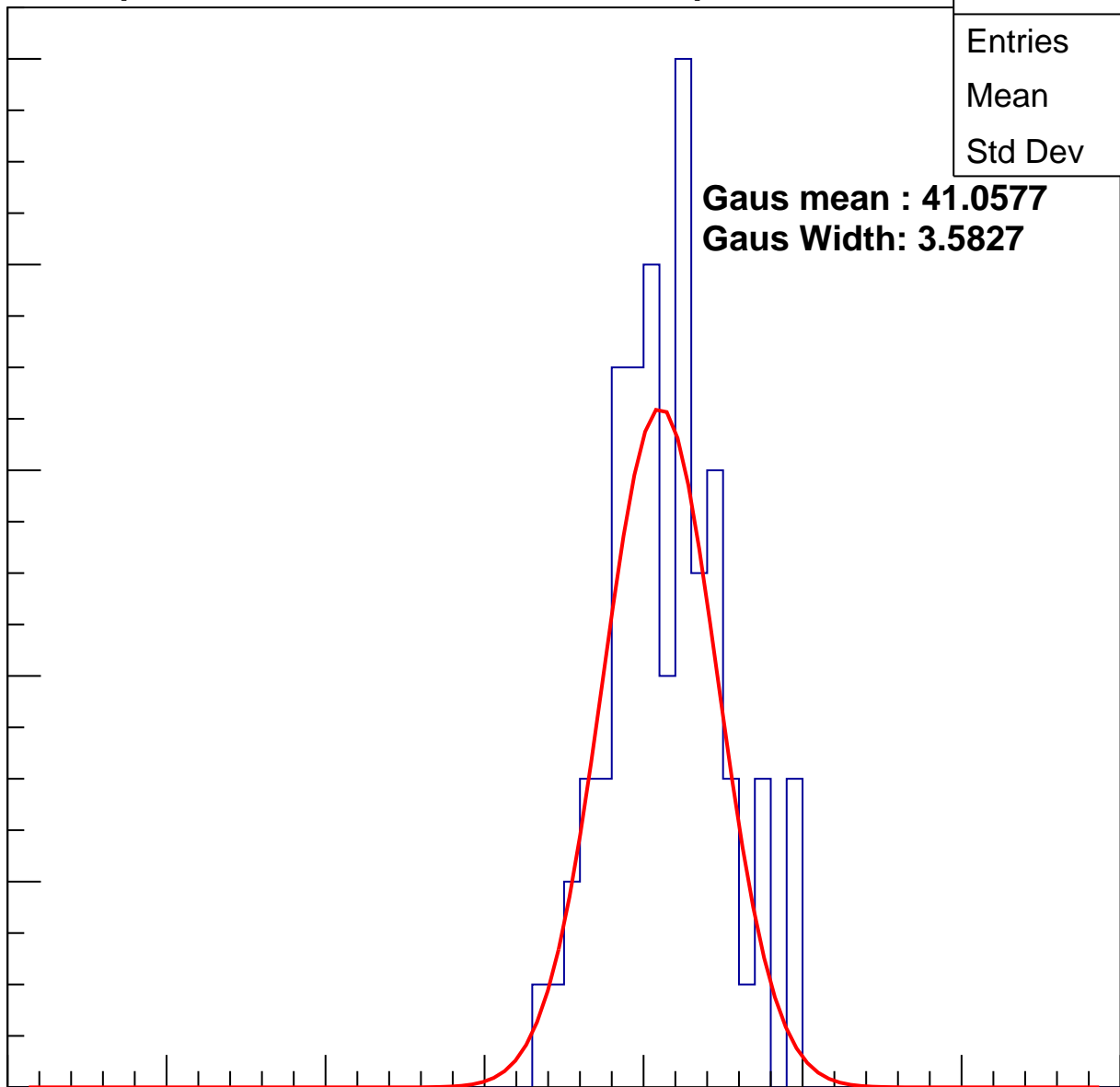
Gaus Width: 3.5827

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

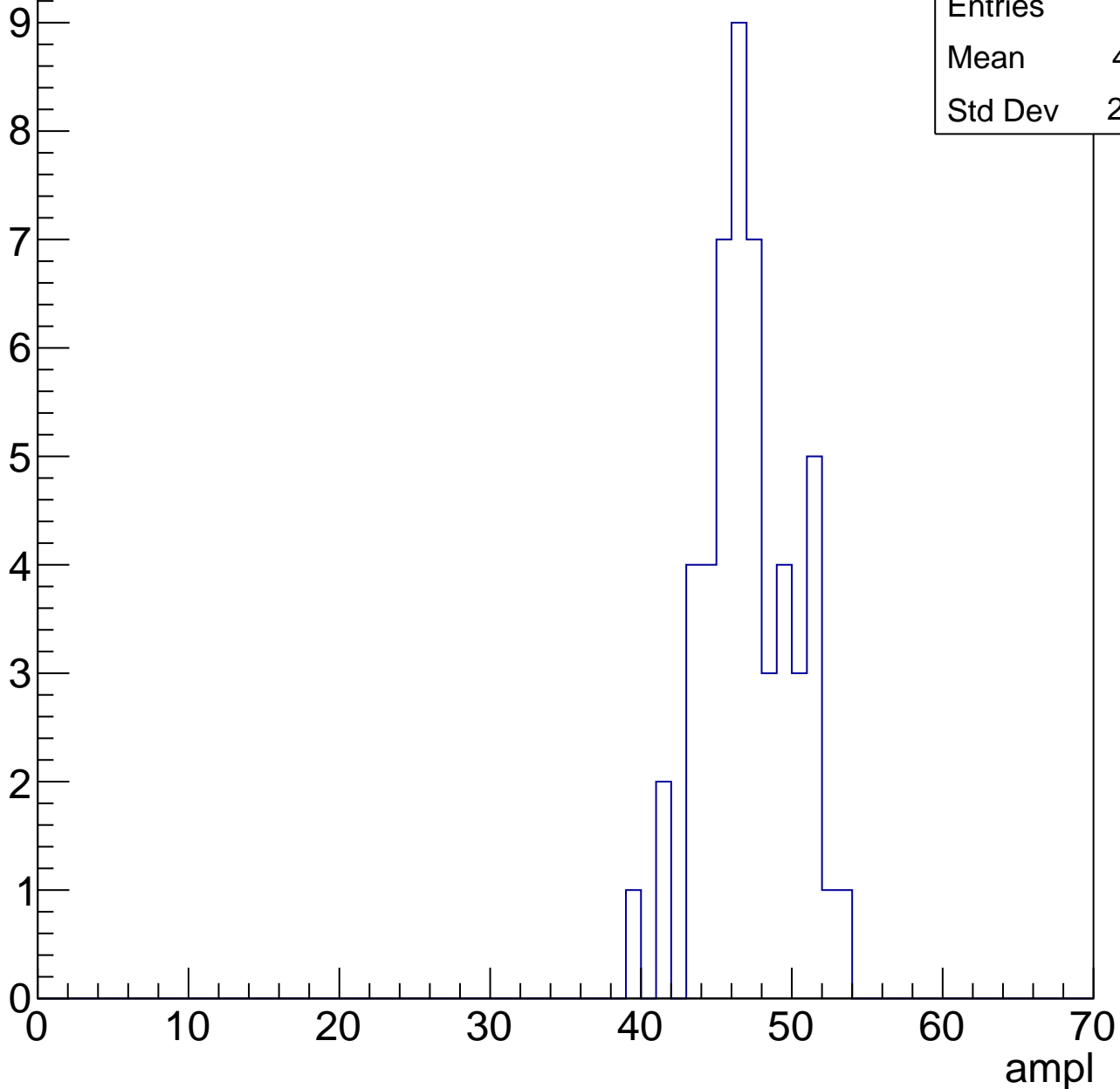


B1L002S, U7-ch6, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

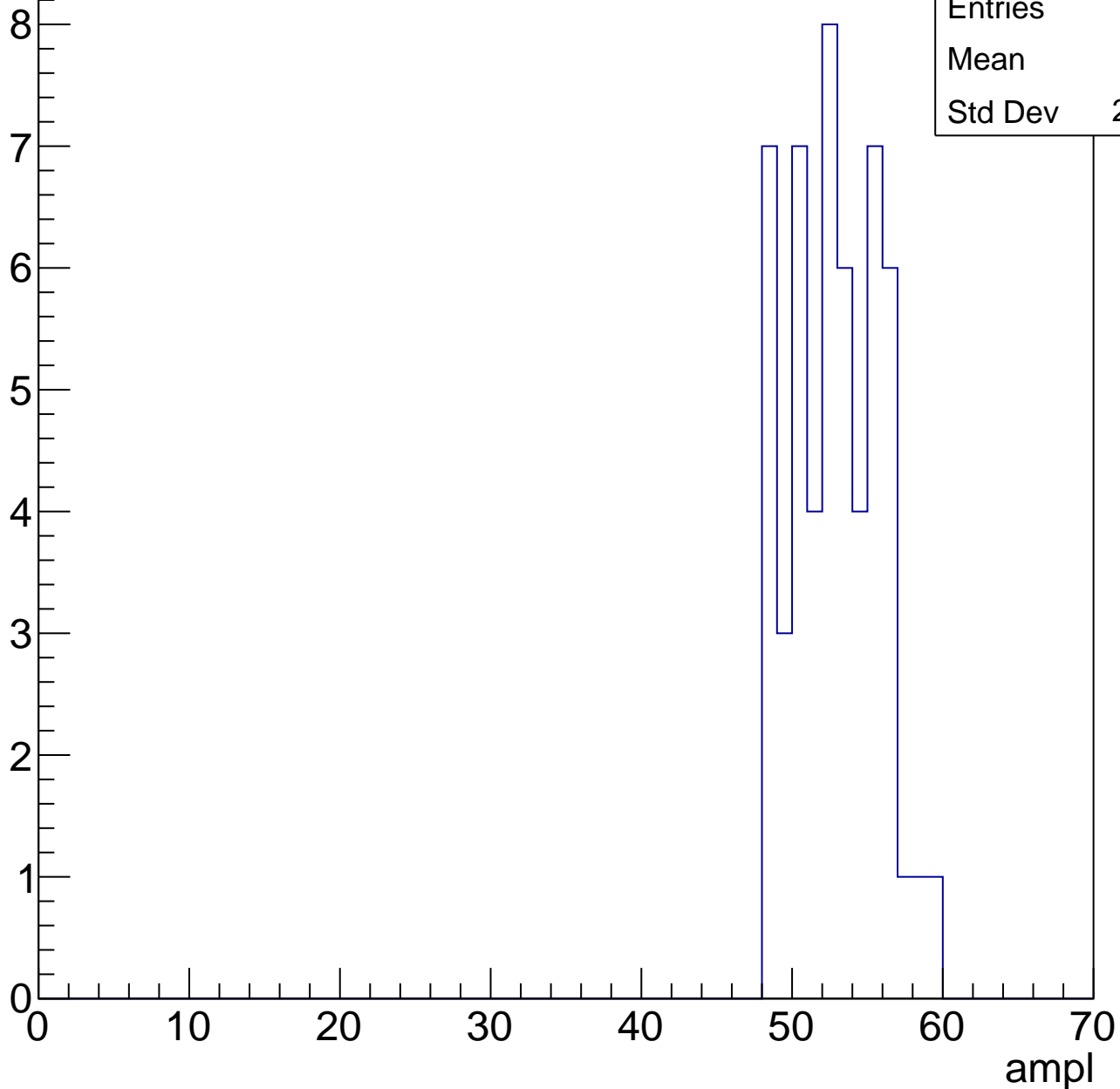
Entries	51
Mean	46.61
Std Dev	2.978



B1L002S, U7-ch6, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



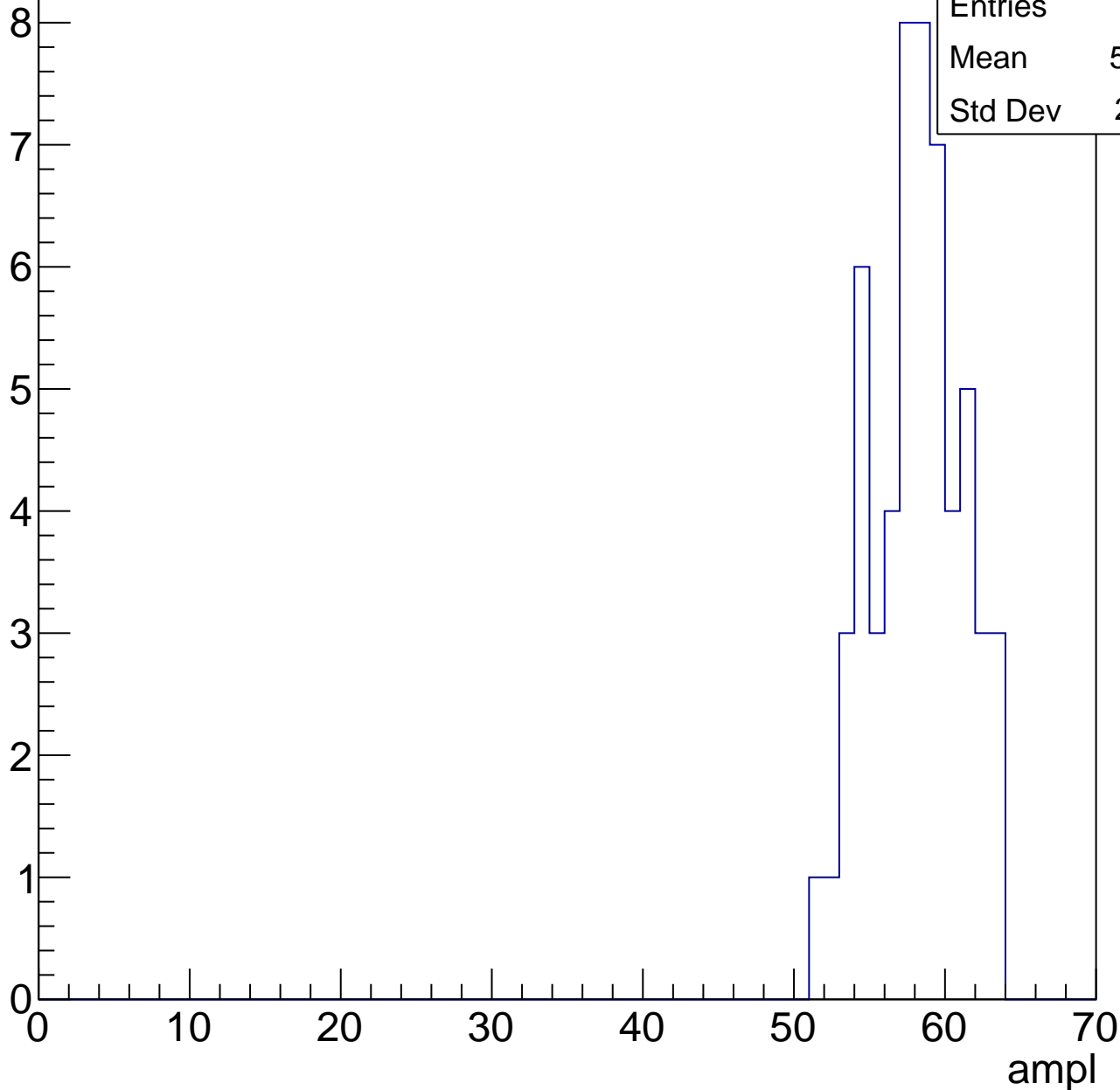
Entries	55
Mean	52.4
Std Dev	2.871

B1L002S, U7-ch6, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.64
Std Dev	2.961

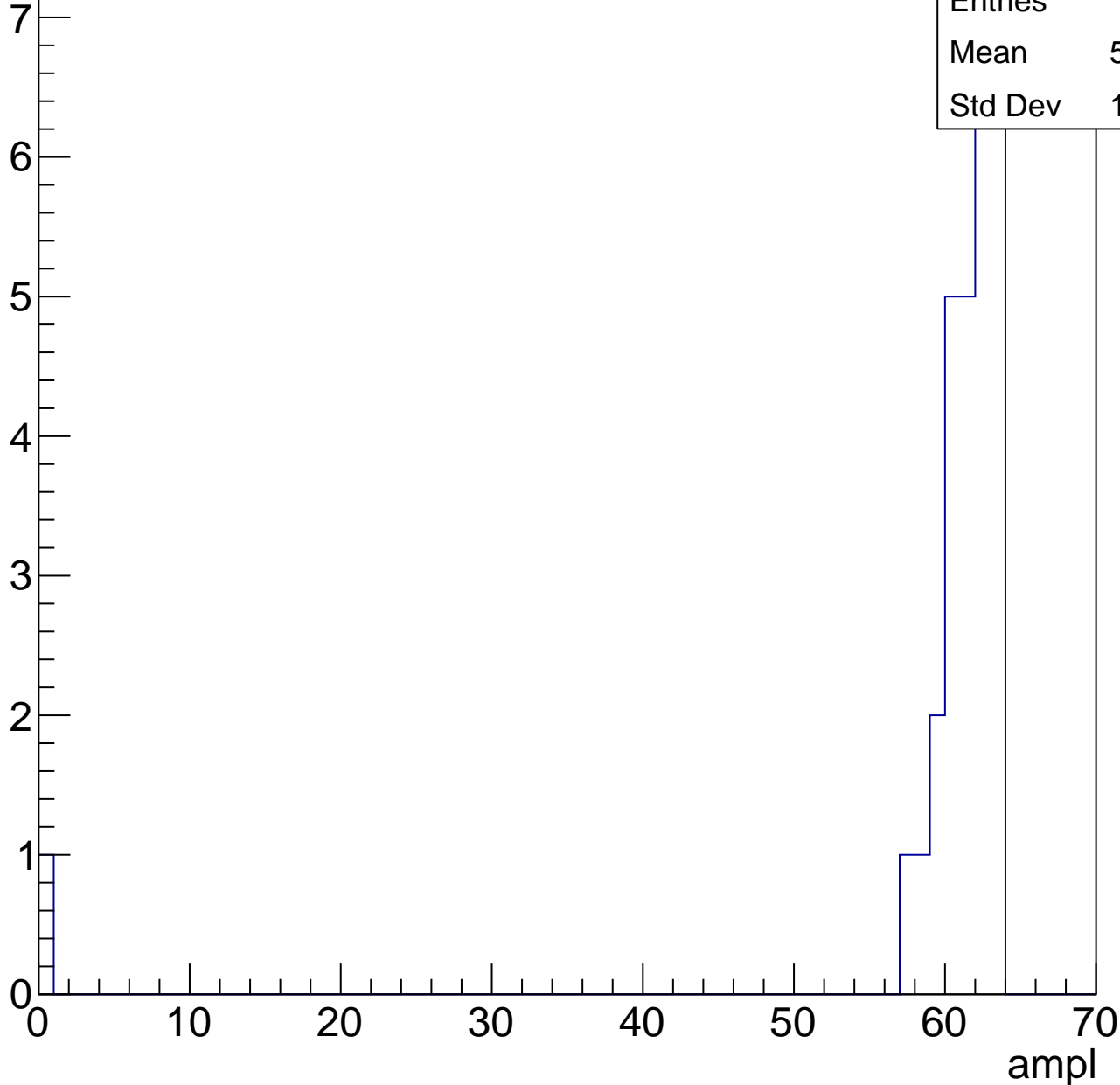


B1L002S, U7-ch6, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

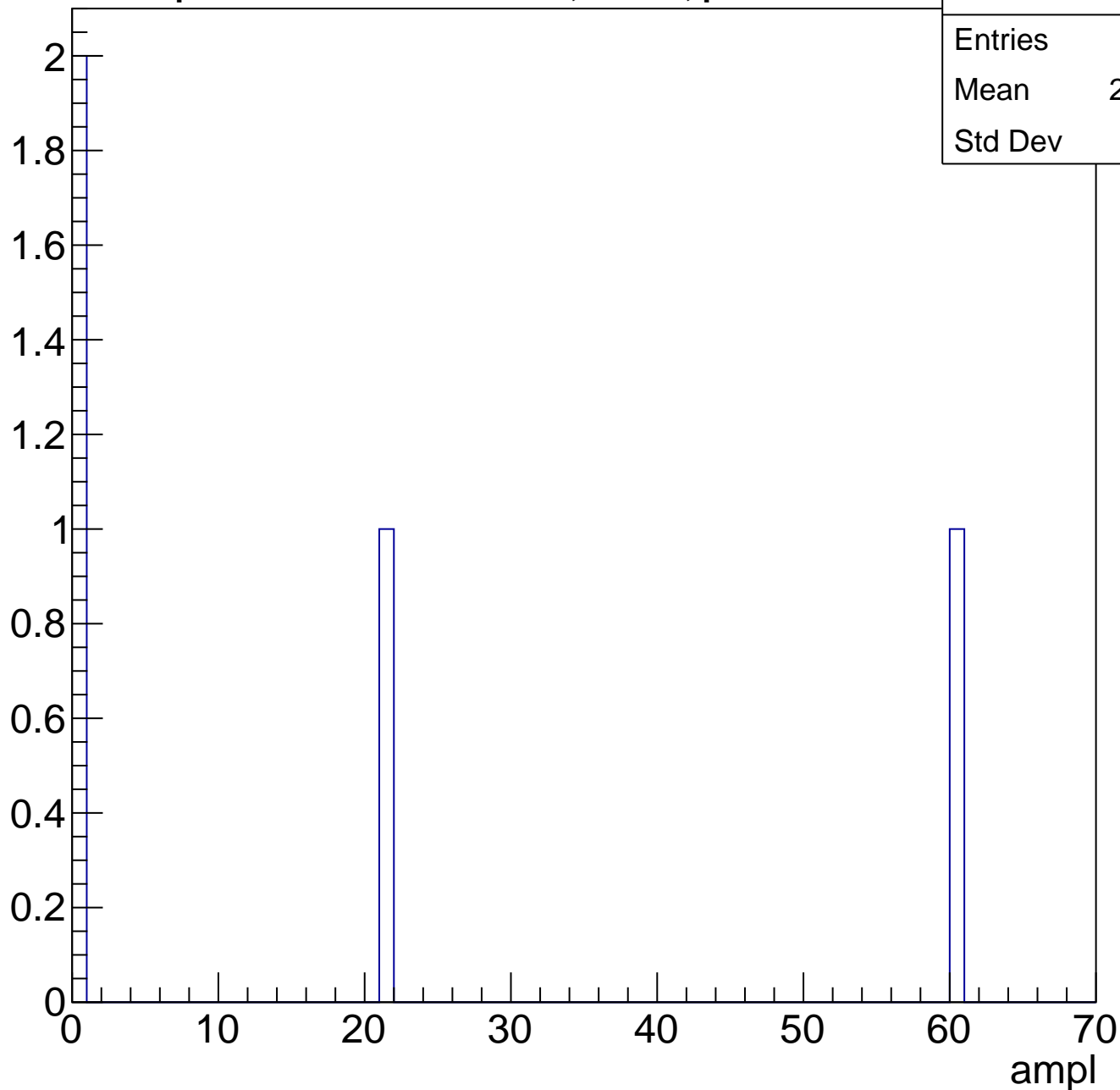
Entries	29
Mean	59.07
Std Dev	11.27



B1L002S, U7-ch6, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.25
Std Dev	24.5

B1L002S, U7-ch7, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	73
Mean	29.95
Std Dev	3.247

Gaus mean : 30.9450

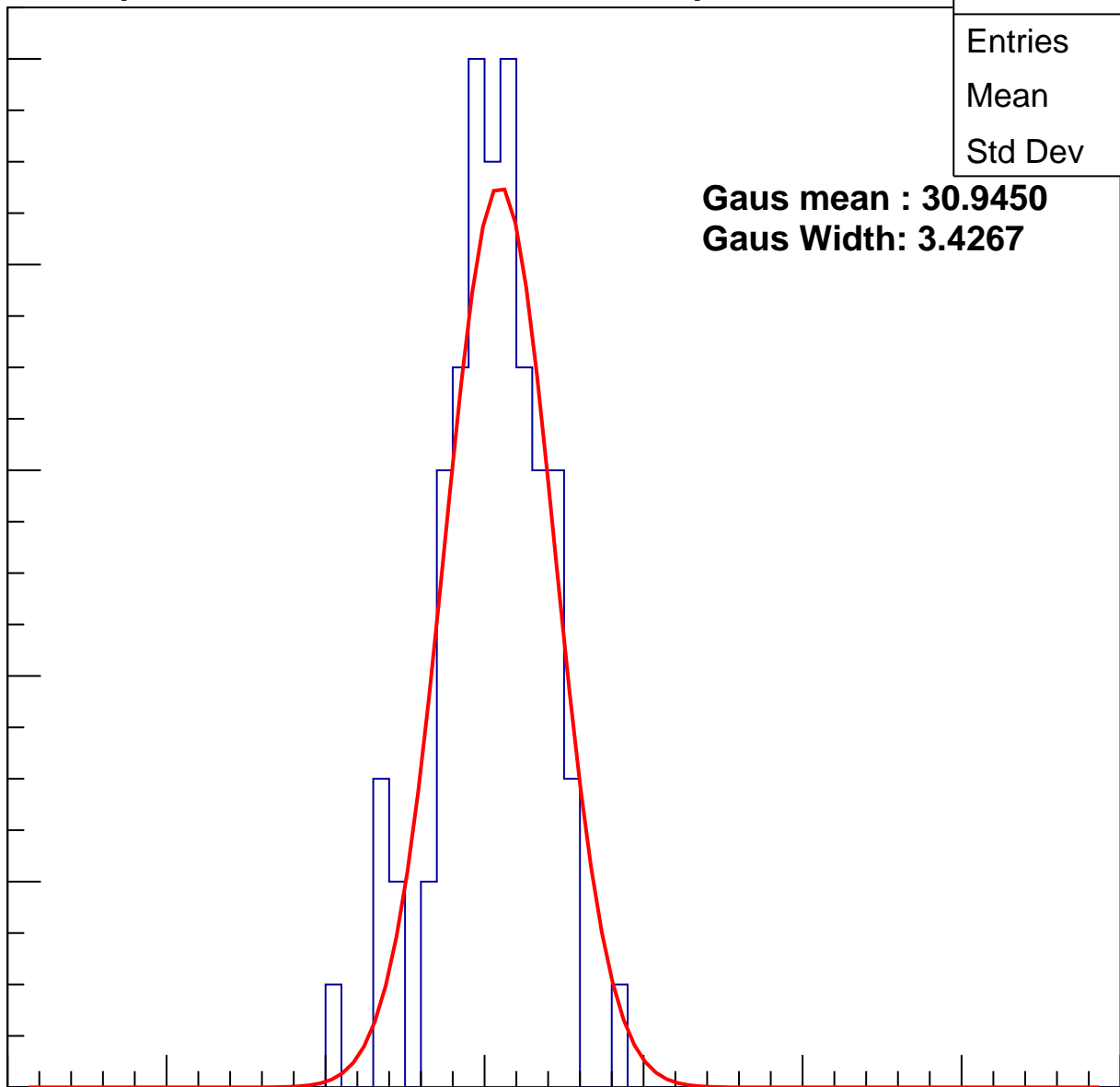
Gaus Width: 3.4267

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



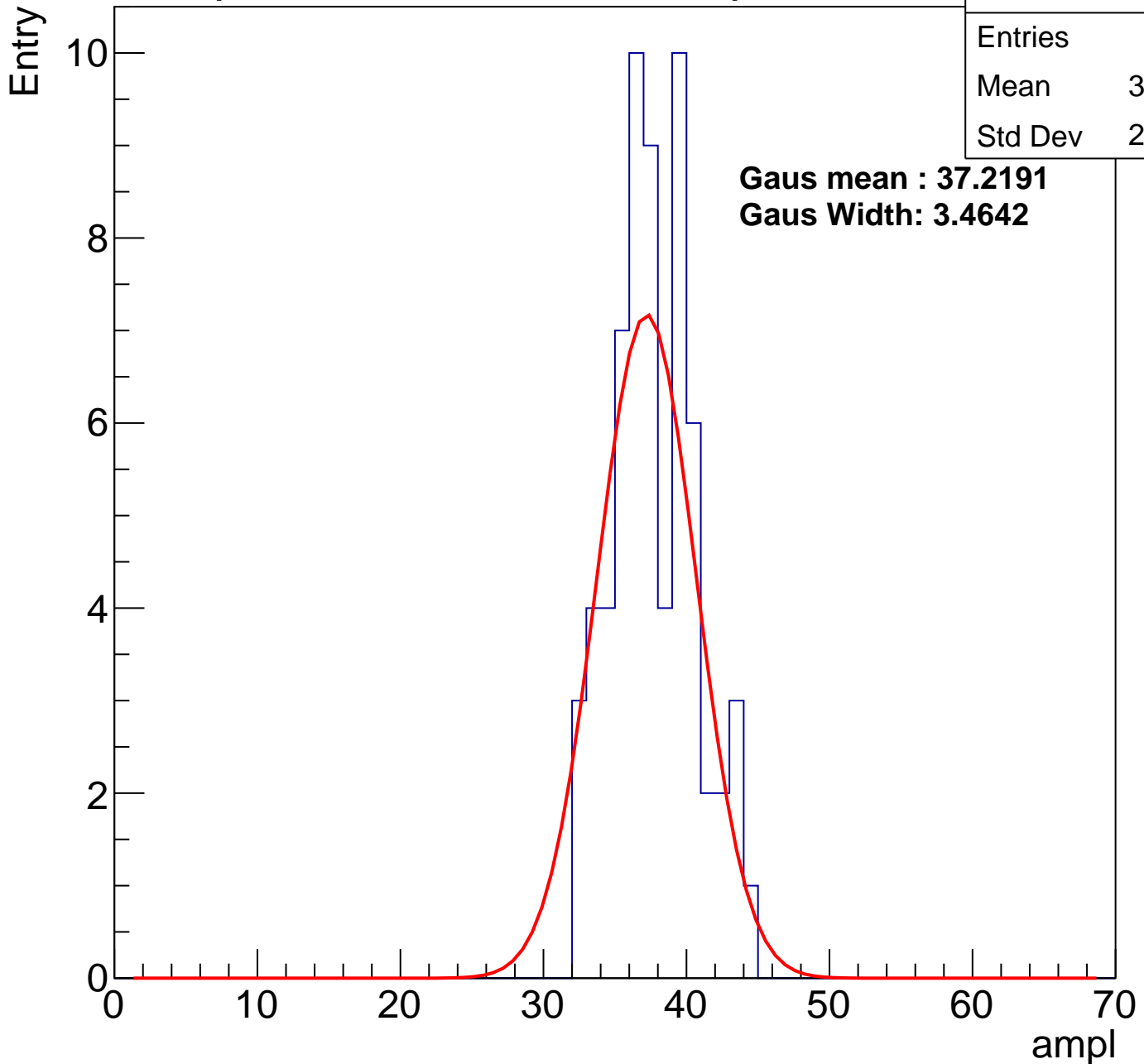
B1L002S, U7-ch7, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	37.28
Std Dev	2.896

Gaus mean : 37.2191

Gaus Width: 3.4642



B1L002S, U7-ch7, adc2

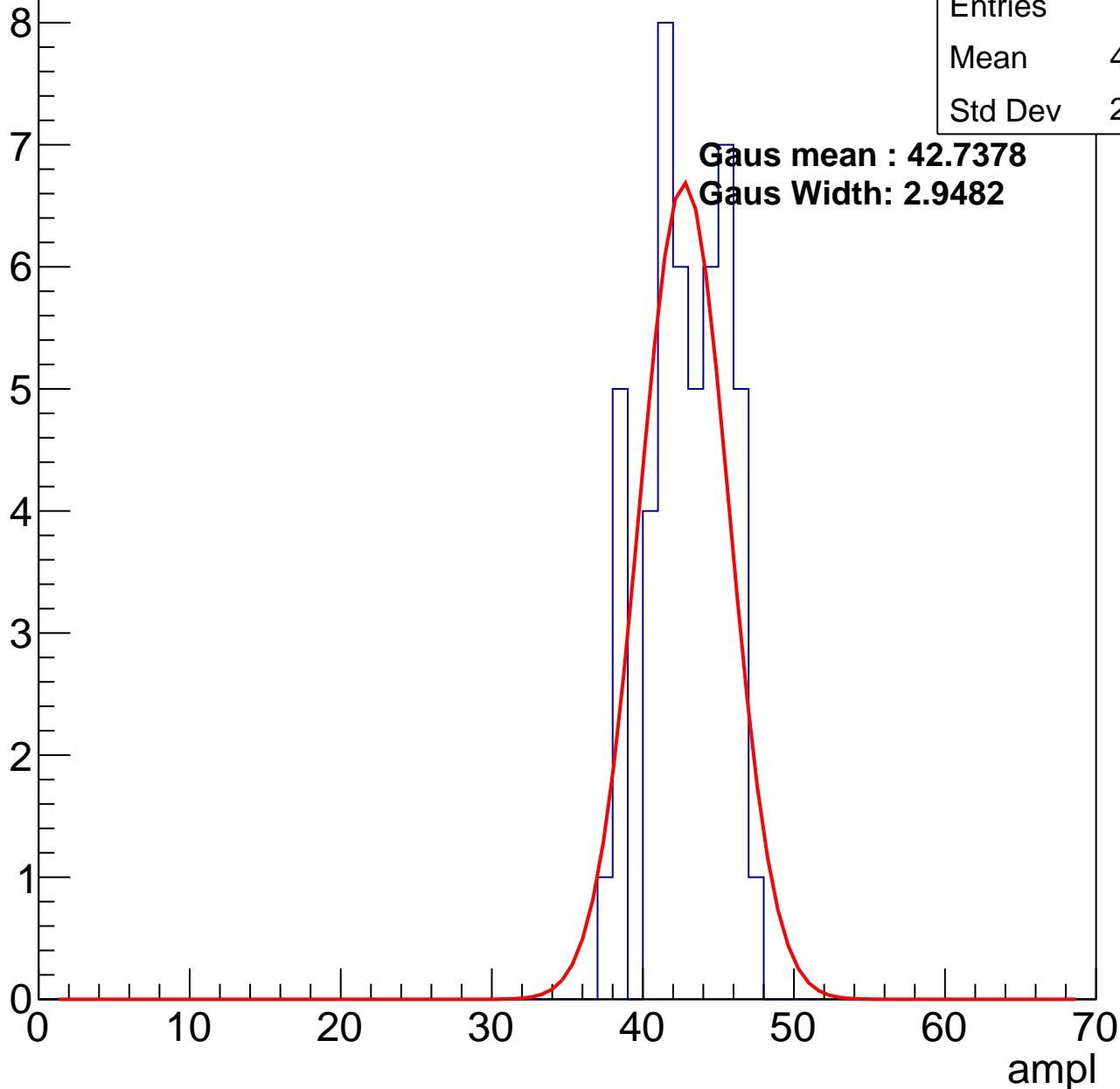
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	48
Mean	42.46
Std Dev	2.565

Gaus mean : 42.7378

Gaus Width: 2.9482

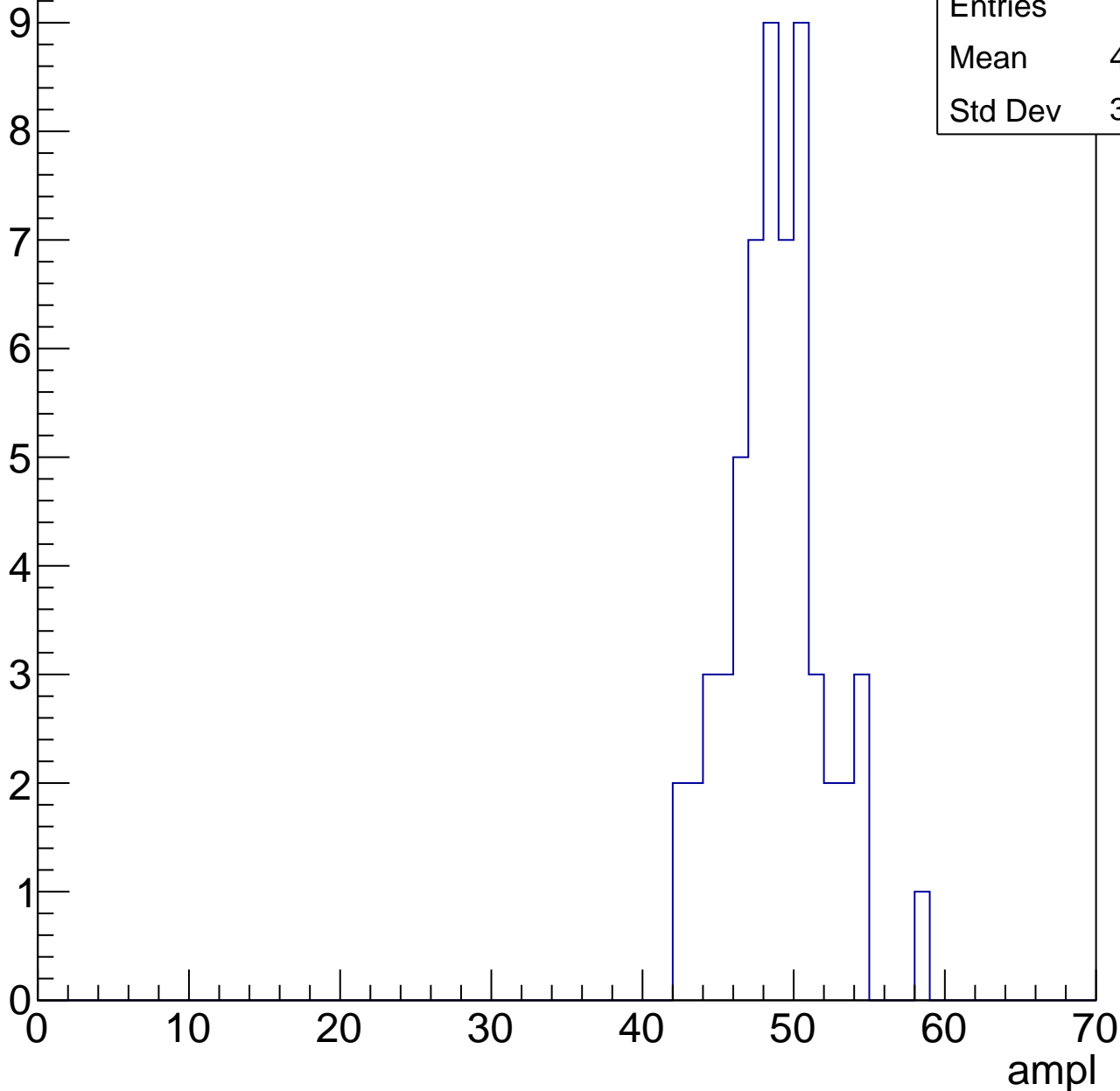


B1L002S, U7-ch7, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

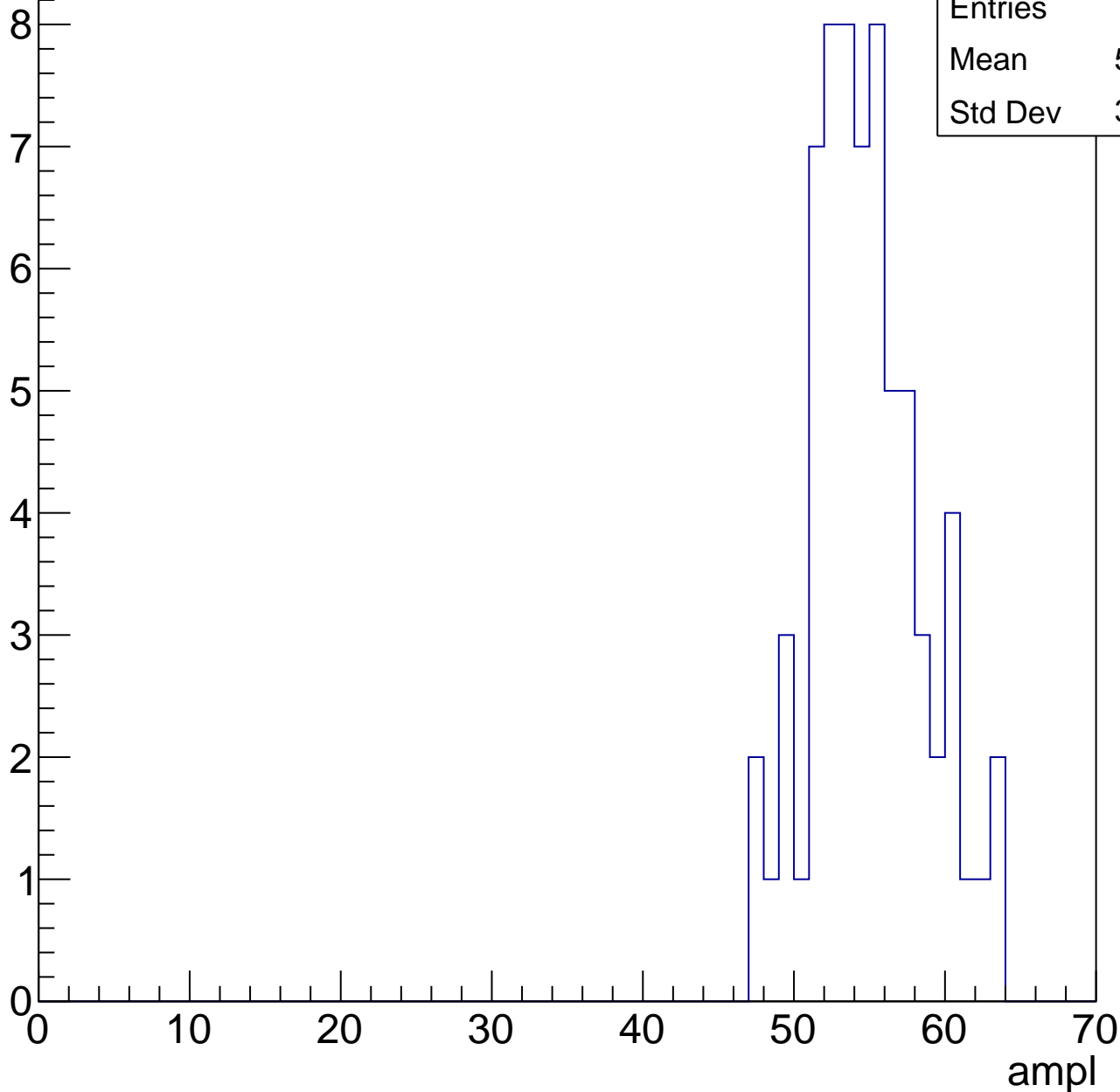
Entries	58
Mean	48.34
Std Dev	3.154



B1L002S, U7-ch7, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



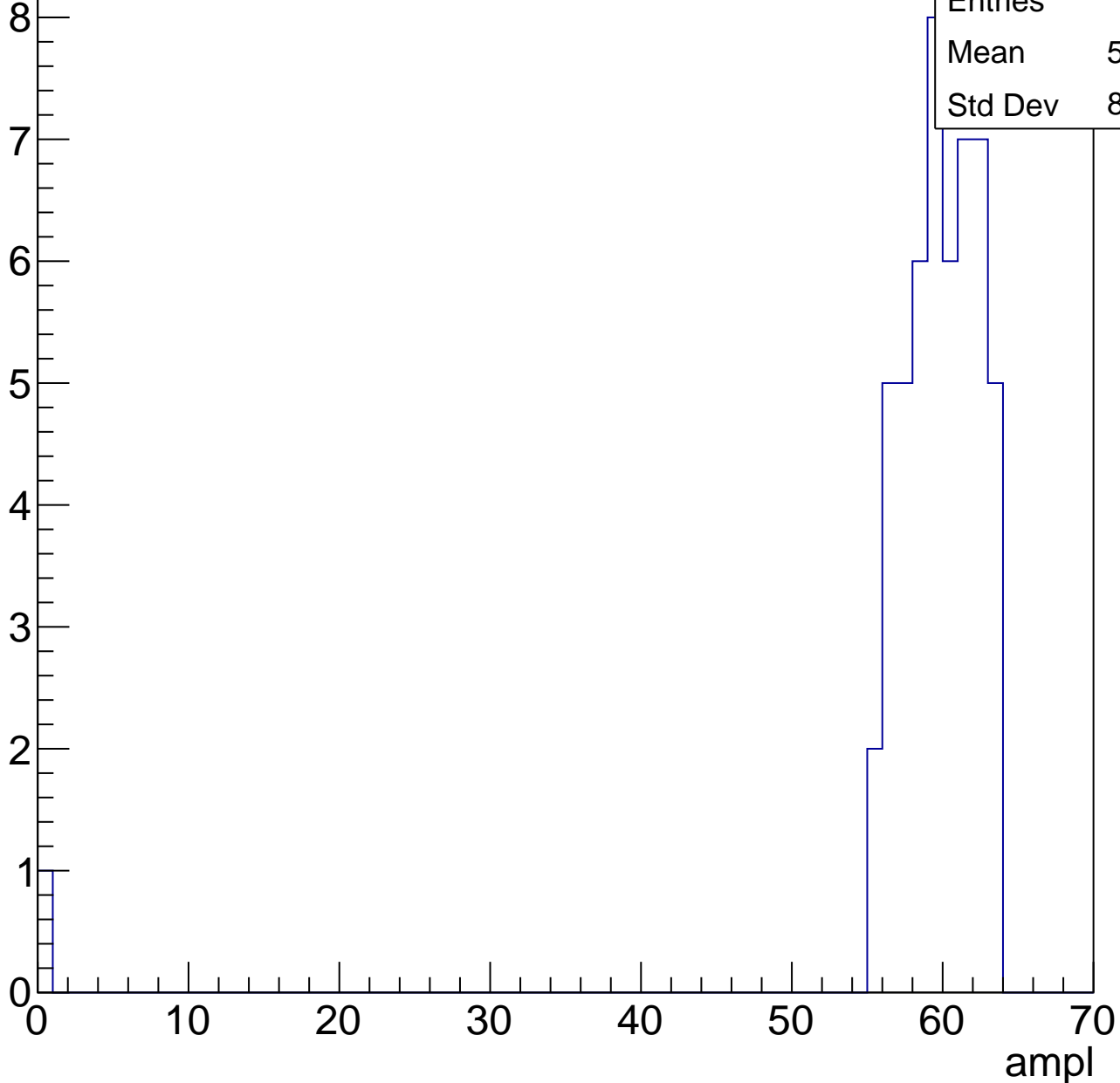
Entries	68
Mean	54.41
Std Dev	3.651

B1L002S, U7-ch7, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

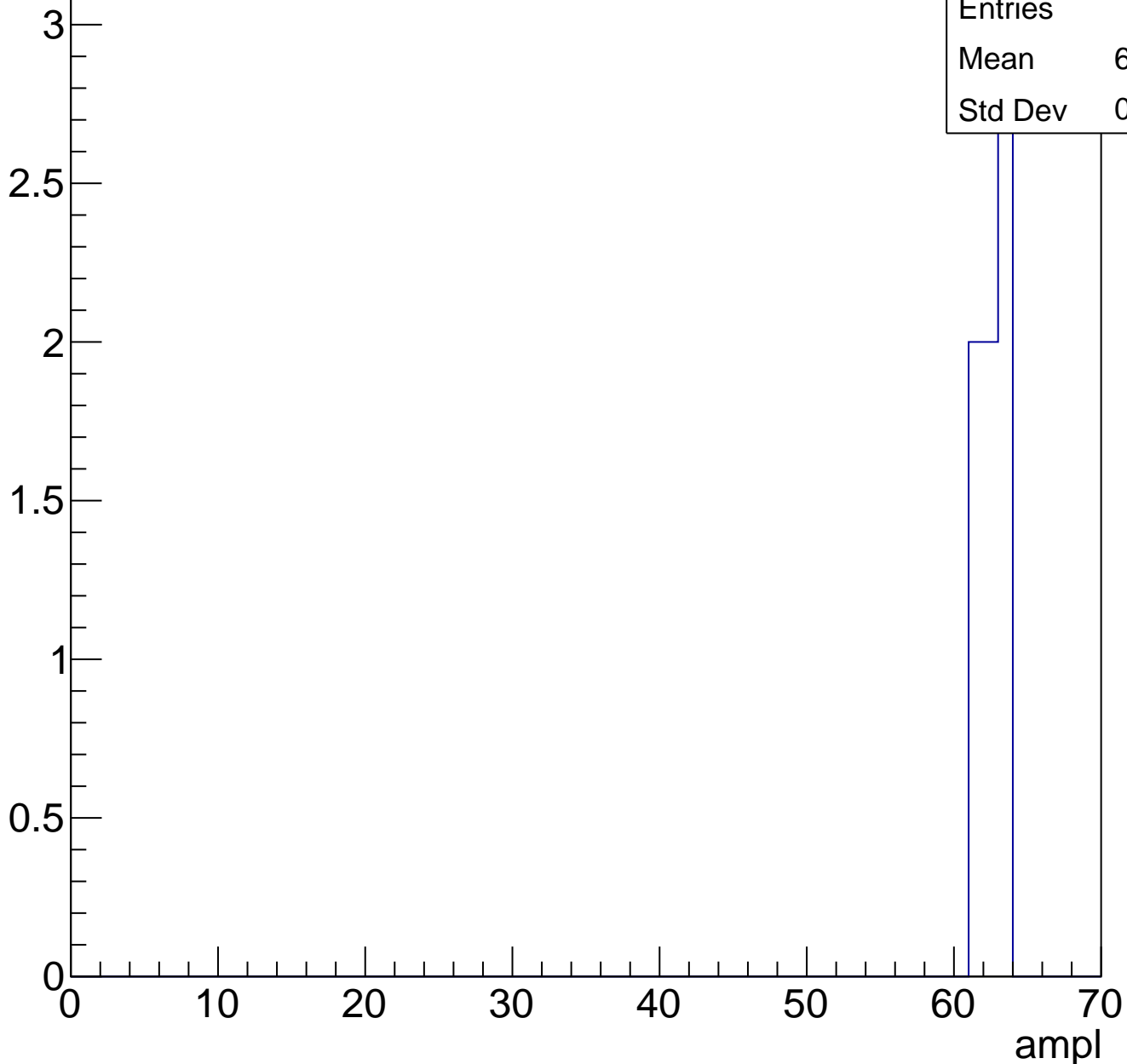
Entries	52
Mean	58.29
Std Dev	8.475



B1L002S, U7-ch7, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch7, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch8, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	27.76
Std Dev	3.068

Gaus mean : 28.5714

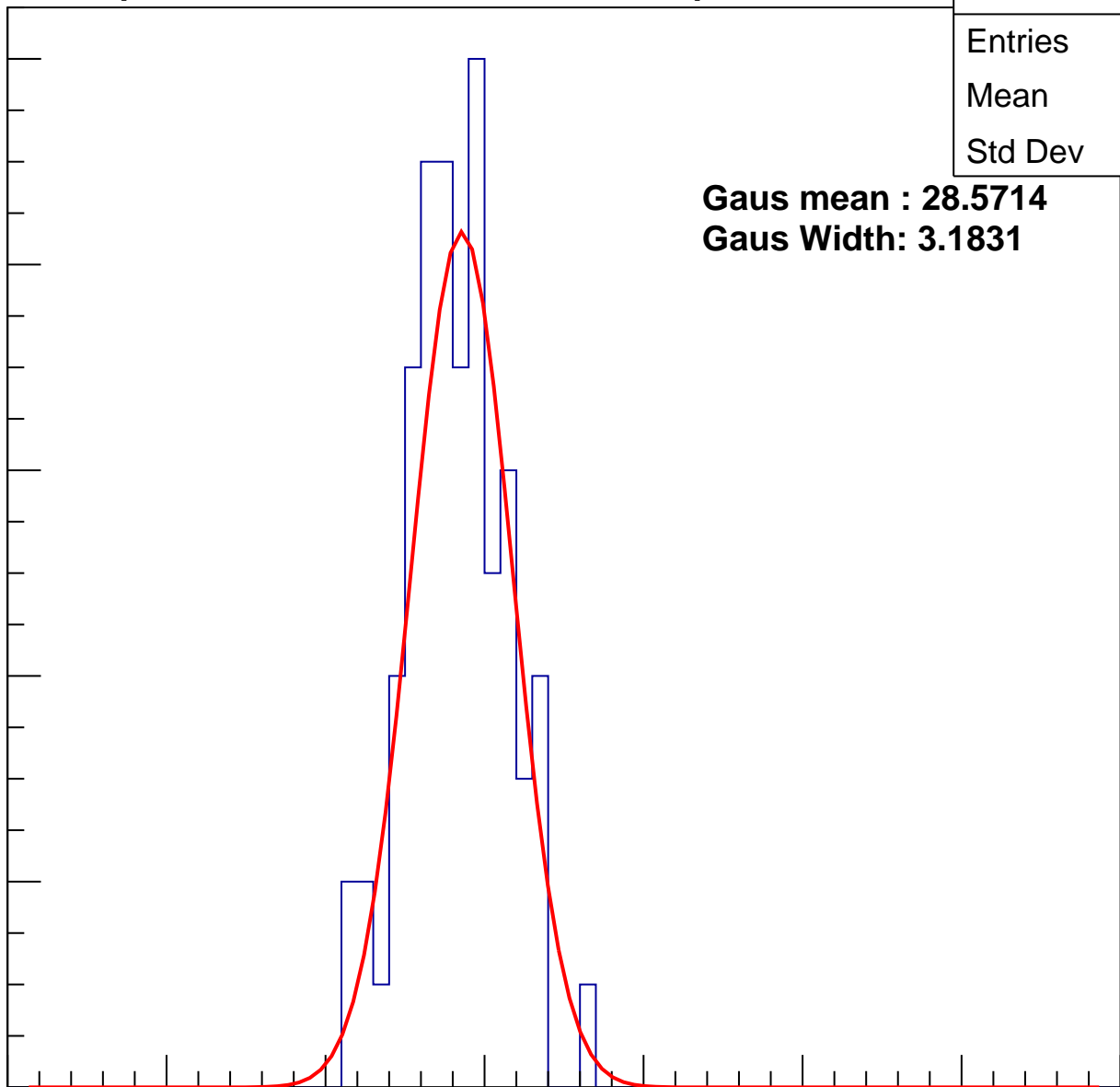
Gaus Width: 3.1831

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



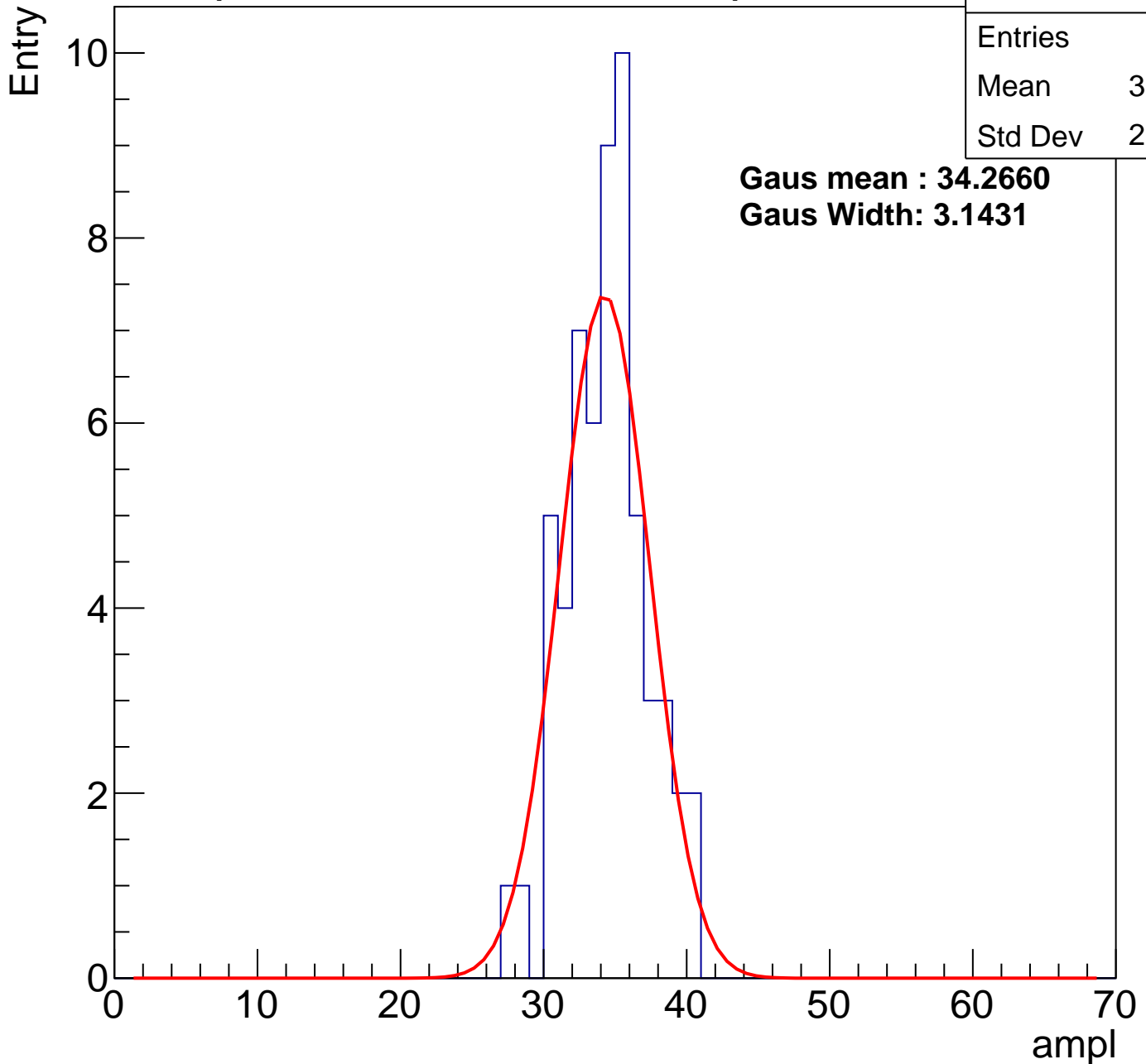
B1L002S, U7-ch8, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	58
Mean	33.97
Std Dev	2.822

Gaus mean : 34.2660

Gaus Width: 3.1431



B1L002S, U7-ch8, adc2

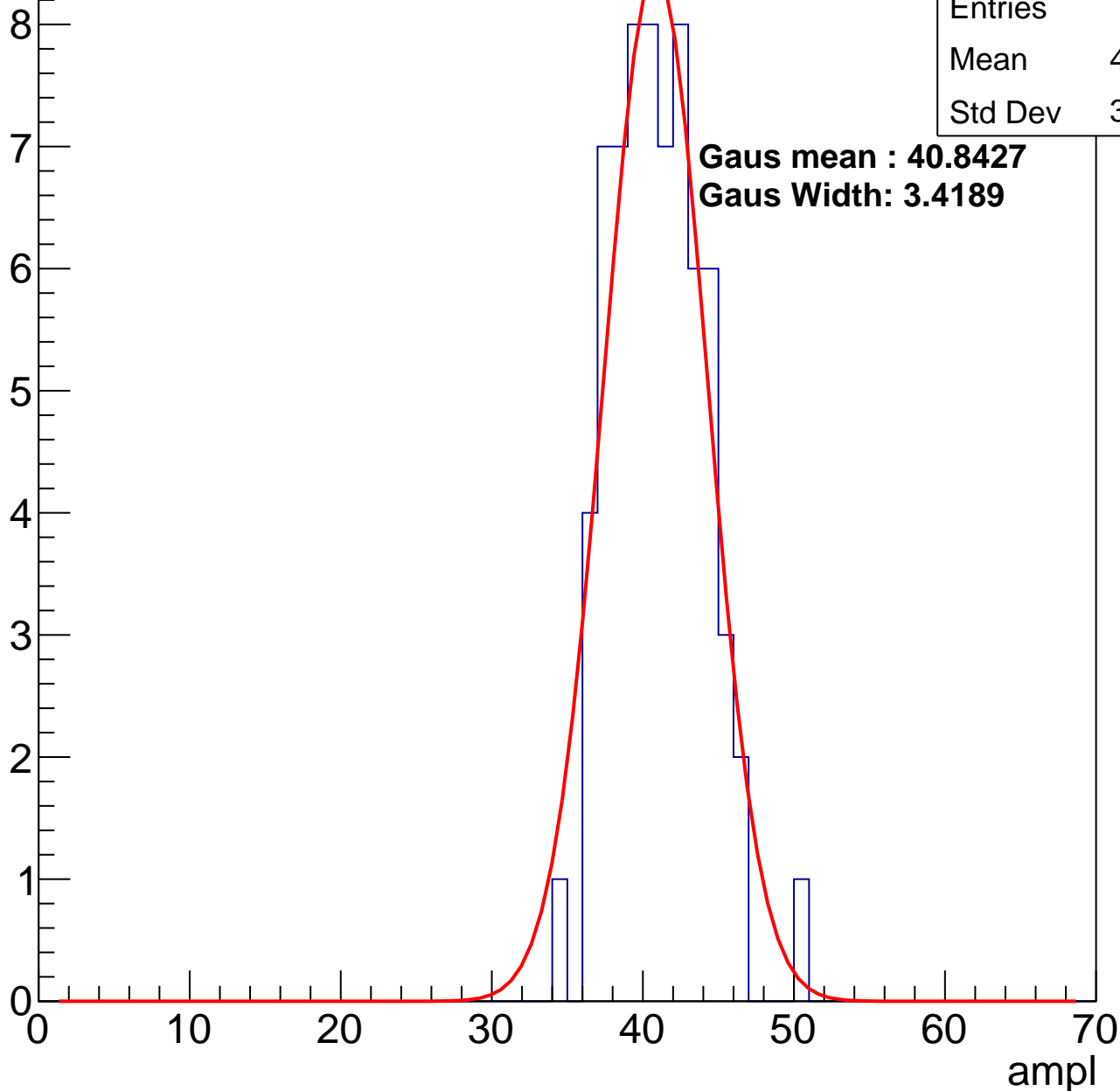
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.54
Std Dev	3.007

Gaus mean : 40.8427

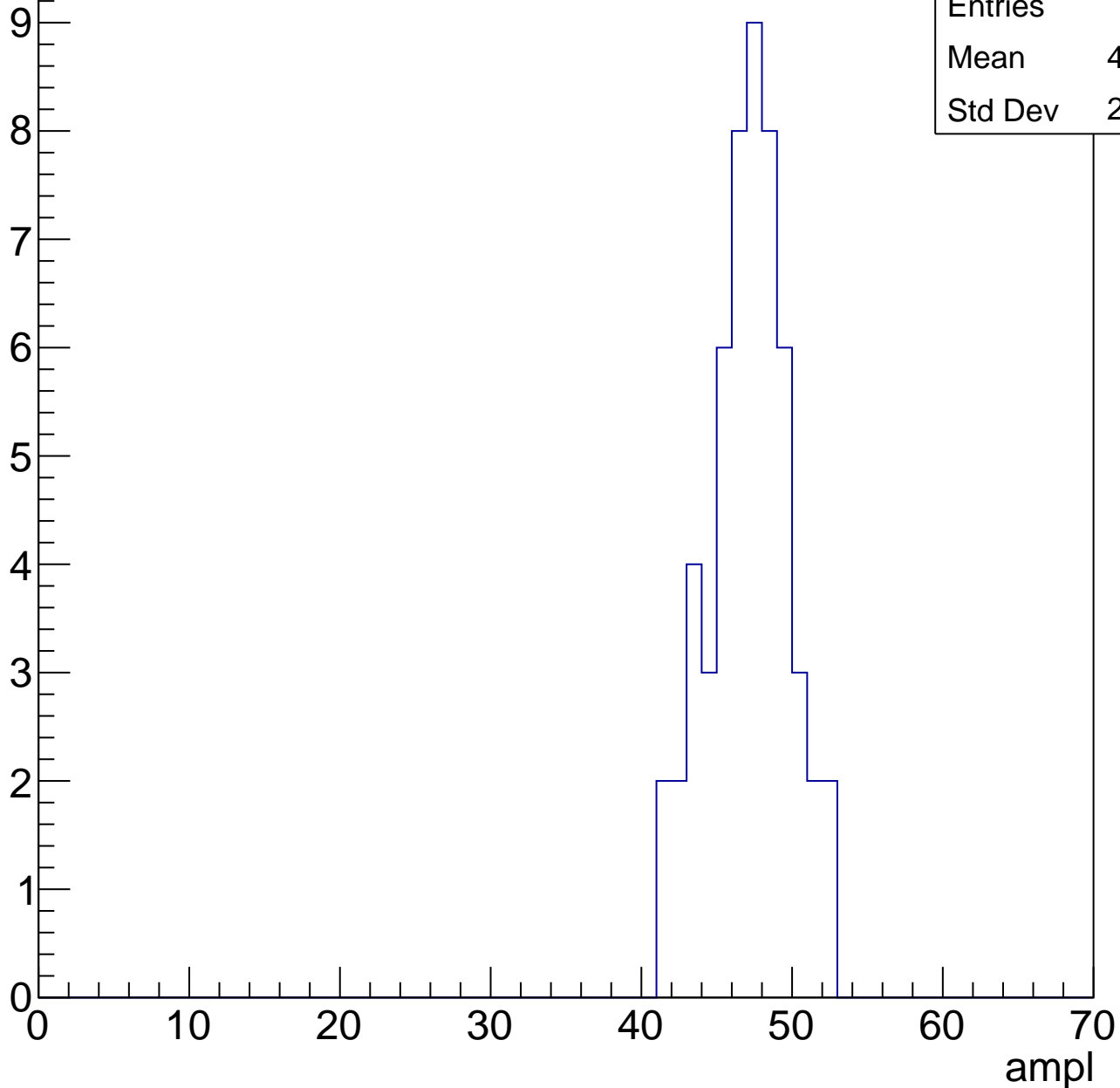
Gaus Width: 3.4189



B1L002S, U7-ch8, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

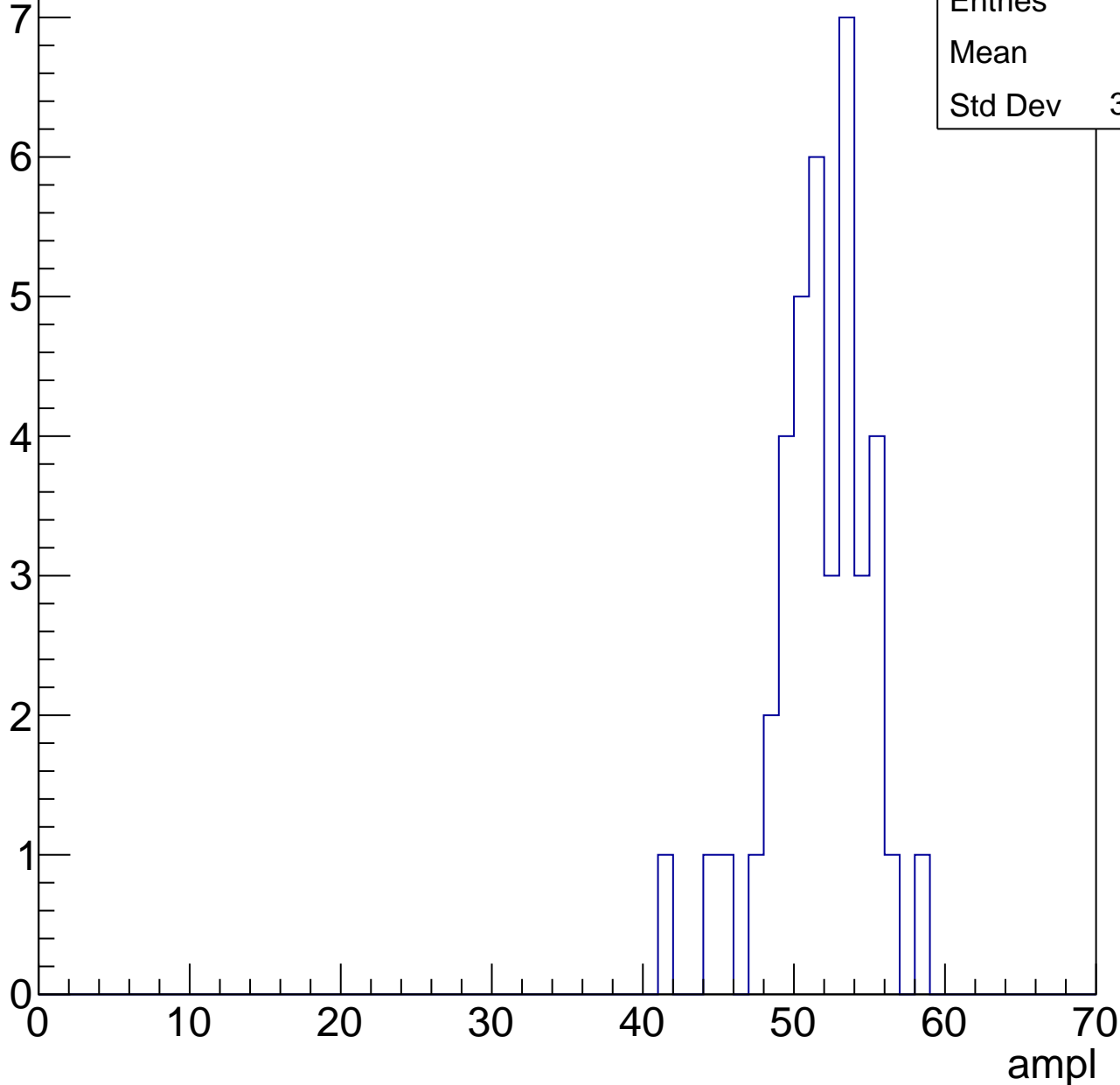


B1L002S, U7-ch8, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	40
Mean	51.2
Std Dev	3.295



B1L002S, U7-ch8, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	79
Mean	56.22
Std Dev	7.038

Entry

10

8

6

4

2

0

0

10

20

30

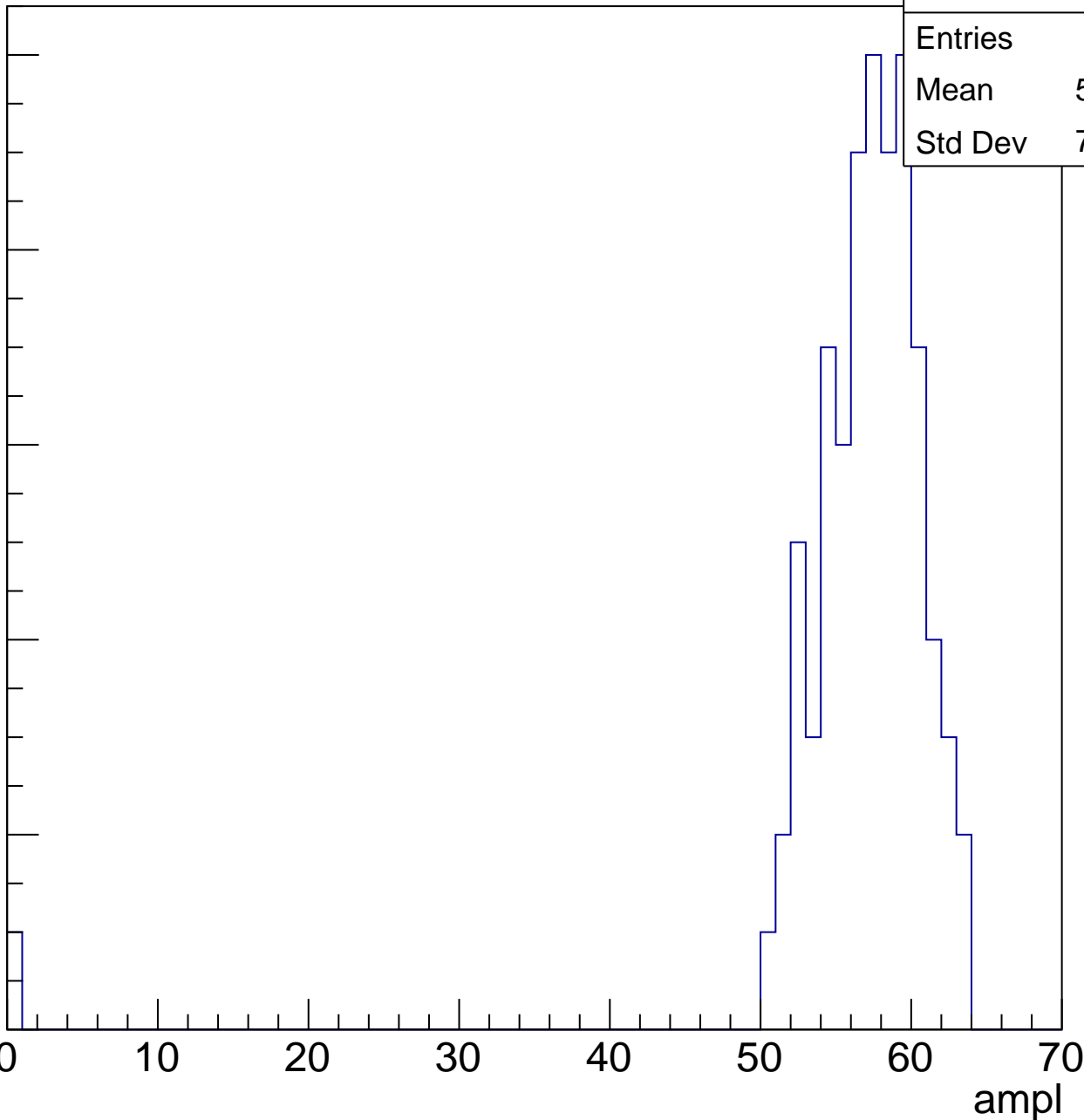
40

50

60

70

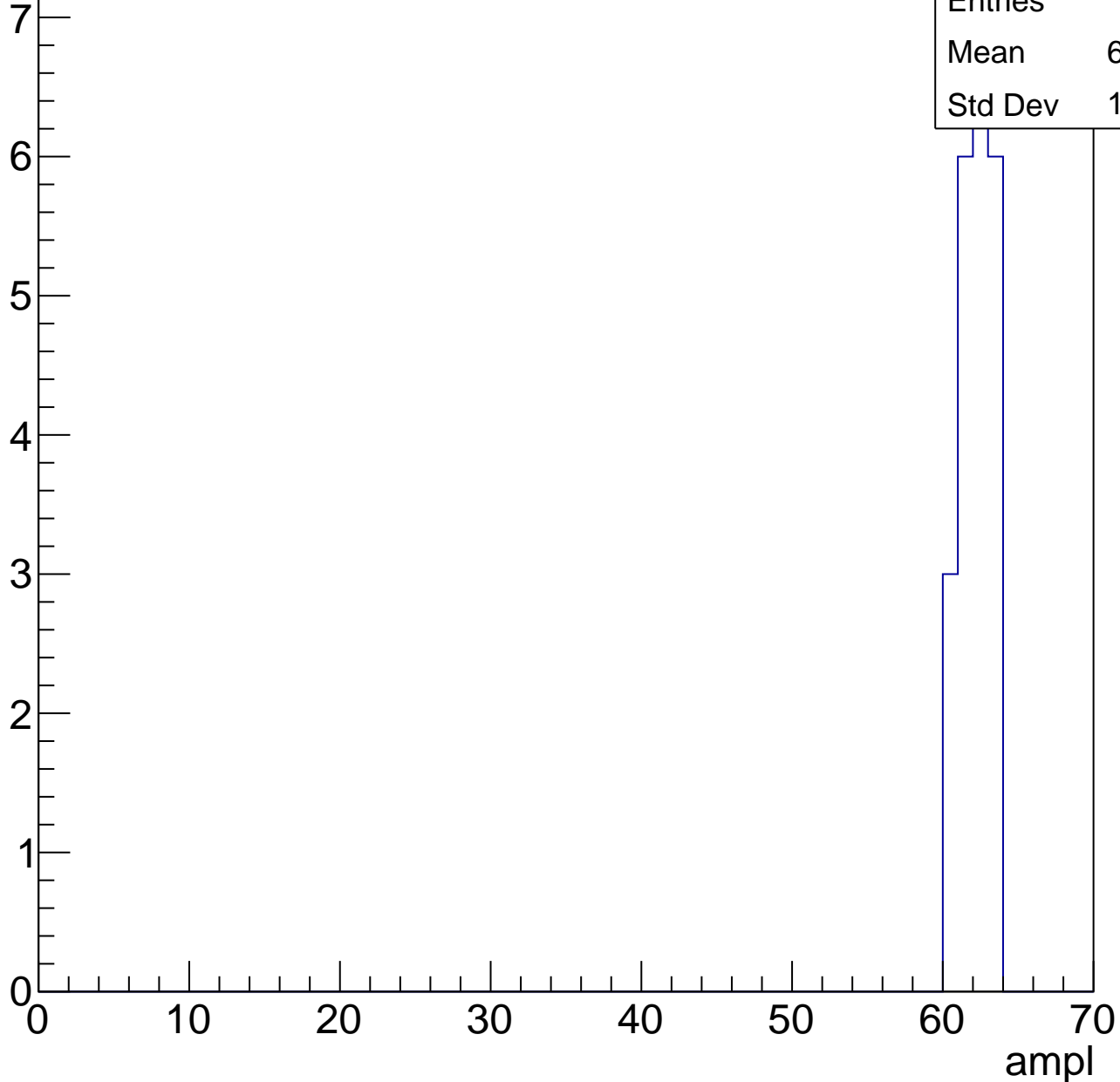
ampl



B1L002S, U7-ch8, adc6

calib_packv5_042523_0143.root, FC#10, port B3

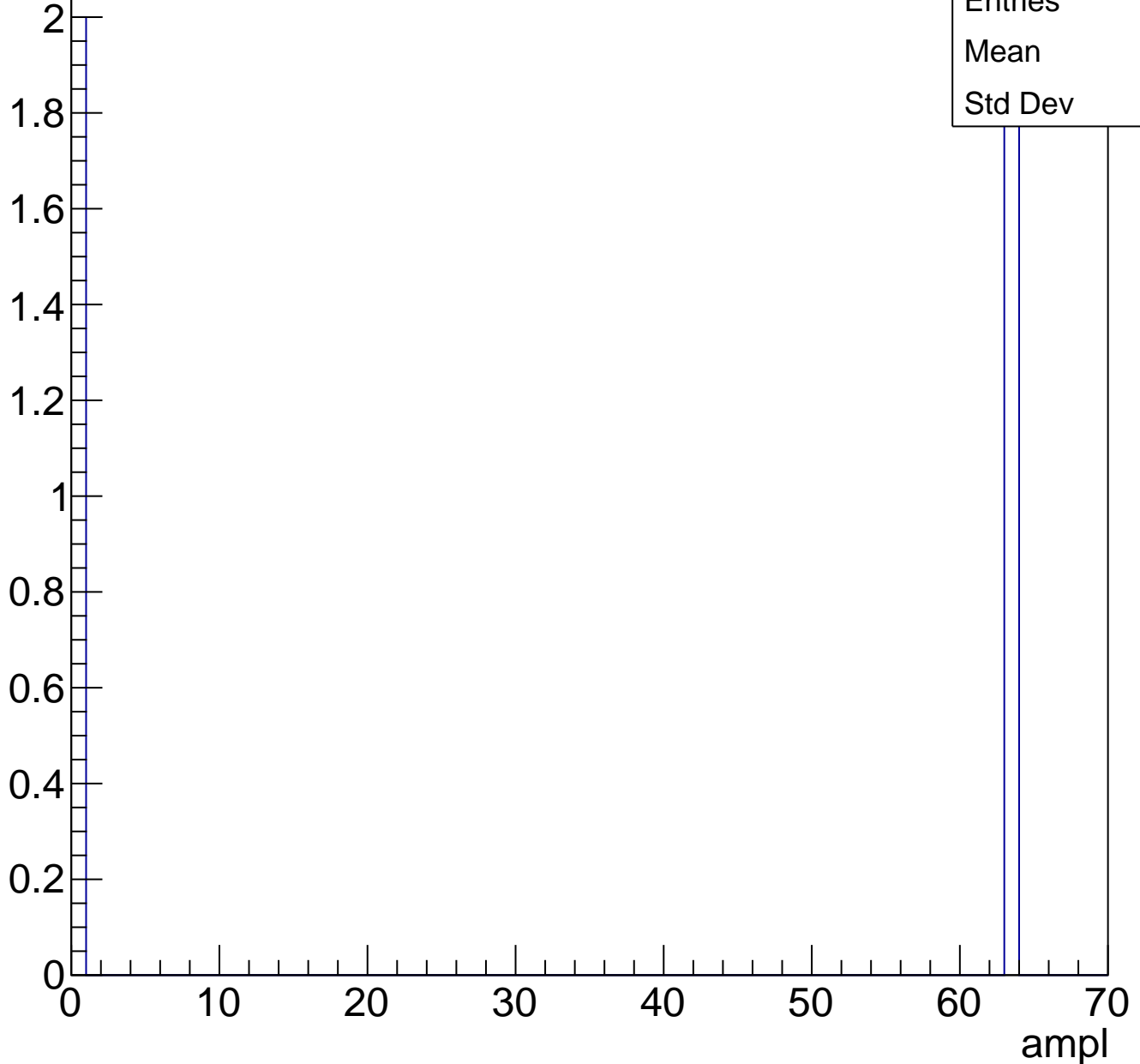
Entry



B1L002S, U7-ch8, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch9, adc0

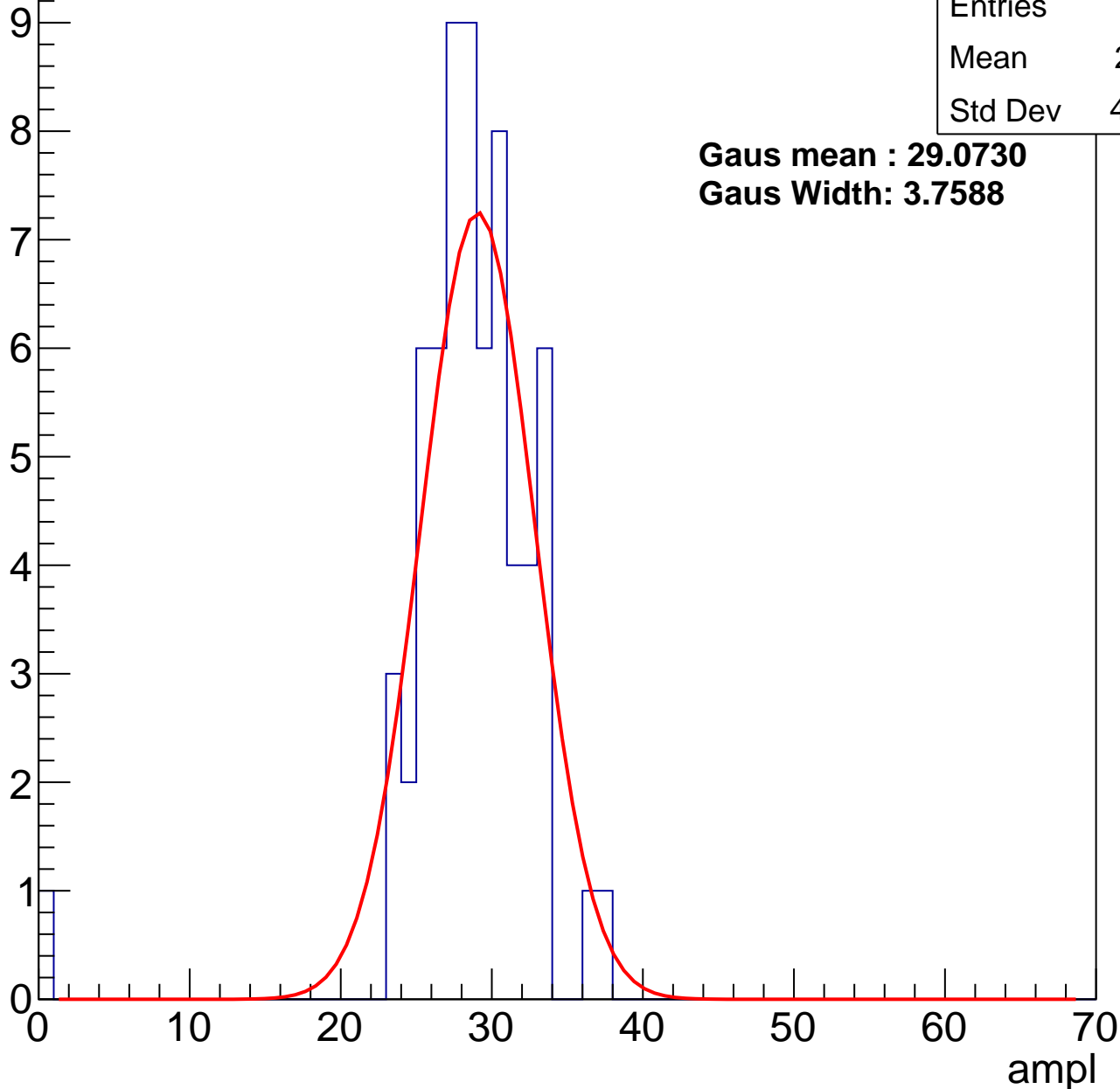
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	28.11
Std Dev	4.623

Gaus mean : 29.0730

Gaus Width: 3.7588



B1L002S, U7-ch9, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	52
Mean	34.21
Std Dev	2.677

Gaus mean : 34.6260

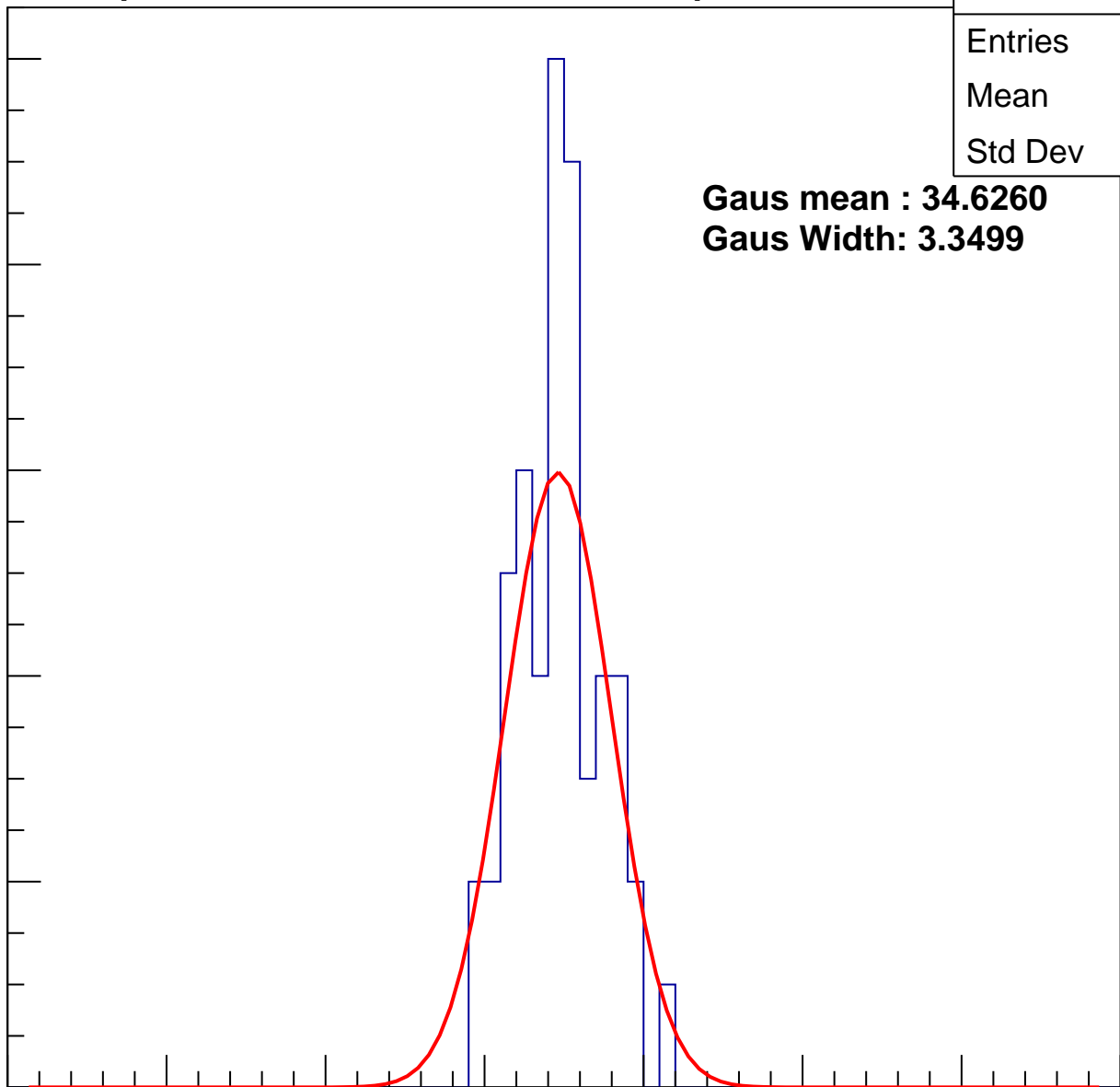
Gaus Width: 3.3499

Entry

10
8
6
4
2
0

ampl

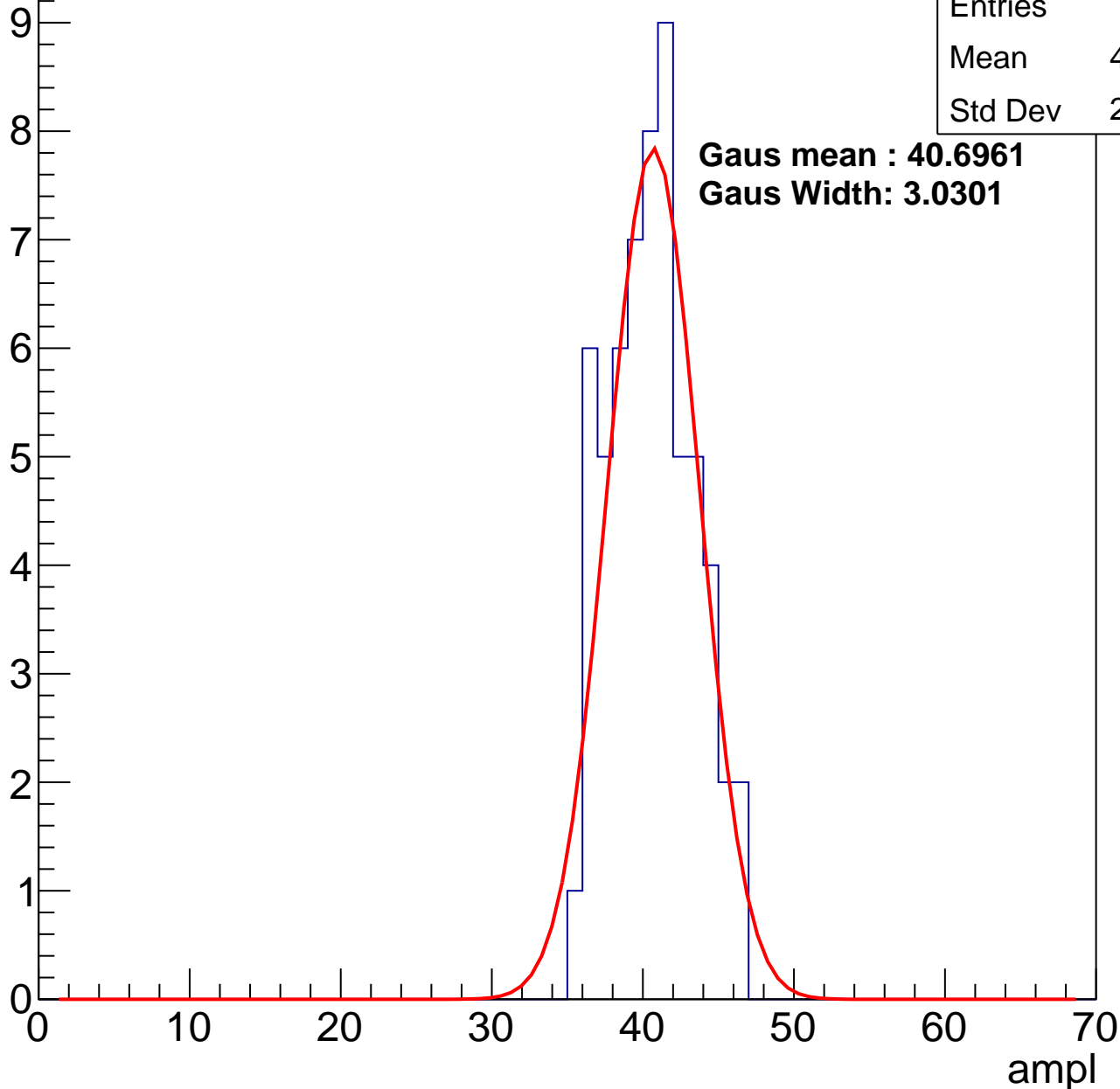
0 10 20 30 40 50 60 70



B1L002S, U7-ch9, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

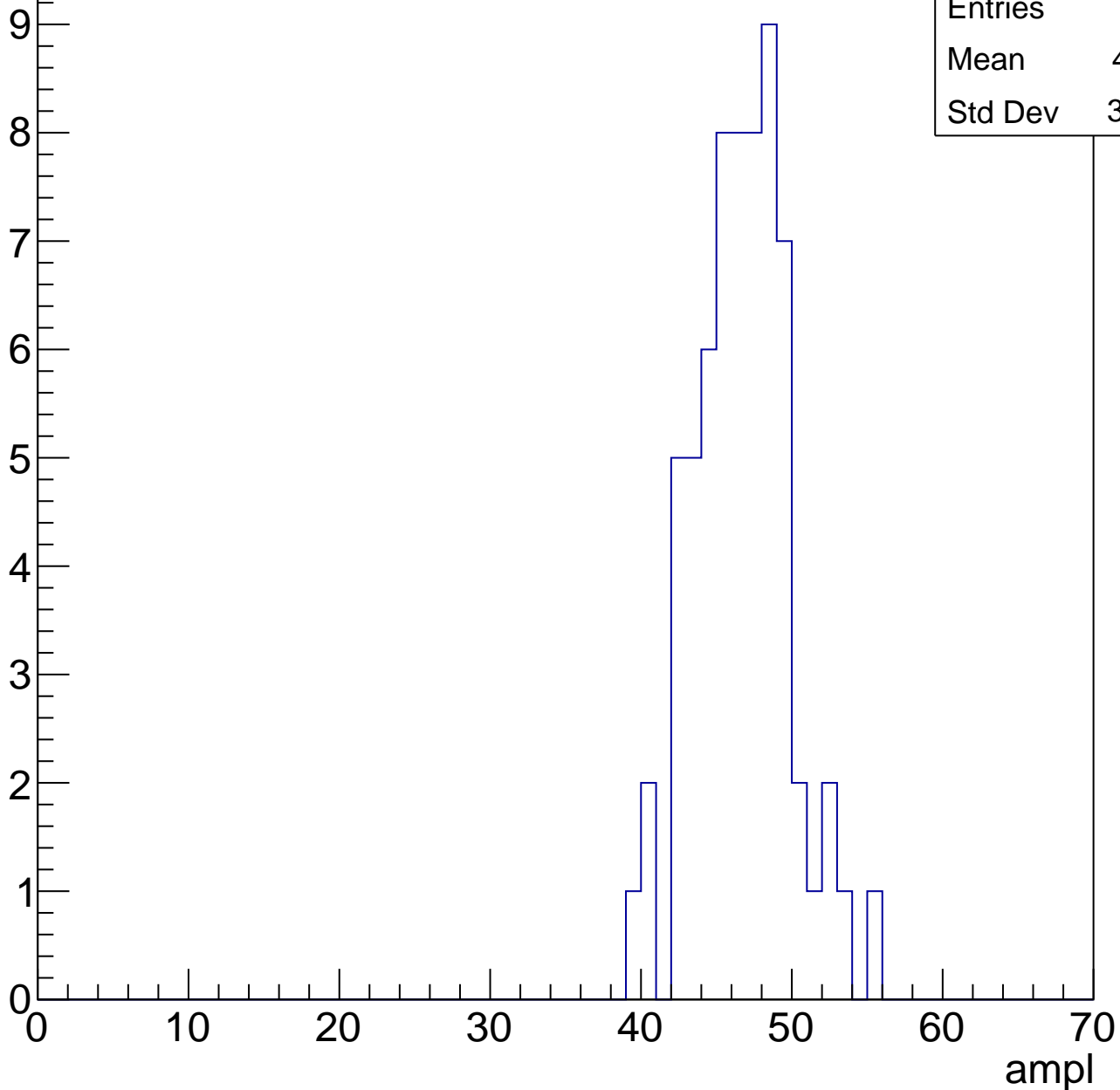


B1L002S, U7-ch9, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.21
Std Dev	3.117

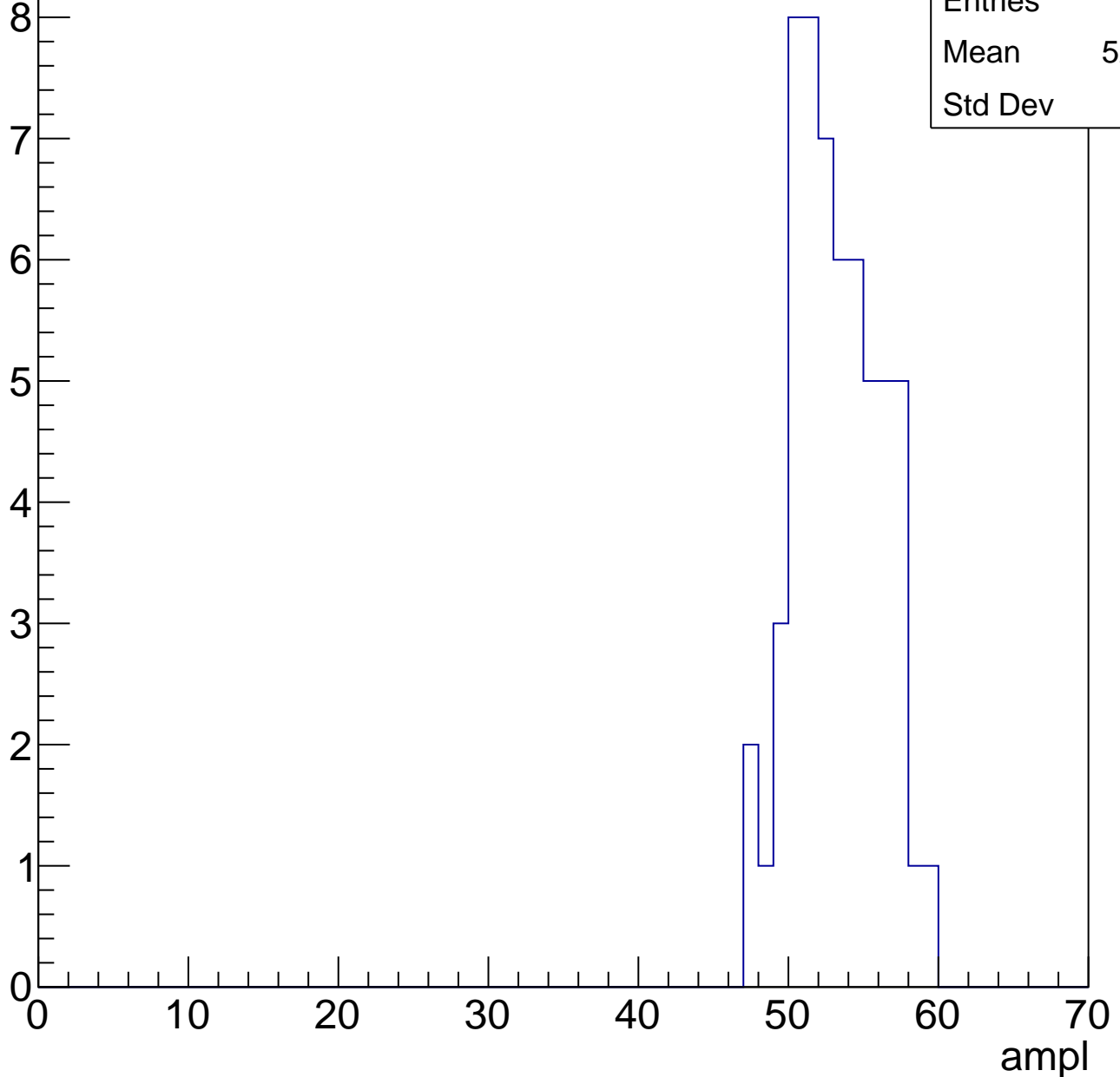


B1L002S, U7-ch9, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.76
Std Dev	2.83



B1L002S, U7-ch9, adc5

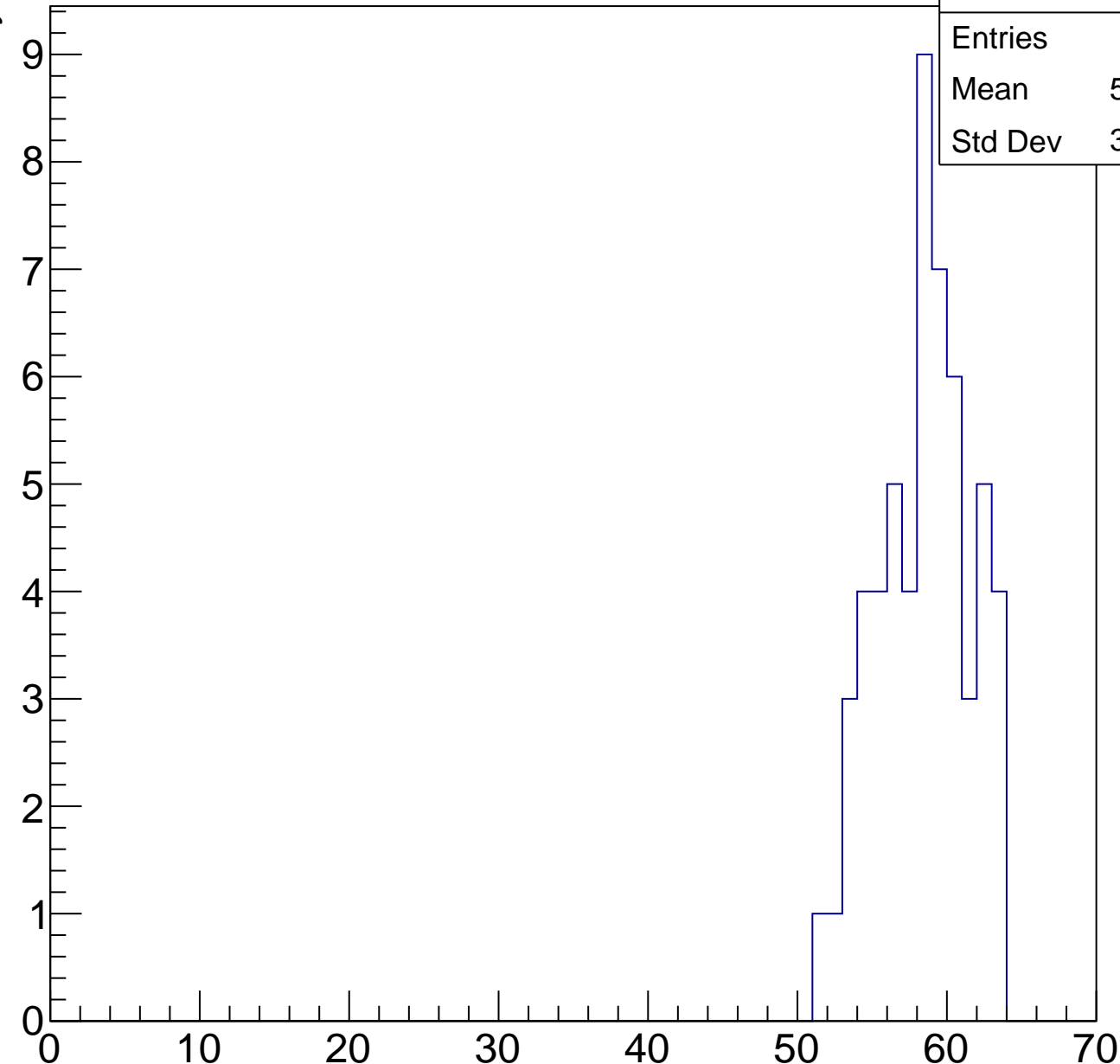
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	57.96
Std Dev	3.053

ampl

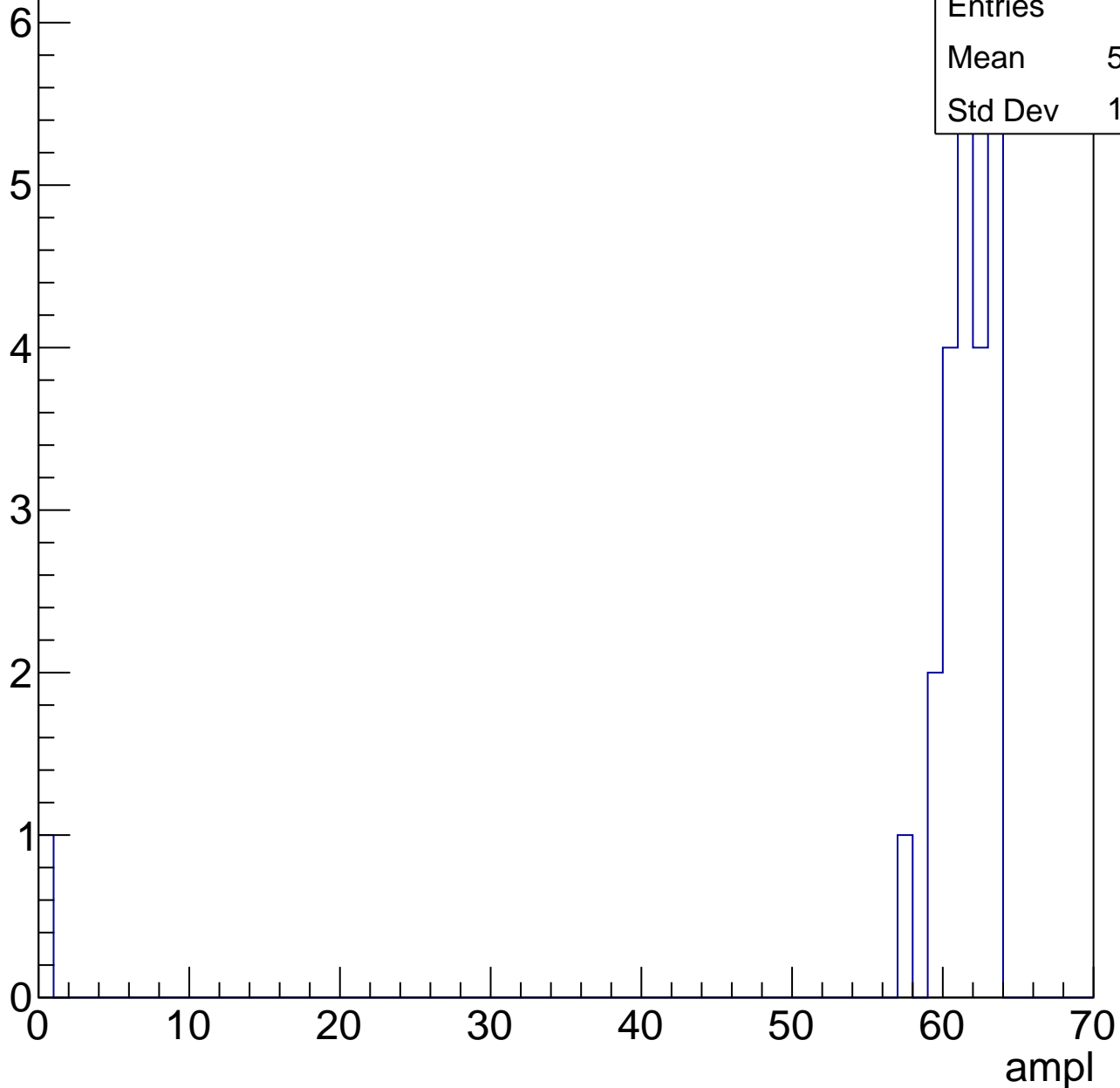


B1L002S, U7-ch9, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

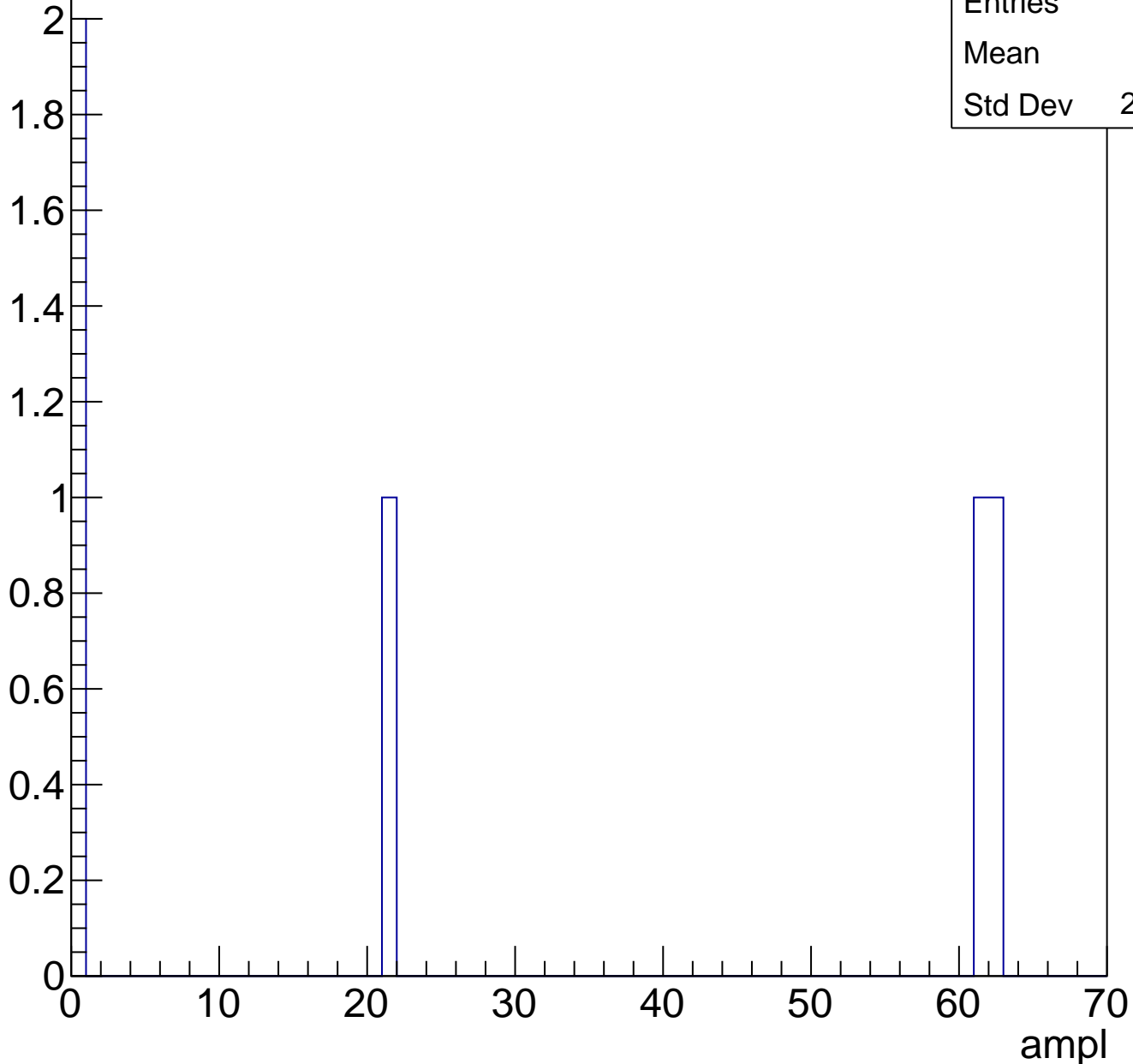
Entries	24
Mean	58.62
Std Dev	12.32



B1L002S, U7-ch9, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	5
Mean	28.8
Std Dev	27.78

B1L002S, U7-ch10, adc0

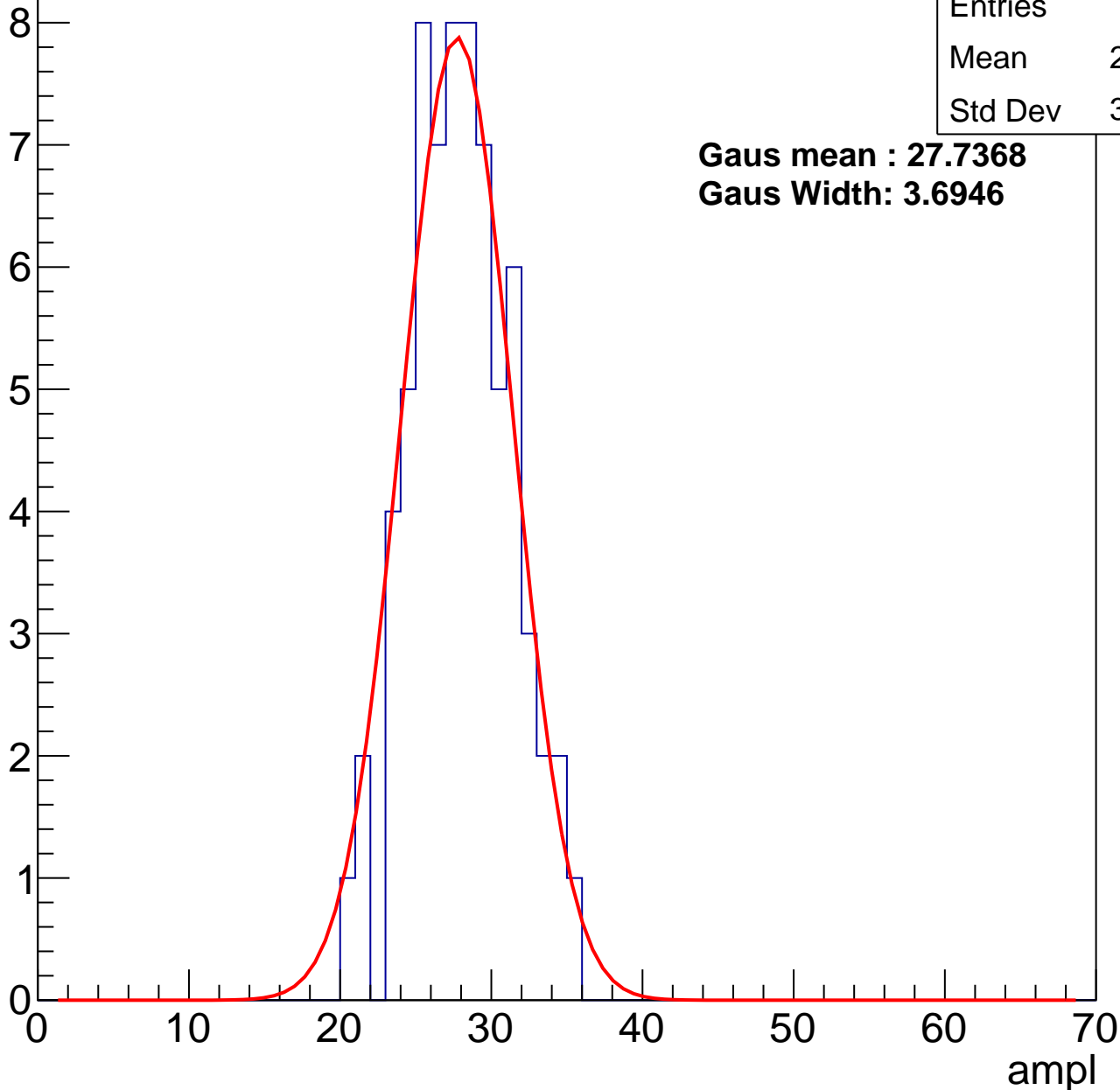
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.54
Std Dev	3.264

Gaus mean : 27.7368

Gaus Width: 3.6946



B1L002S, U7-ch10, adc1

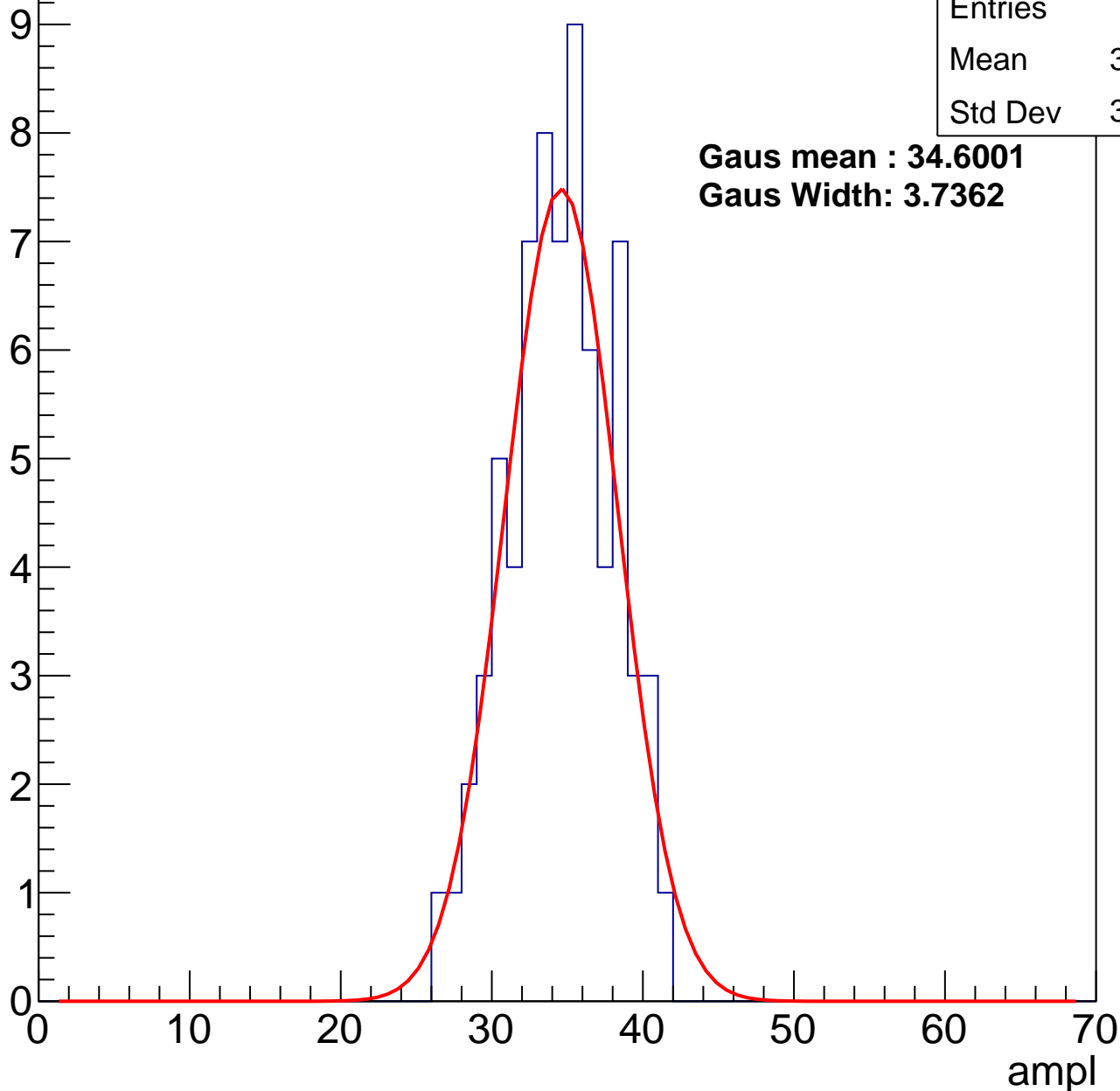
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.07
Std Dev	3.408

Gaus mean : 34.6001

Gaus Width: 3.7362



B1L002S, U7-ch10, adc2

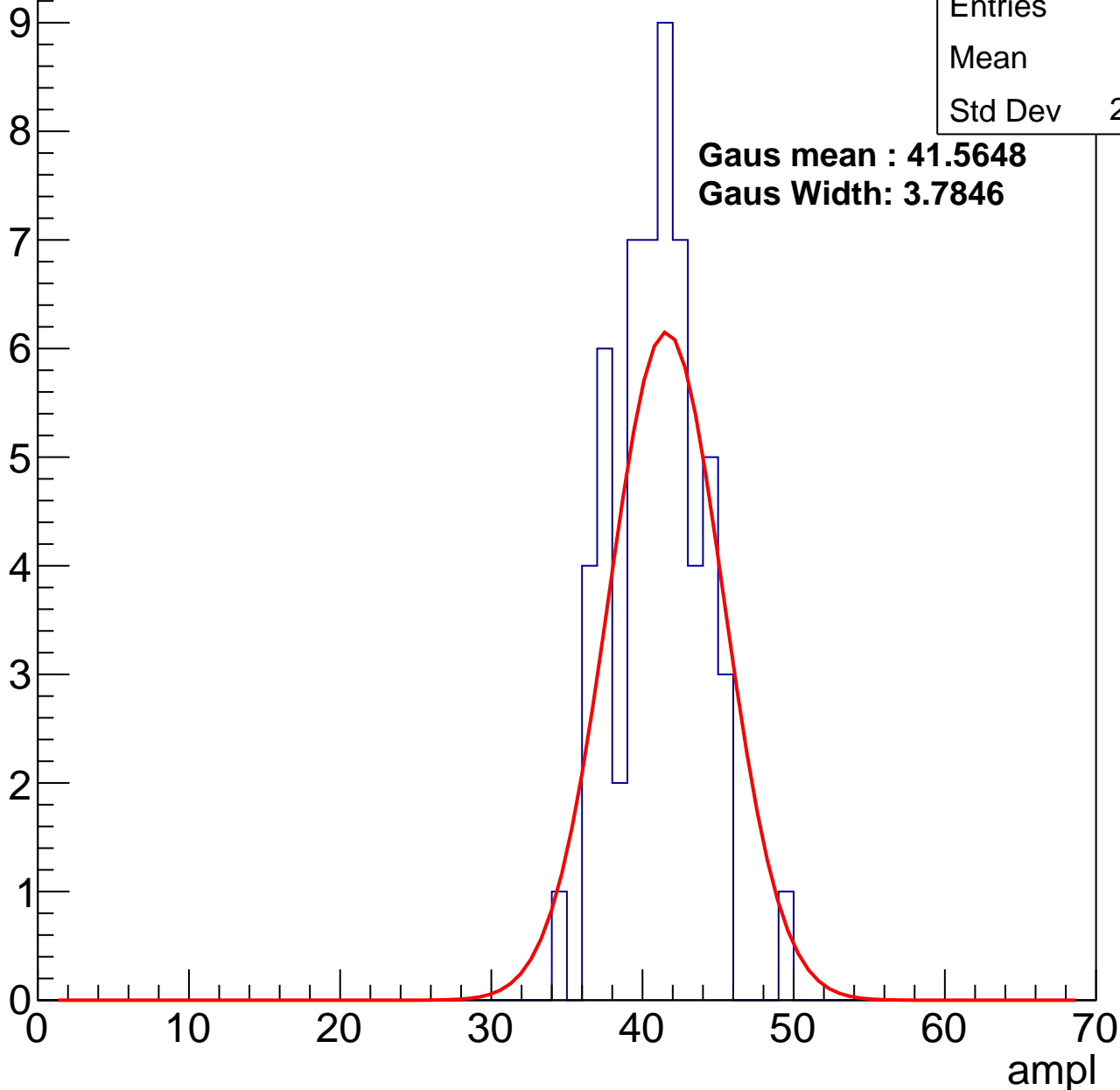
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.5
Std Dev	2.878

Gaus mean : 41.5648

Gaus Width: 3.7846

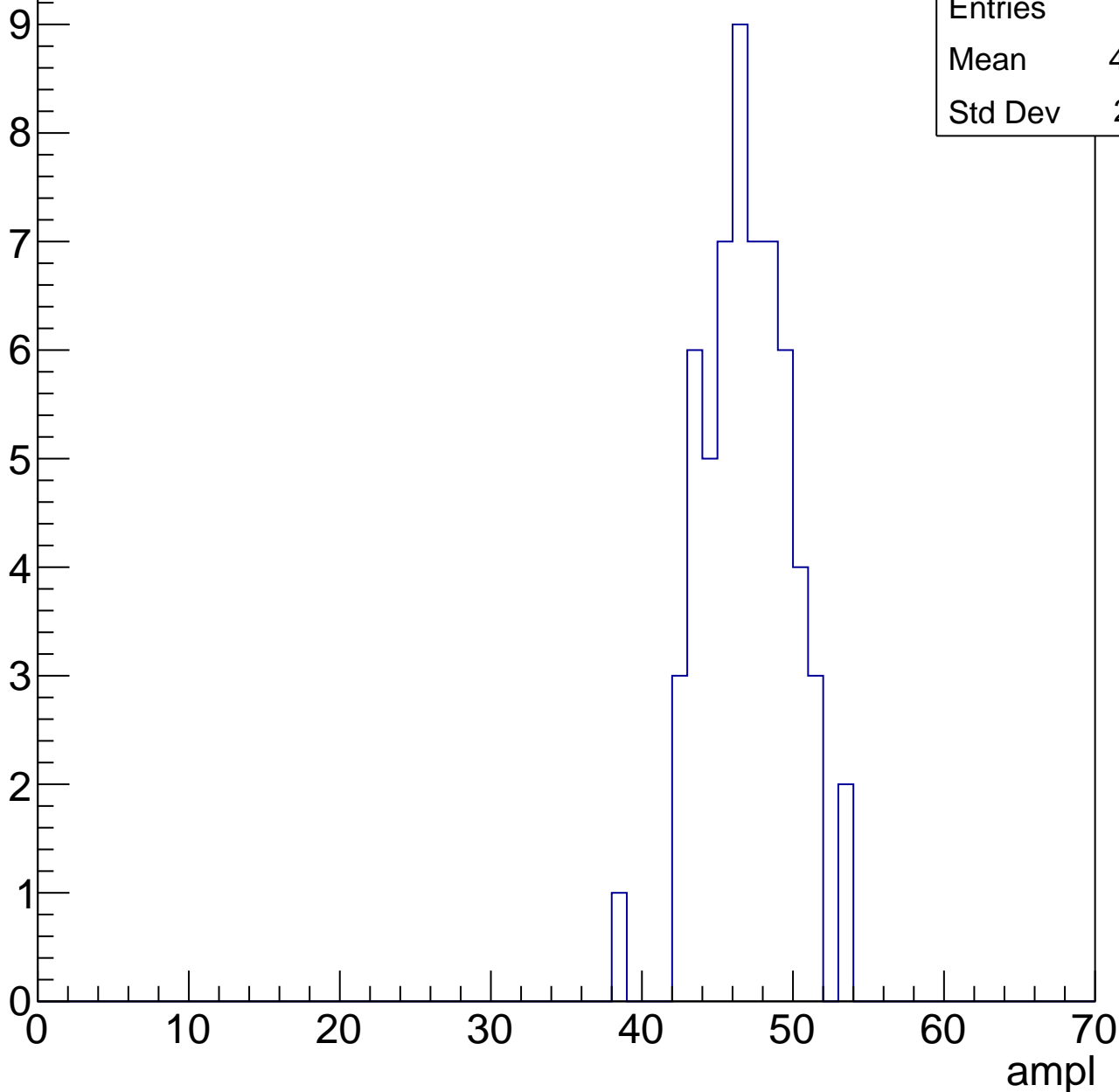


B1L002S, U7-ch10, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.48
Std Dev	2.901

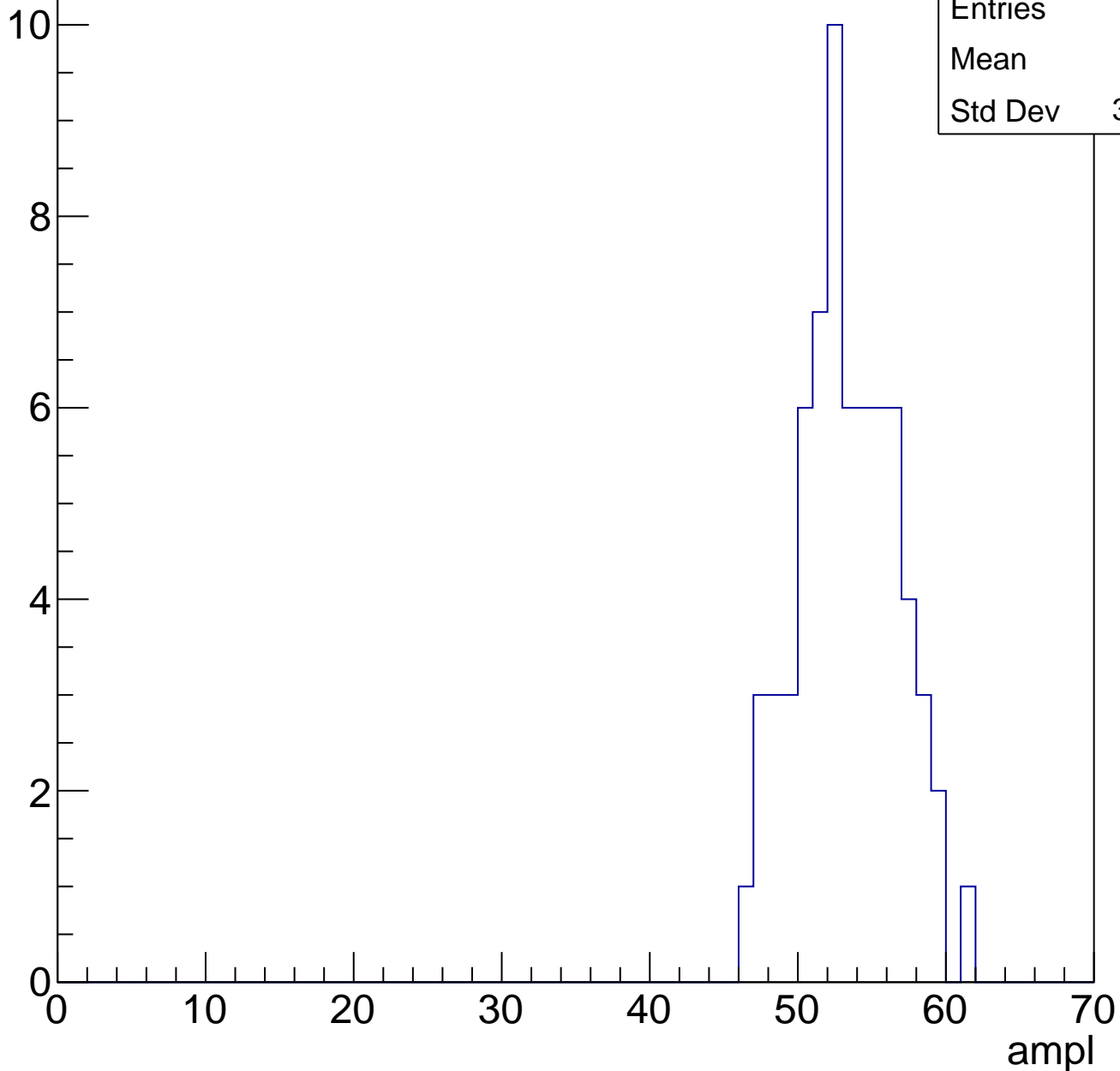


B1L002S, U7-ch10, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	52.9
Std Dev	3.301

Entry

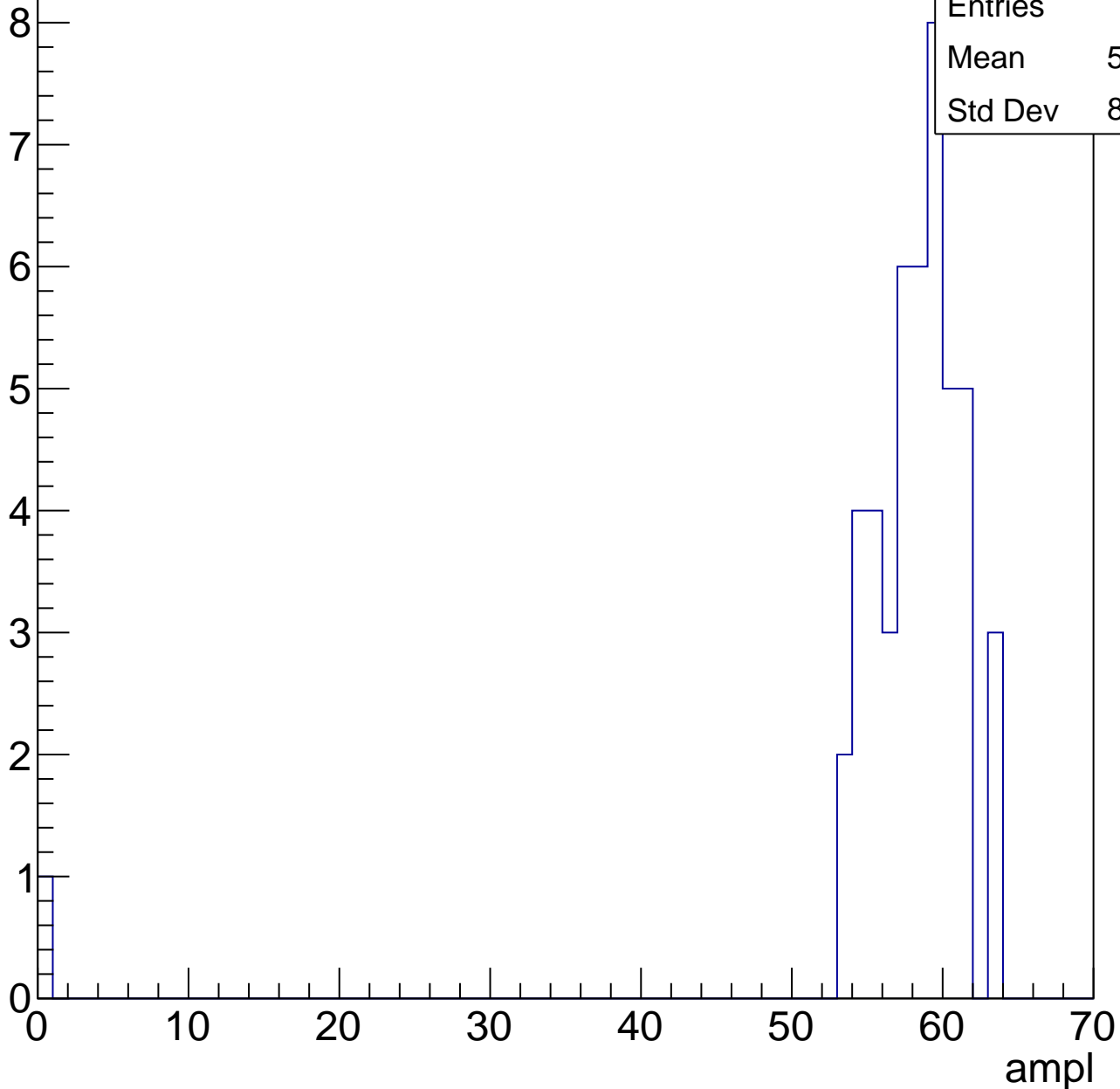


B1L002S, U7-ch10, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	56.72
Std Dev	8.756

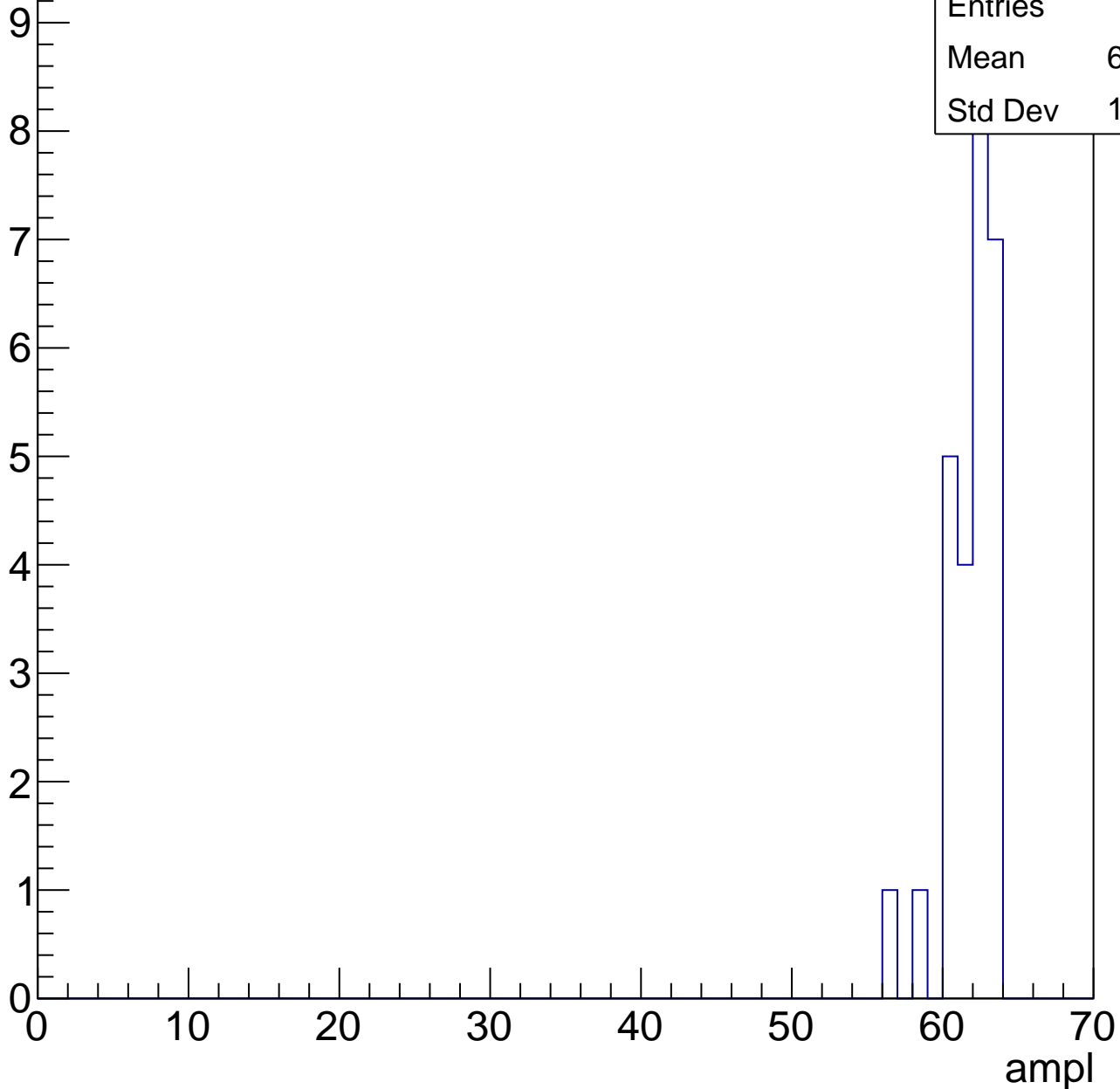


B1L002S, U7-ch10, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

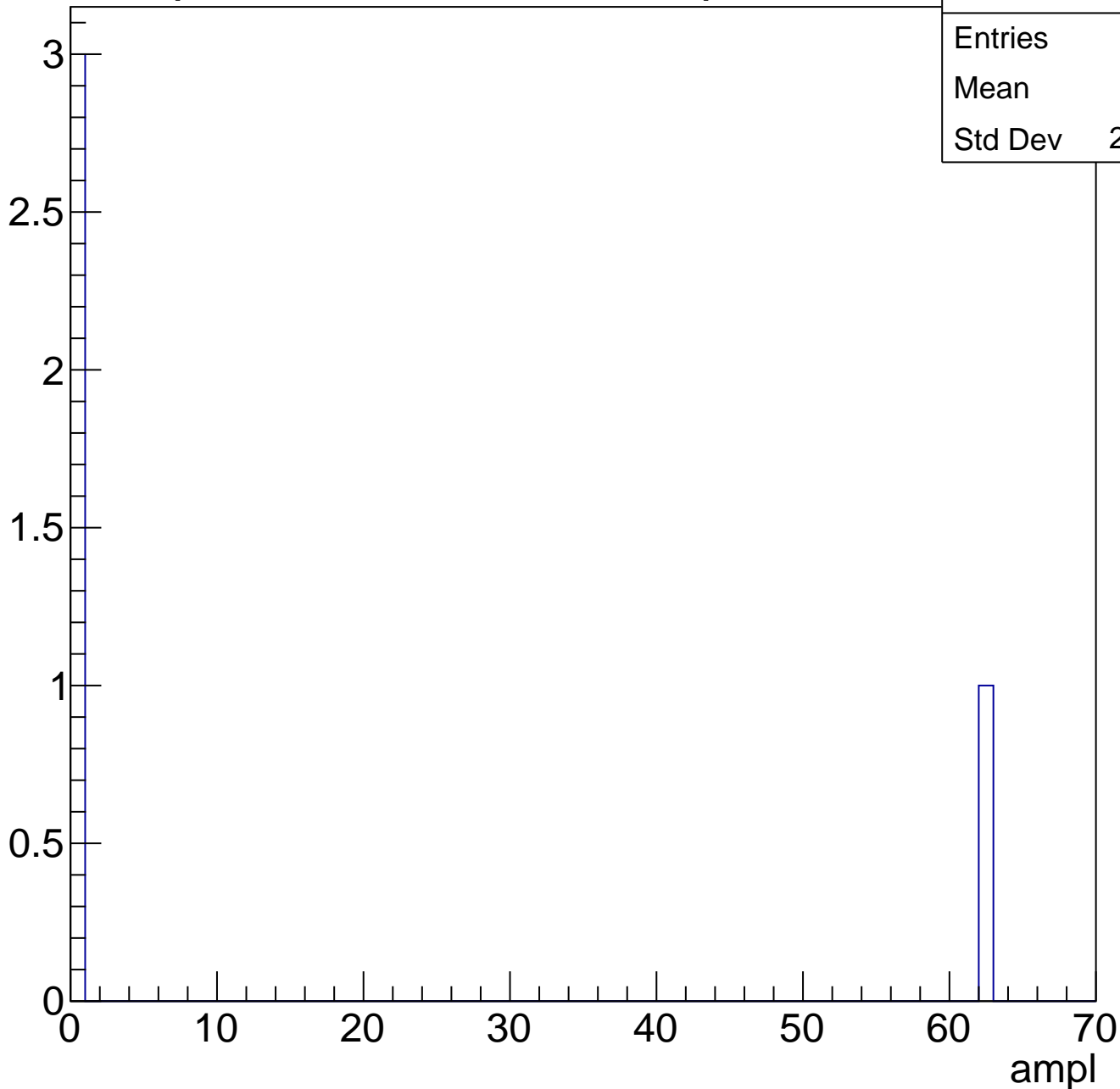
Entries	27
Mean	61.37
Std Dev	1.636



B1L002S, U7-ch10, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch11, adc0

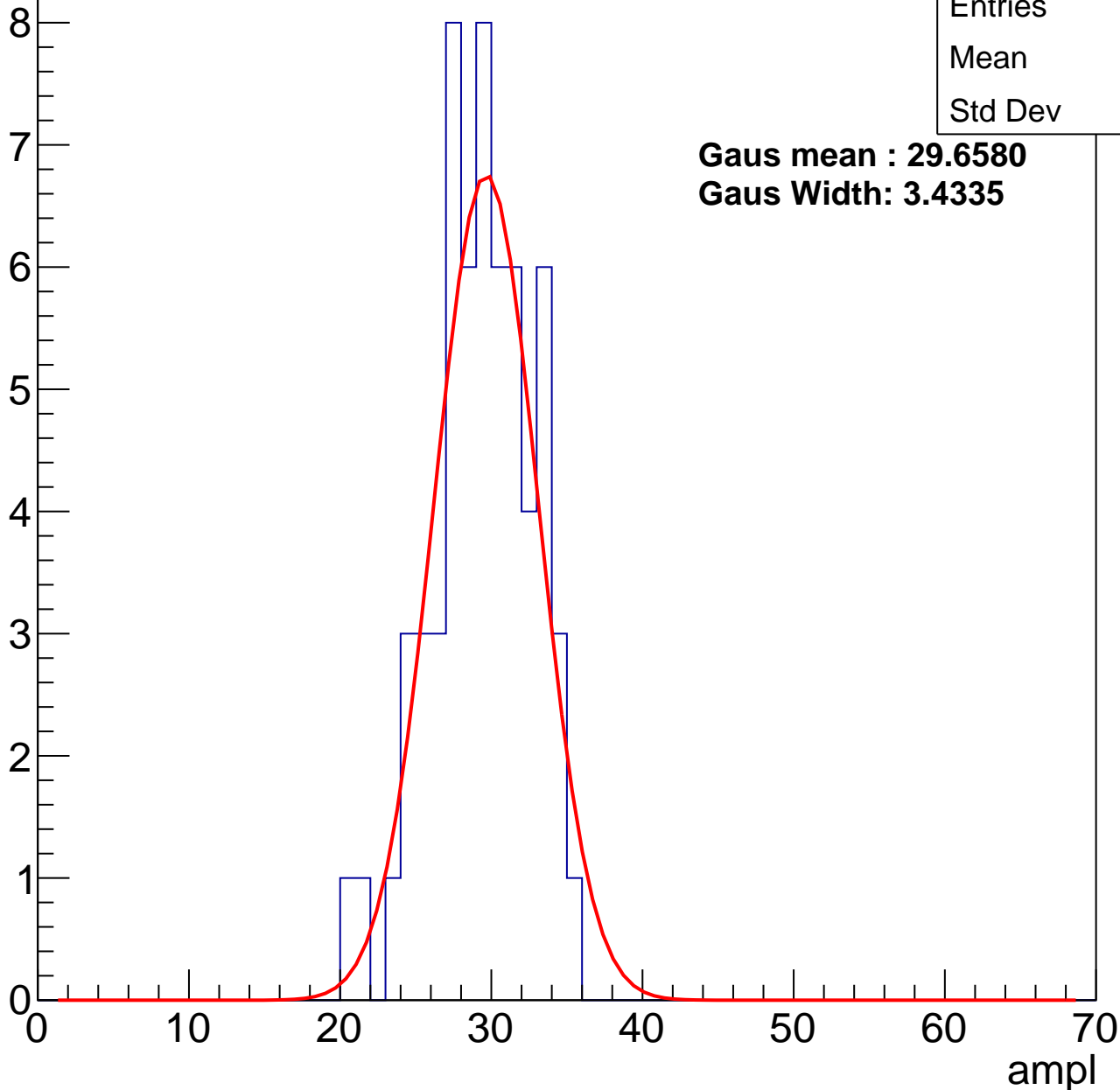
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.9
Std Dev	3.27

Gaus mean : 29.6580

Gaus Width: 3.4335



B1L002S, U7-ch11, adc1

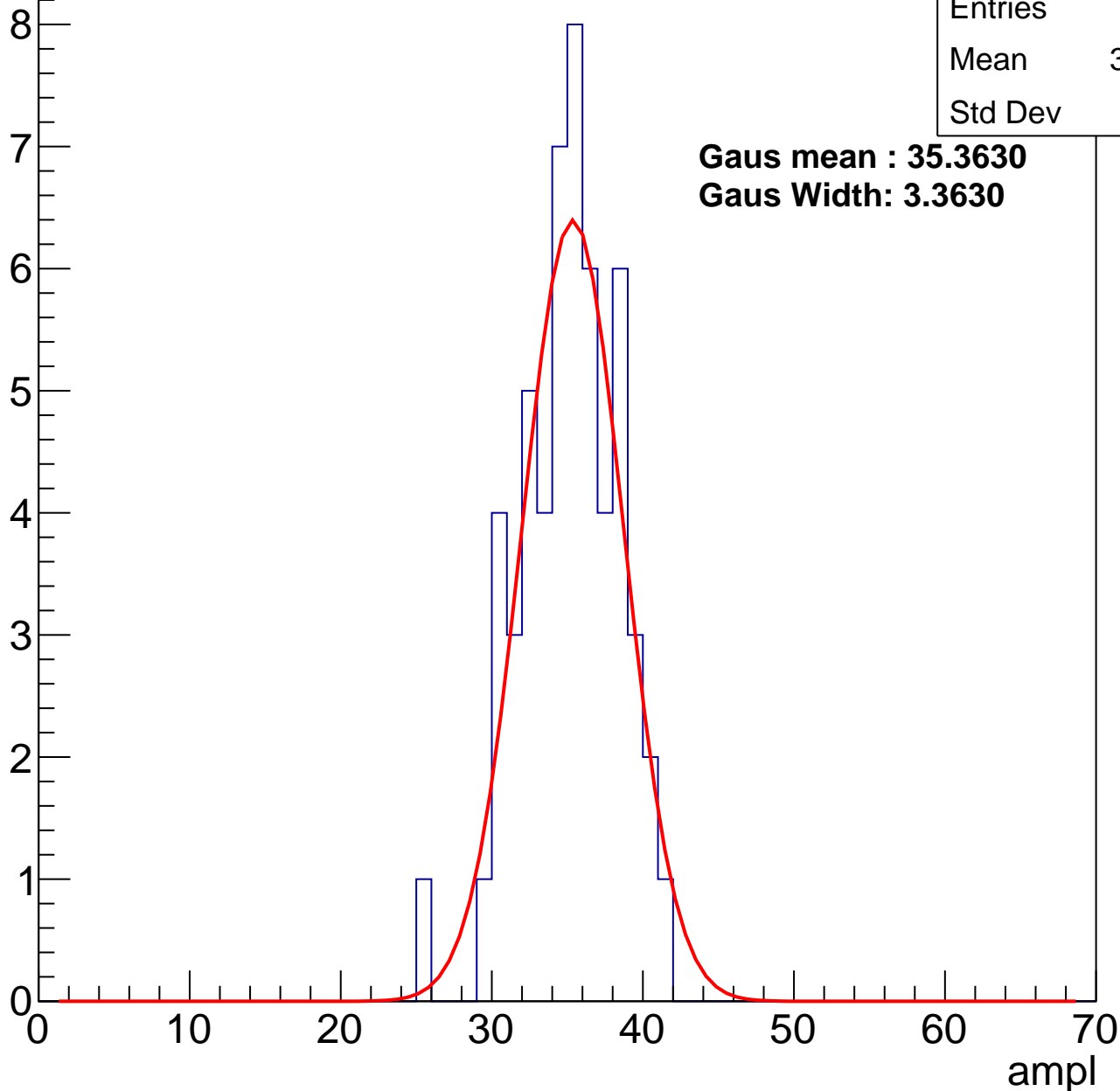
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.67
Std Dev	3.18

Gaus mean : 35.3630

Gaus Width: 3.3630



B1L002S, U7-ch11, adc2

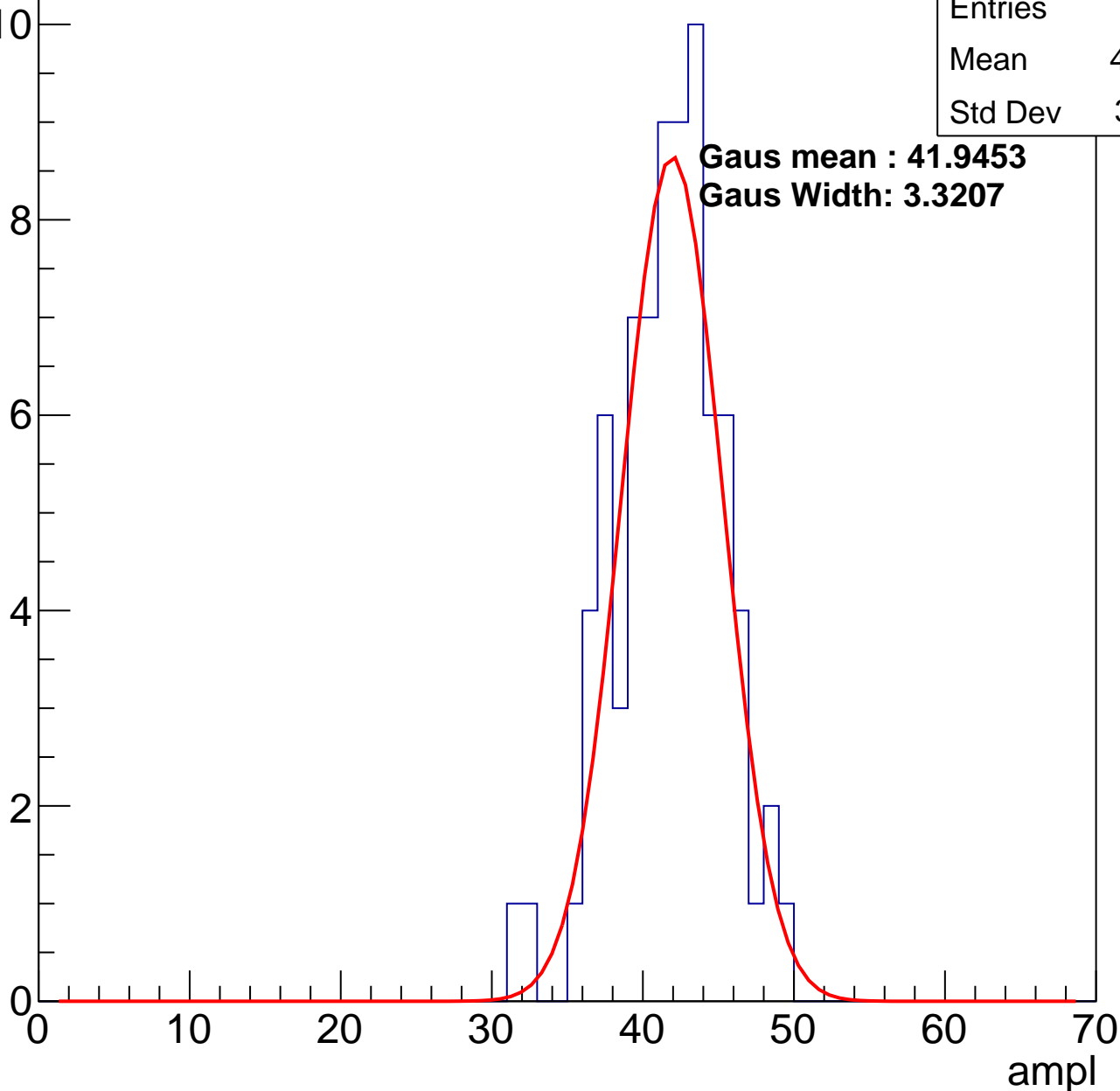
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	78
Mean	41.26
Std Dev	3.521

Gaus mean : 41.9453

Gaus Width: 3.3207

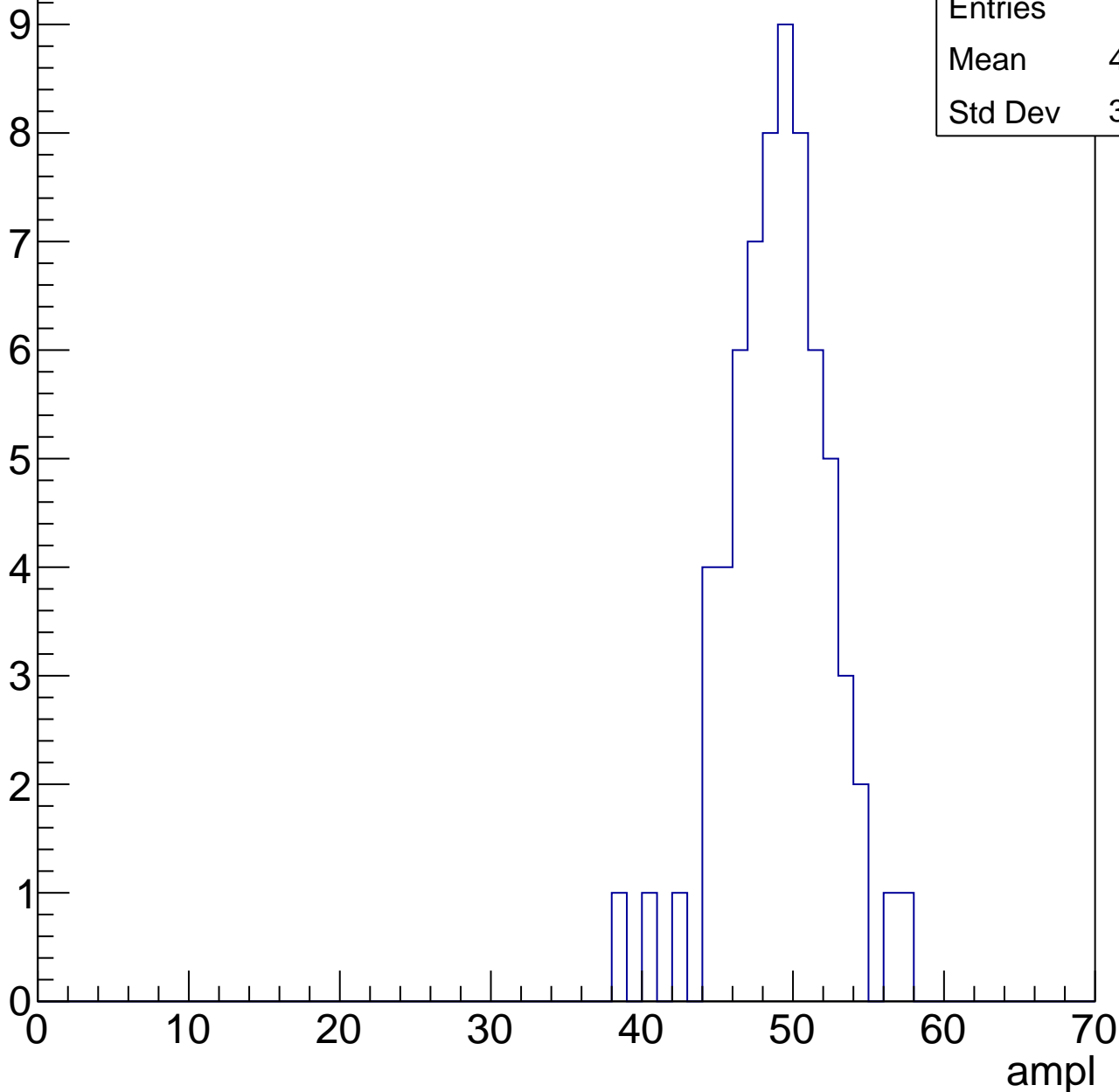


B1L002S, U7-ch11, adc3

calib_packv5_042523_0143.root, FC#10, port B3

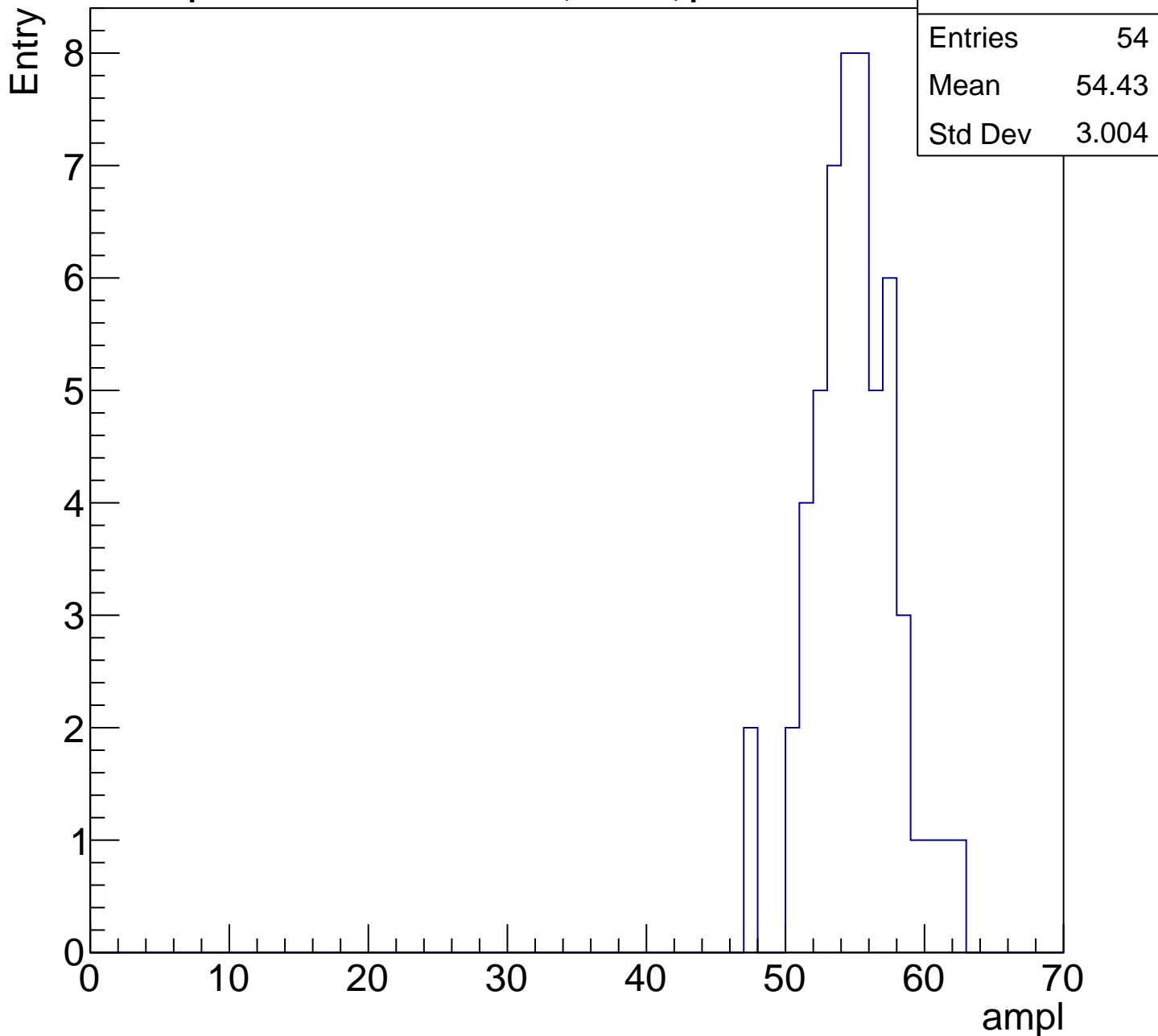
Entry

Entries	67
Mean	48.54
Std Dev	3.409



B1L002S, U7-ch11, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch11, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.7
Std Dev	8.22

ampl

0

10

20

30

40

50

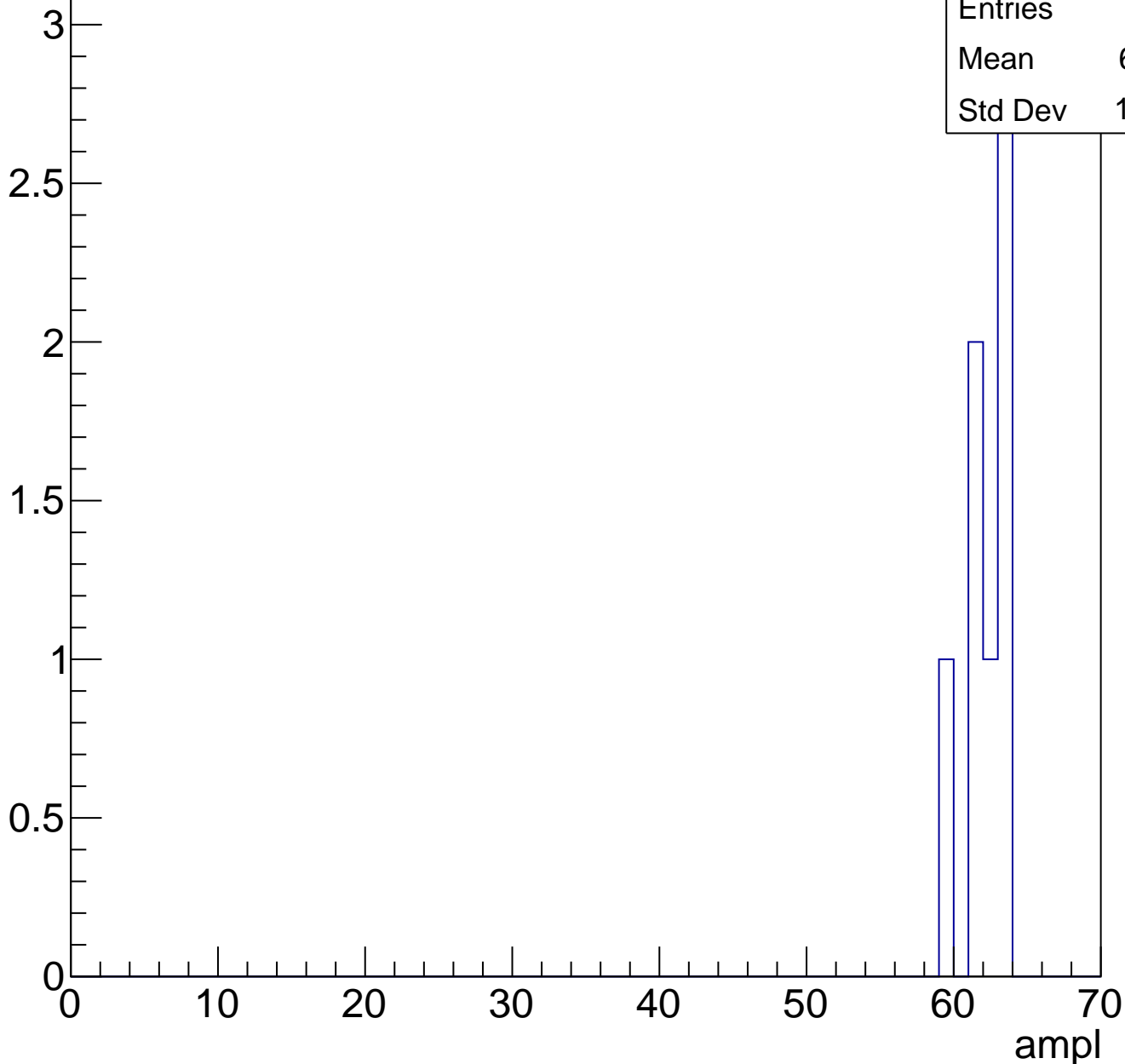
60

70

B1L002S, U7-ch11, adc6

calib_packv5_042523_0143.root, FC#10, port B3

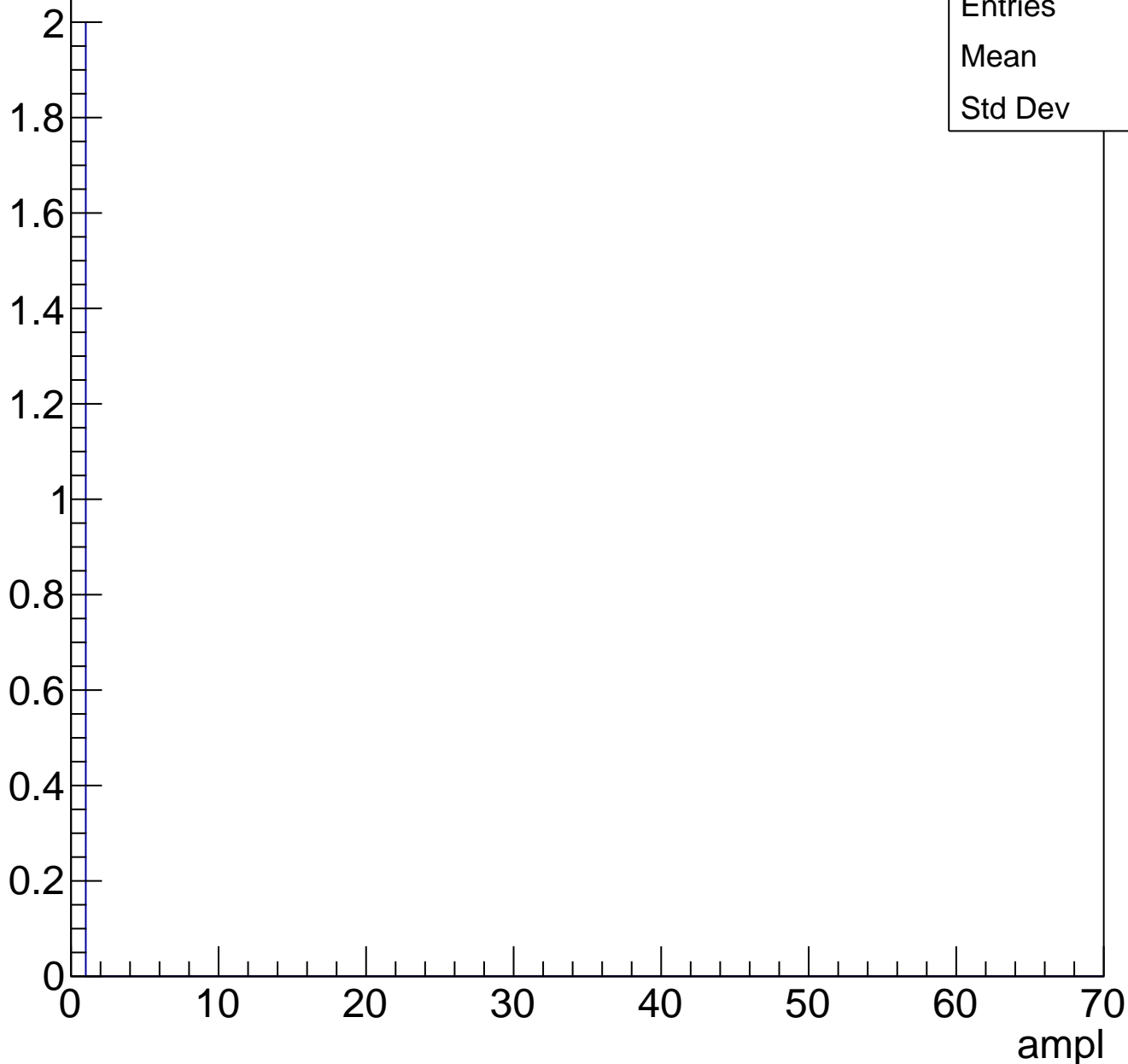
Entry



B1L002S, U7-ch11, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch12, adc0

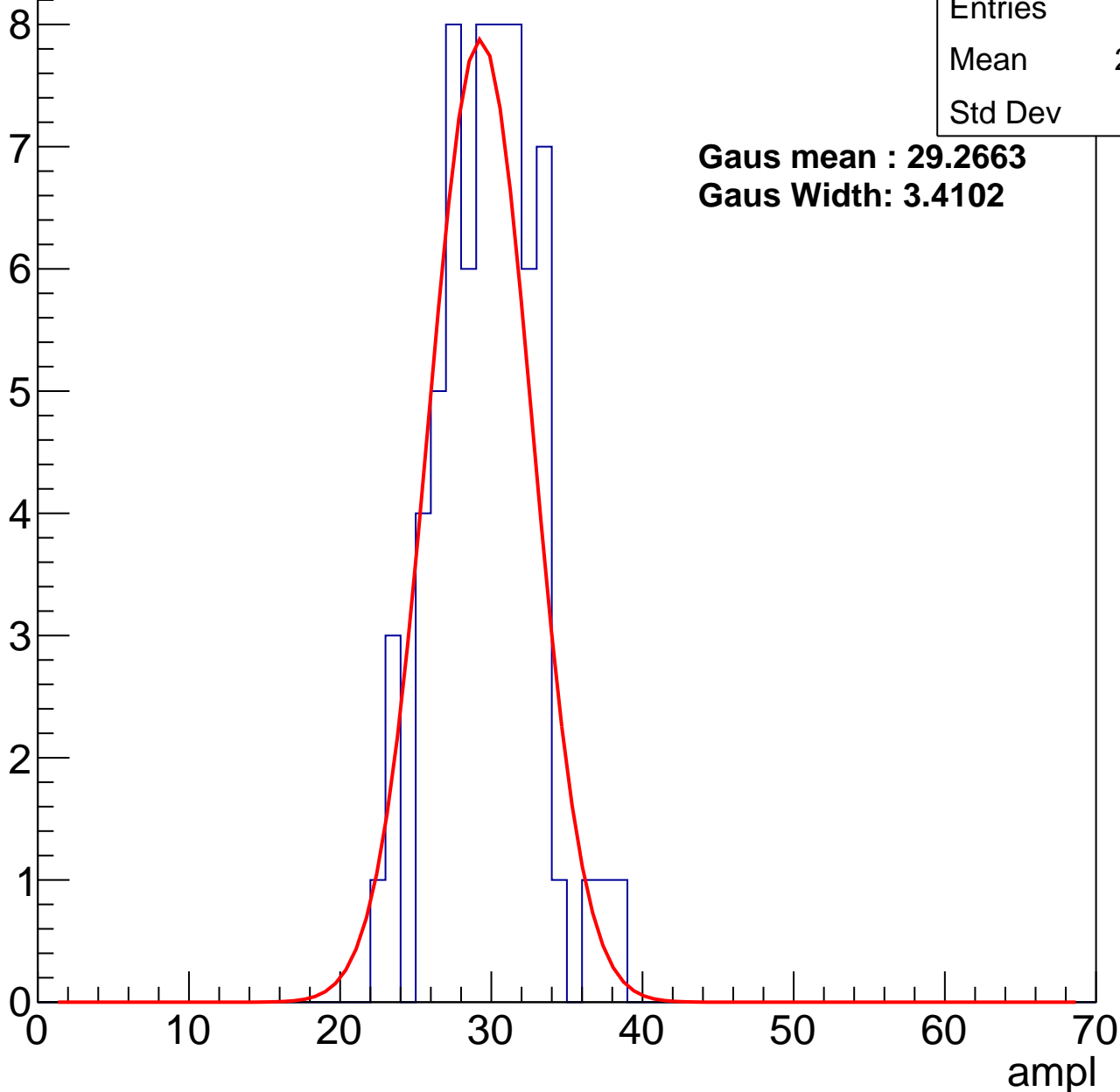
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	29.31
Std Dev	3.26

Gaus mean : 29.2663

Gaus Width: 3.4102



B1L002S, U7-ch12, adc1

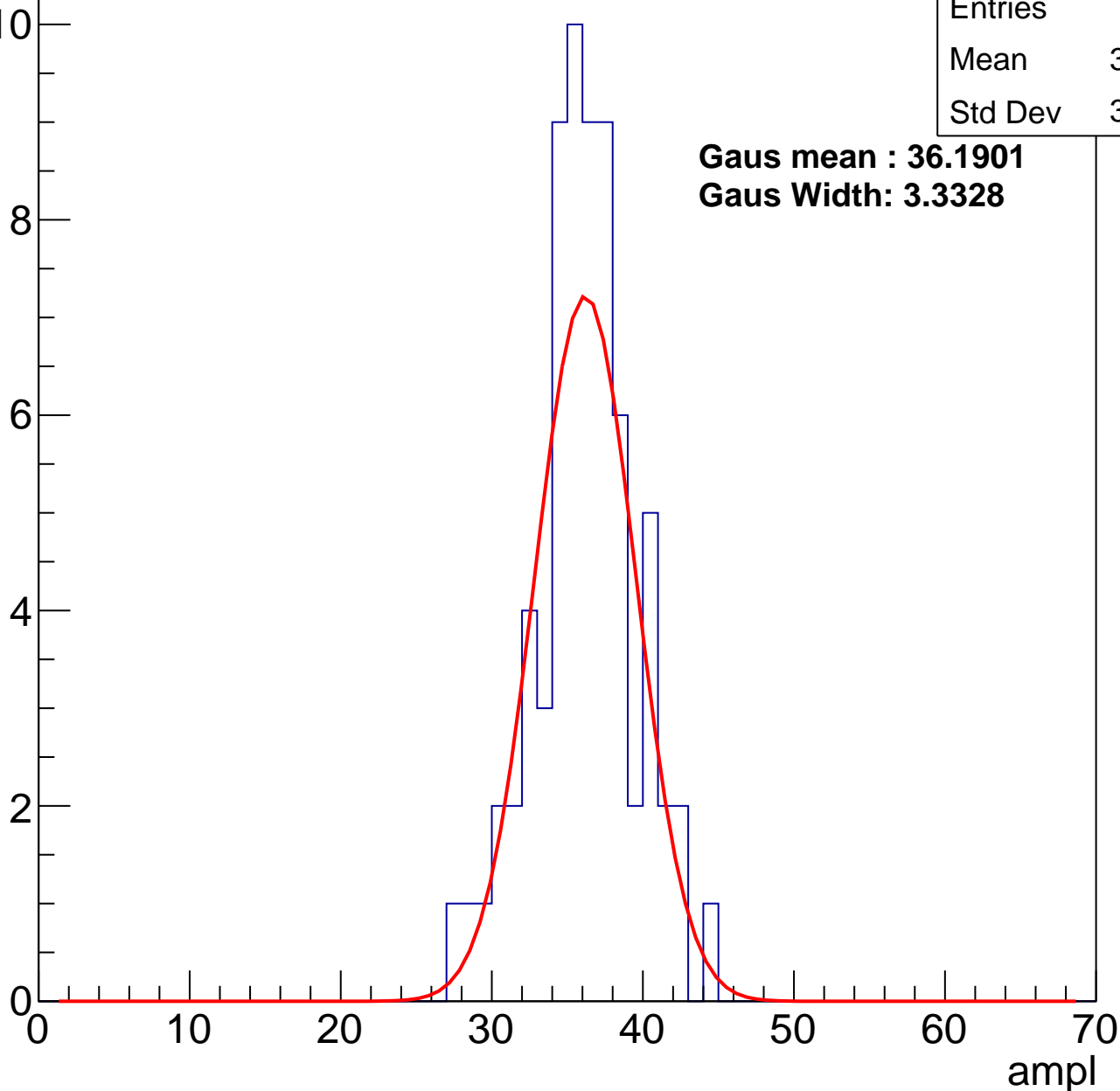
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	35.68
Std Dev	3.325

Gaus mean : 36.1901

Gaus Width: 3.3328



B1L002S, U7-ch12, adc2

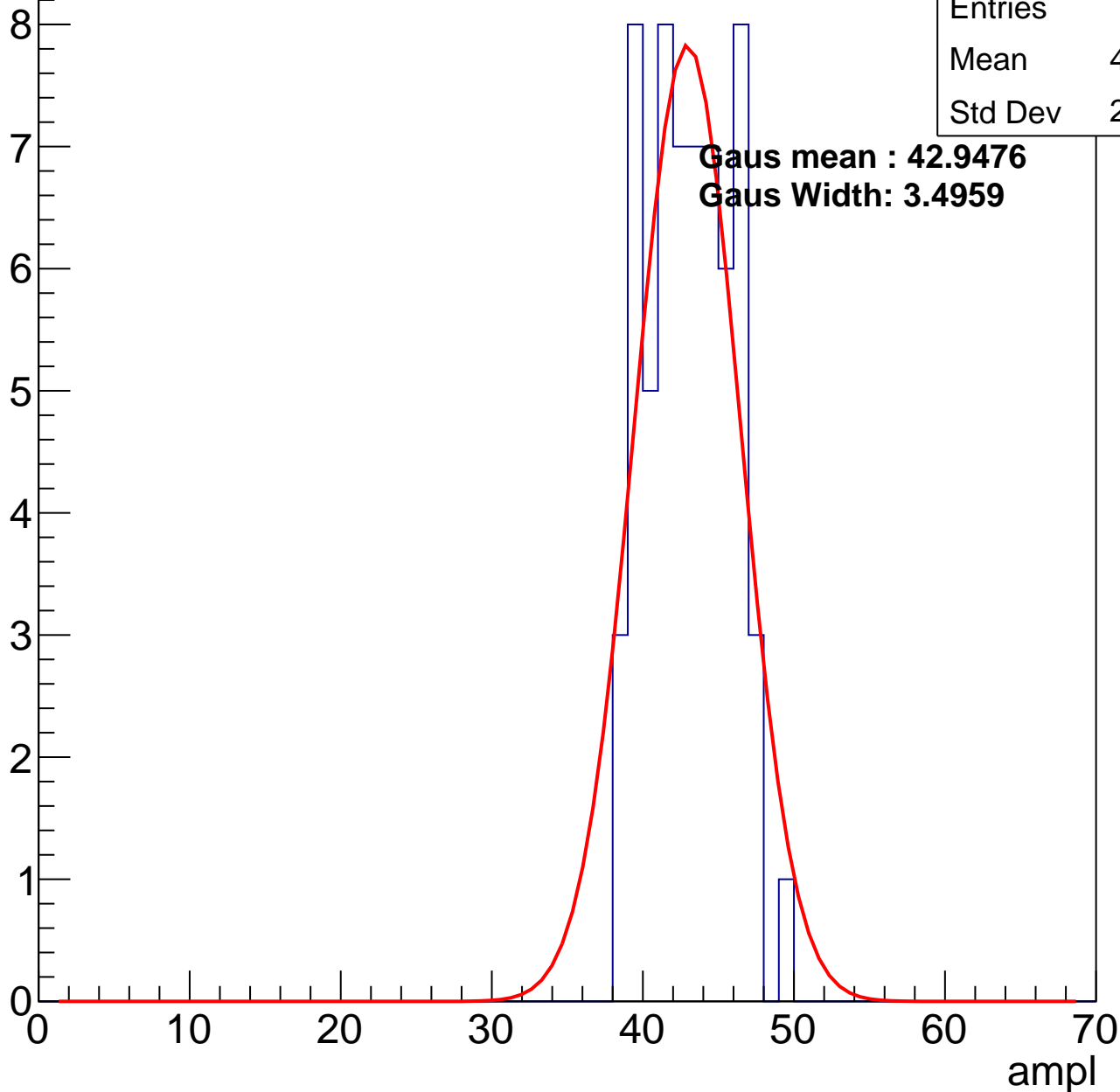
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	42.62
Std Dev	2.716

Gaus mean : 42.9476

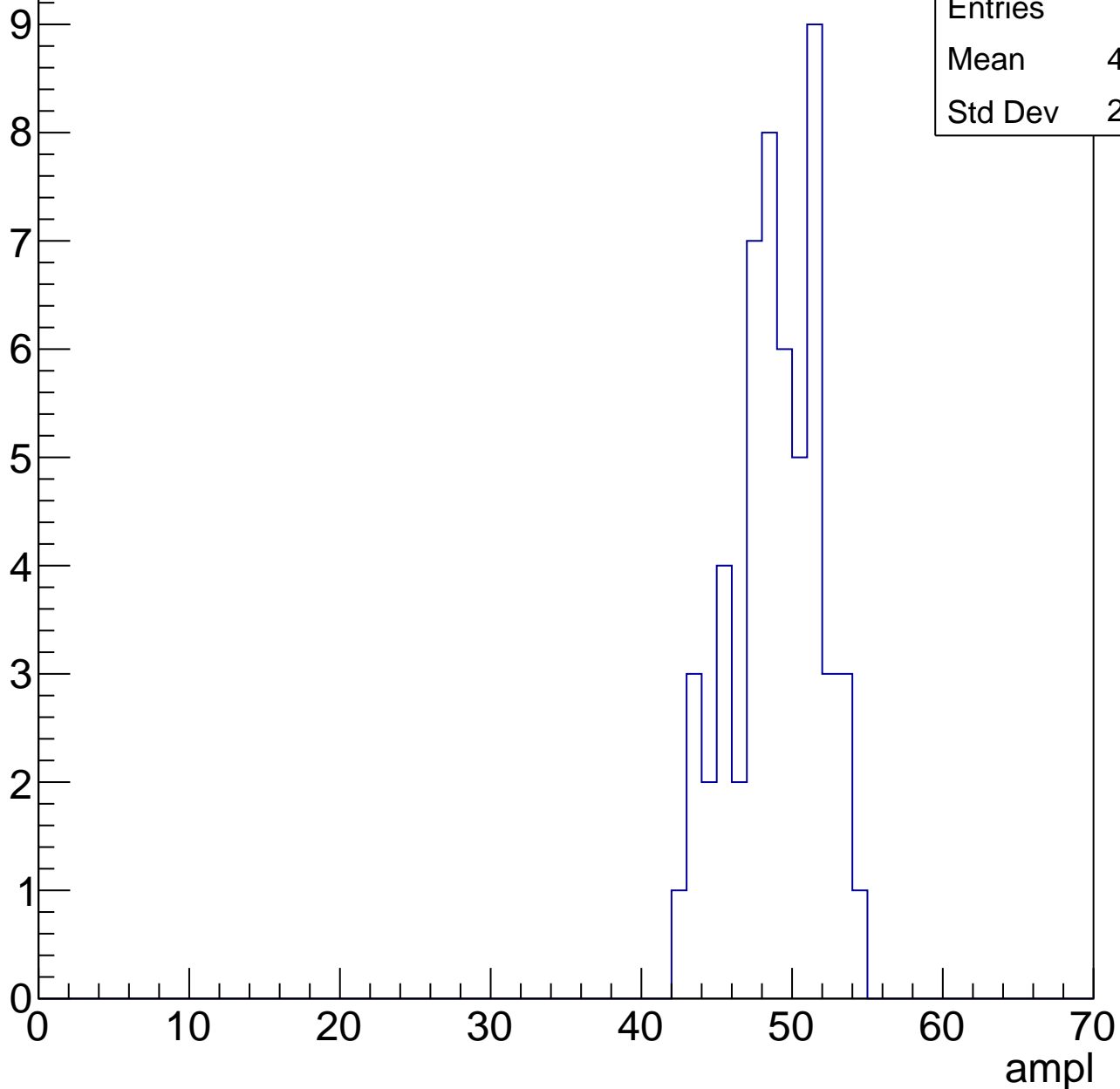
Gaus Width: 3.4959



B1L002S, U7-ch12, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



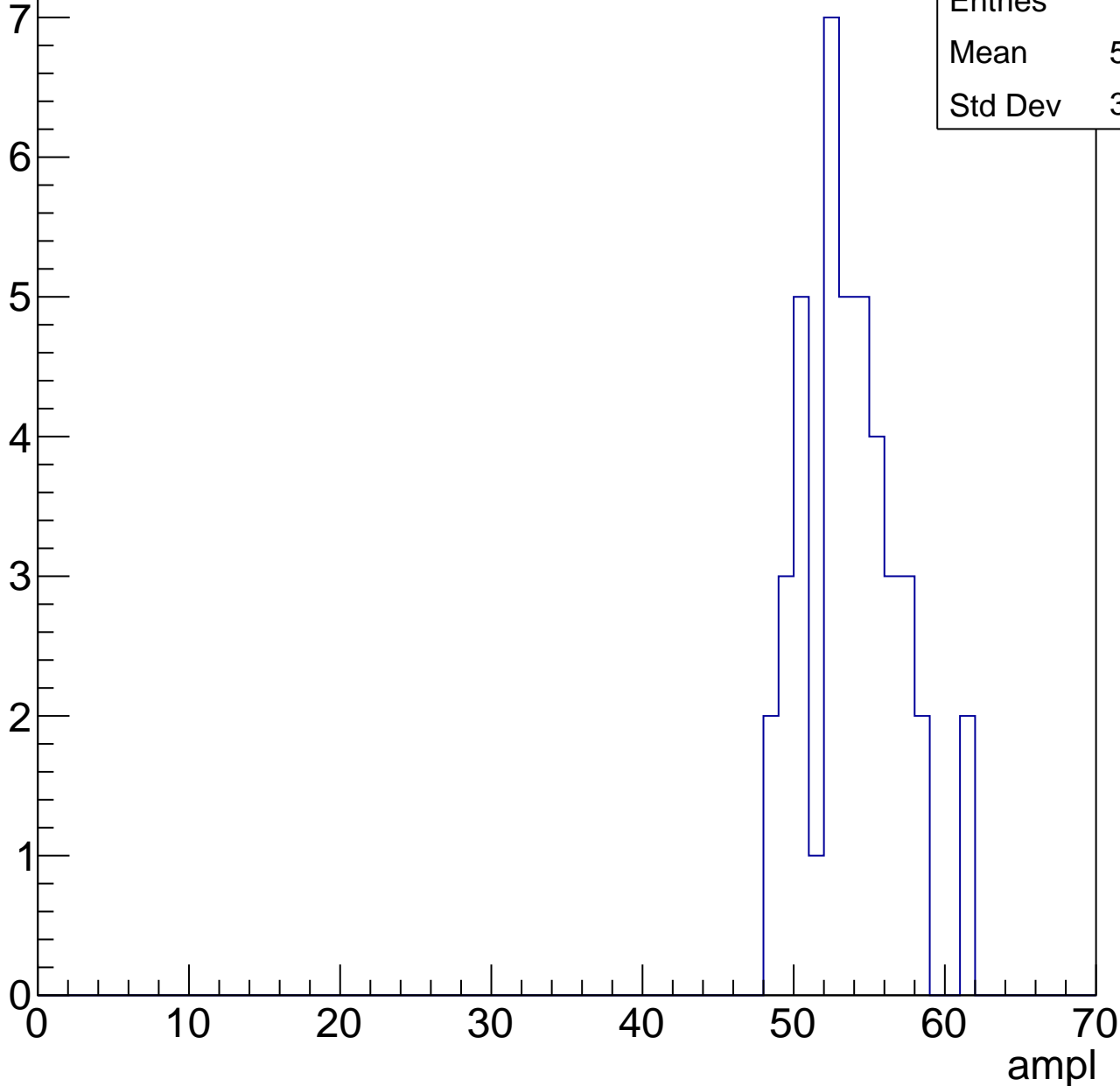
Entries	54
Mean	48.44
Std Dev	2.885

B1L002S, U7-ch12, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	42
Mean	53.33
Std Dev	3.175



B1L002S, U7-ch12, adc5

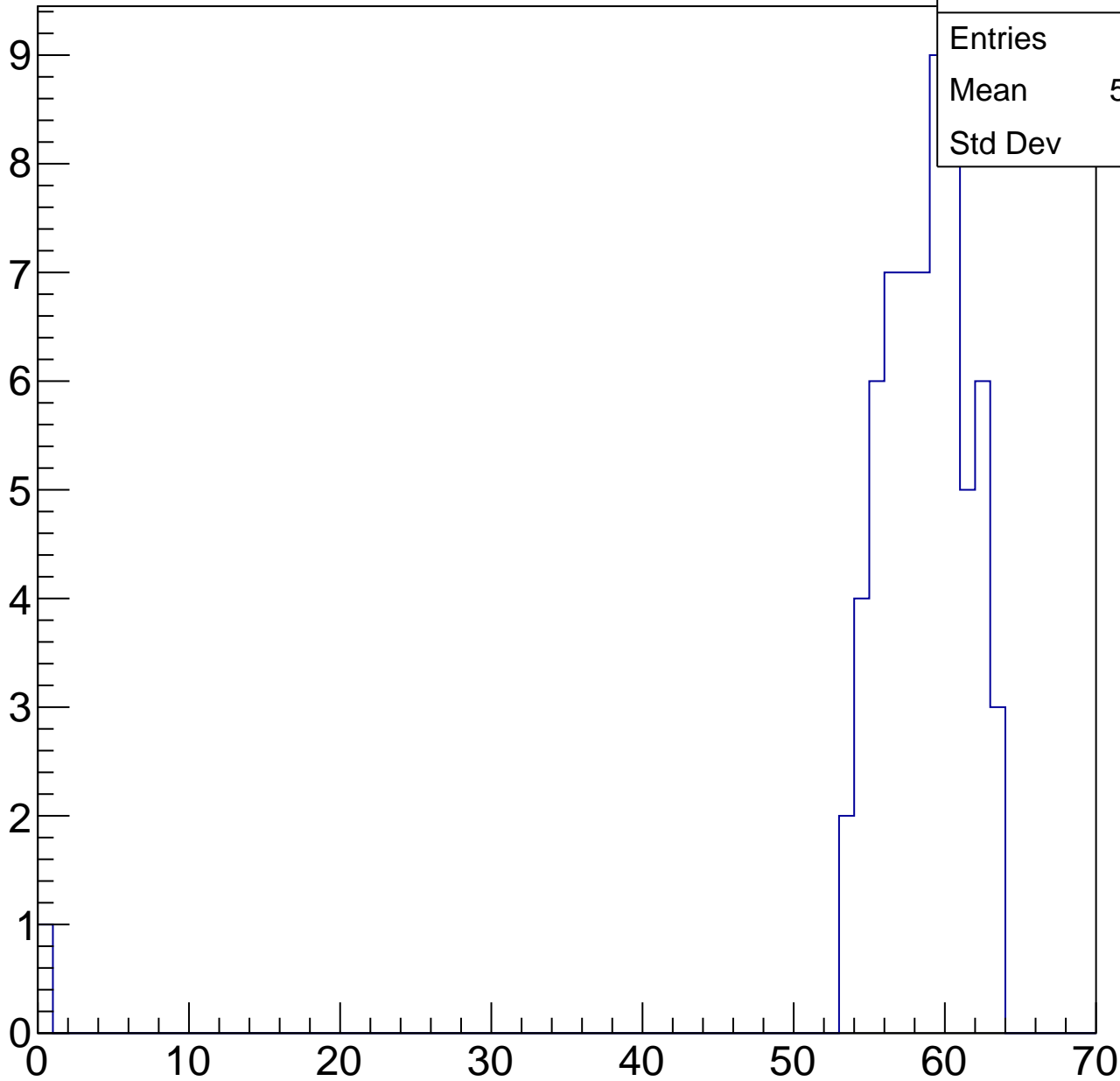
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	57.32
Std Dev	7.64

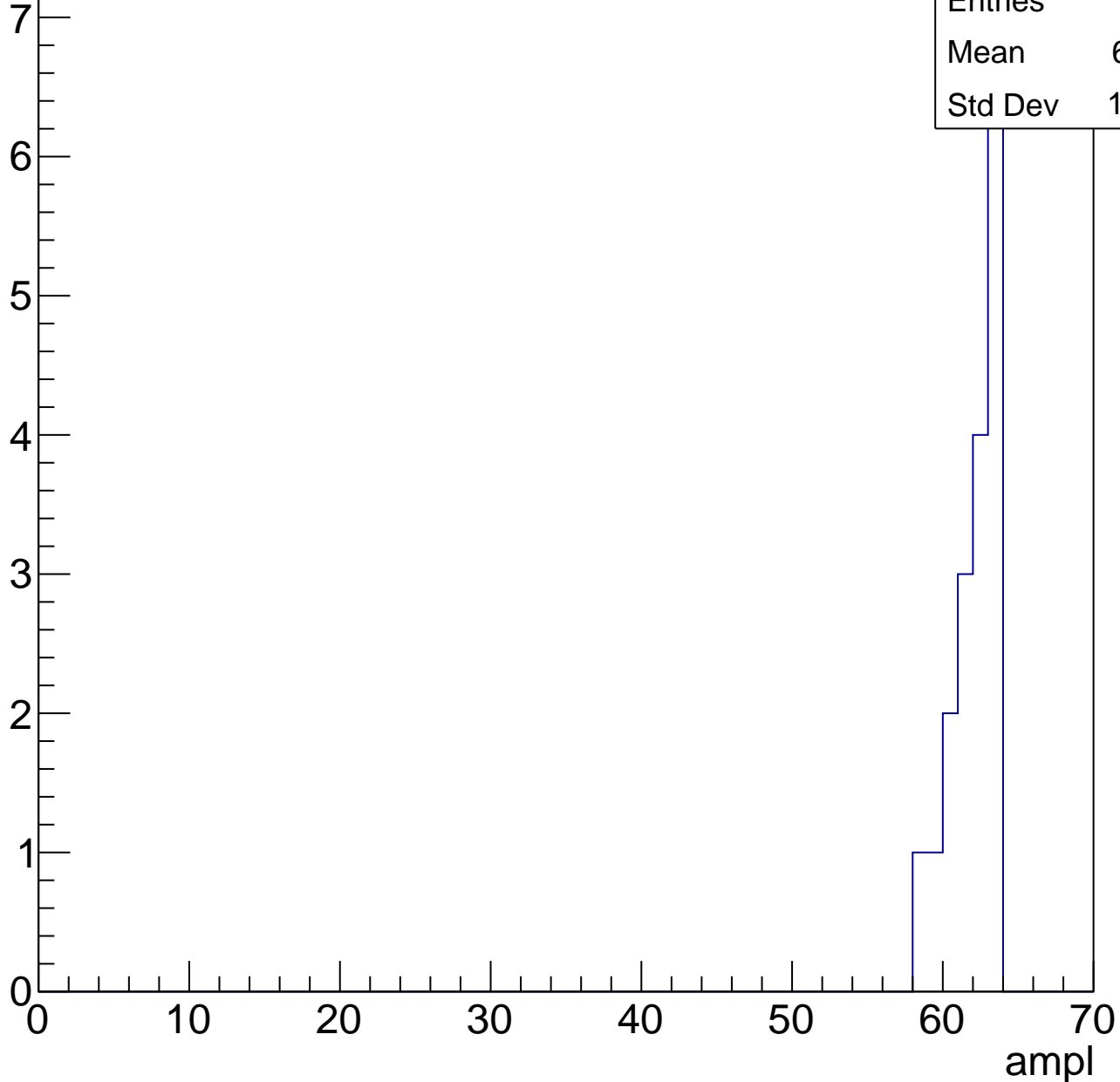
ampl



B1L002S, U7-ch12, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

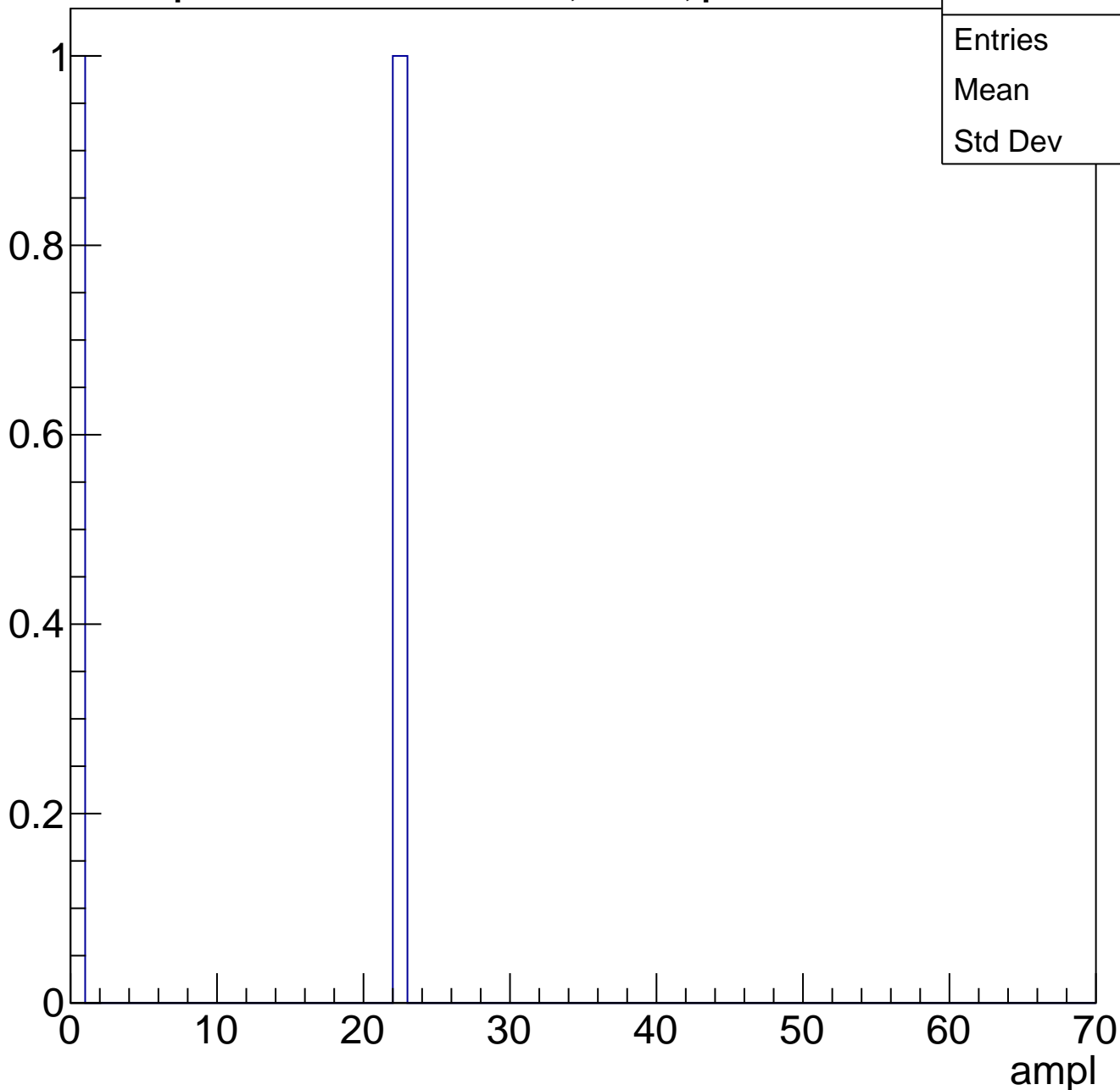


Entries	18
Mean	61.61
Std Dev	1.496

B1L002S, U7-ch12, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

B1L002S, U7-ch13, adc0

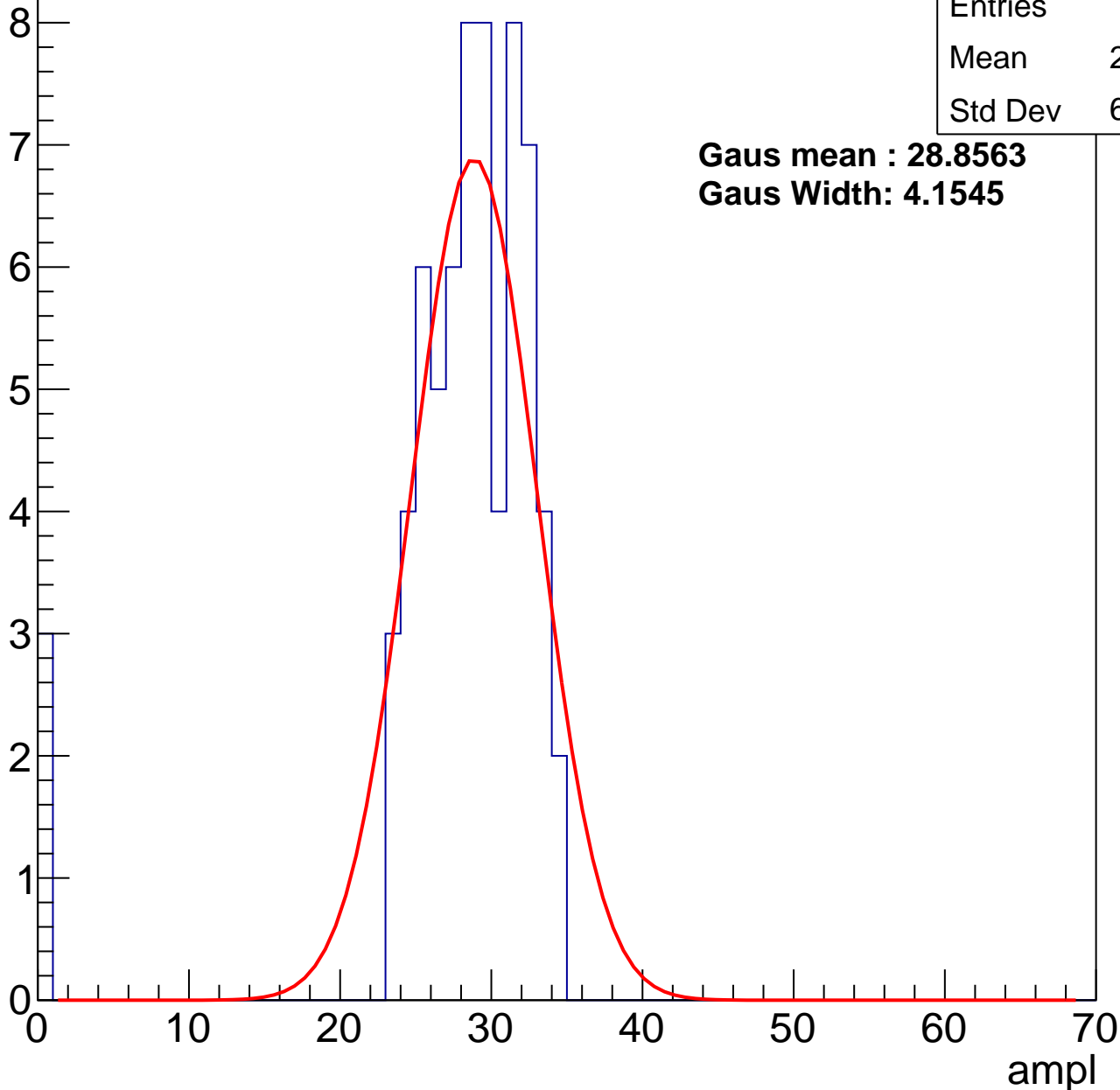
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.28
Std Dev	6.548

Gaus mean : 28.8563

Gaus Width: 4.1545



B1L002S, U7-ch13, adc1

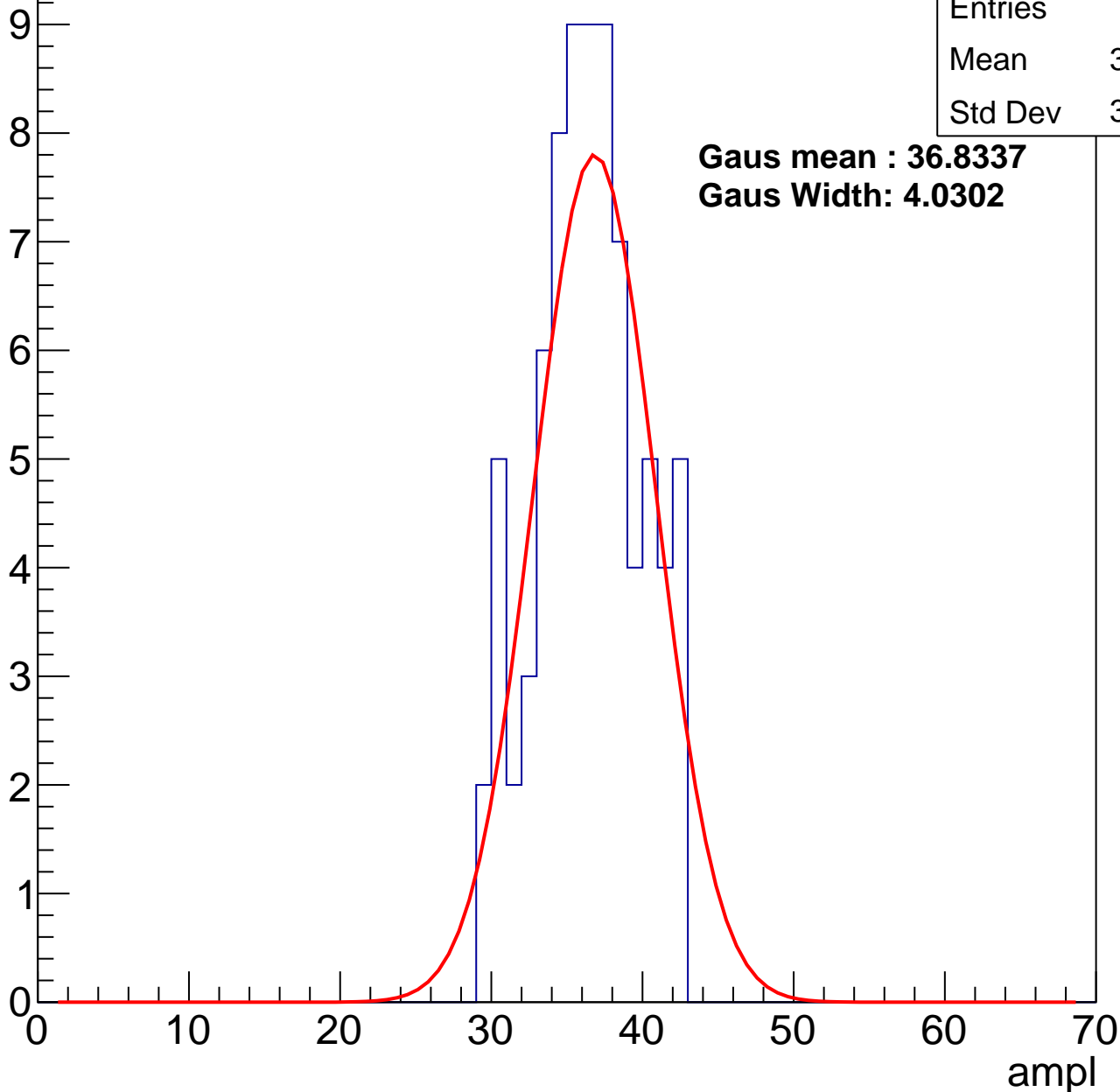
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	78
Mean	35.95
Std Dev	3.404

Gaus mean : 36.8337

Gaus Width: 4.0302



B1L002S, U7-ch13, adc2

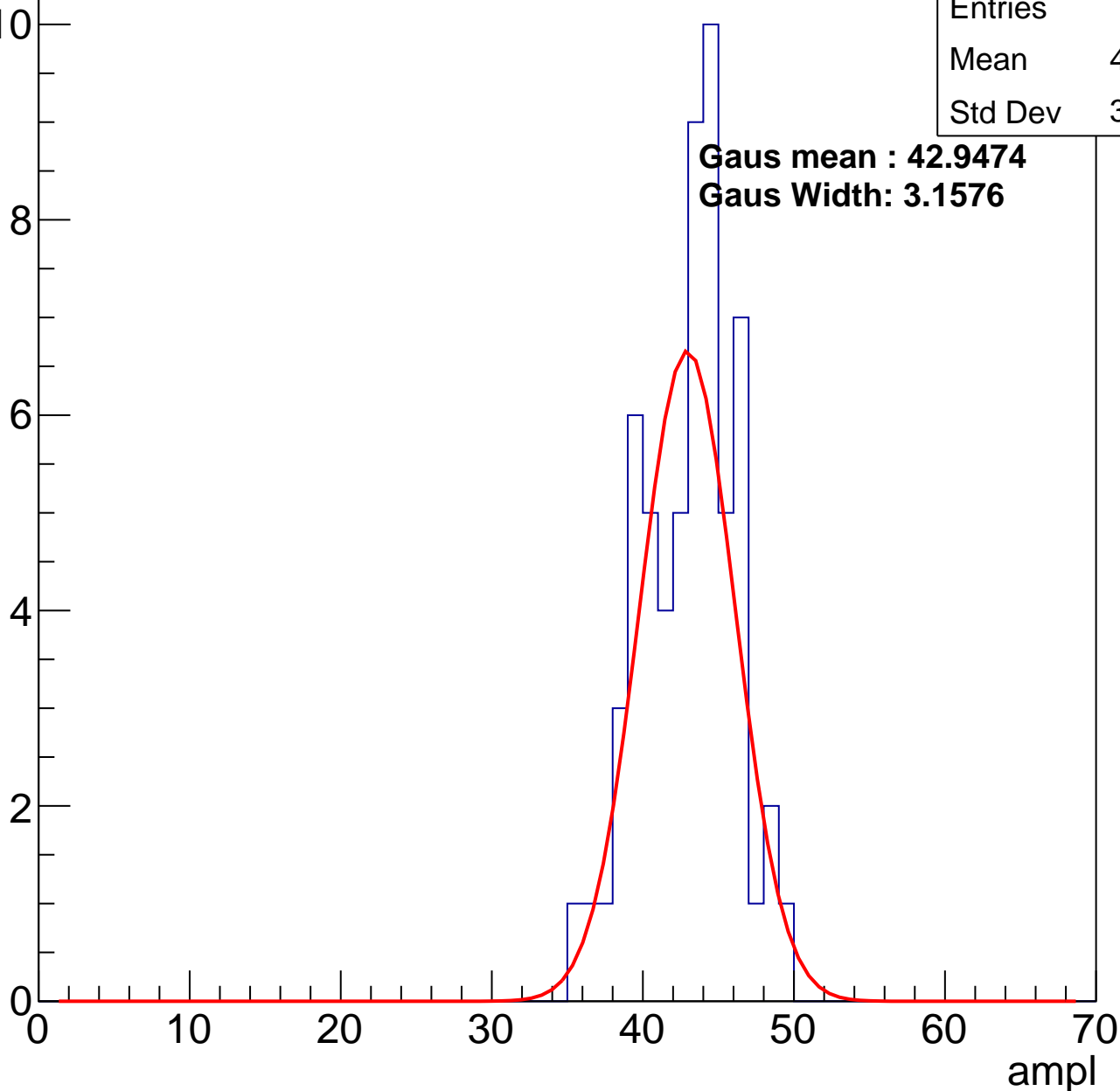
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	42.56
Std Dev	3.054

Gaus mean : 42.9474

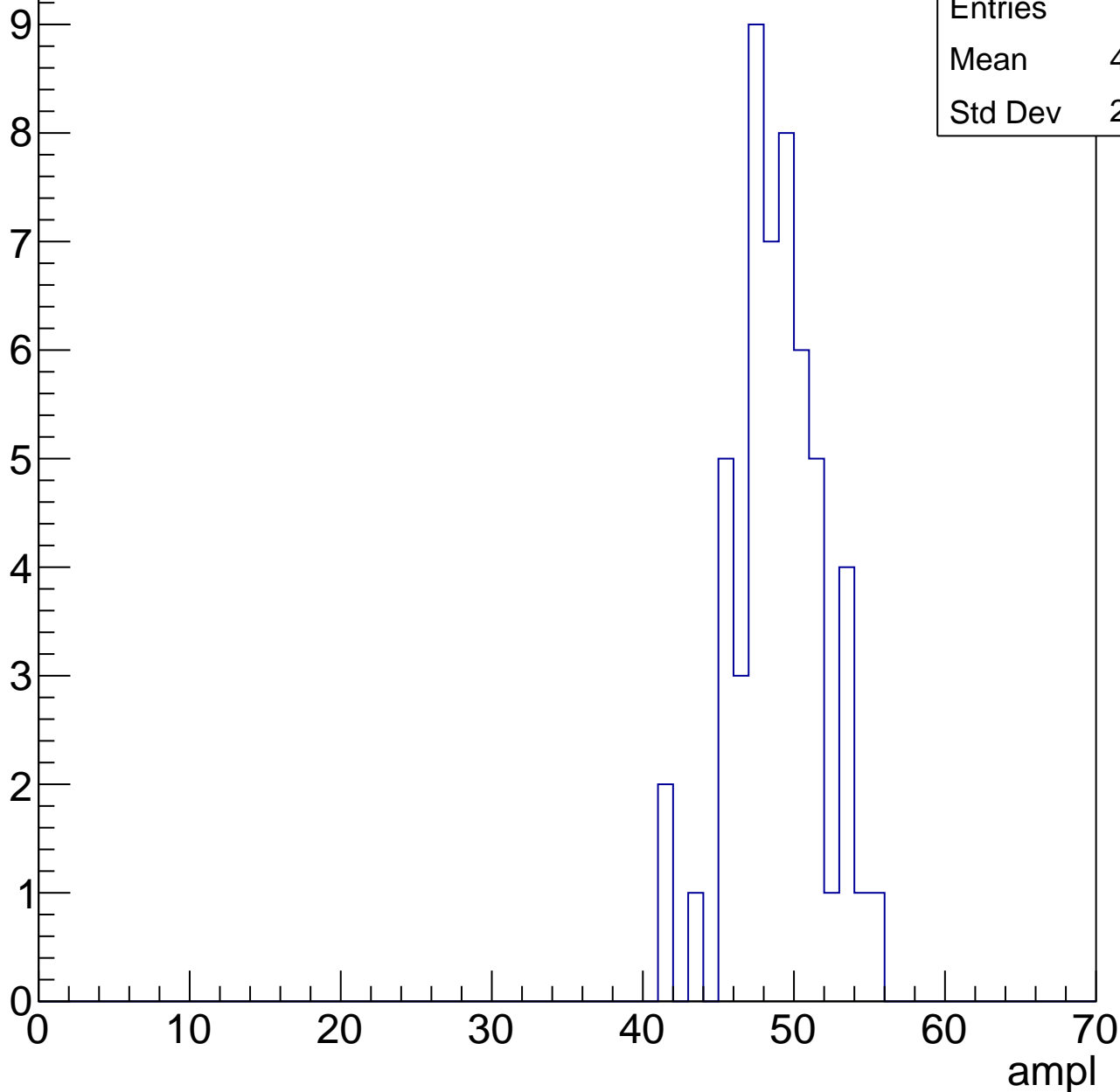
Gaus Width: 3.1576



B1L002S, U7-ch13, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

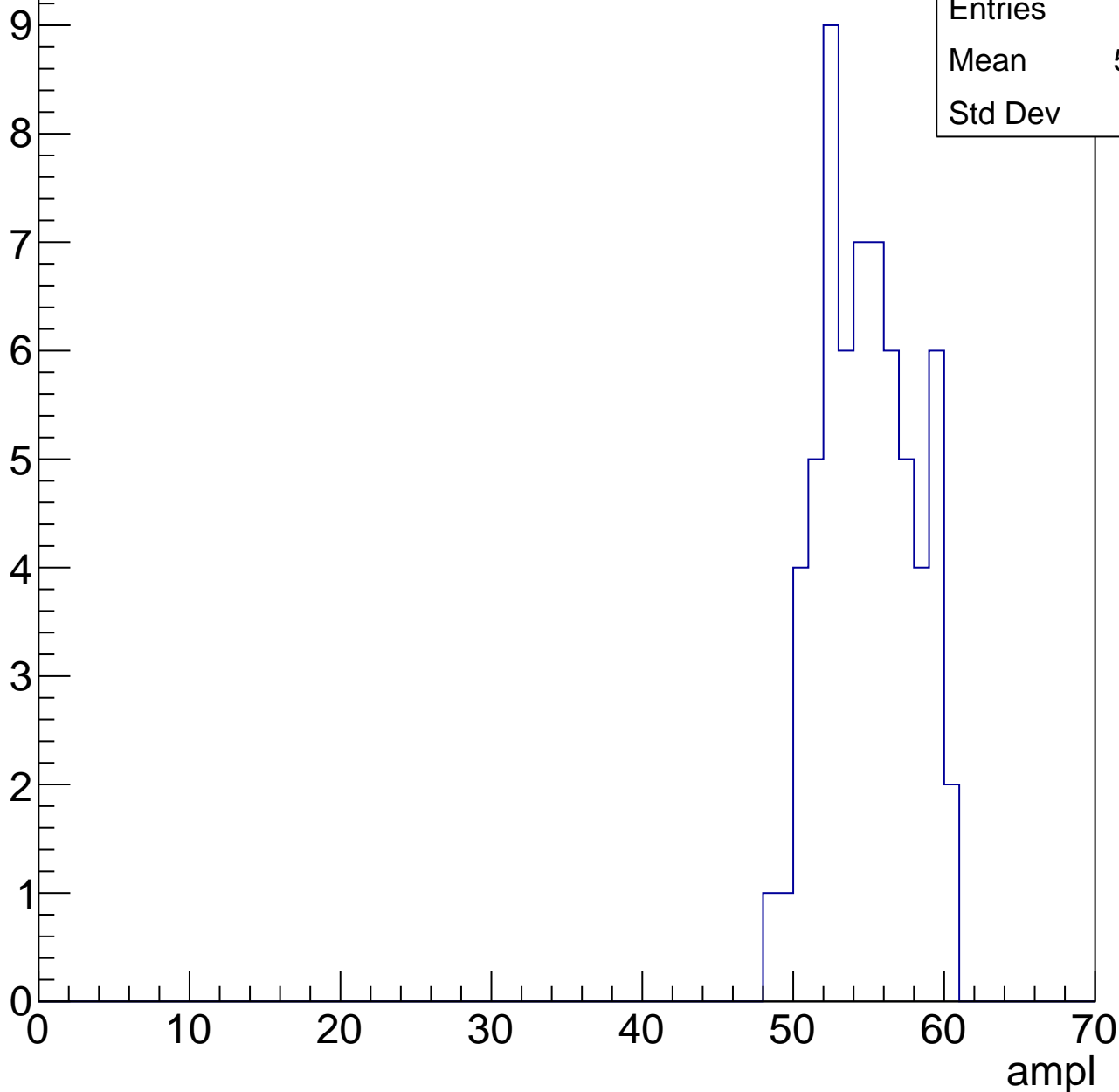


Entries	53
Mean	48.43
Std Dev	2.936

B1L002S, U7-ch13, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch13, adc5

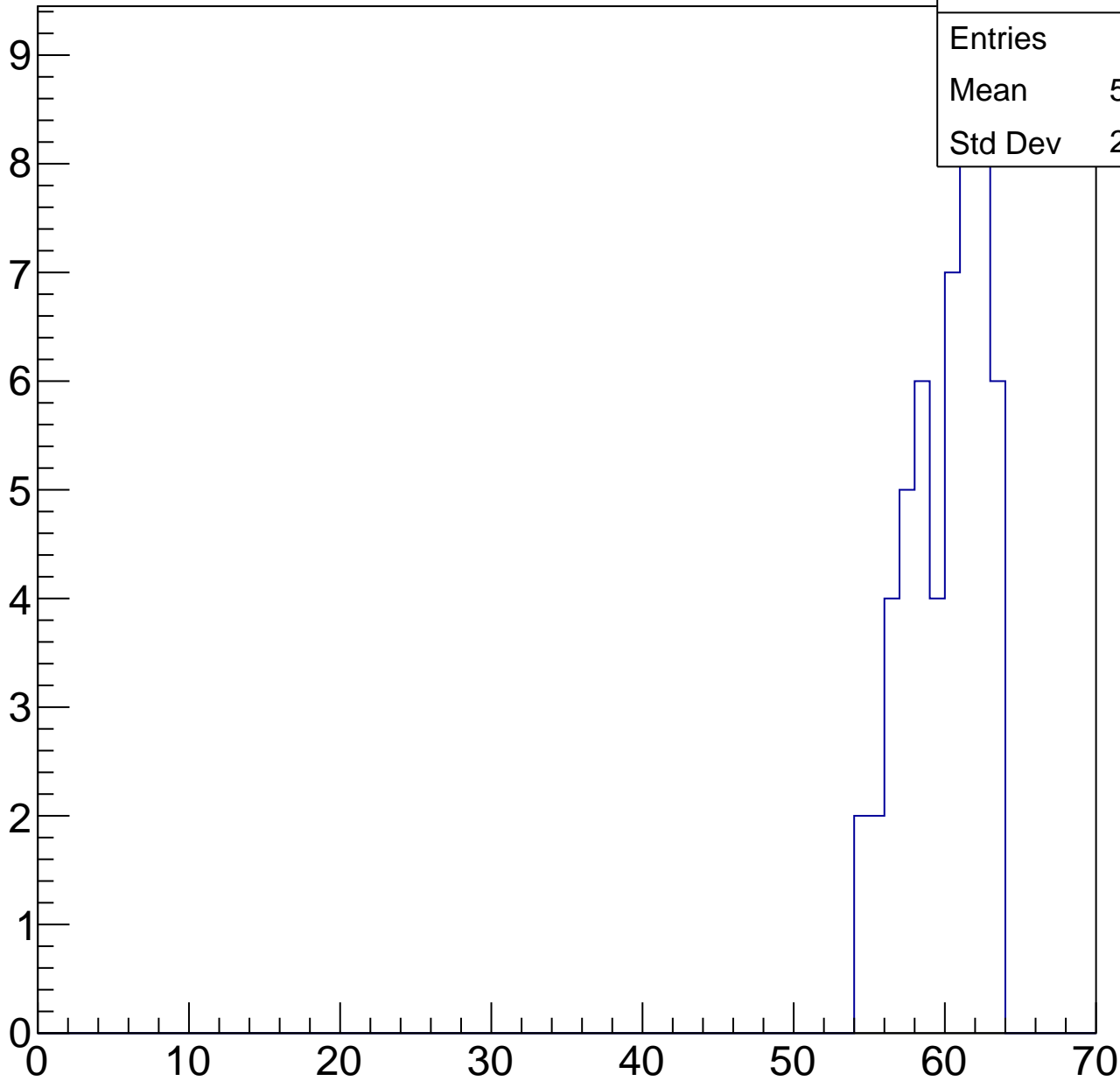
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.53
Std Dev	2.552

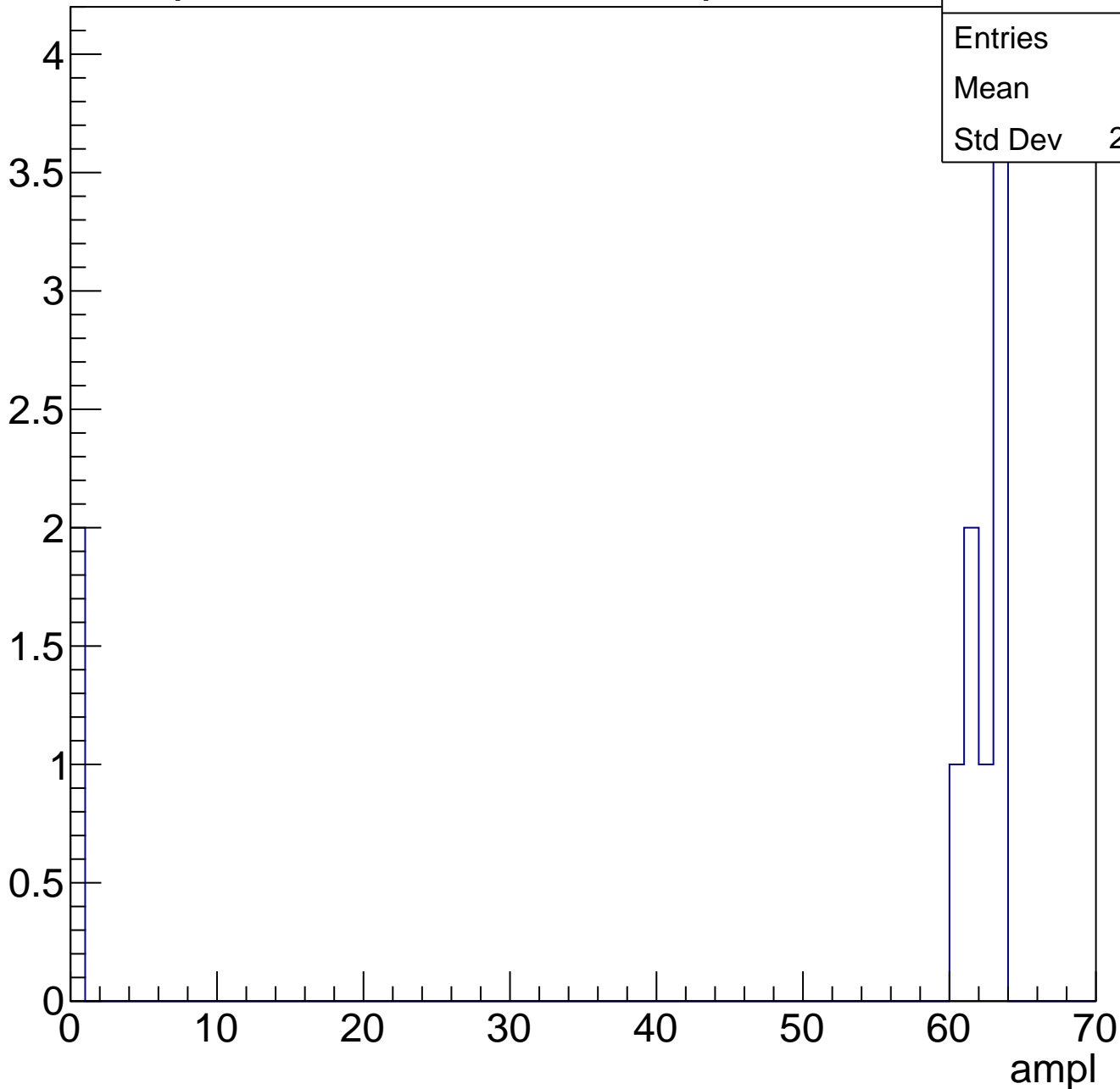
ampl



B1L002S, U7-ch13, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

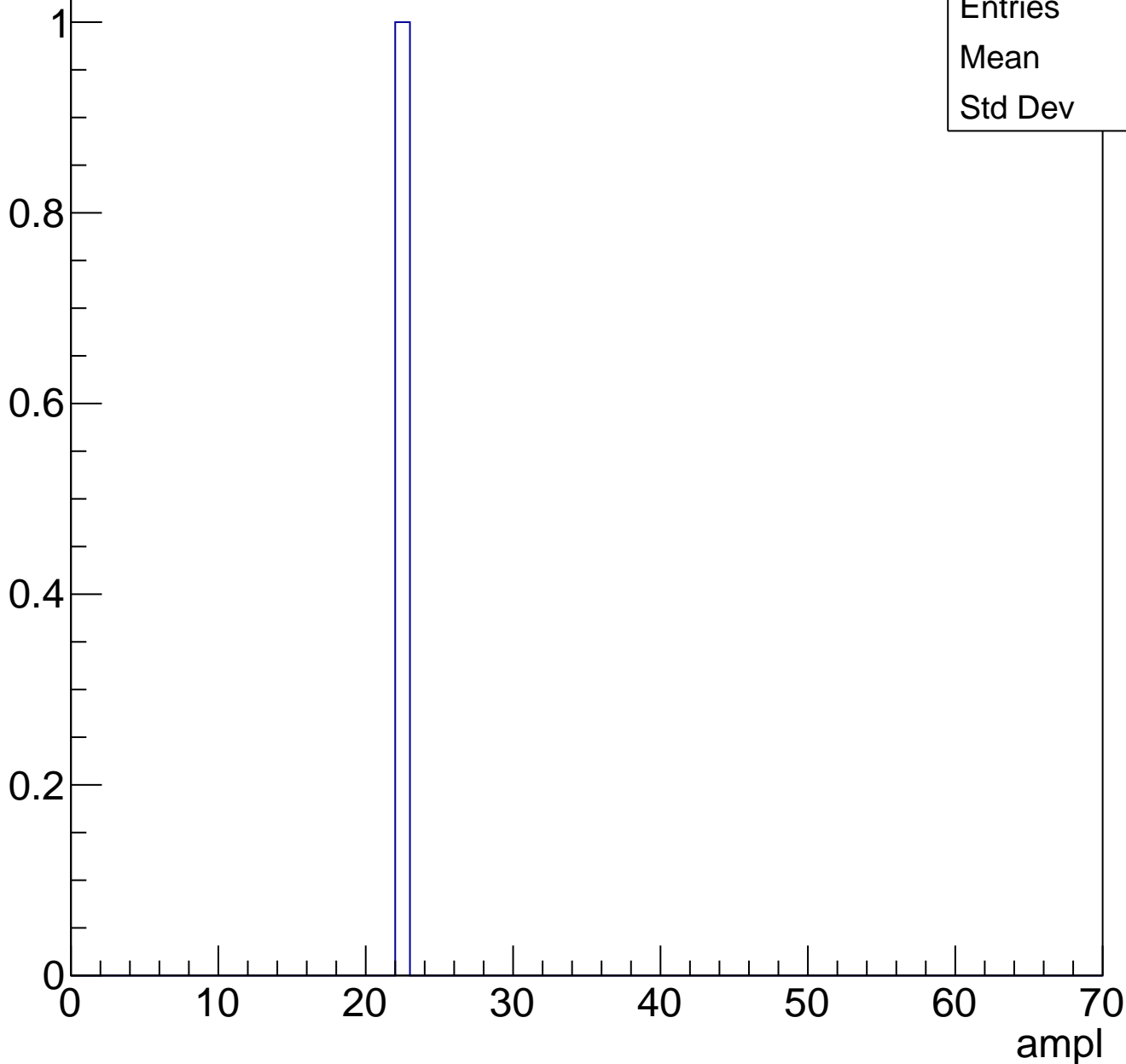


Entries	10
Mean	49.6
Std Dev	24.82

B1L002S, U7-ch13, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch14, adc0

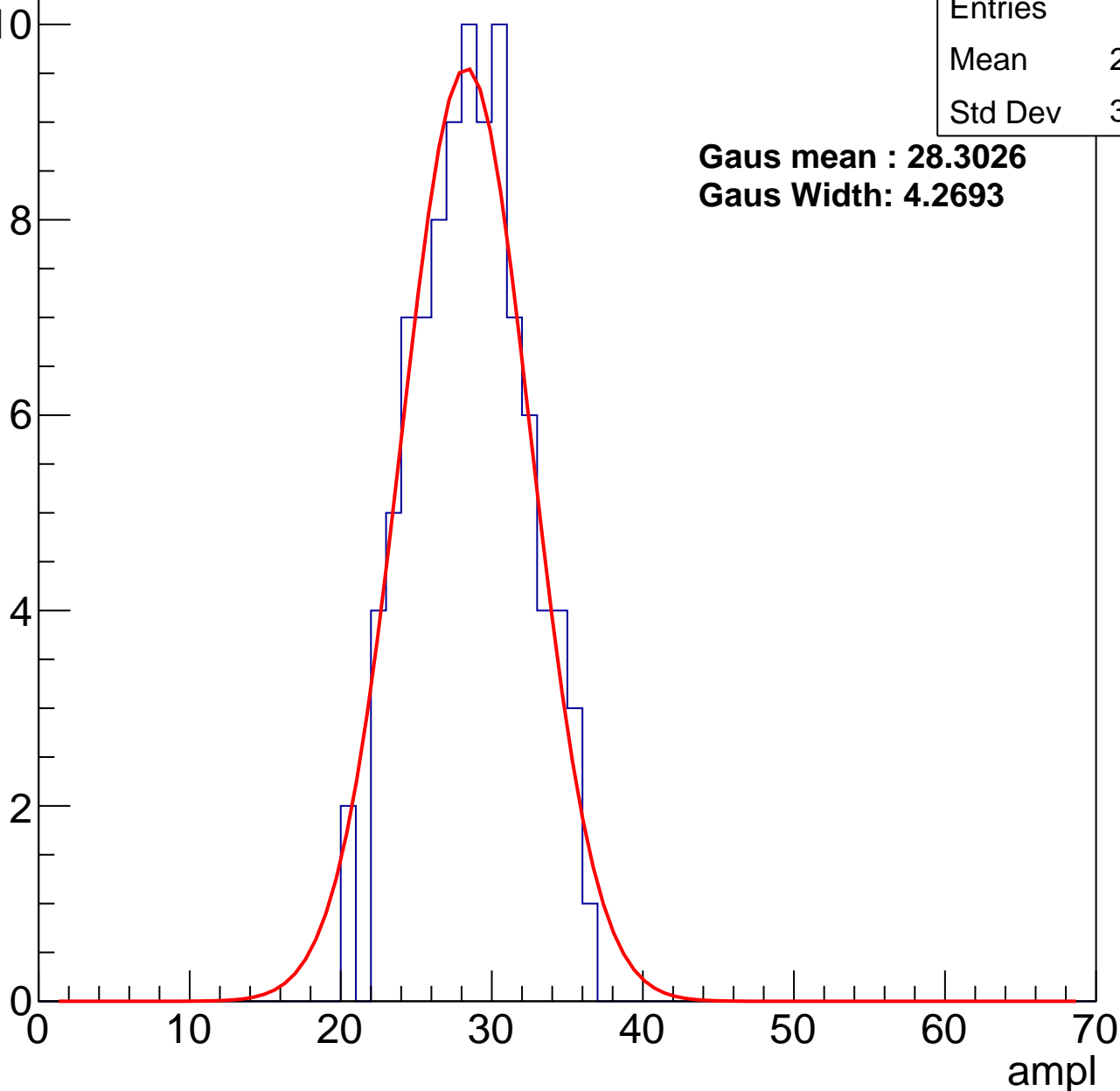
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	96
Mean	28.08
Std Dev	3.645

Gaus mean : 28.3026

Gaus Width: 4.2693



B1L002S, U7-ch14, adc1

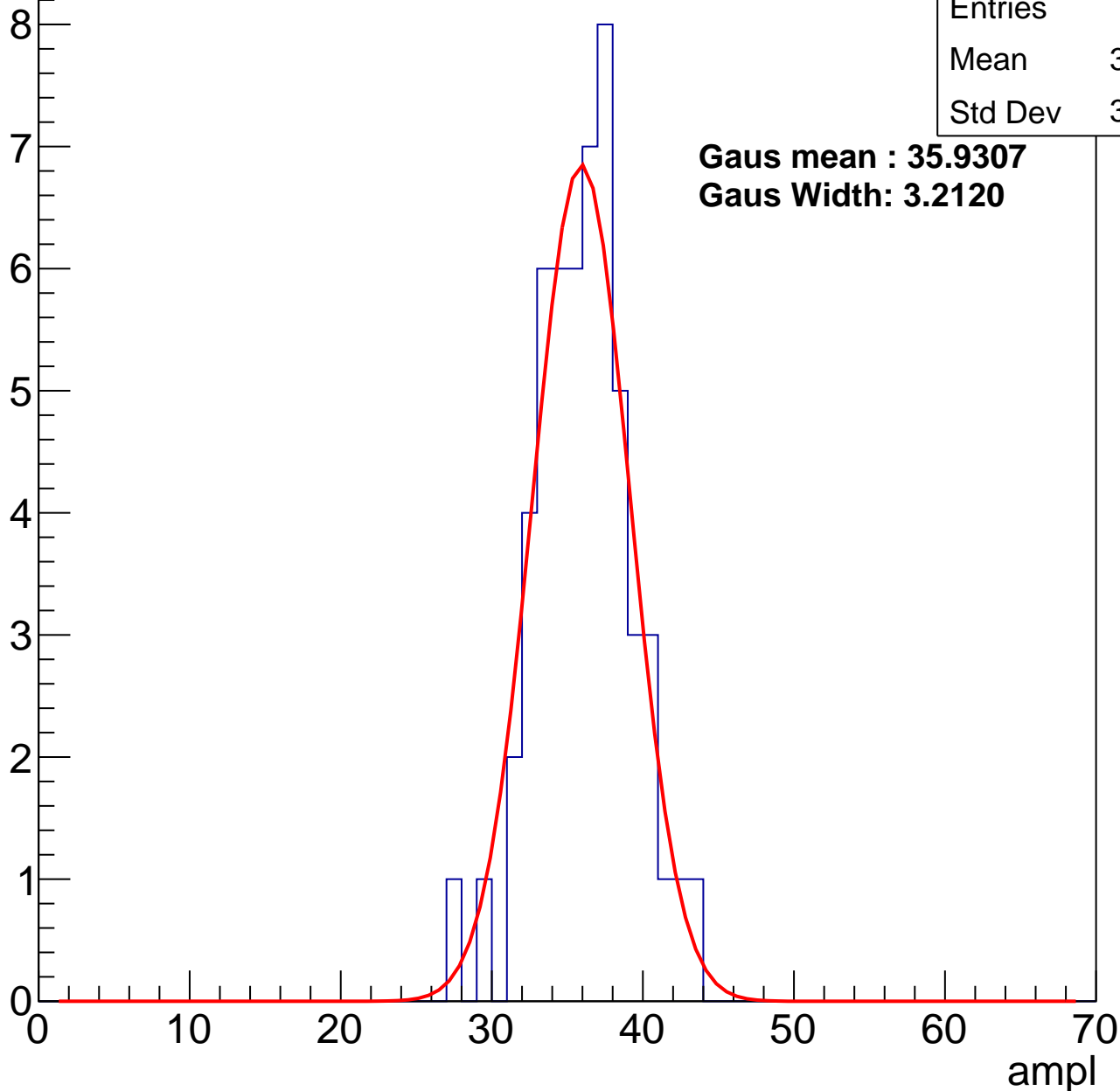
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	35.62
Std Dev	3.113

Gaus mean : 35.9307

Gaus Width: 3.2120



B1L002S, U7-ch14, adc2

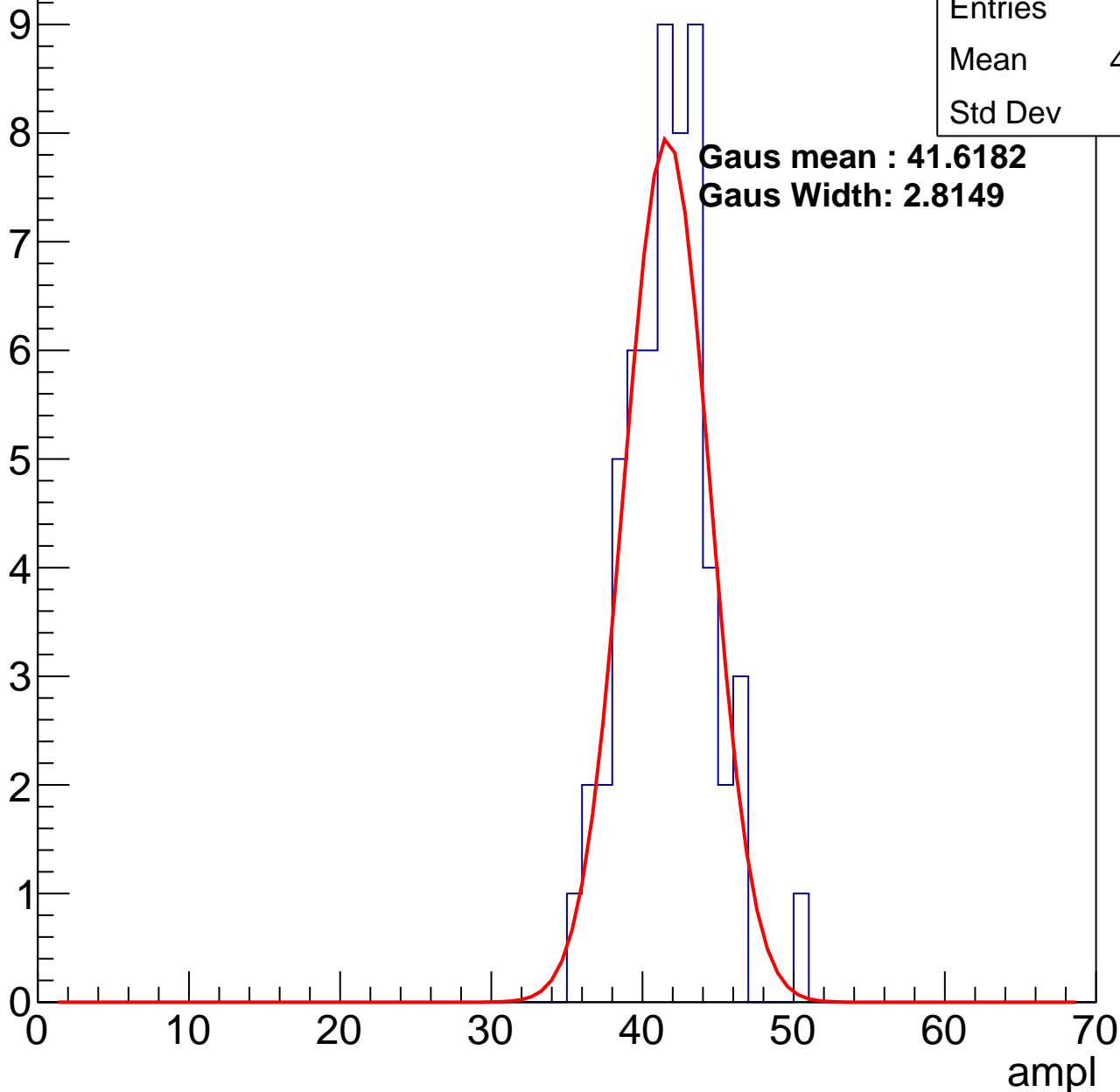
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.22
Std Dev	2.81

Gaus mean : 41.6182

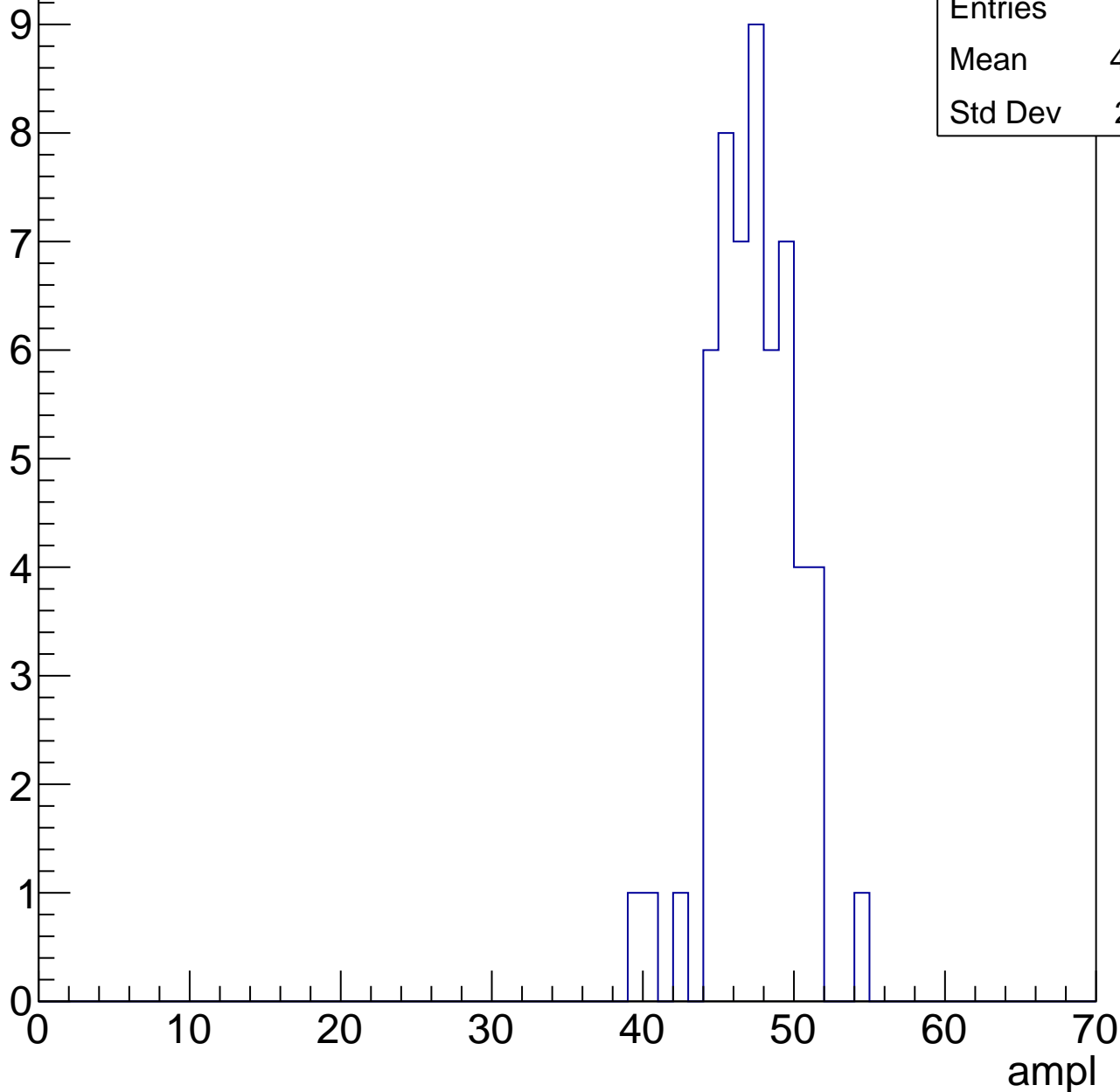
Gaus Width: 2.8149



B1L002S, U7-ch14, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch14, adc4

calib_packv5_042523_0143.root, FC#10, port B3

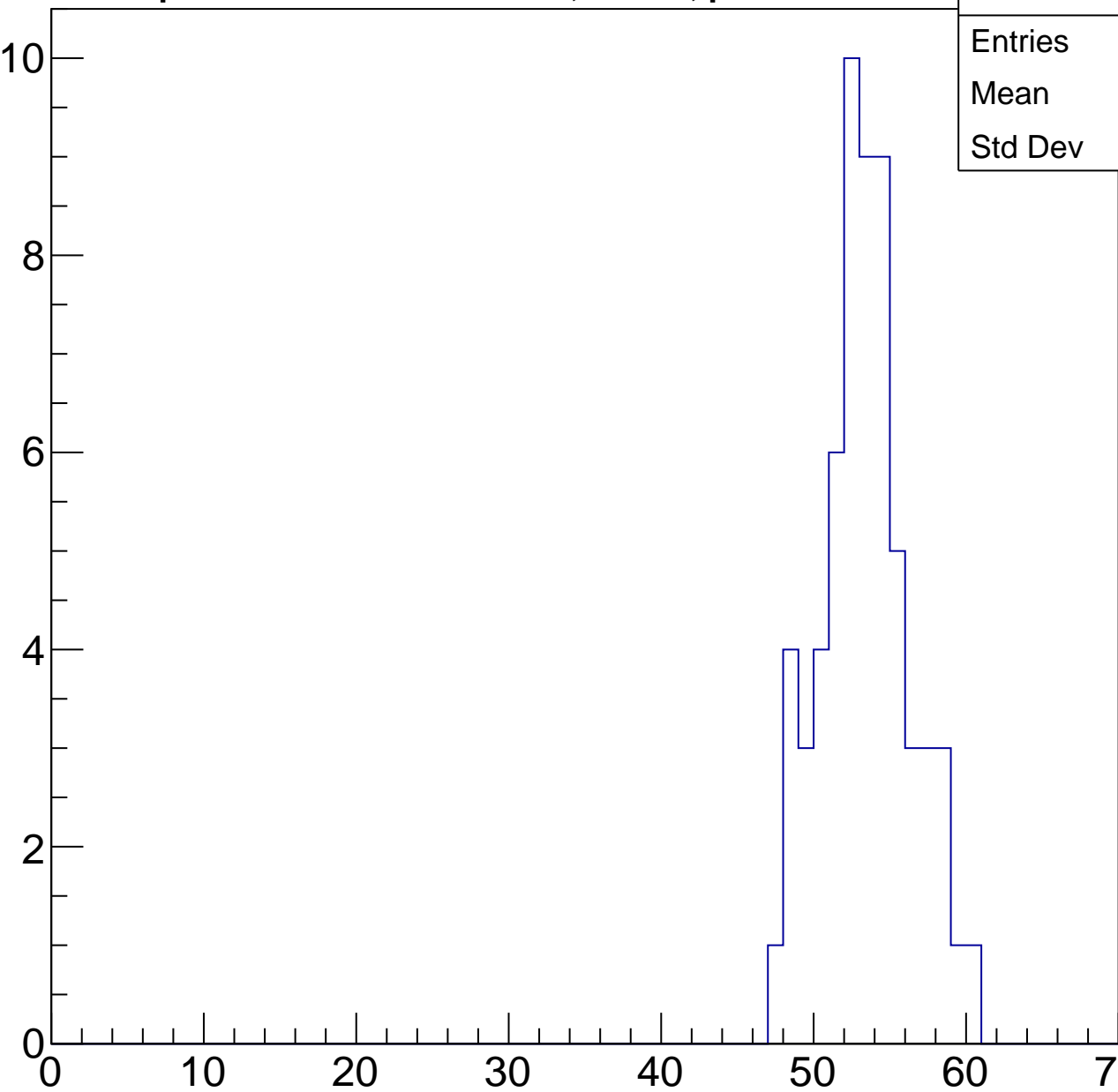
Entries	62
Mean	52.94
Std Dev	2.89

Entry

10
8
6
4
2
0

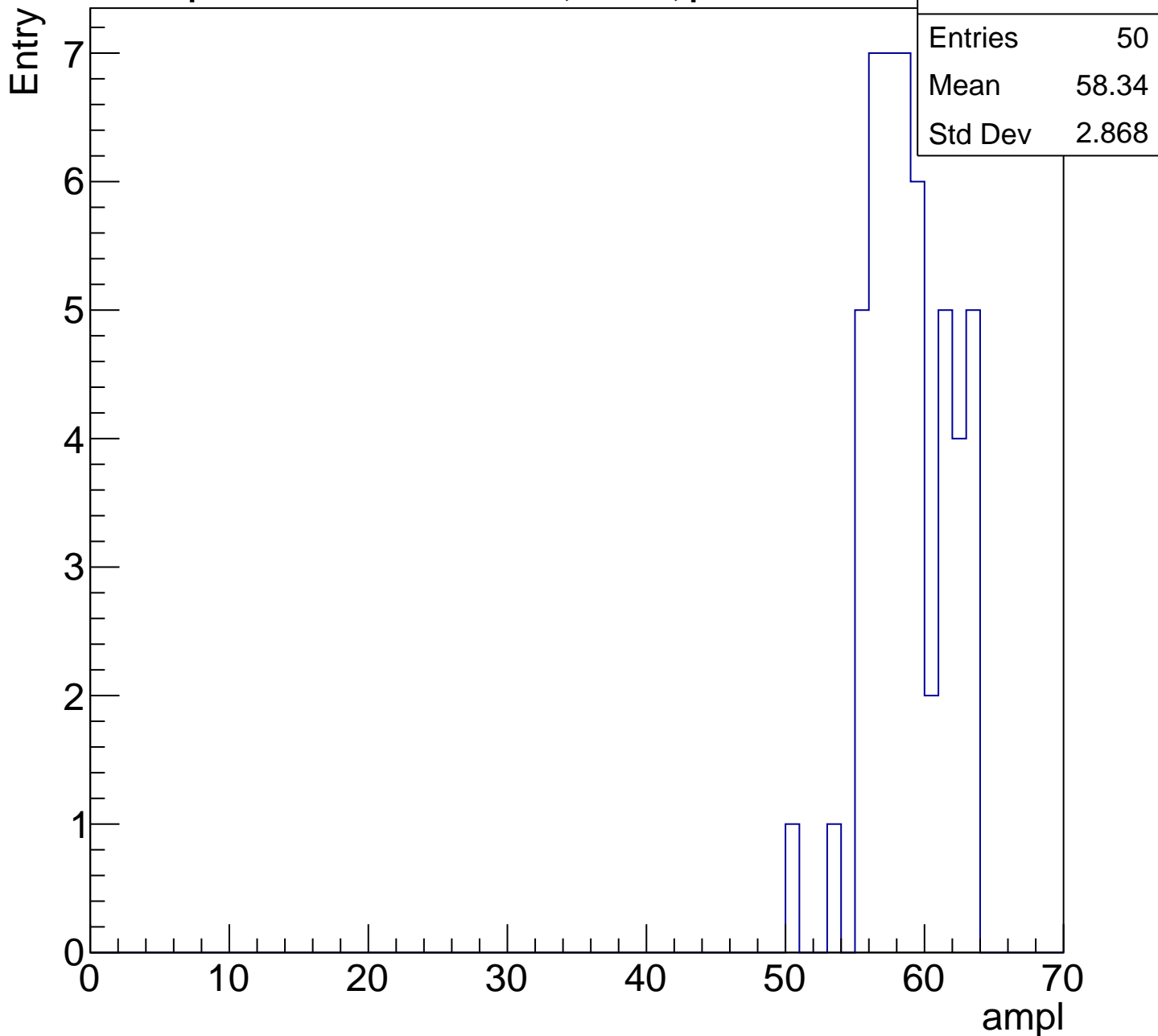
0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch14, adc5

calib_packv5_042523_0143.root, FC#10, port B3

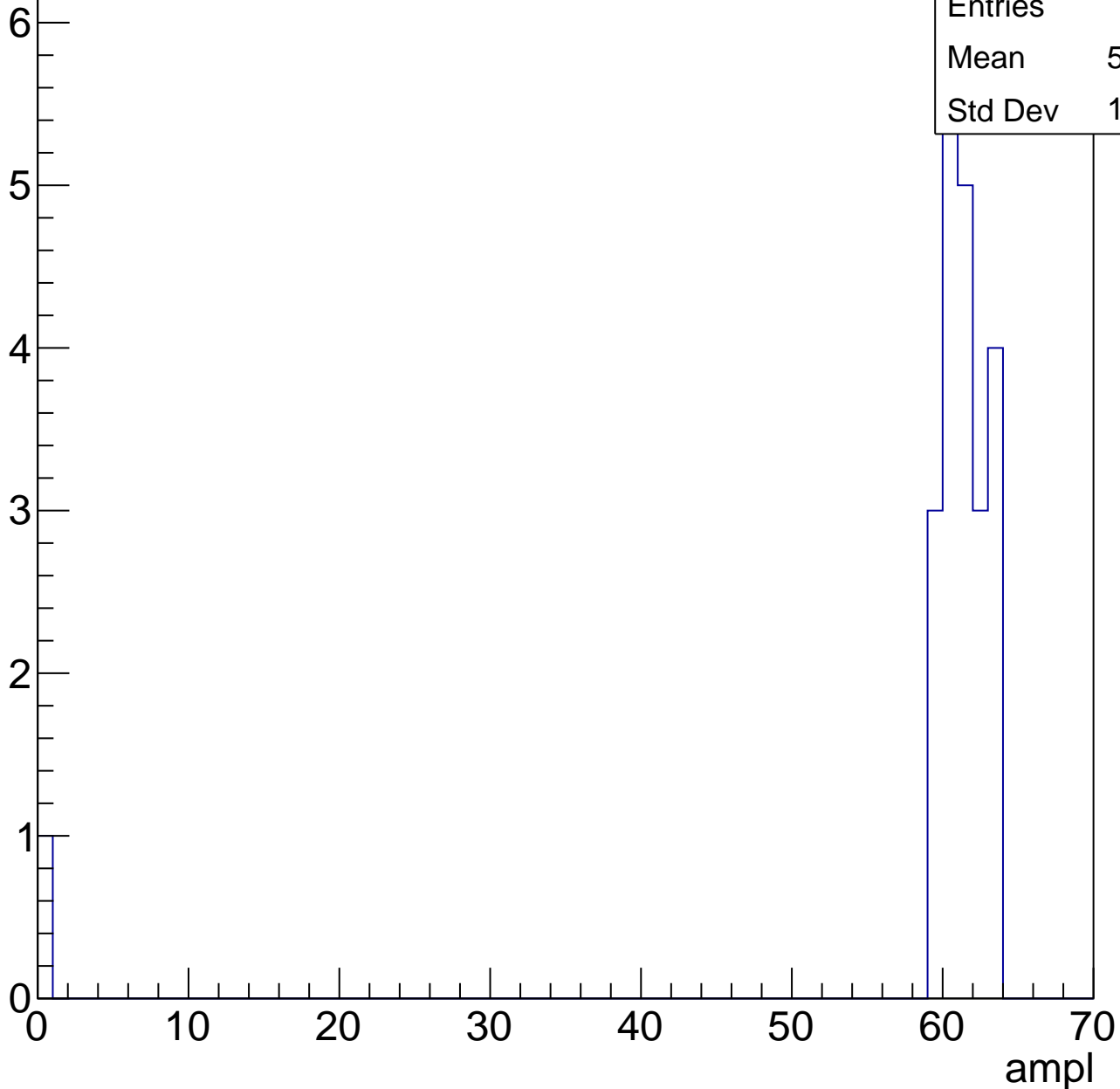


B1L002S, U7-ch14, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

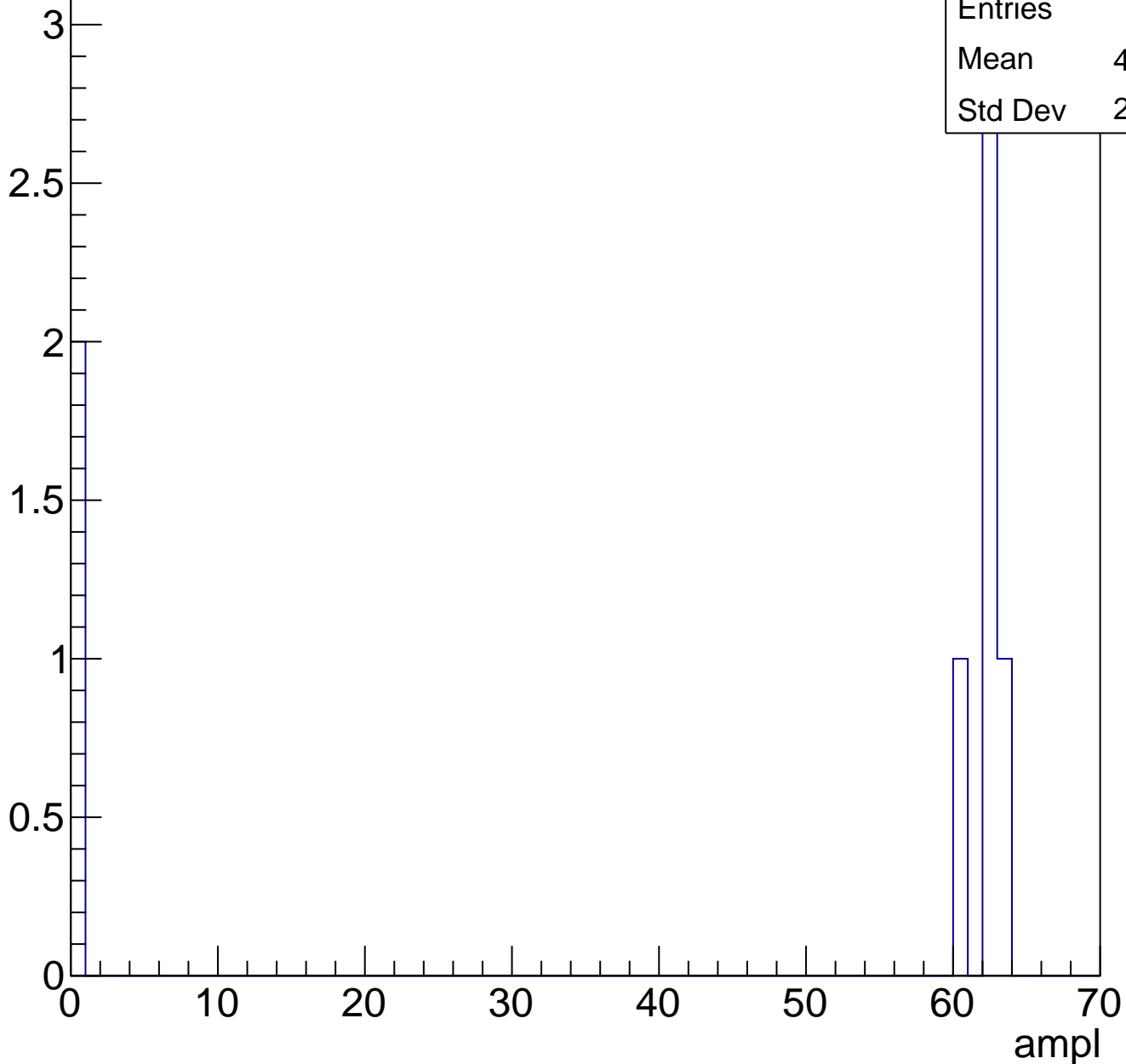
Entries	22
Mean	58.18
Std Dev	12.76



B1L002S, U7-ch14, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch15, adc0

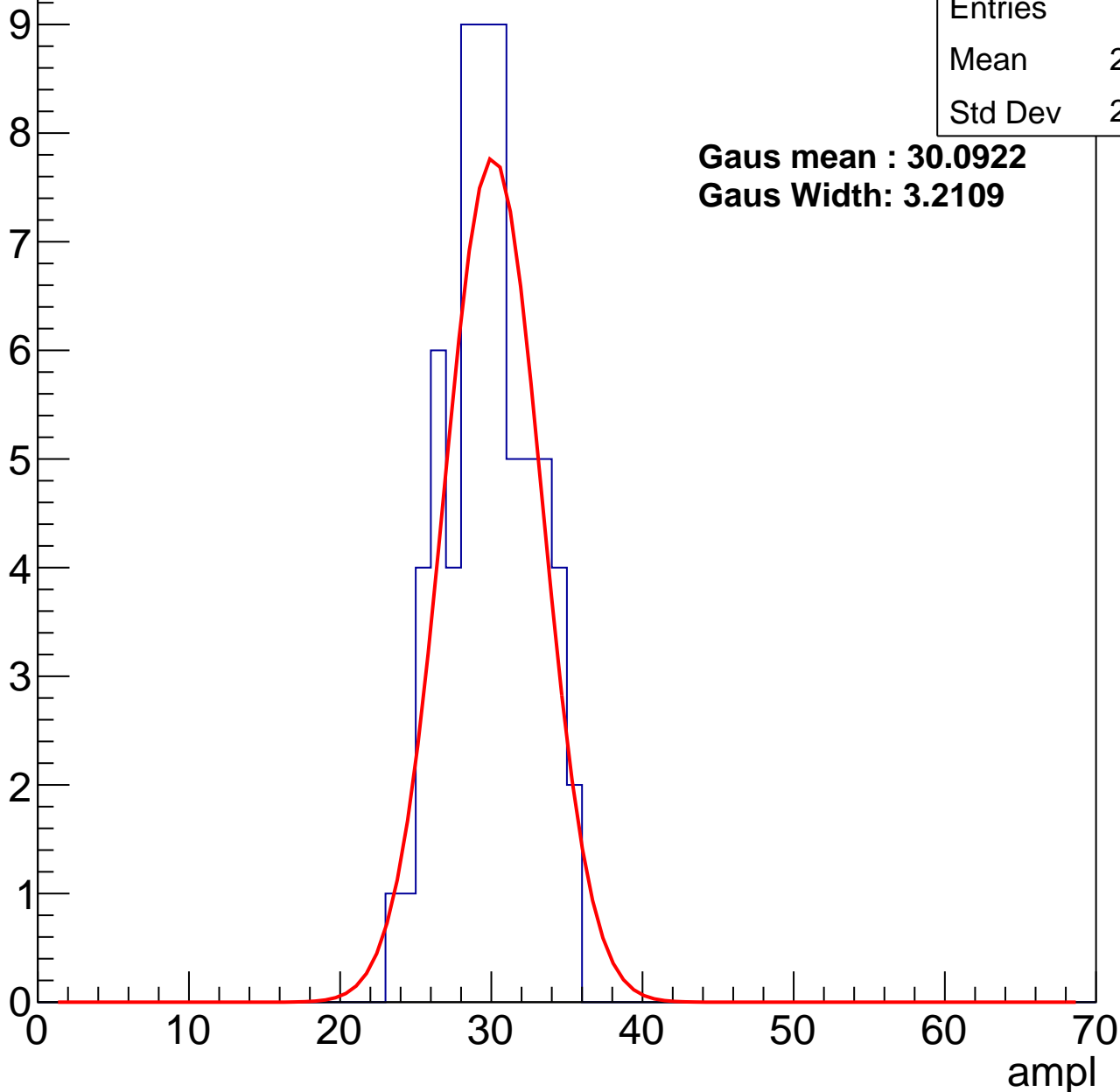
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	29.38
Std Dev	2.853

Gaus mean : 30.0922

Gaus Width: 3.2109



B1L002S, U7-ch15, adc1

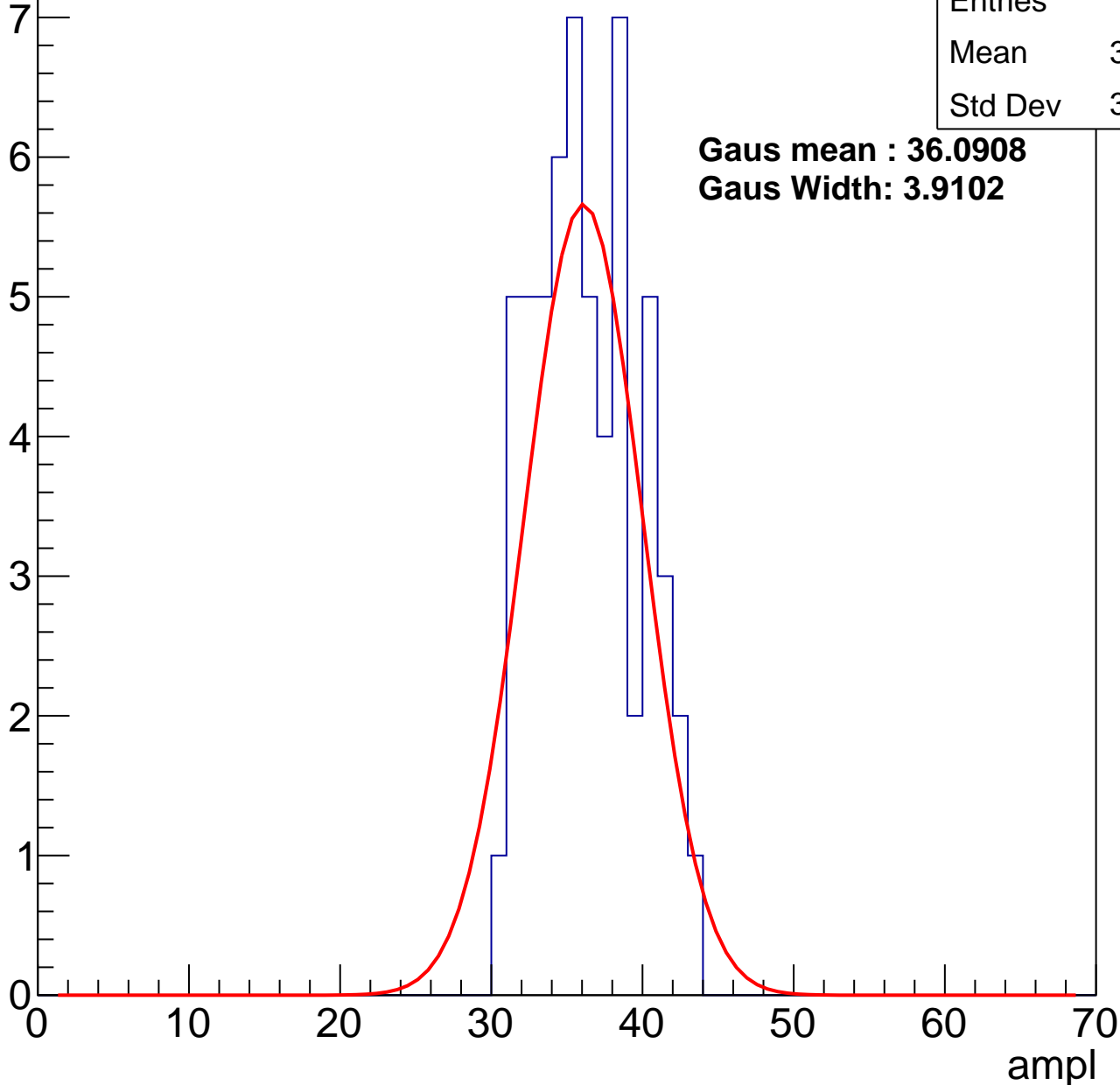
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.88
Std Dev	3.327

Gaus mean : 36.0908

Gaus Width: 3.9102



B1L002S, U7-ch15, adc2

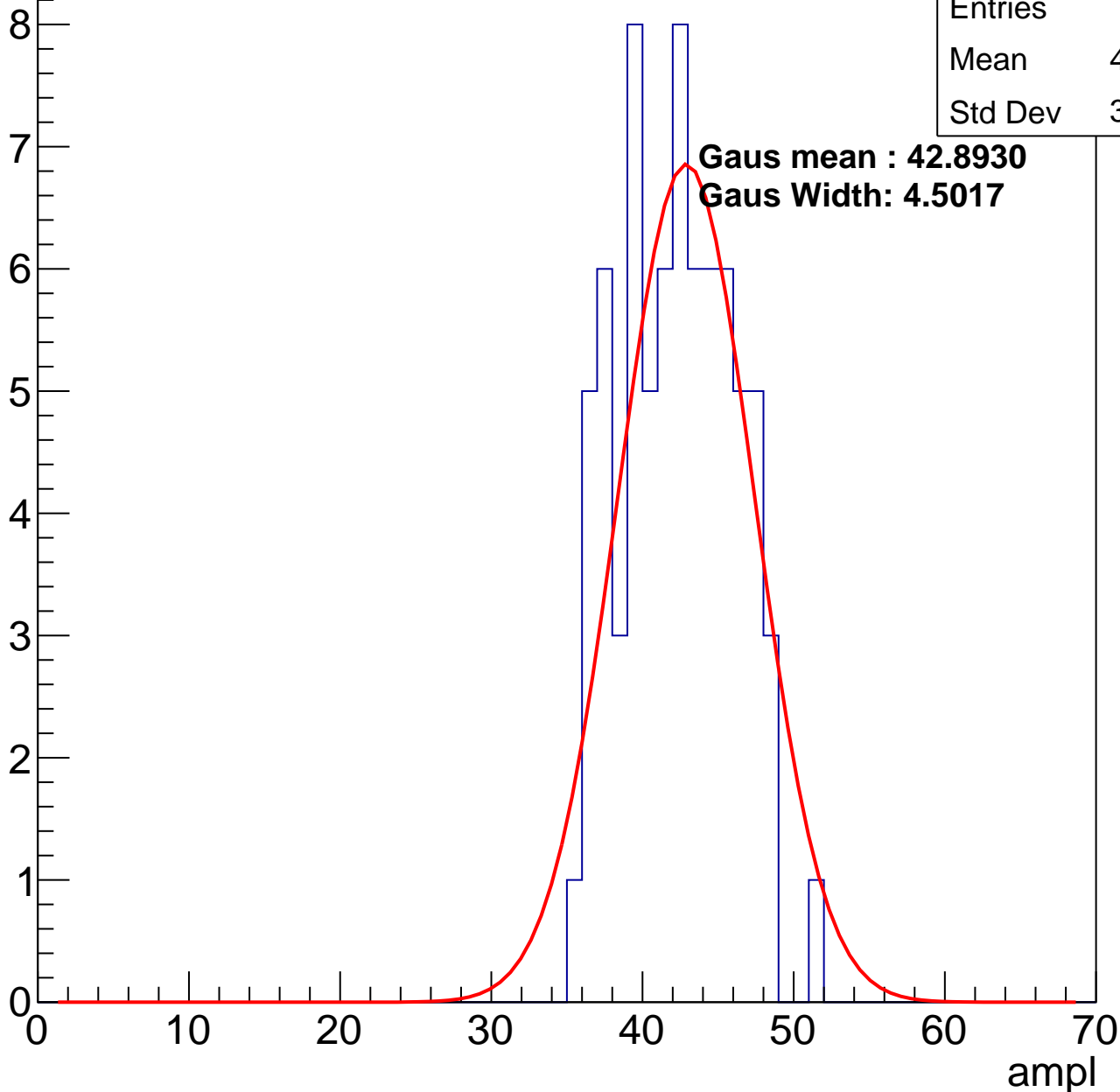
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	41.85
Std Dev	3.679

Gaus mean : 42.8930

Gaus Width: 4.5017

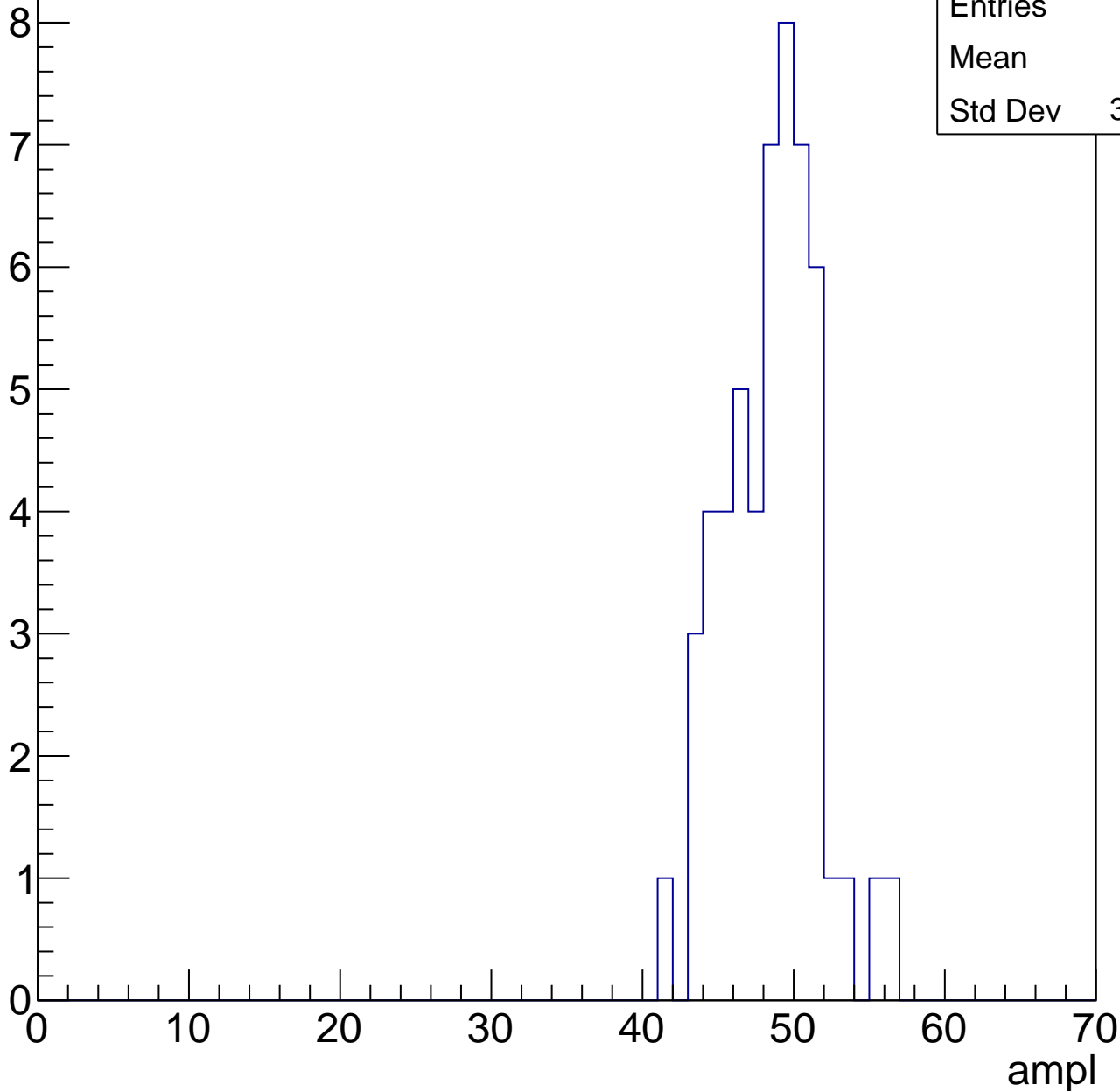


B1L002S, U7-ch15, adc3

calib_packv5_042523_0143.root, FC#10, port B3

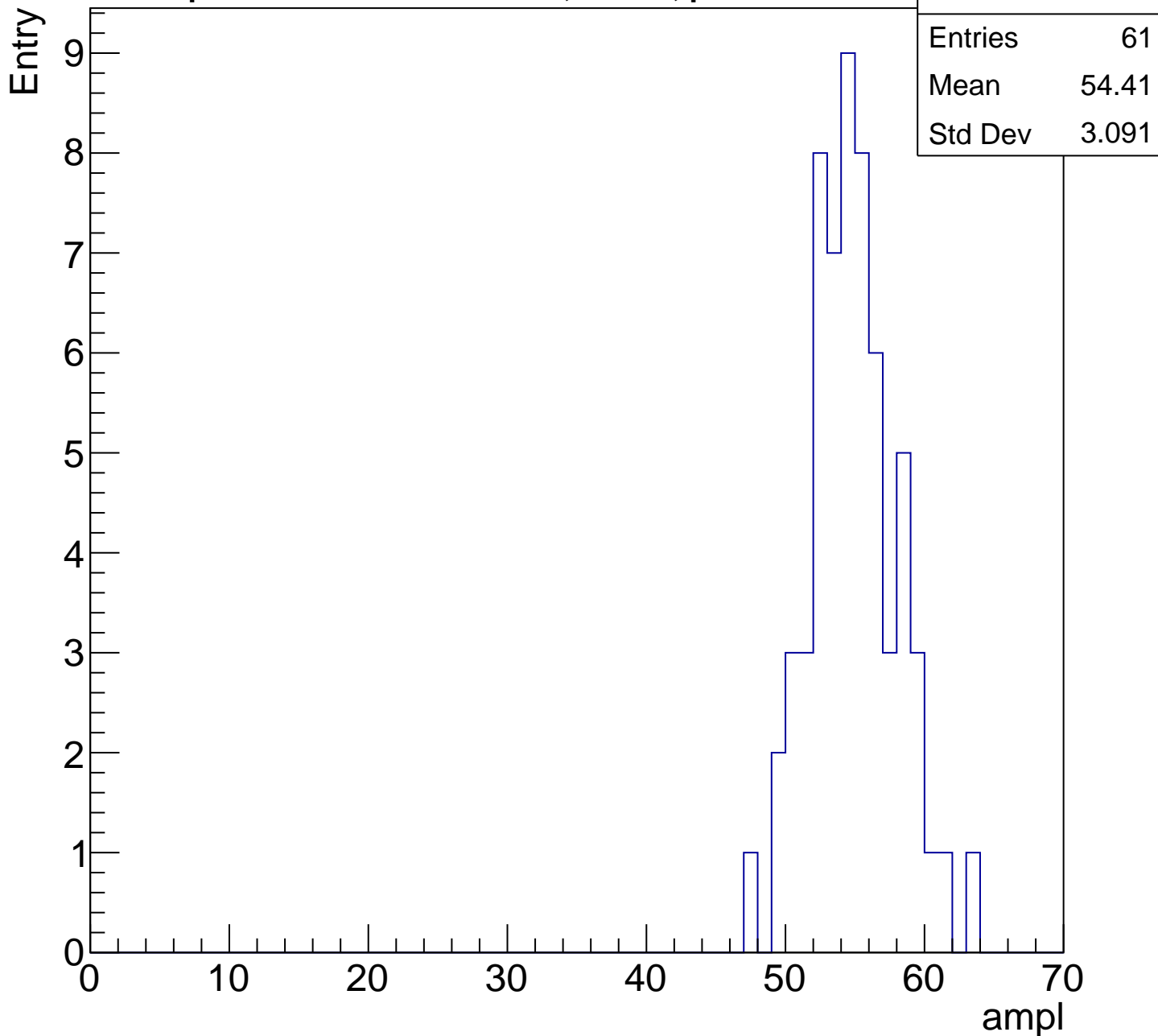
Entry

Entries	53
Mean	48
Std Dev	3.047



B1L002S, U7-ch15, adc4

calib_packv5_042523_0143.root, FC#10, port B3

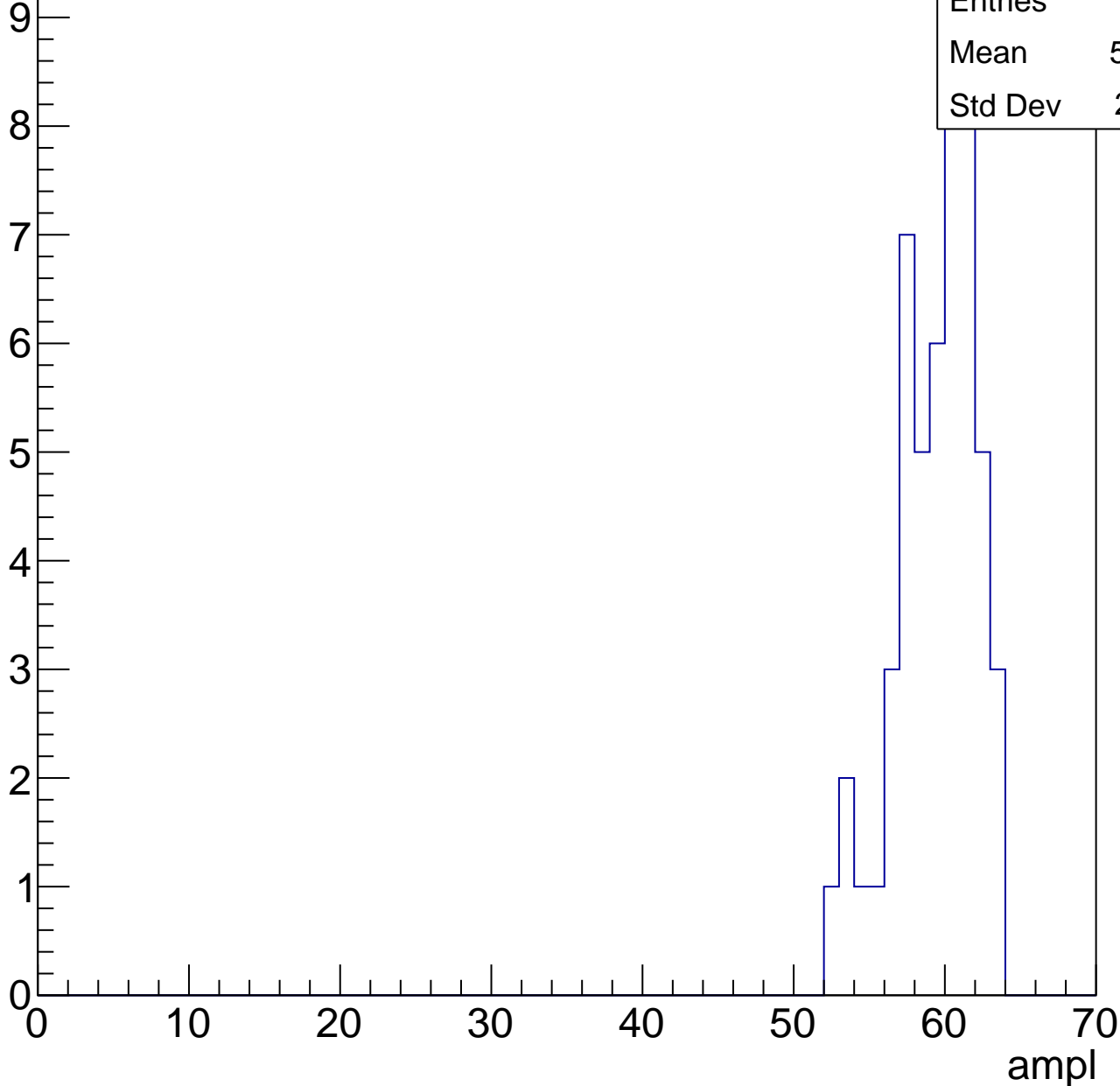


B1L002S, U7-ch15, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.92
Std Dev	2.641

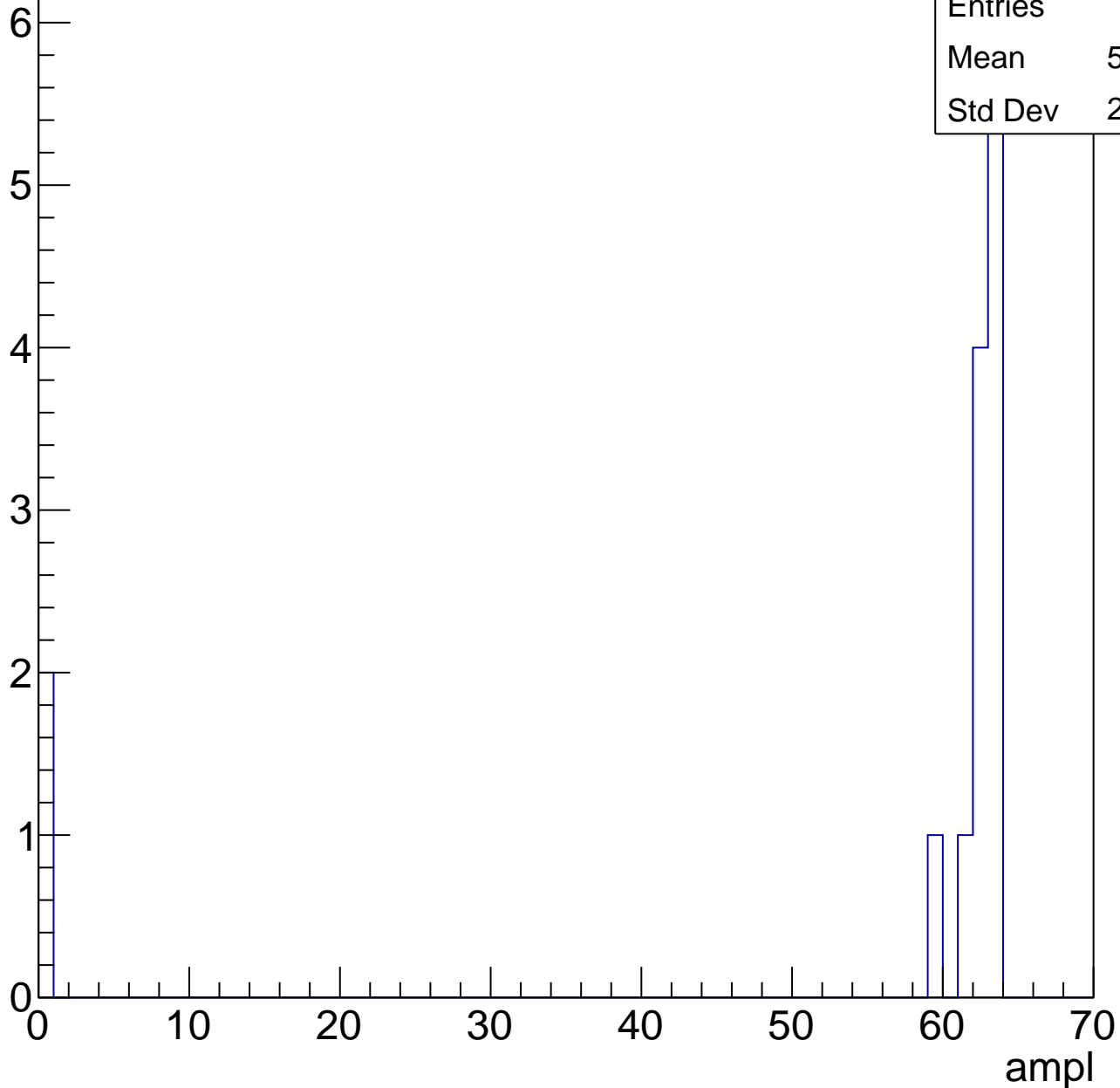


B1L002S, U7-ch15, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

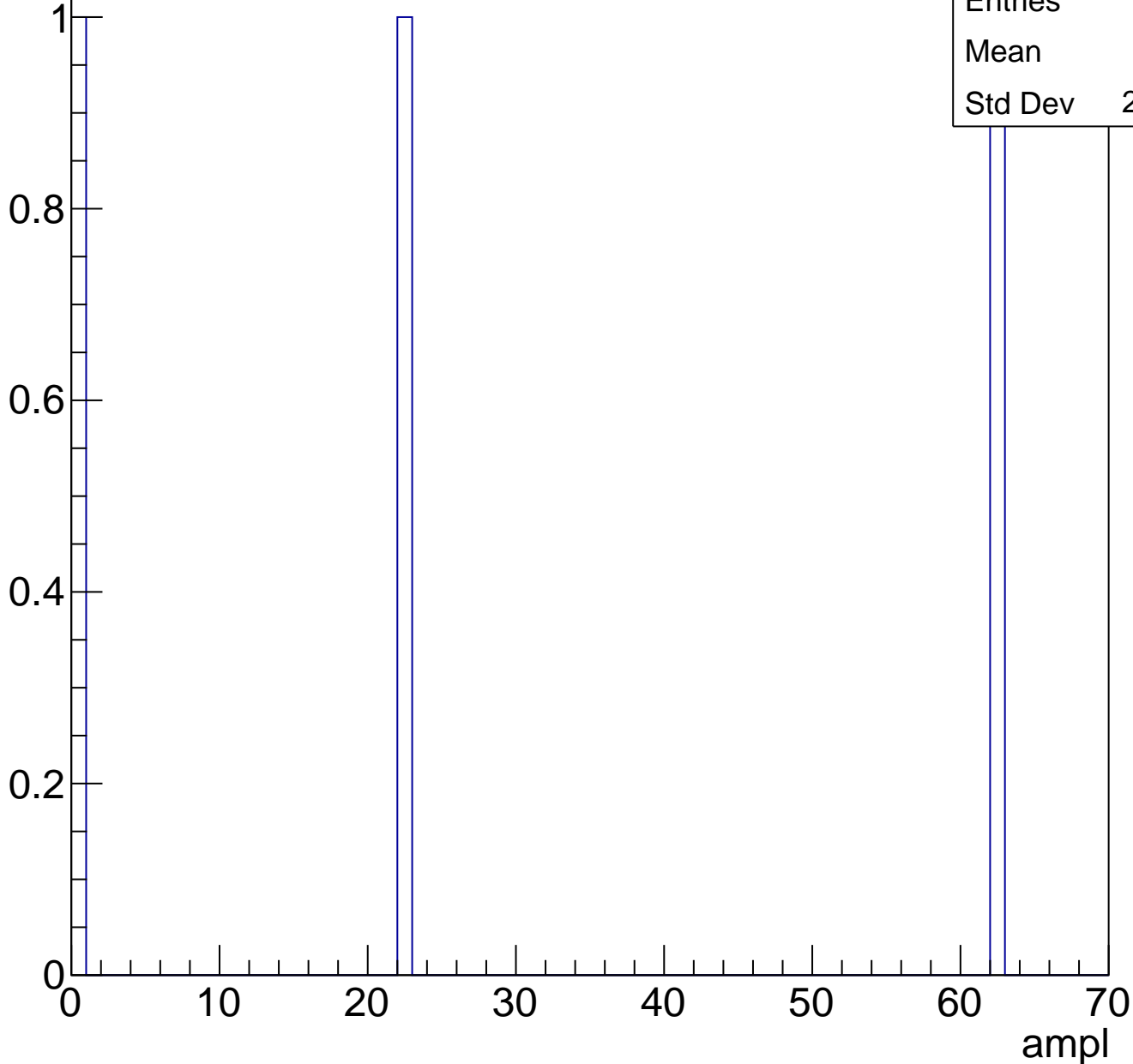
Entries	14
Mean	53.29
Std Dev	21.78



B1L002S, U7-ch15, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch16, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	84
Mean	28
Std Dev	3.259

Gaus mean : 28.2305

Gaus Width: 3.4748

10

8

6

4

2

0

0

10

20

30

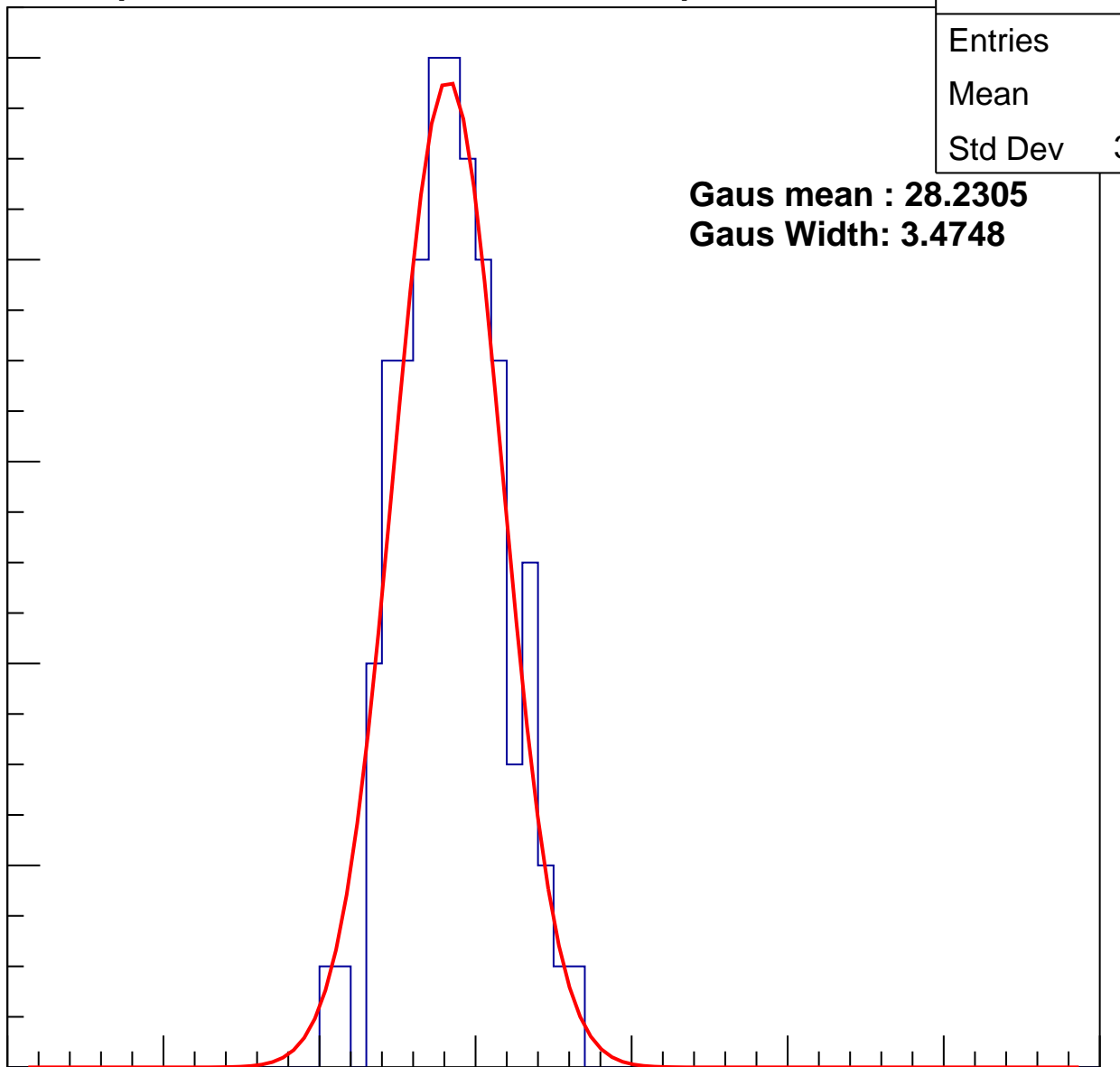
40

50

60

70

ampl



B1L002S, U7-ch16, adc1

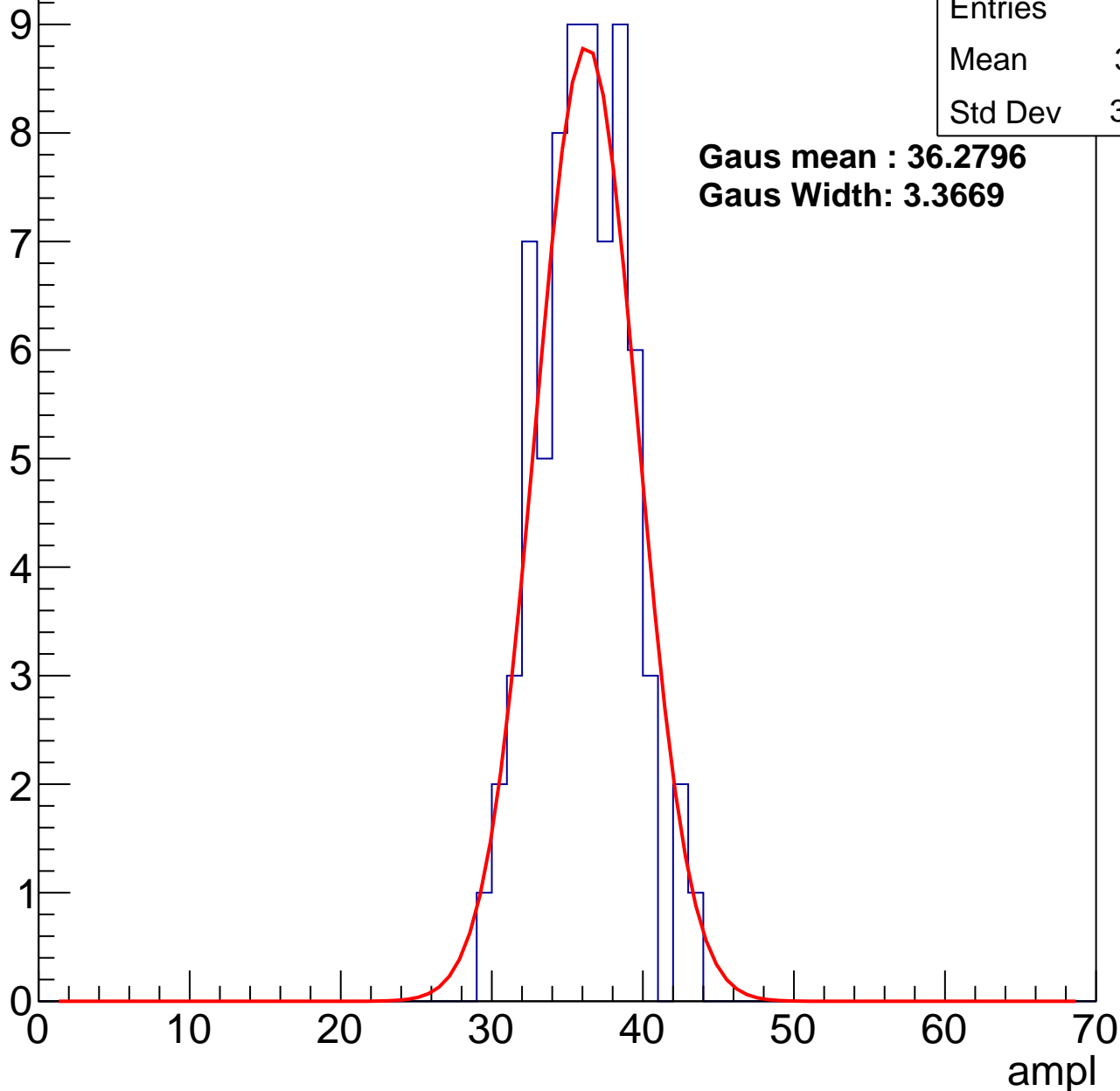
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	35.61
Std Dev	3.003

Gaus mean : 36.2796

Gaus Width: 3.3669



B1L002S, U7-ch16, adc2

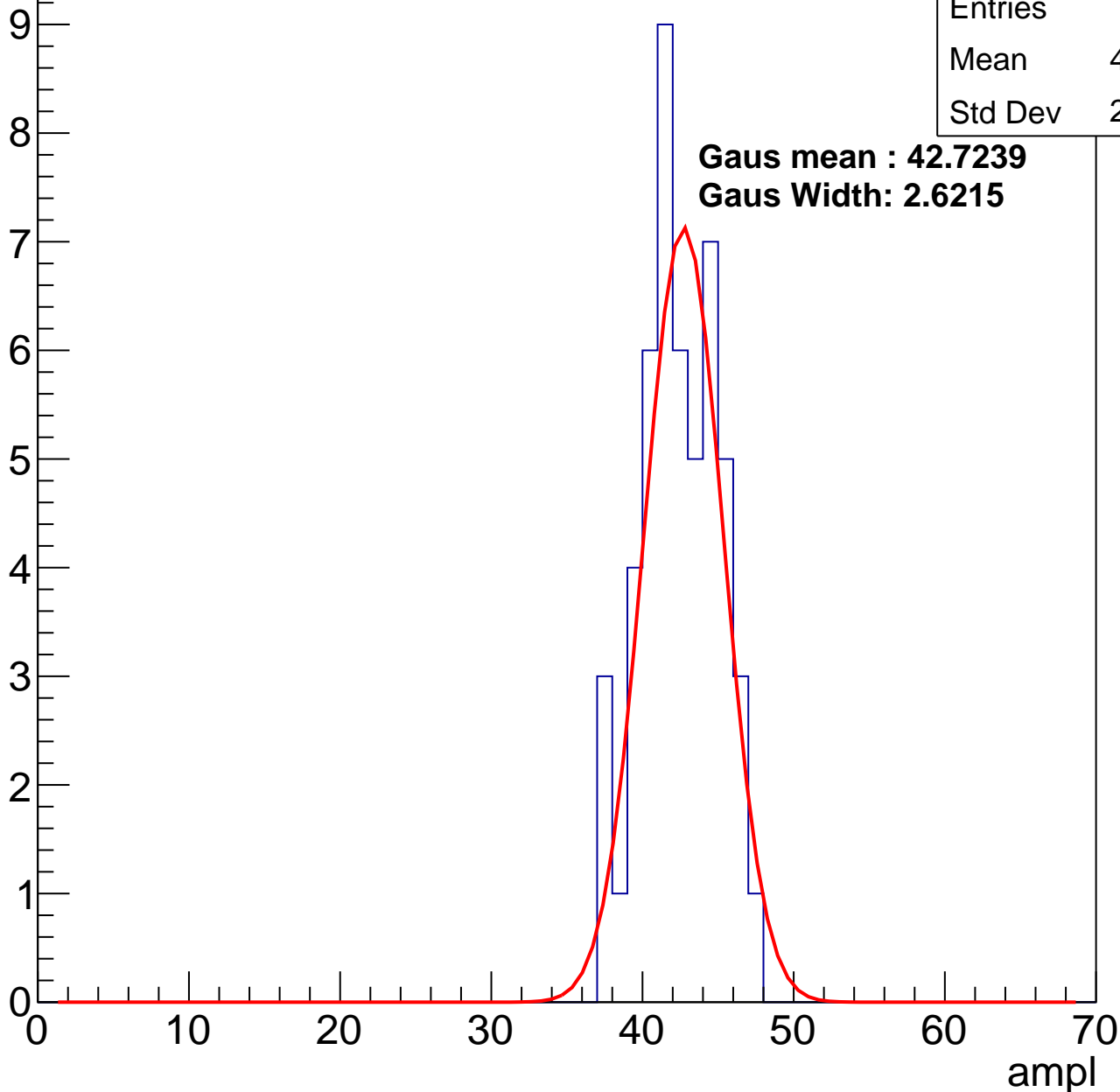
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	41.98
Std Dev	2.494

Gaus mean : 42.7239

Gaus Width: 2.6215



B1L002S, U7-ch16, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	63
Mean	47.3
Std Dev	2.953

Entry

10

8

6

4

2

0

0

10

20

30

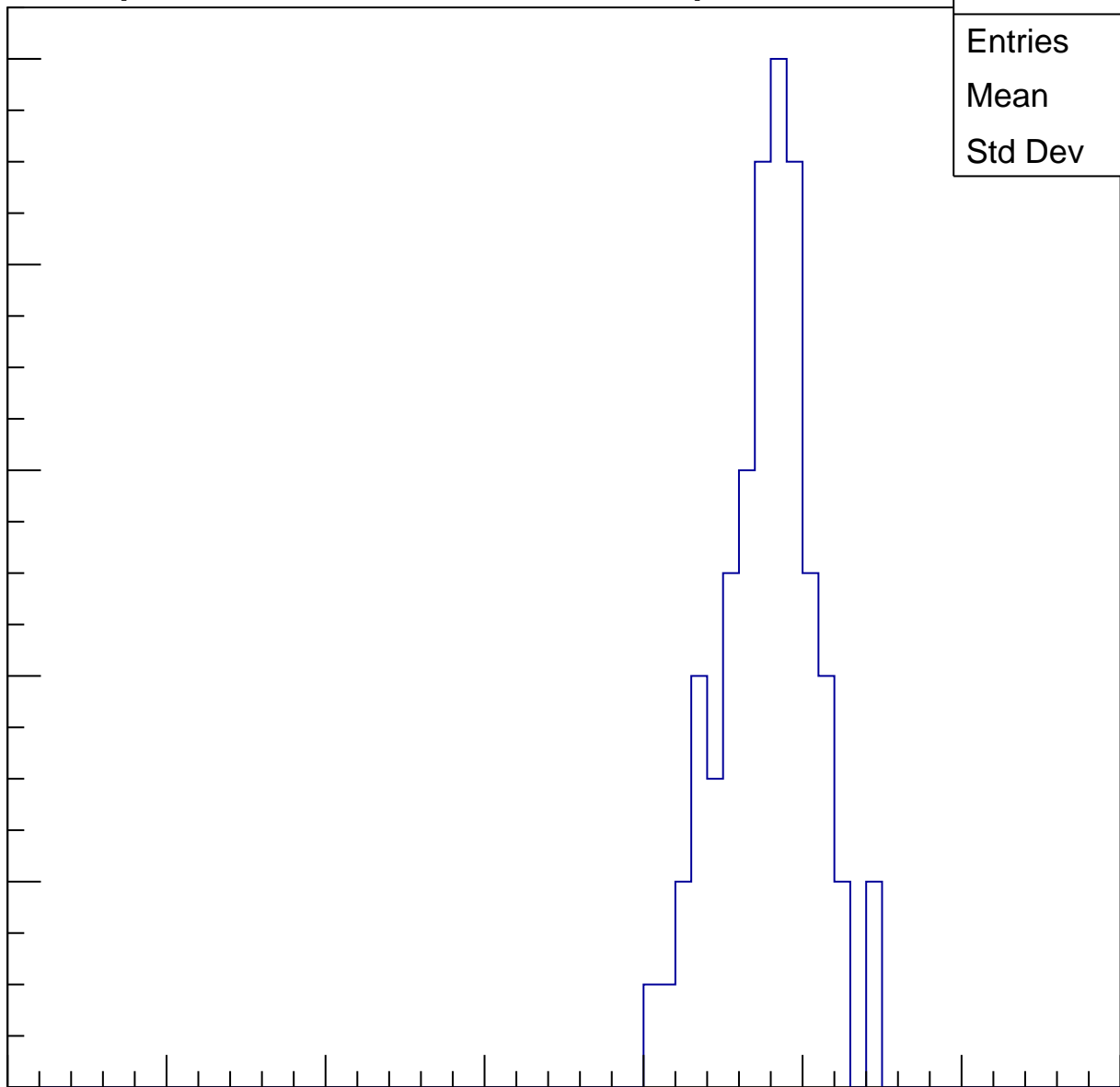
40

50

60

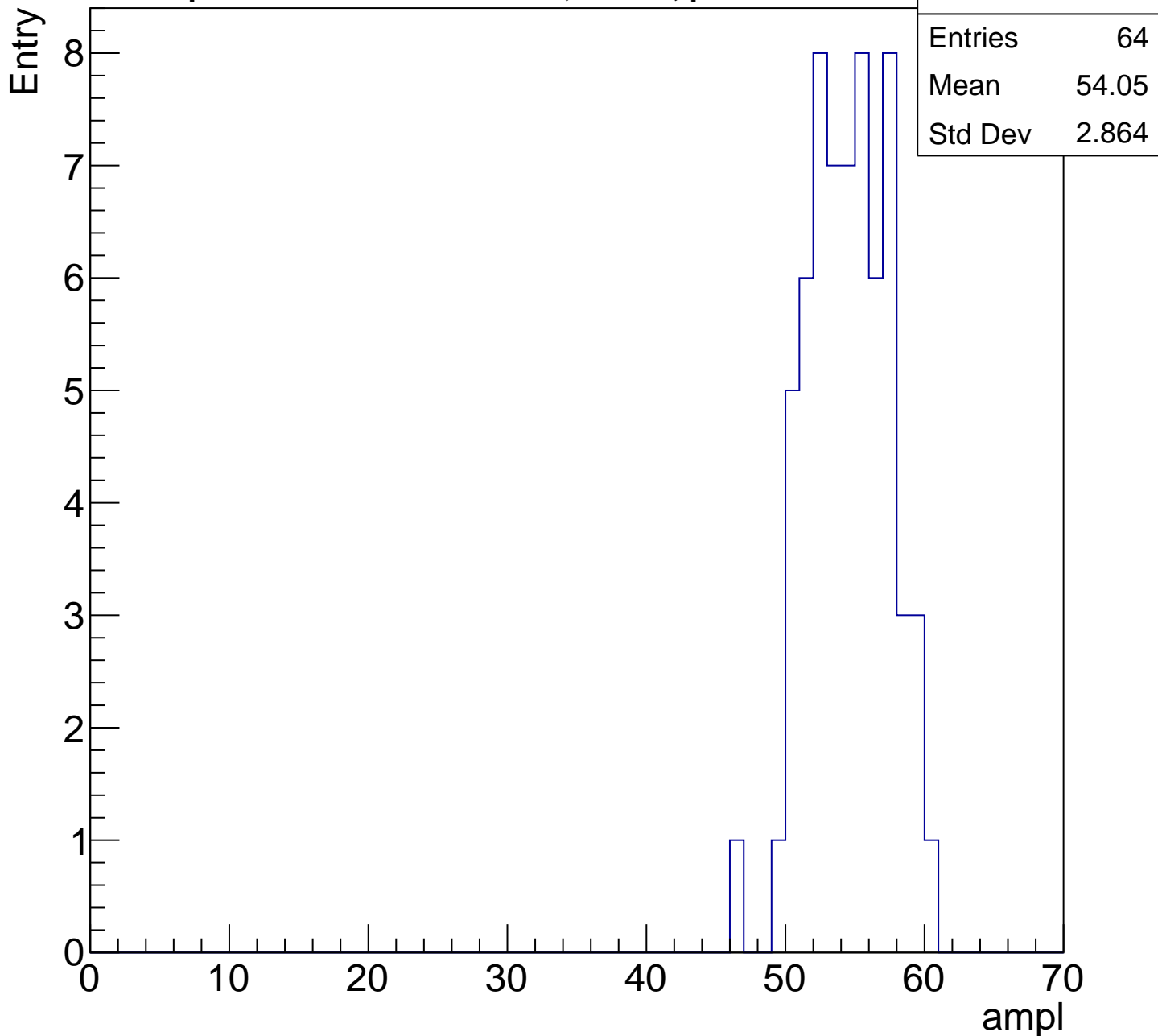
70

ampl



B1L002S, U7-ch16, adc4

calib_packv5_042523_0143.root, FC#10, port B3

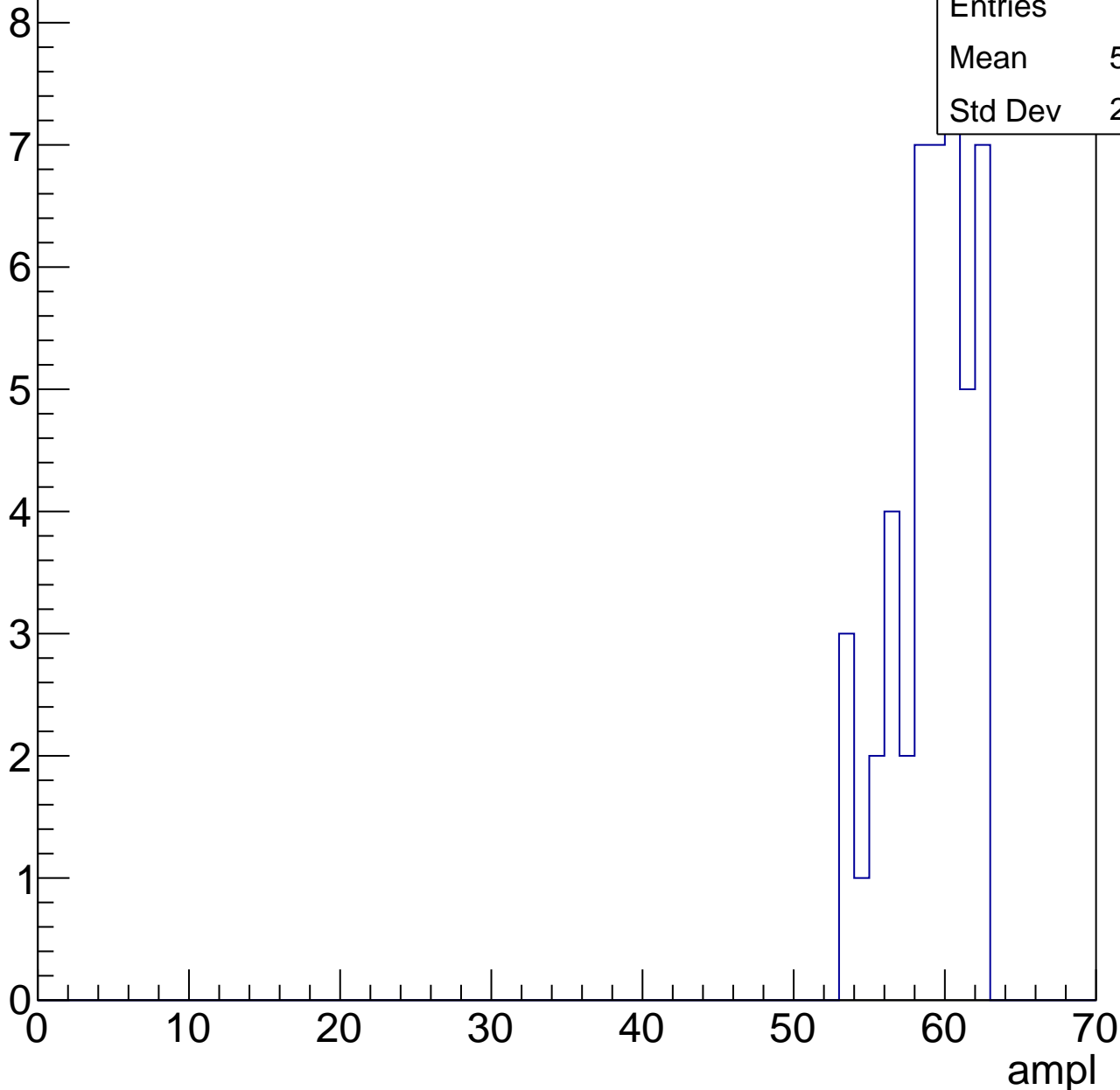


B1L002S, U7-ch16, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

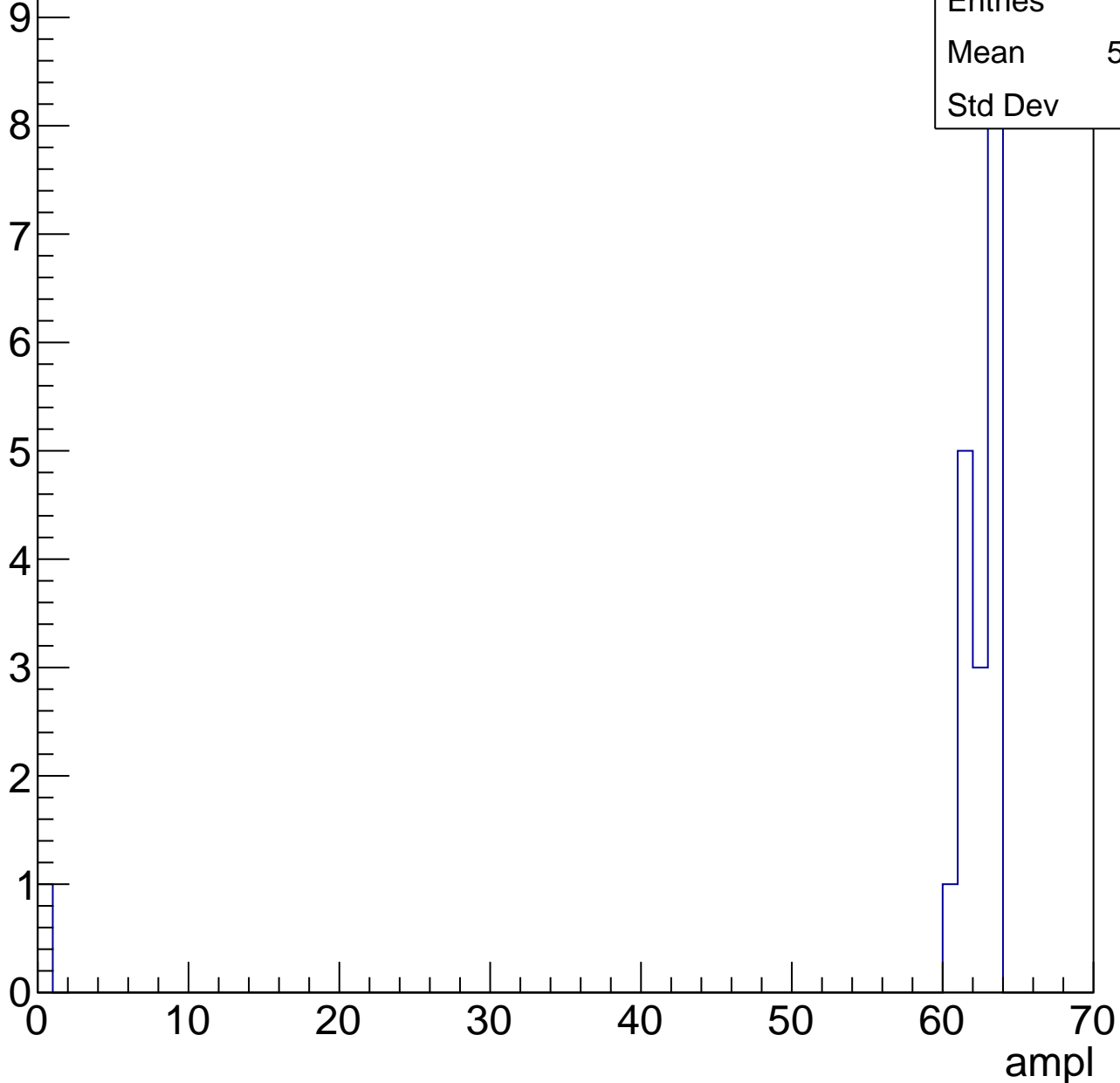
Entries	46
Mean	58.67
Std Dev	2.563



B1L002S, U7-ch16, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

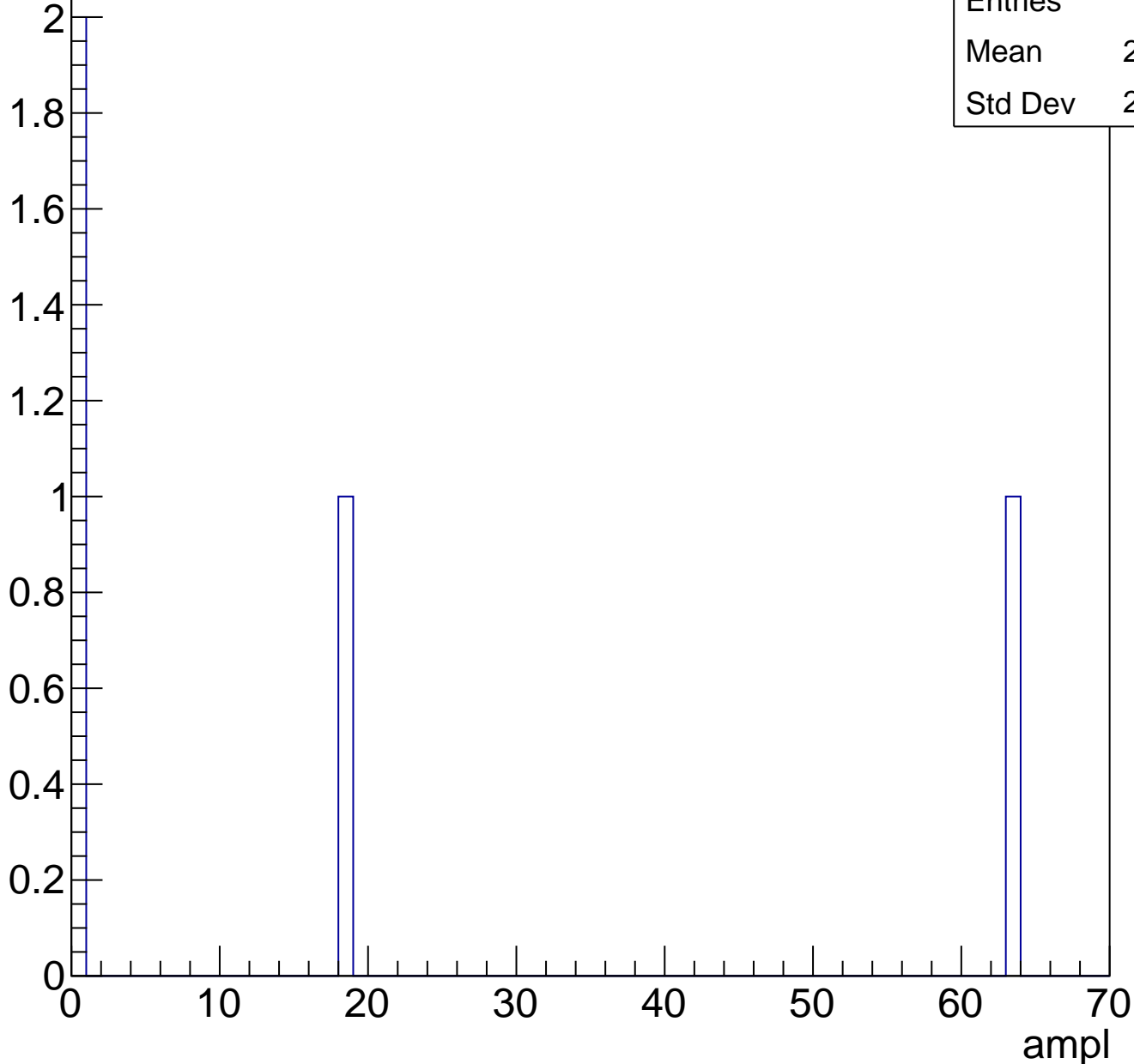


Entries	19
Mean	58.84
Std Dev	13.9

B1L002S, U7-ch16, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.25
Std Dev	25.75

B1L002S, U7-ch17, adc0

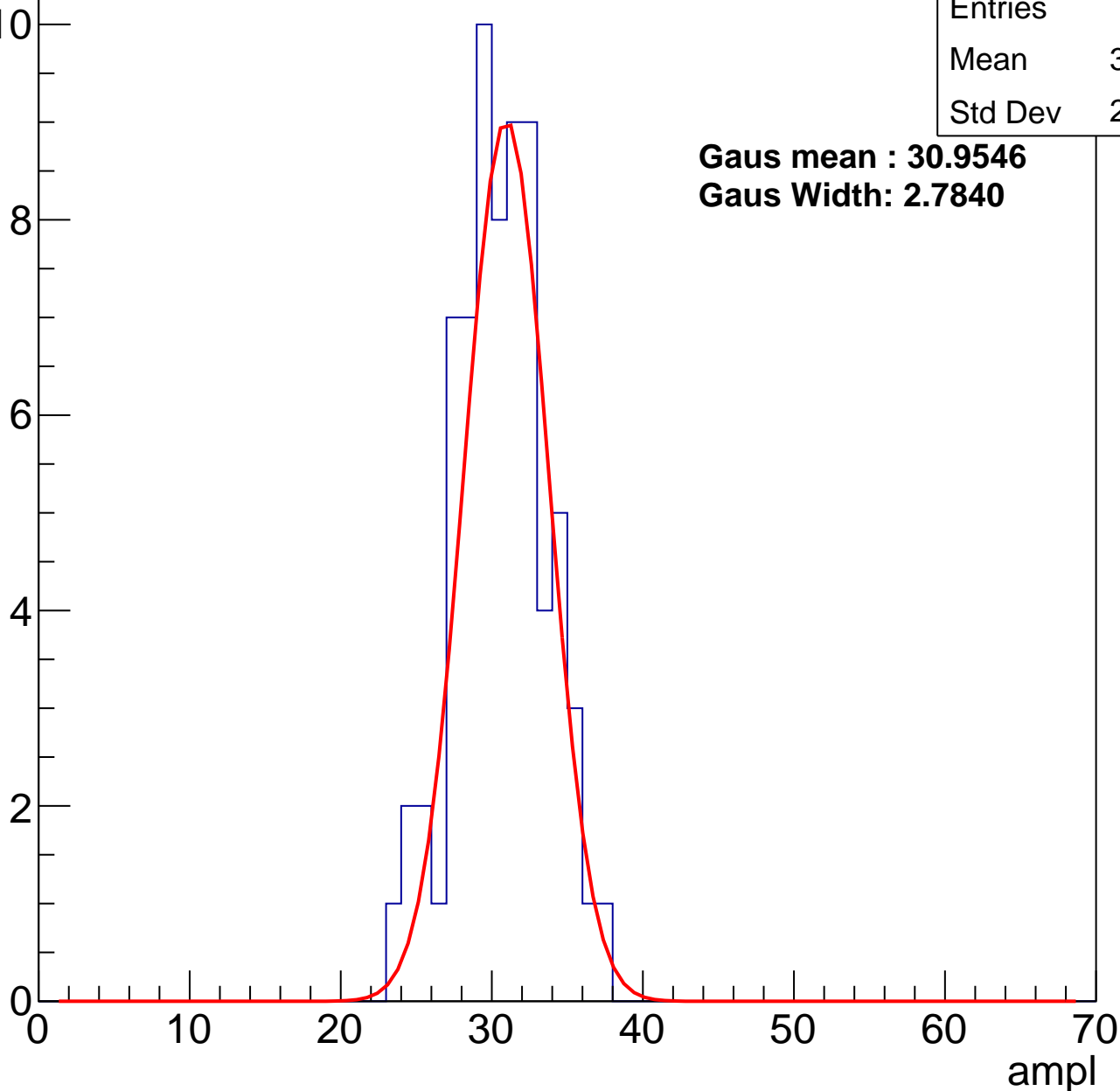
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	30.13
Std Dev	2.947

Gaus mean : 30.9546

Gaus Width: 2.7840



B1L002S, U7-ch17, adc1

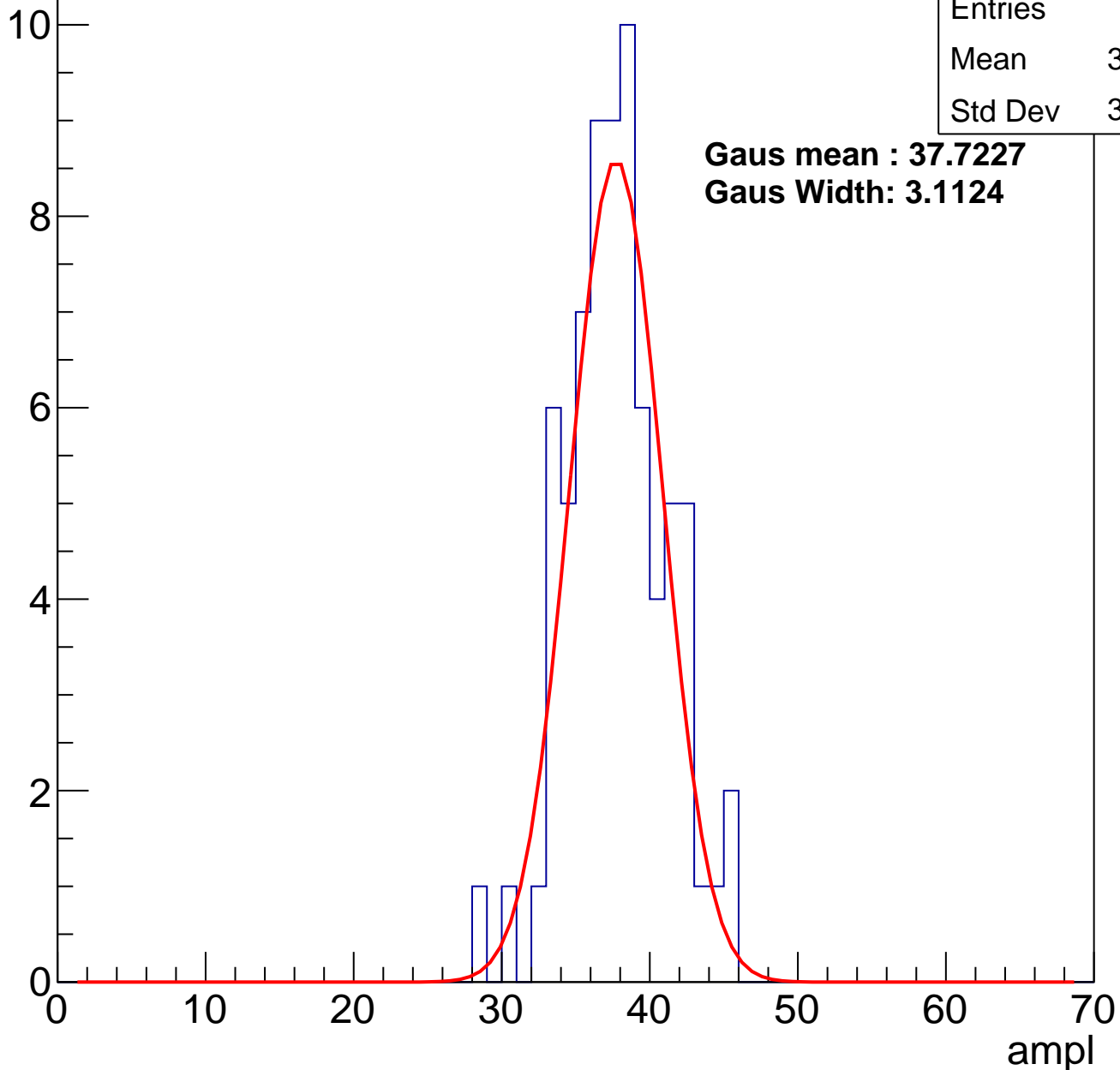
calib_packv5_042523_0143.root, FC#10, port B3

Entries	73
Mean	37.34
Std Dev	3.336

Gaus mean : 37.7227

Gaus Width: 3.1124

Entry



B1L002S, U7-ch17, adc2

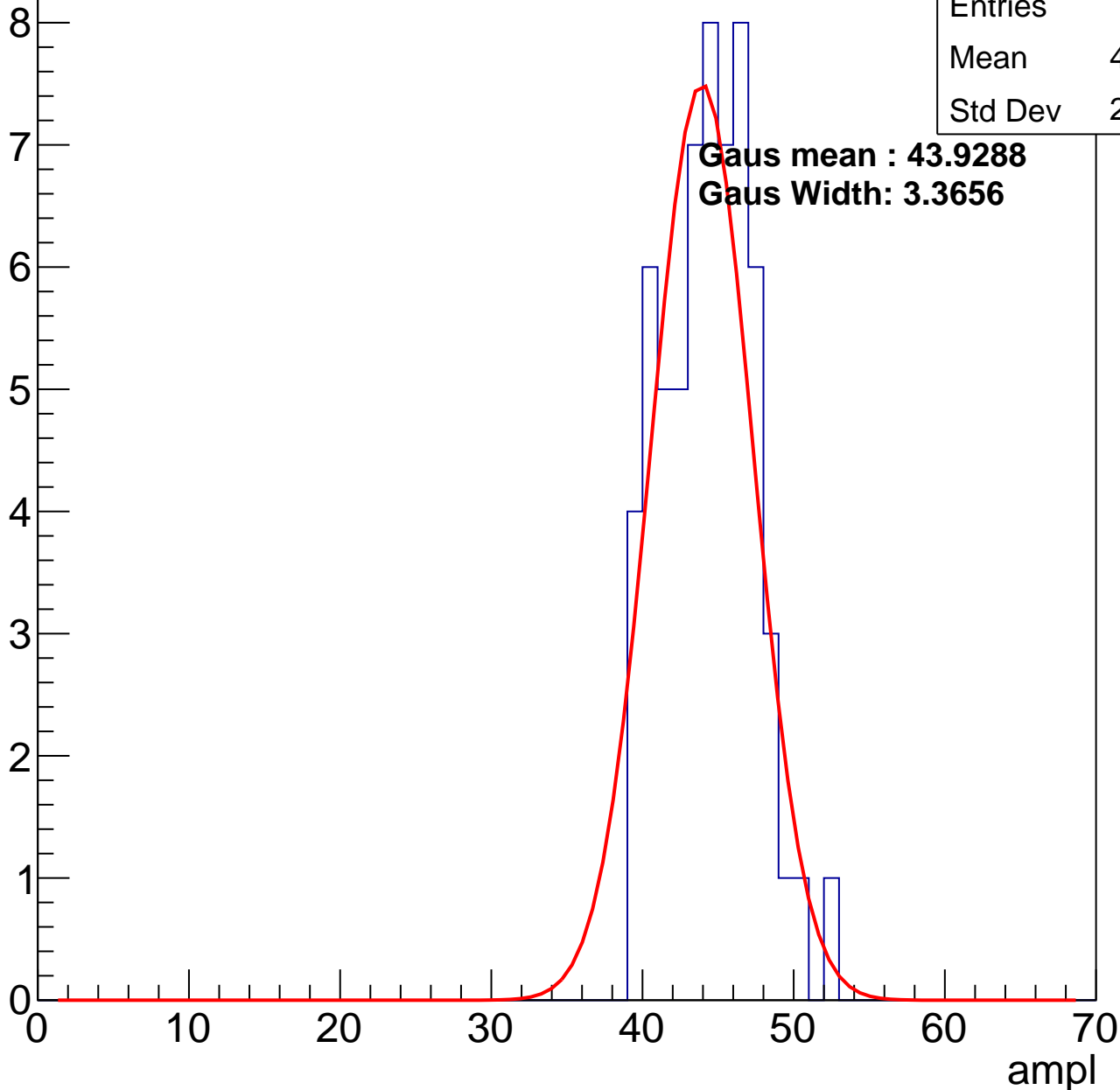
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	43.94
Std Dev	2.934

Gaus mean : 43.9288

Gaus Width: 3.3656

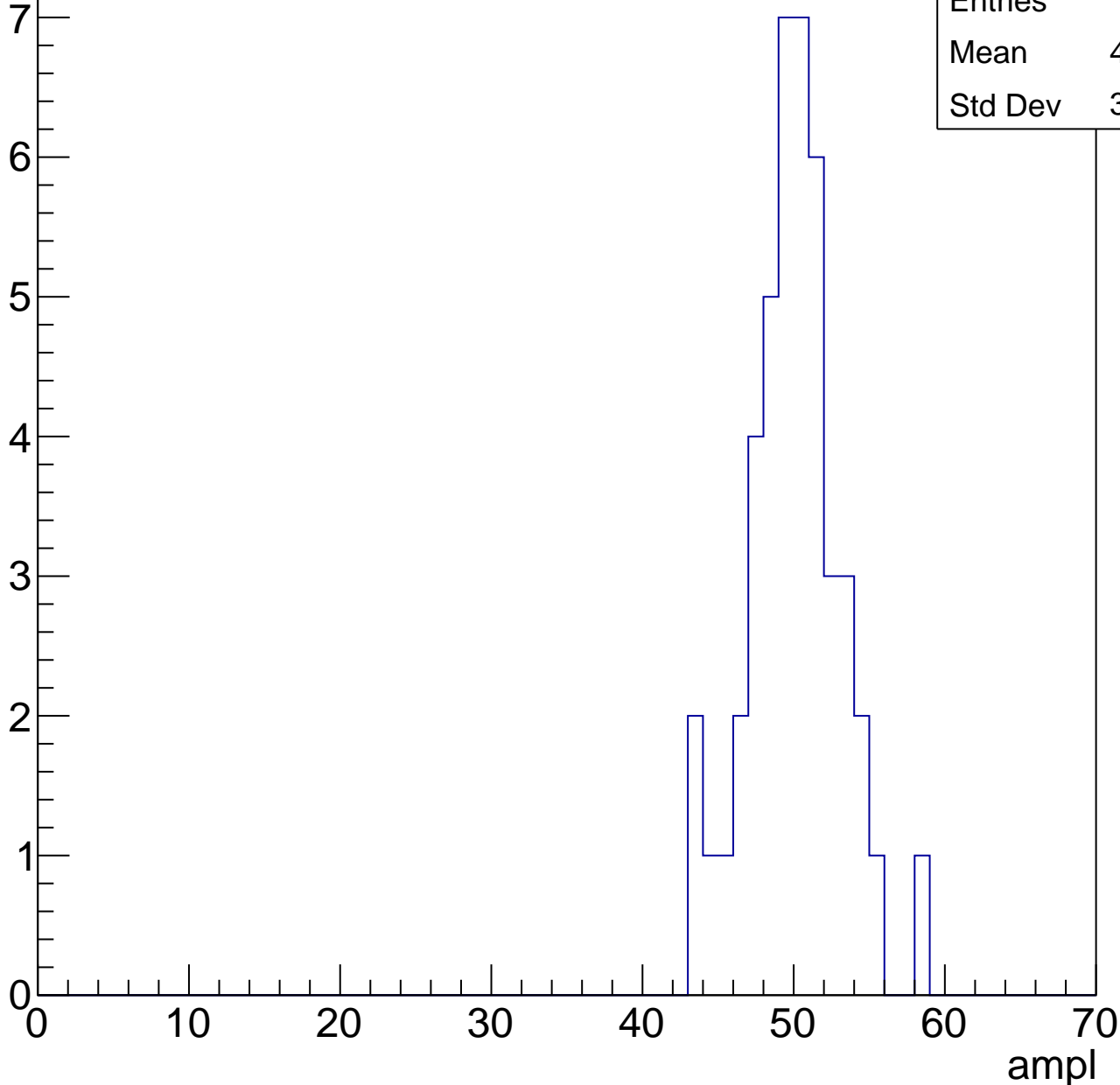


B1L002S, U7-ch17, adc3

calib_packv5_042523_0143.root, FC#10, port B3

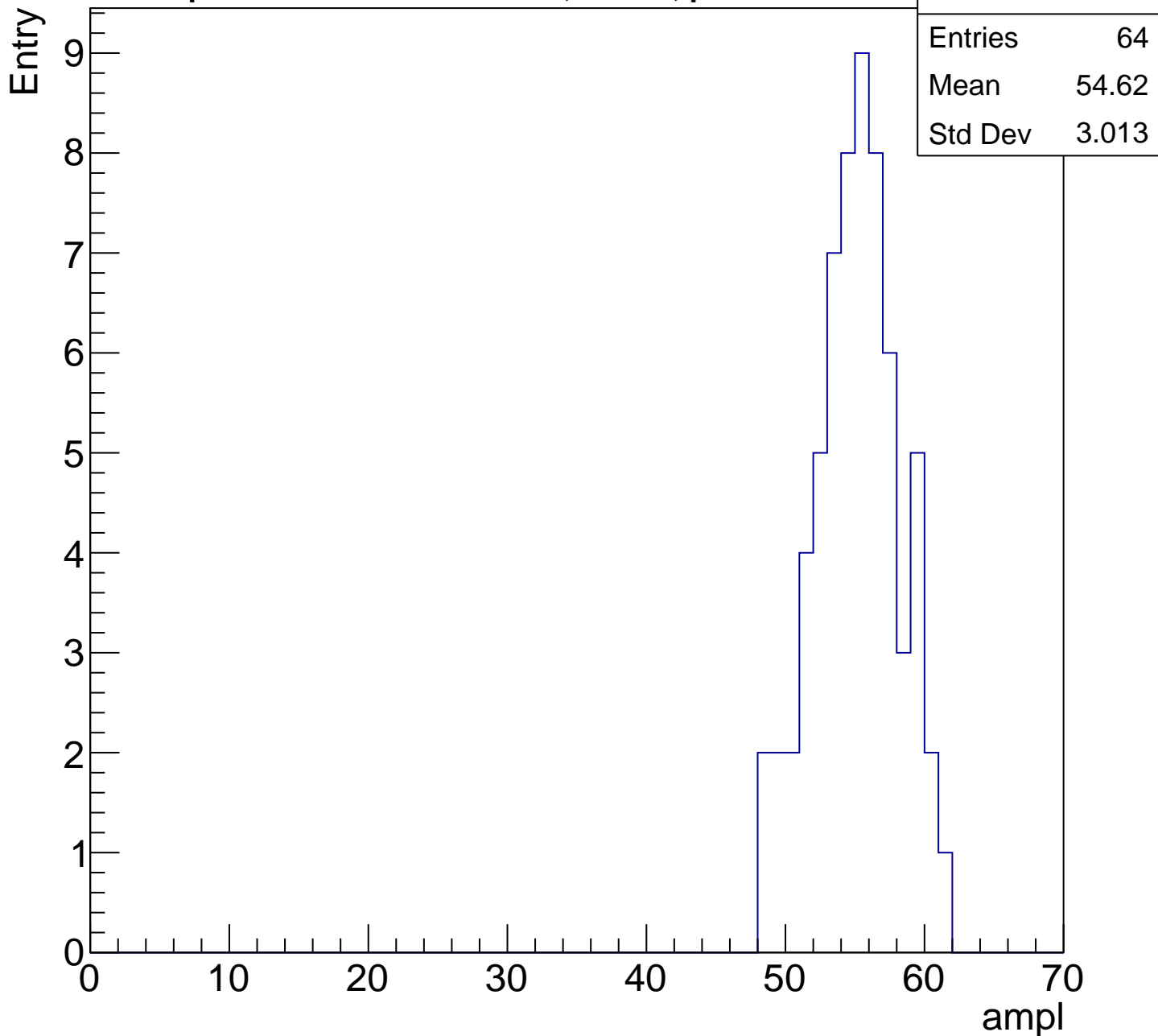
Entry

Entries	45
Mean	49.56
Std Dev	3.023



B1L002S, U7-ch17, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch17, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

10

8

6

4

2

0

0

10

20

30

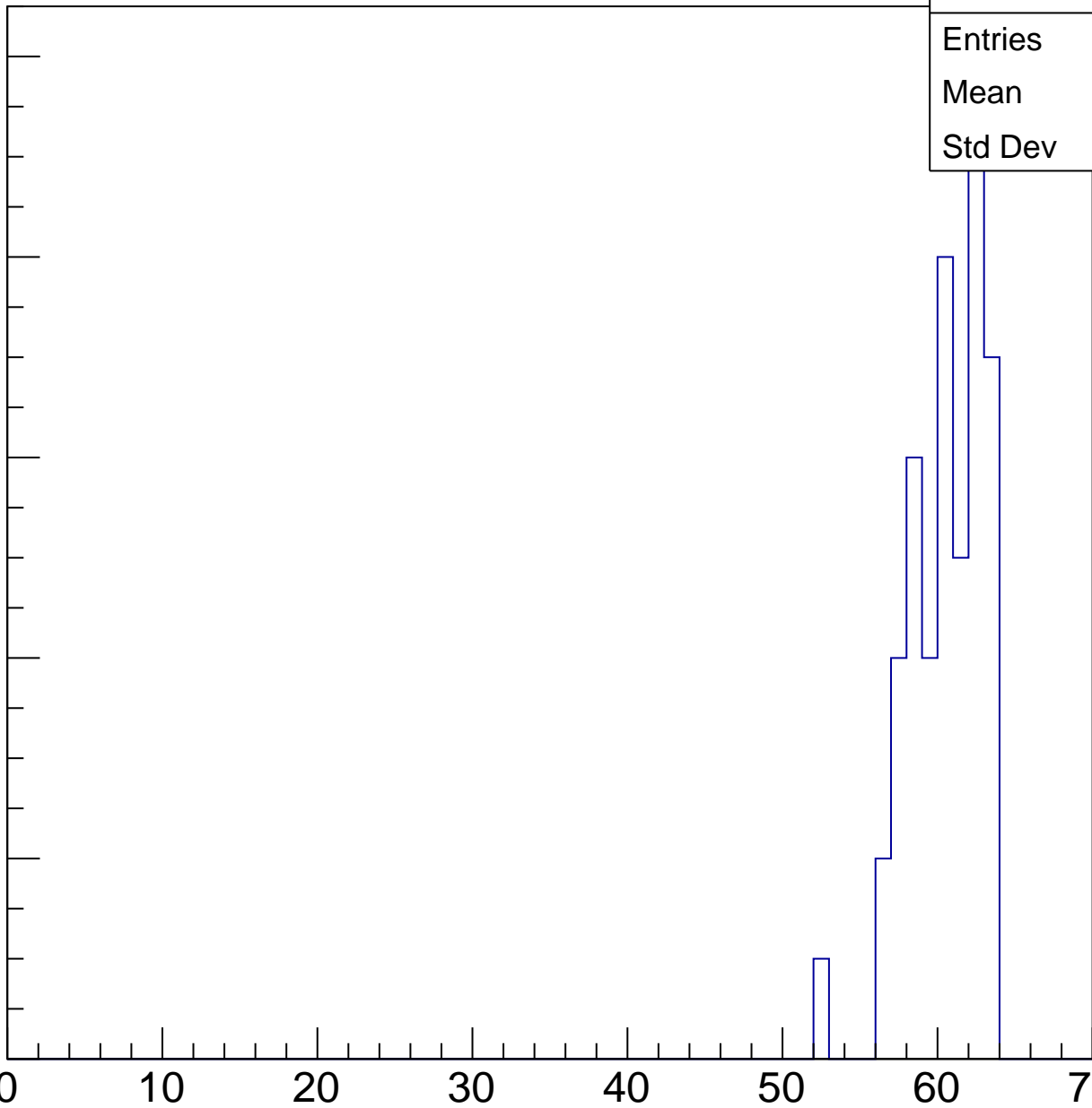
40

50

60

ampl

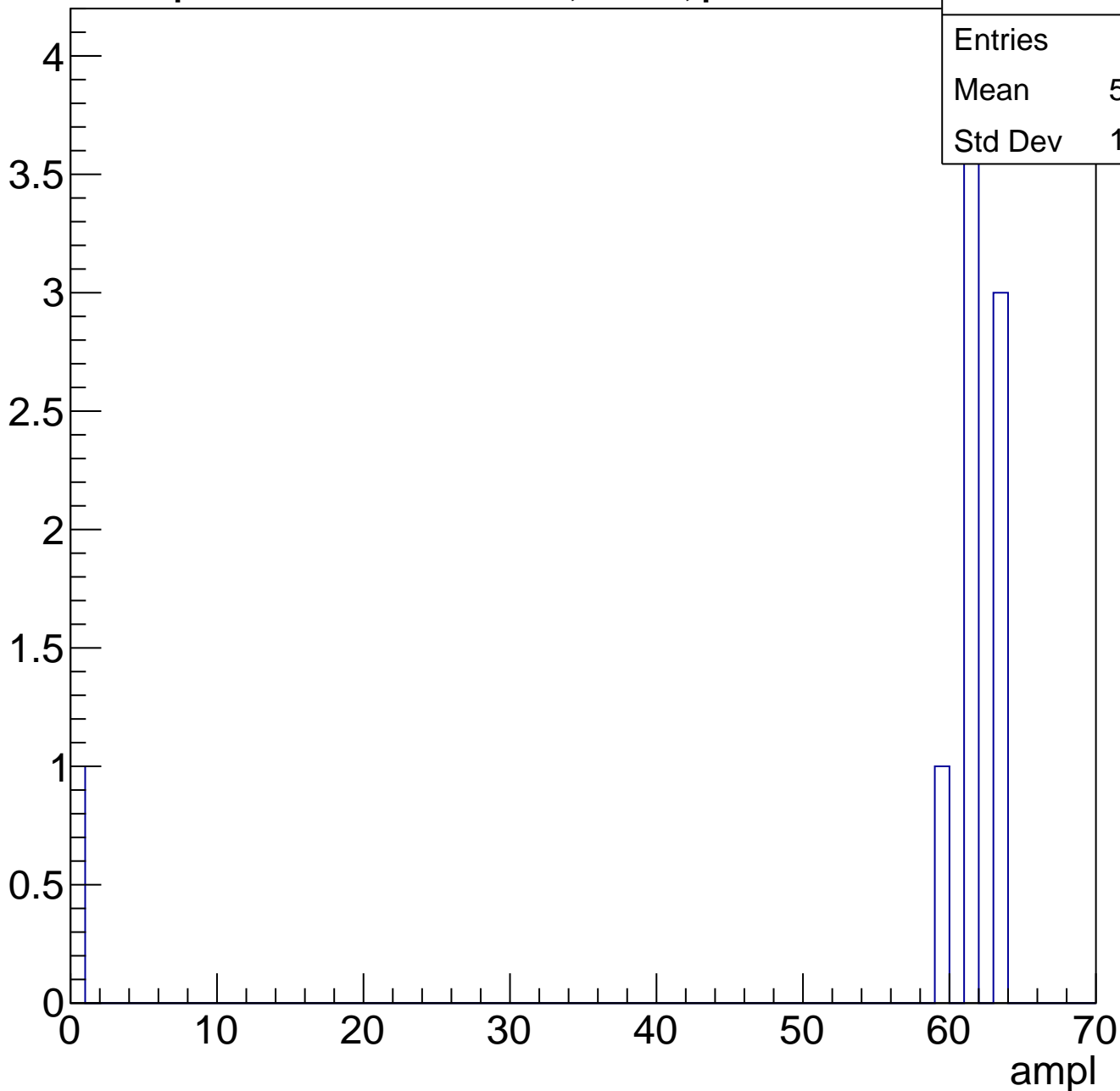
Entries	47
Mean	60.04
Std Dev	2.388



B1L002S, U7-ch17, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch17, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch18, adc0

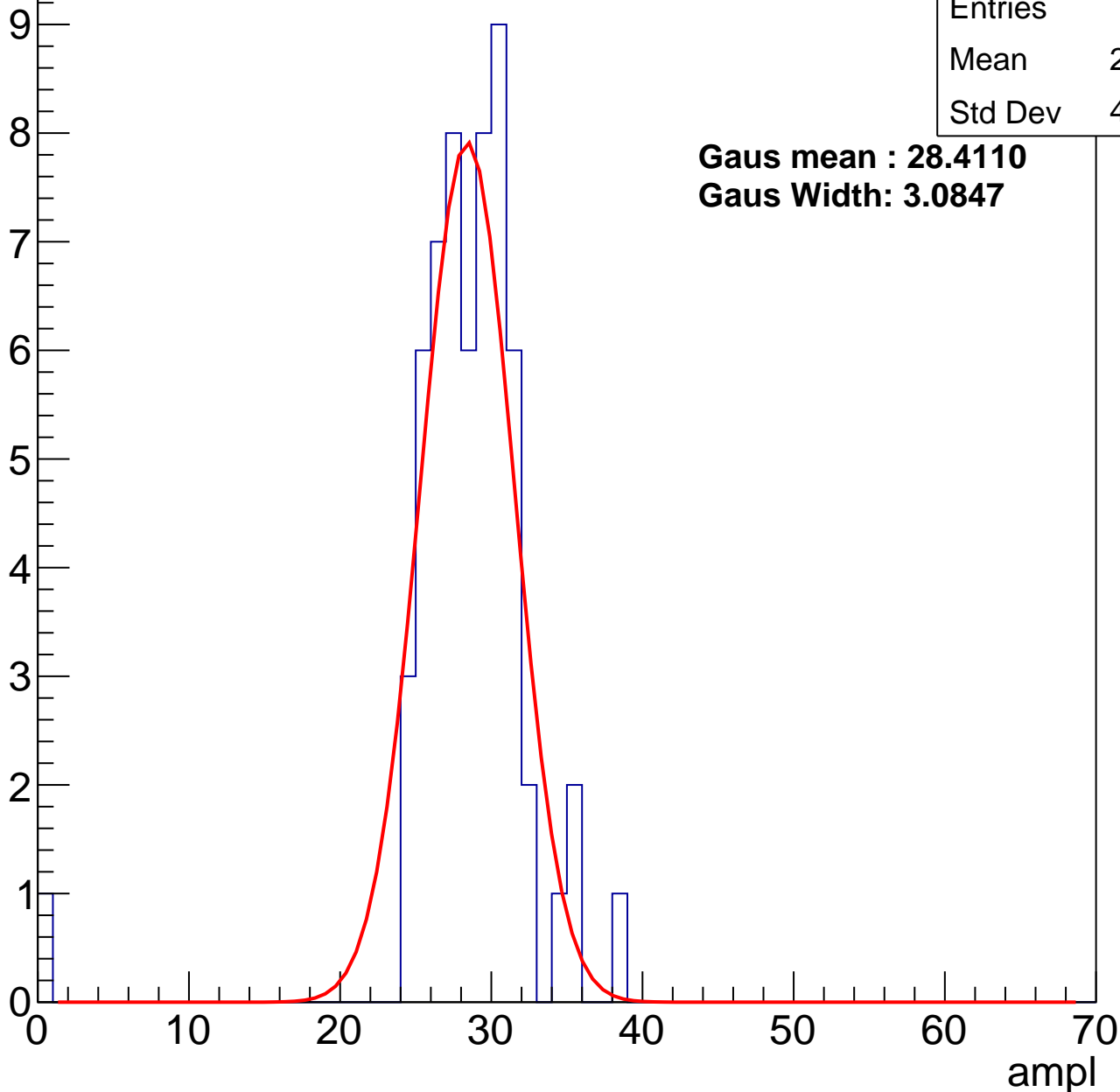
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.03
Std Dev	4.629

Gaus mean : 28.4110

Gaus Width: 3.0847



B1L002S, U7-ch18, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	73
Mean	35.22
Std Dev	3.026

Gaus mean : 35.6652

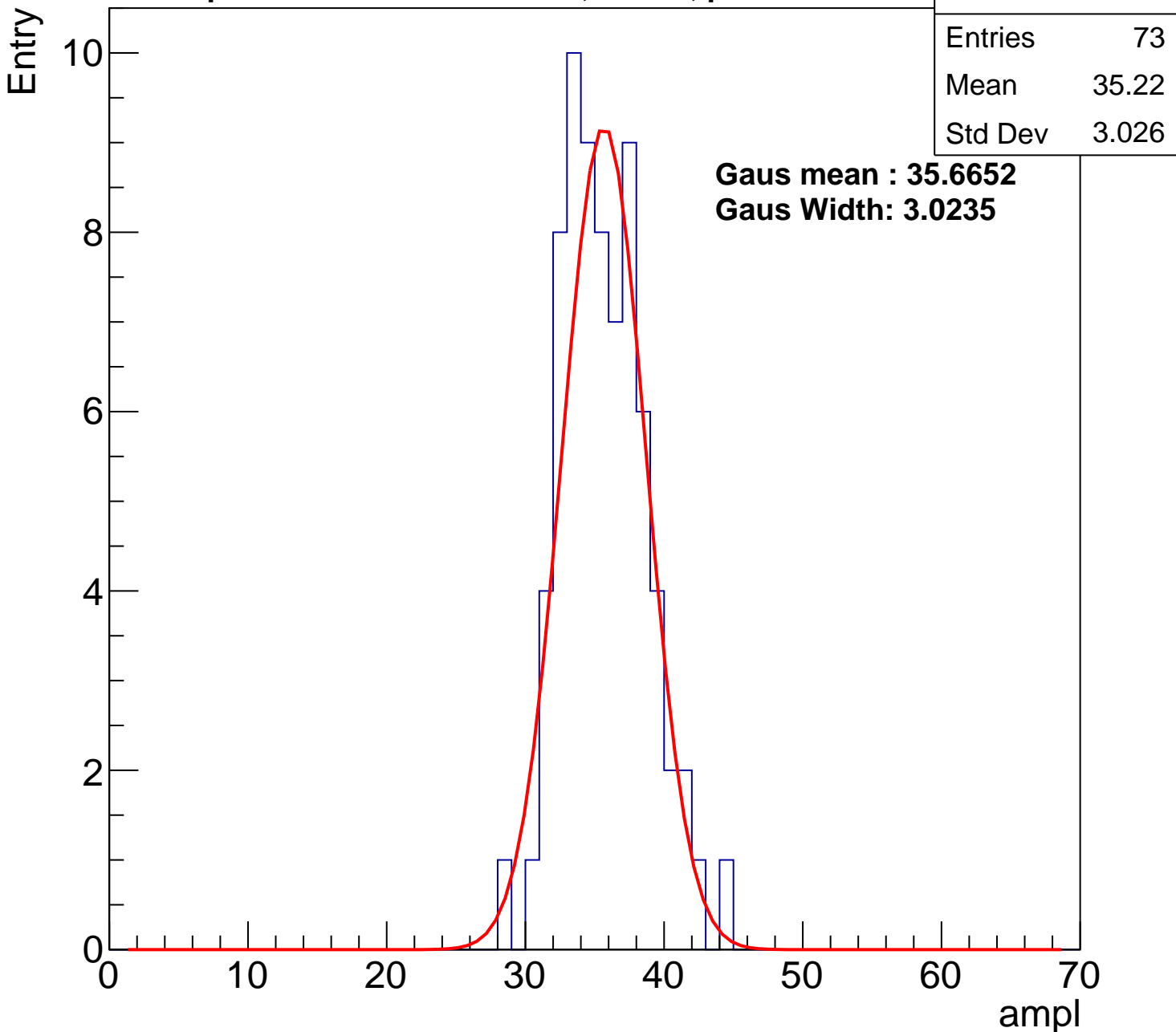
Gaus Width: 3.0235

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch18, adc2

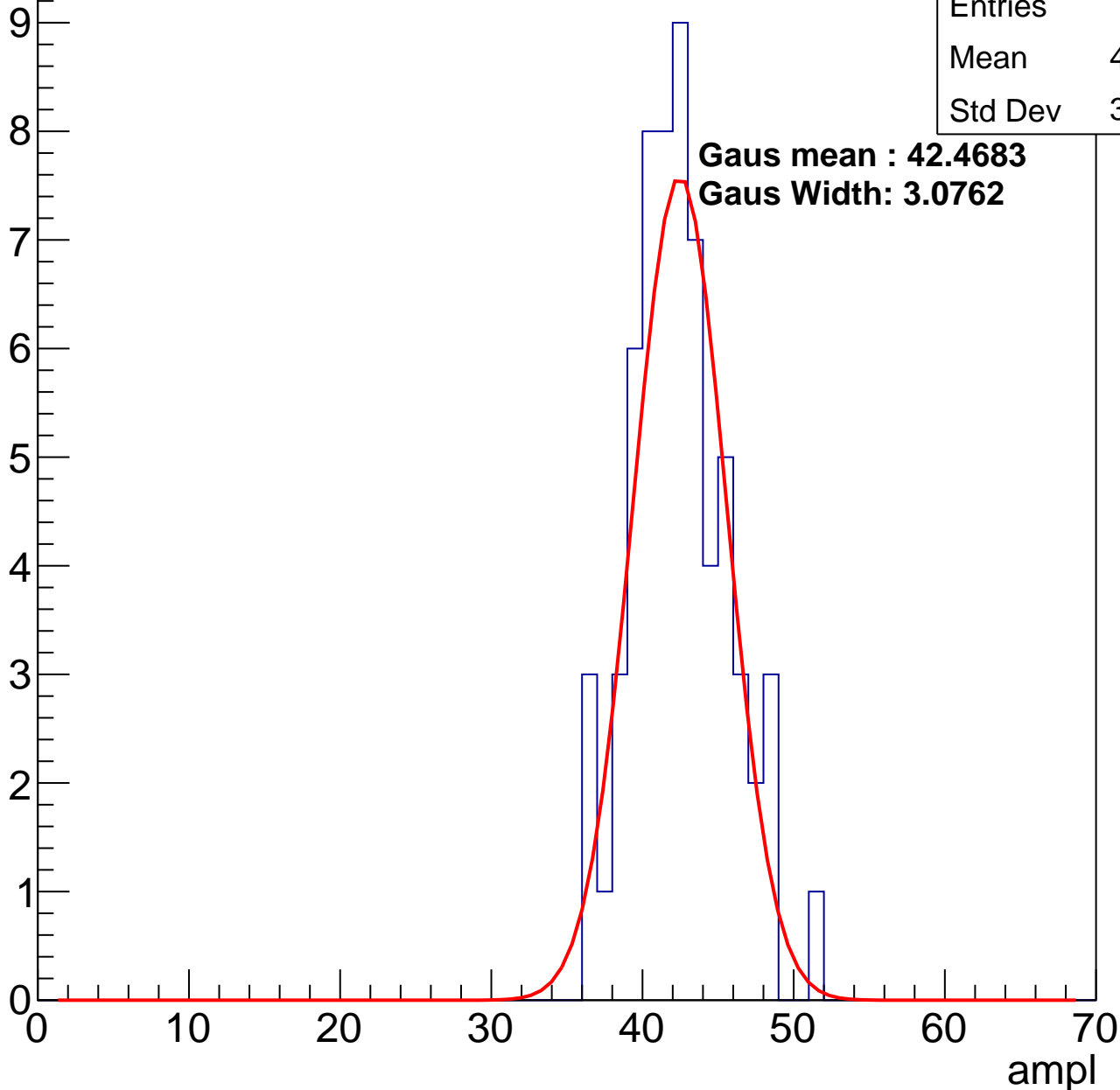
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	42.03
Std Dev	3.162

Gaus mean : 42.4683

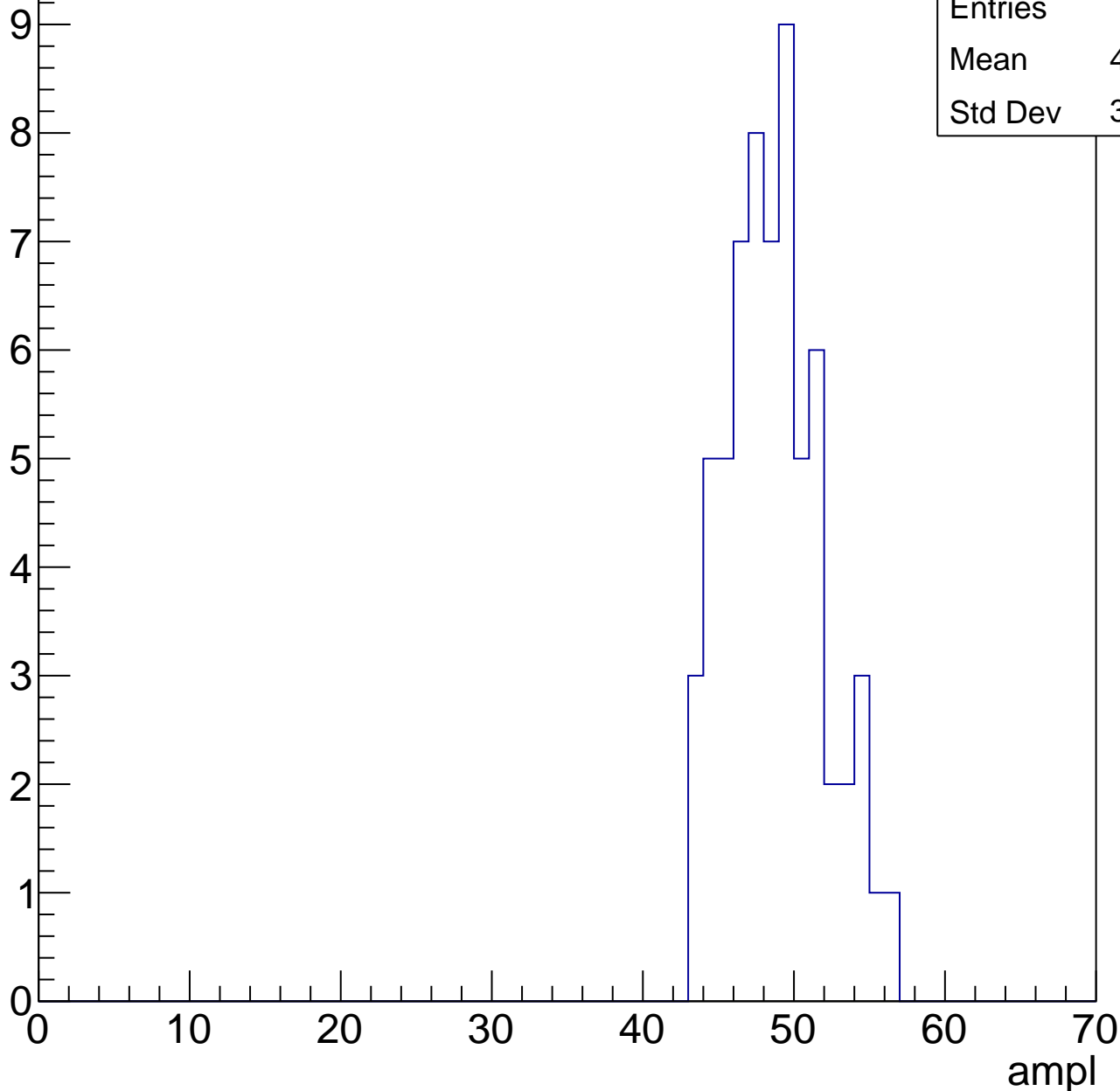
Gaus Width: 3.0762



B1L002S, U7-ch18, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



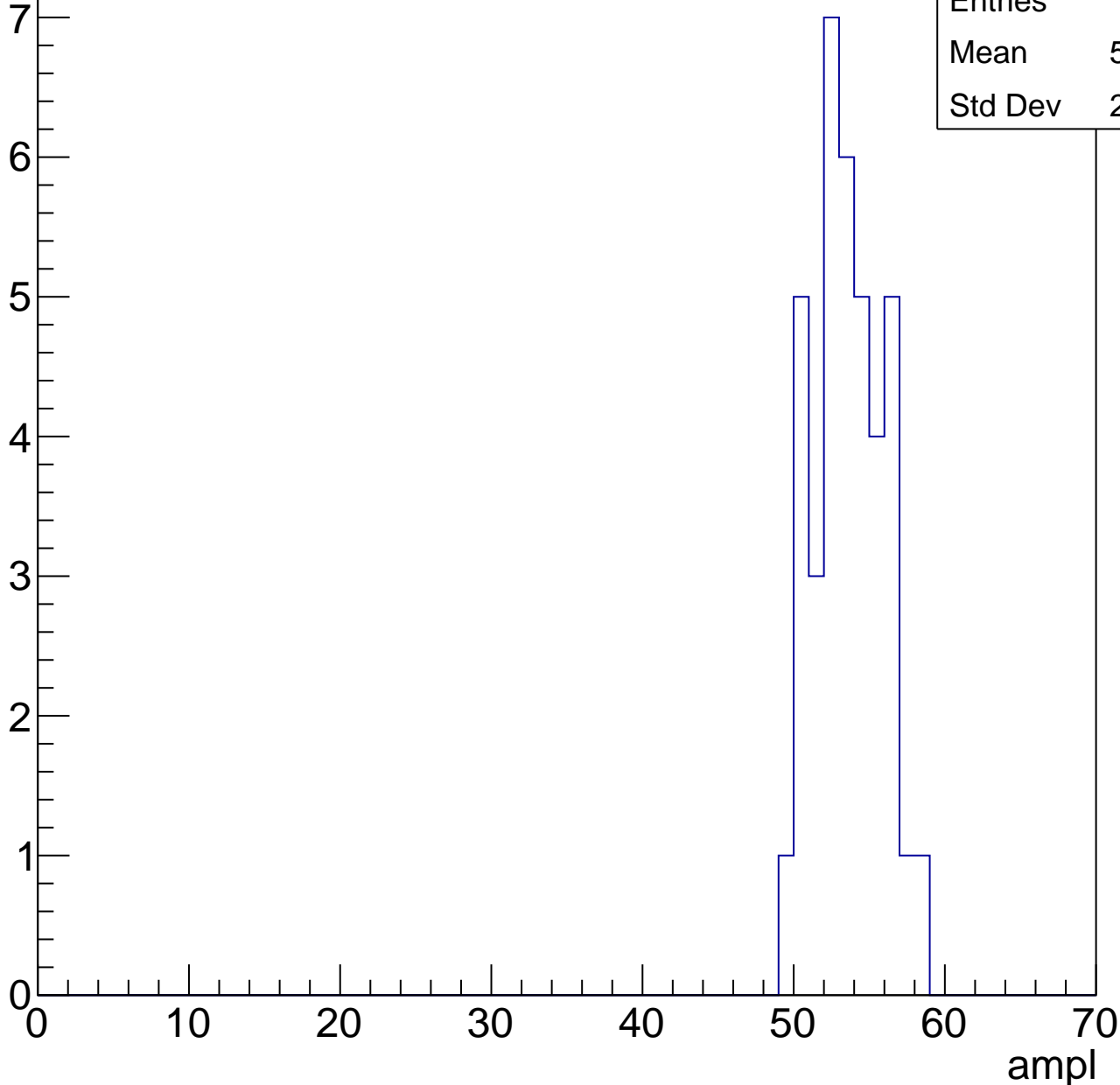
Entries	64
Mean	48.25
Std Dev	3.107

B1L002S, U7-ch18, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

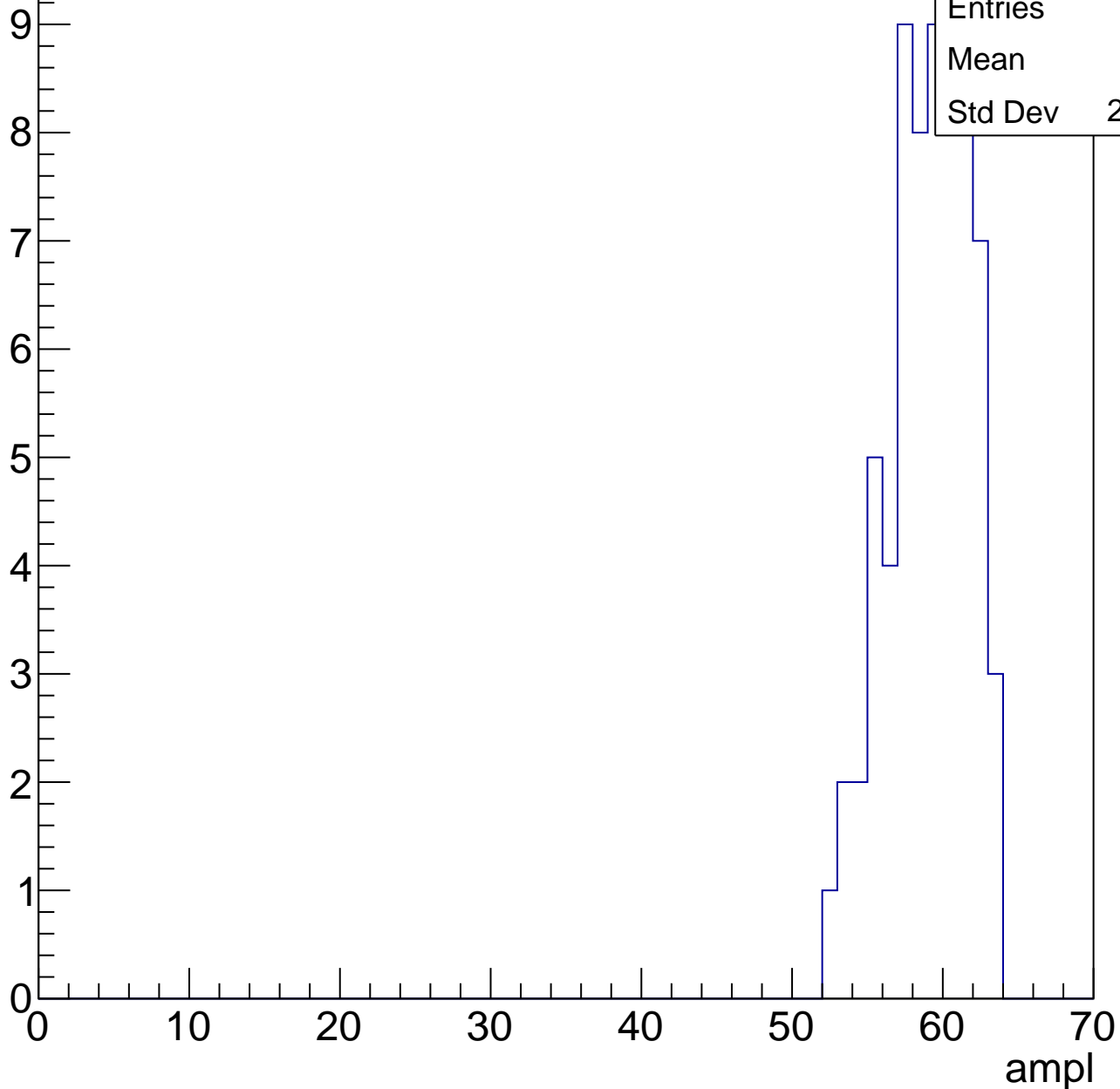
Entries	38
Mean	53.13
Std Dev	2.214



B1L002S, U7-ch18, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



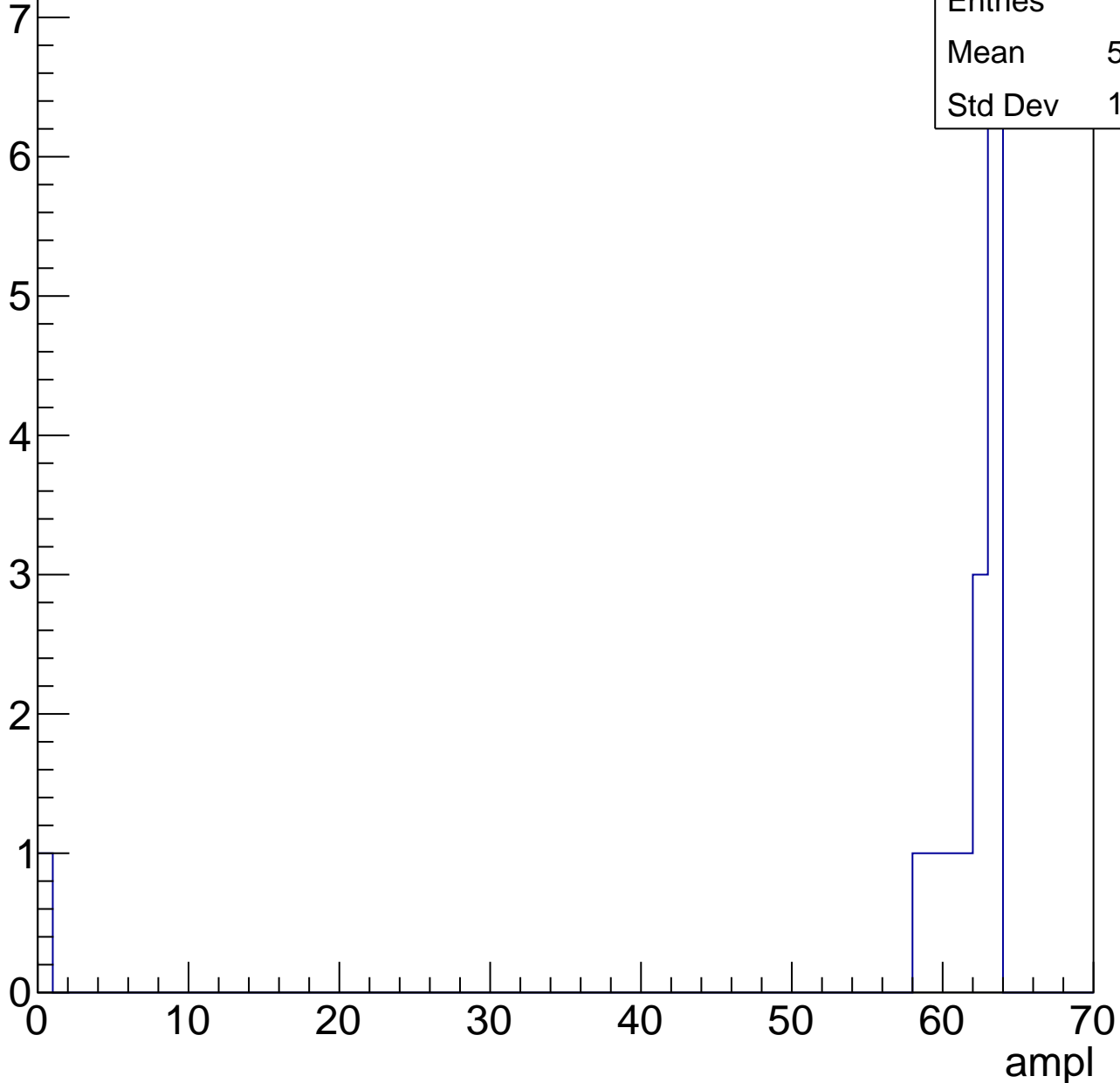
Entries	68
Mean	58.6
Std Dev	2.647

B1L002S, U7-ch18, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

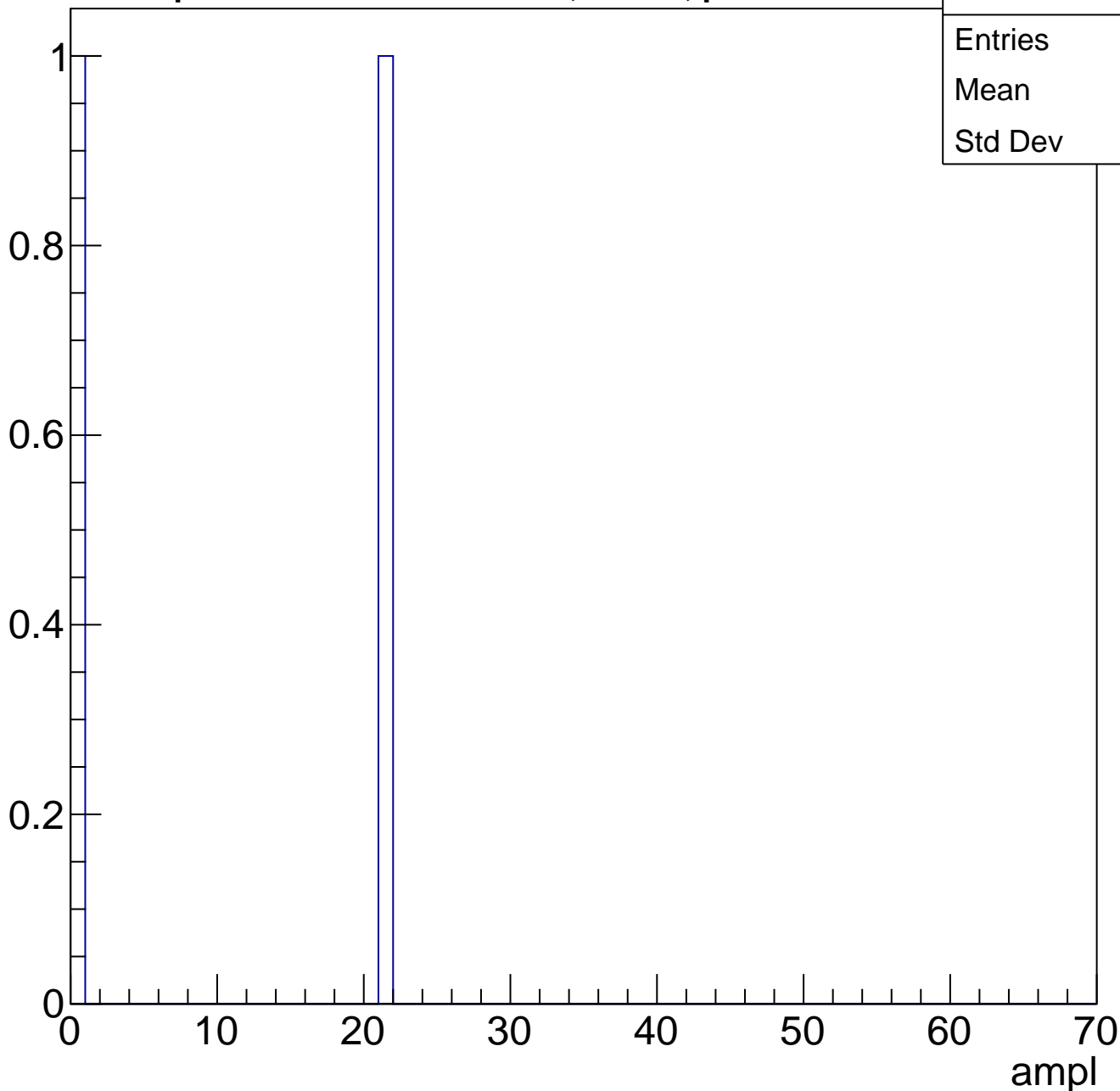
Entries	15
Mean	57.67
Std Dev	15.49



B1L002S, U7-ch18, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L002S, U7-ch19, adc0

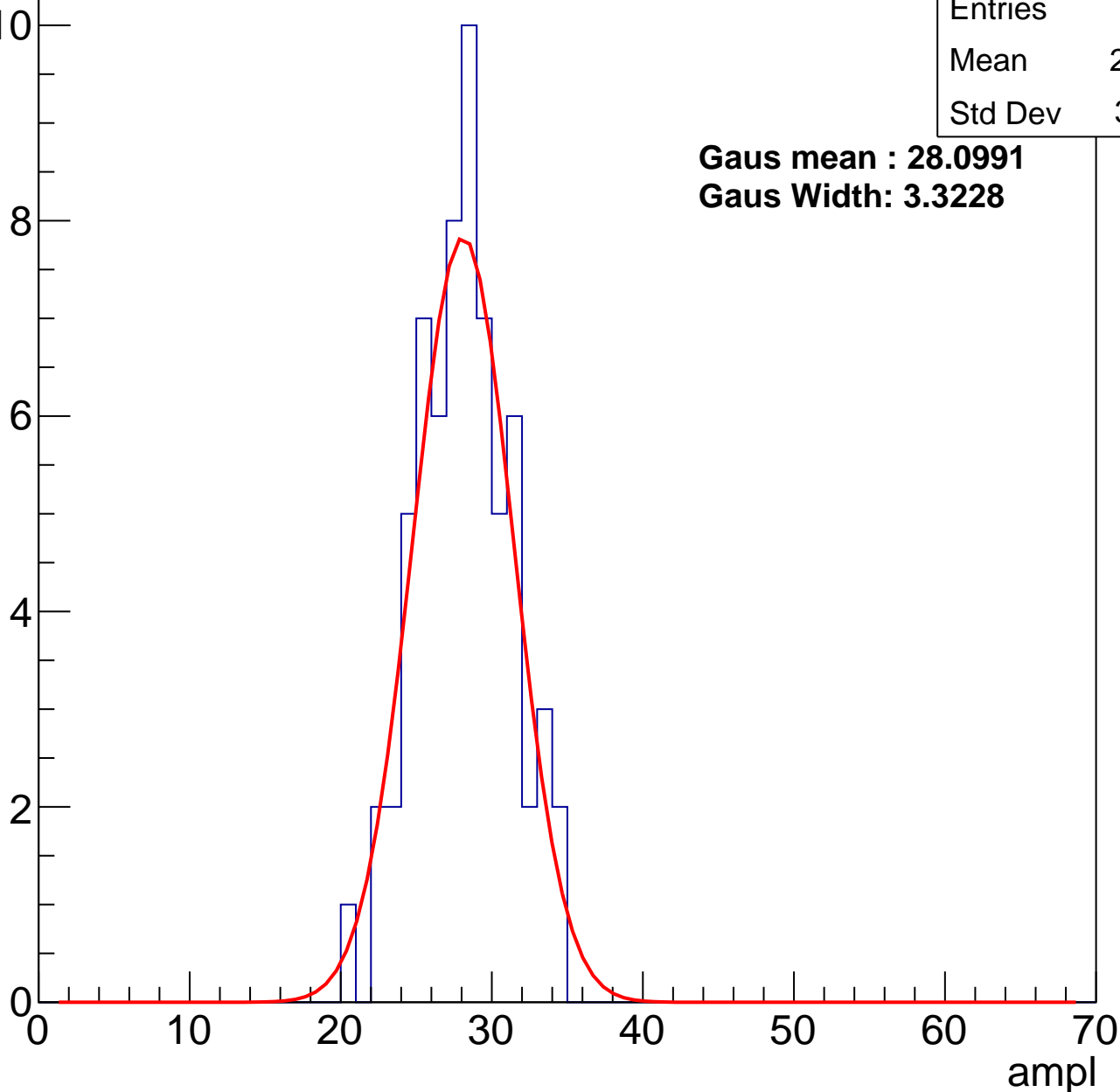
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.68
Std Dev	3.051

Gaus mean : 28.0991

Gaus Width: 3.3228



B1L002S, U7-ch19, adc1

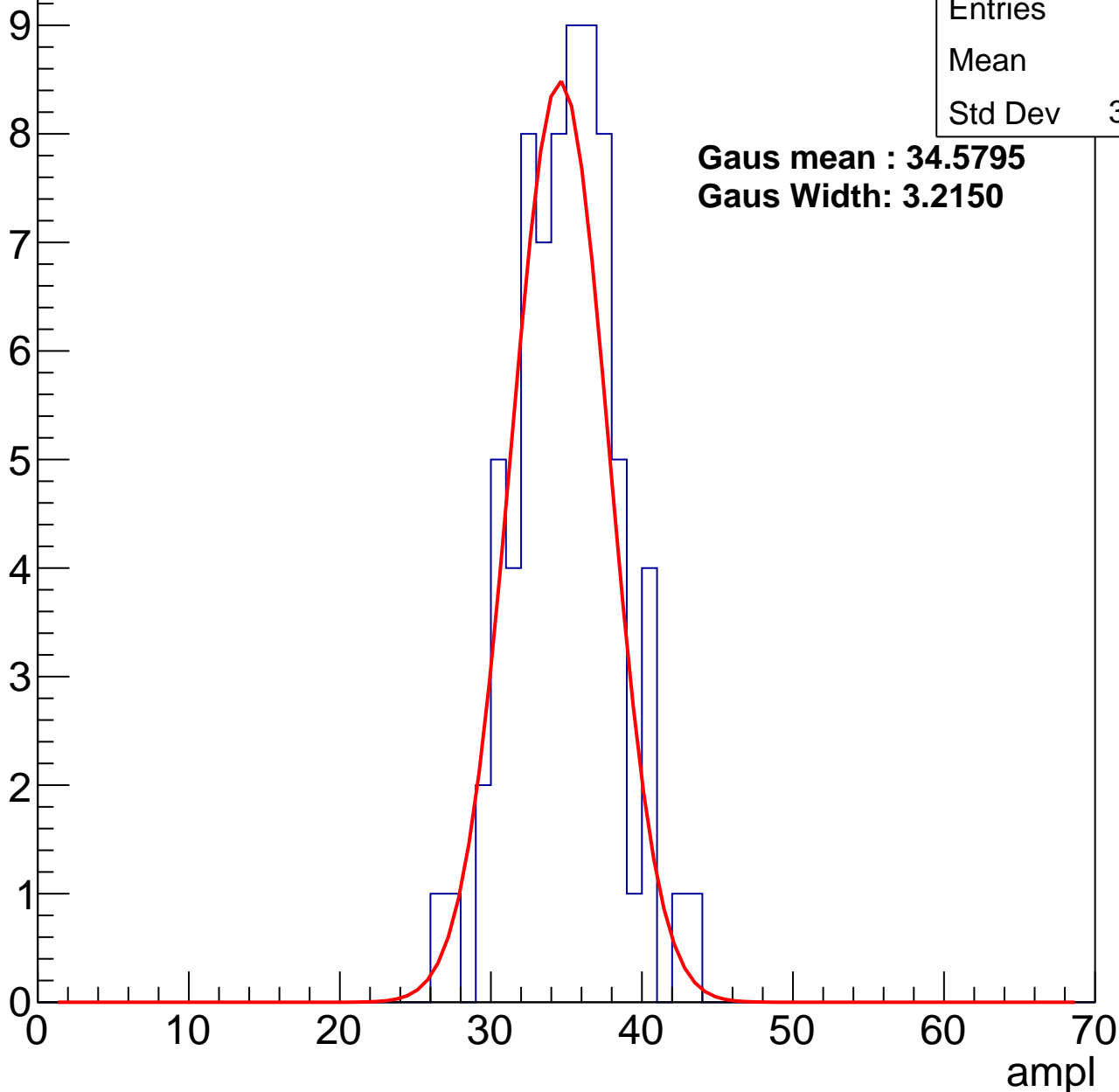
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	34.5
Std Dev	3.309

Gaus mean : 34.5795

Gaus Width: 3.2150



B1L002S, U7-ch19, adc2

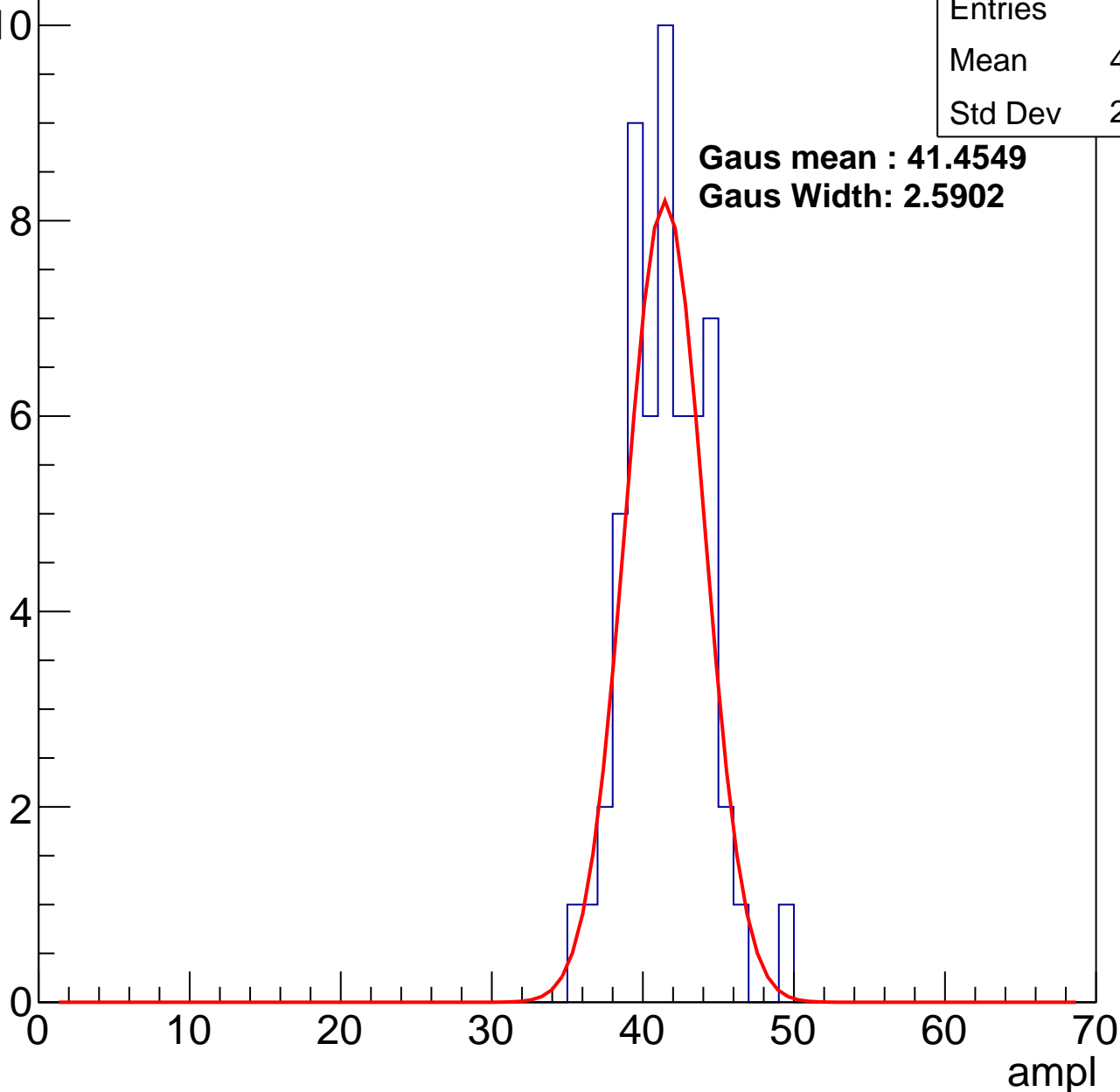
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.04
Std Dev	2.629

Gaus mean : 41.4549

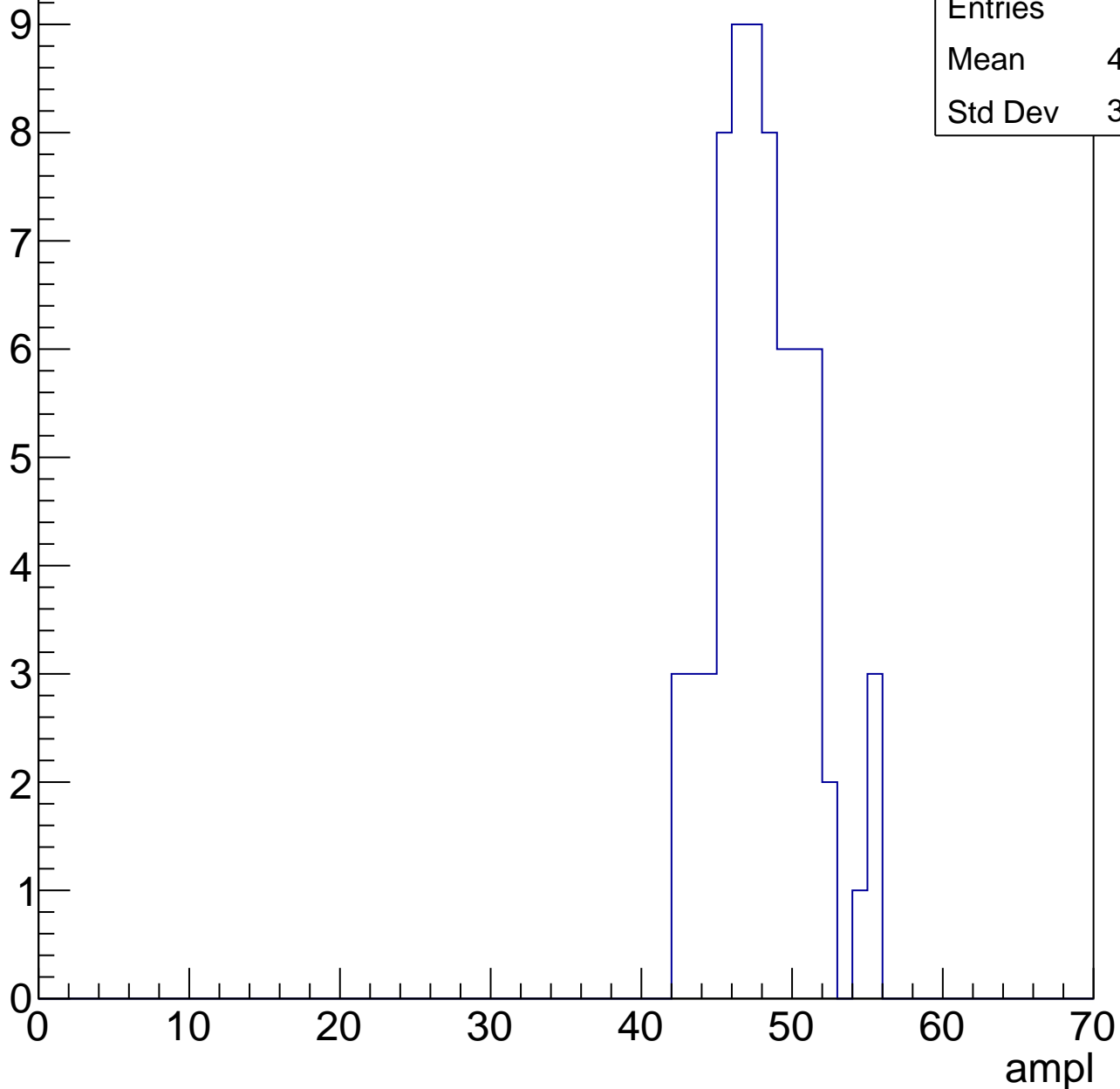
Gaus Width: 2.5902



B1L002S, U7-ch19, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	67
Mean	47.63
Std Dev	3.085

B1L002S, U7-ch19, adc4

calib_packv5_042523_0143.root, FC#10, port B3

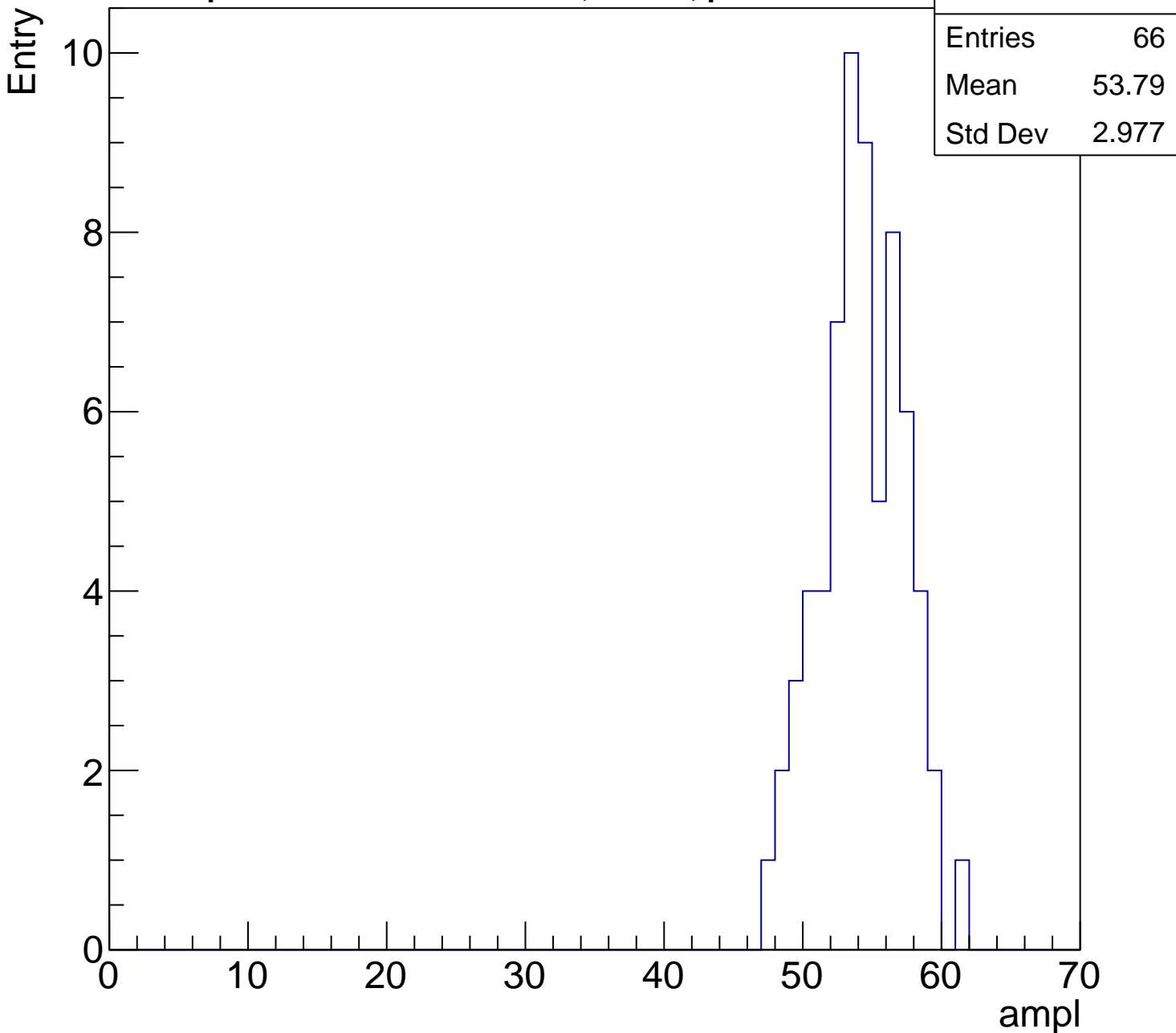
Entries	66
Mean	53.79
Std Dev	2.977

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

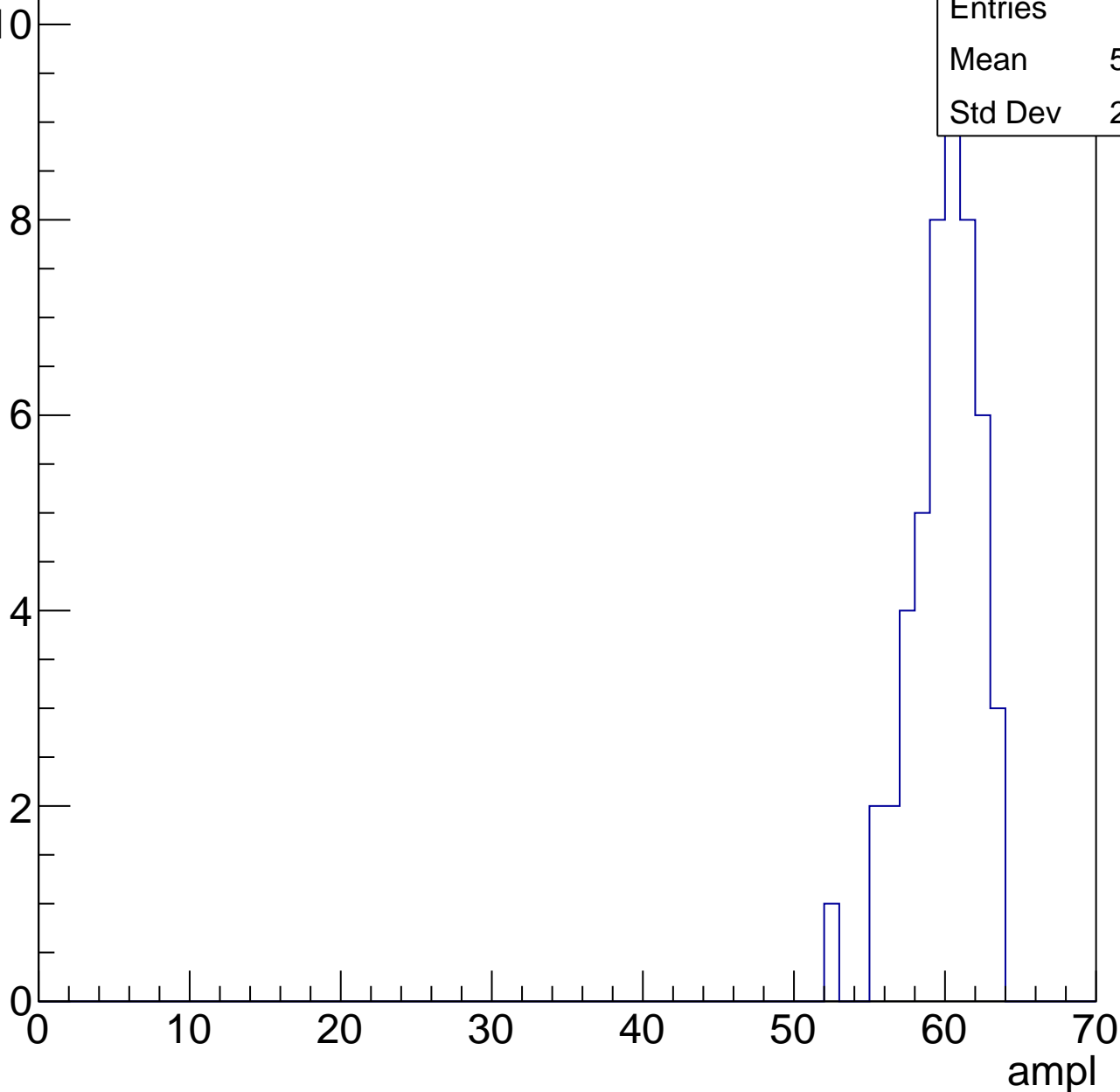


B1L002S, U7-ch19, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	59.45
Std Dev	2.277

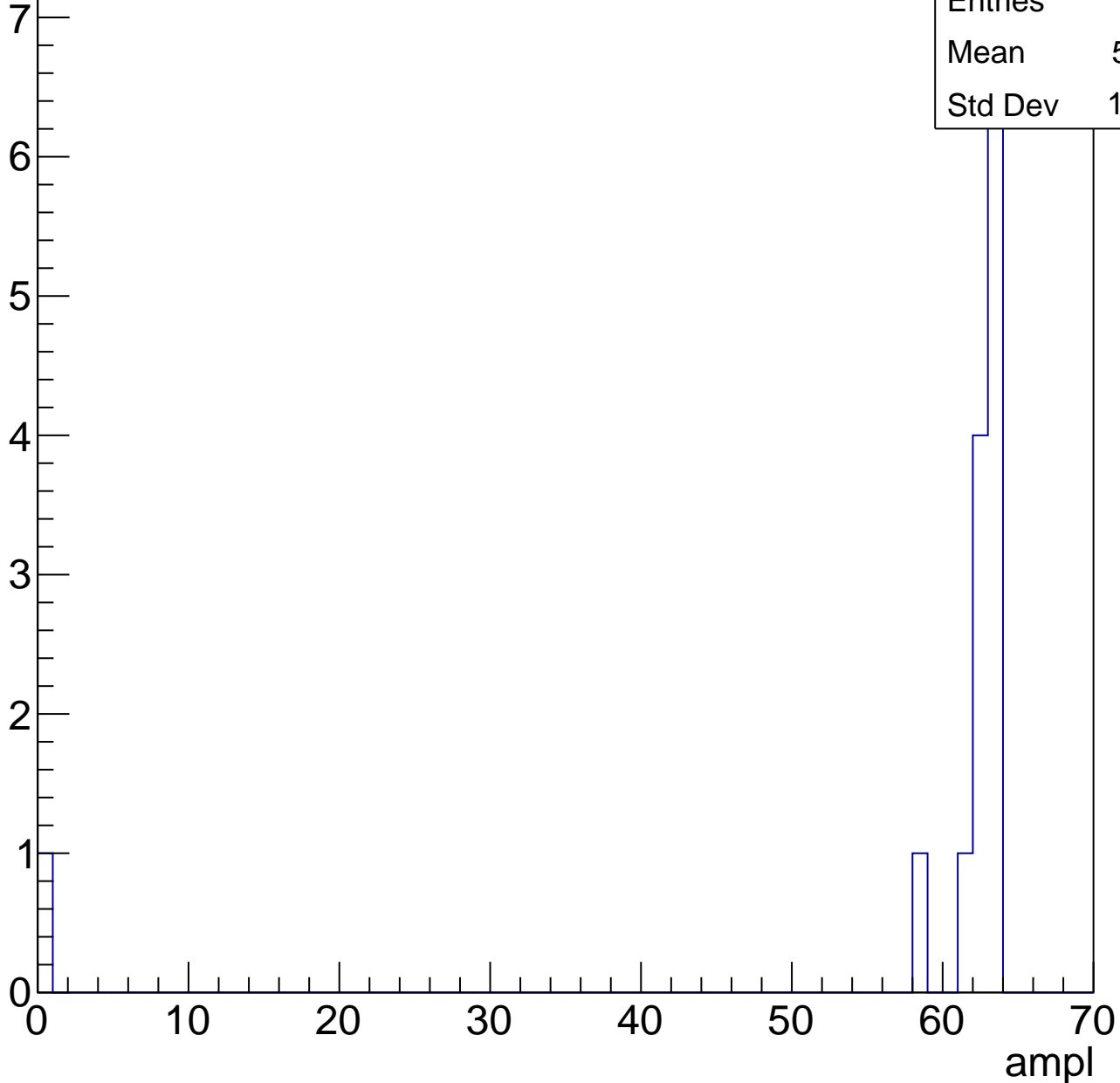


B1L002S, U7-ch19, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.71
Std Dev	16.06



B1L002S, U7-ch19, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch20, adc0

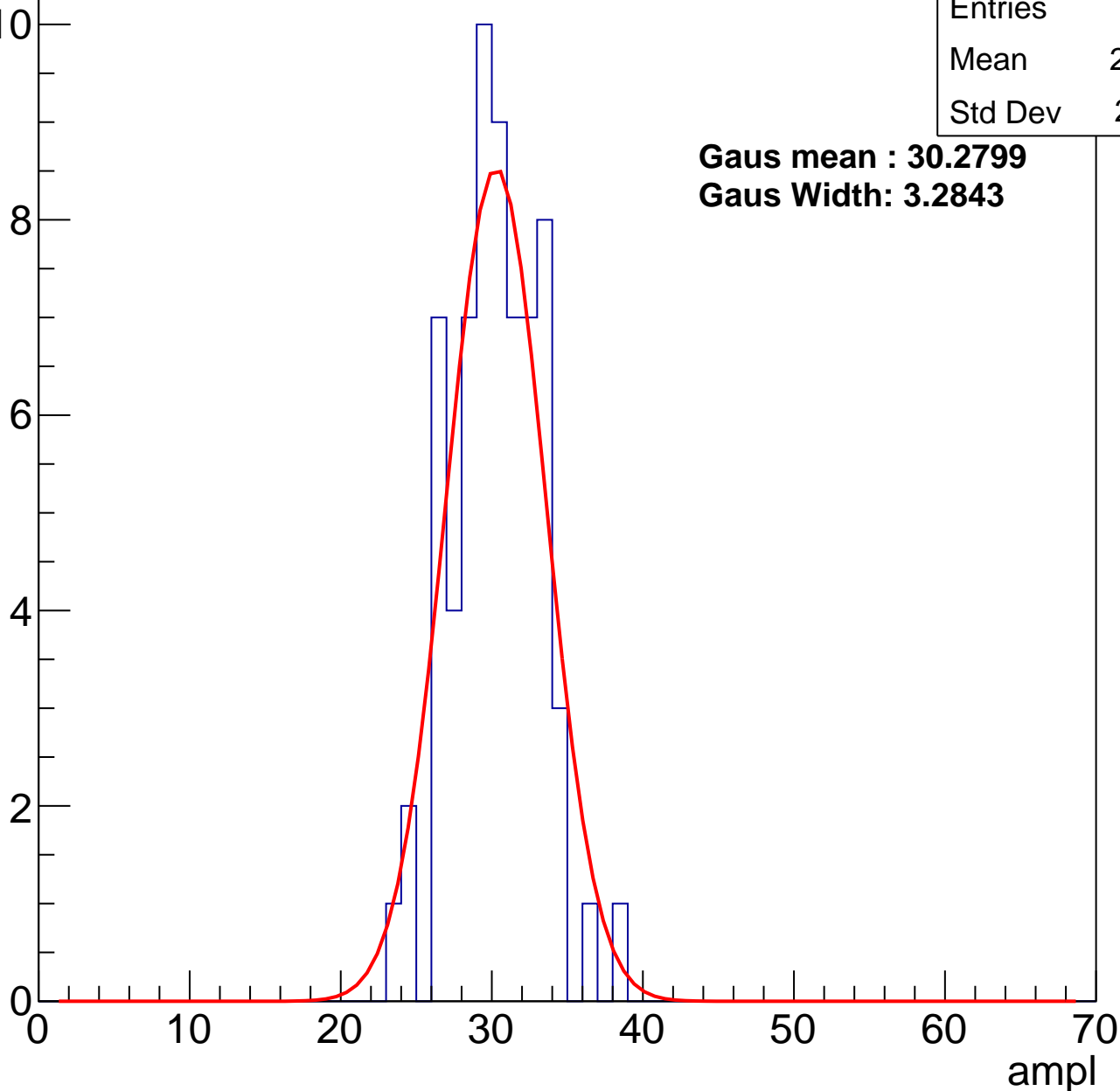
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.82
Std Dev	2.891

Gaus mean : 30.2799

Gaus Width: 3.2843



B1L002S, U7-ch20, adc1

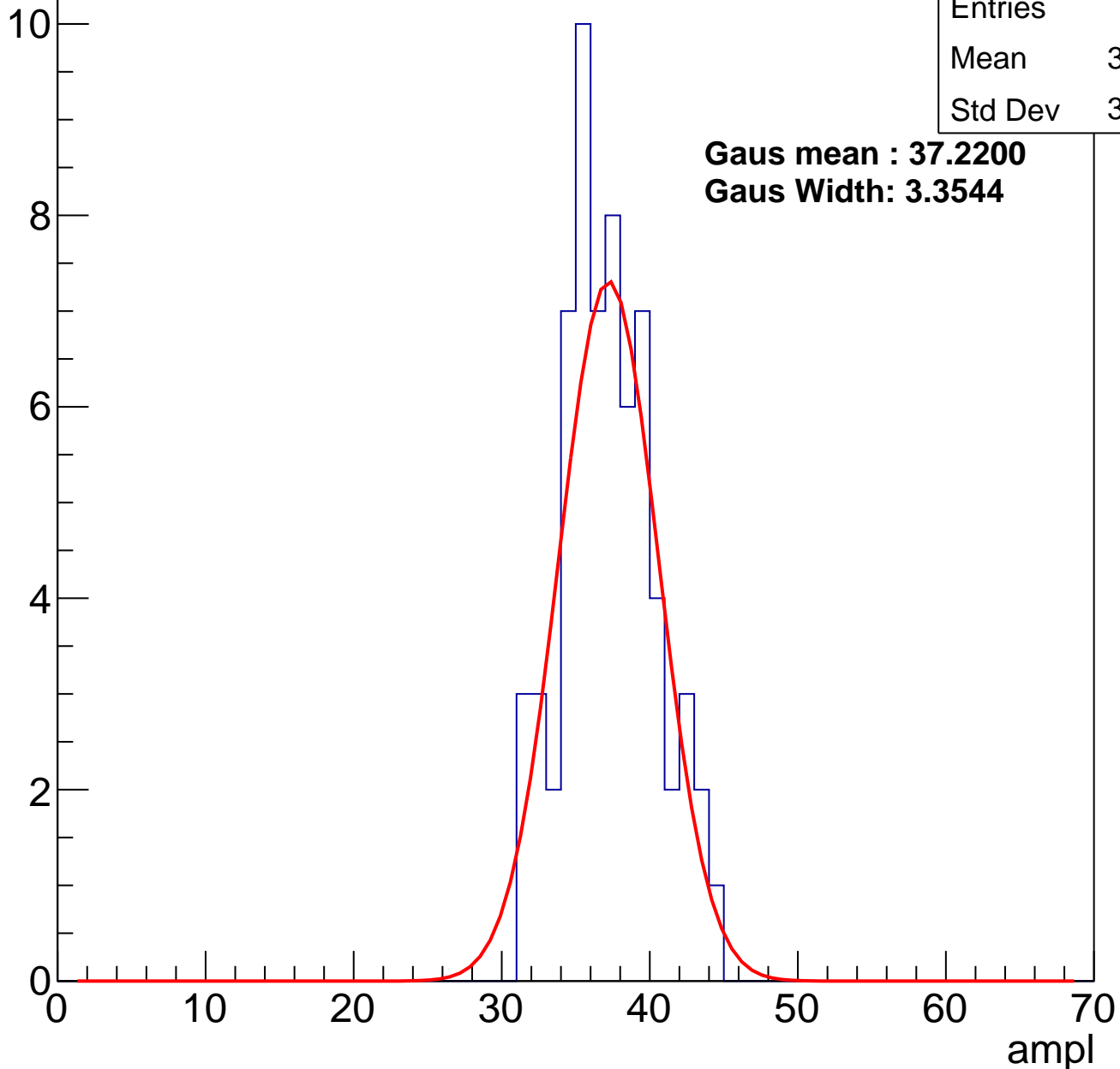
calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	36.77
Std Dev	3.087

Gaus mean : 37.2200

Gaus Width: 3.3544

Entry



B1L002S, U7-ch20, adc2

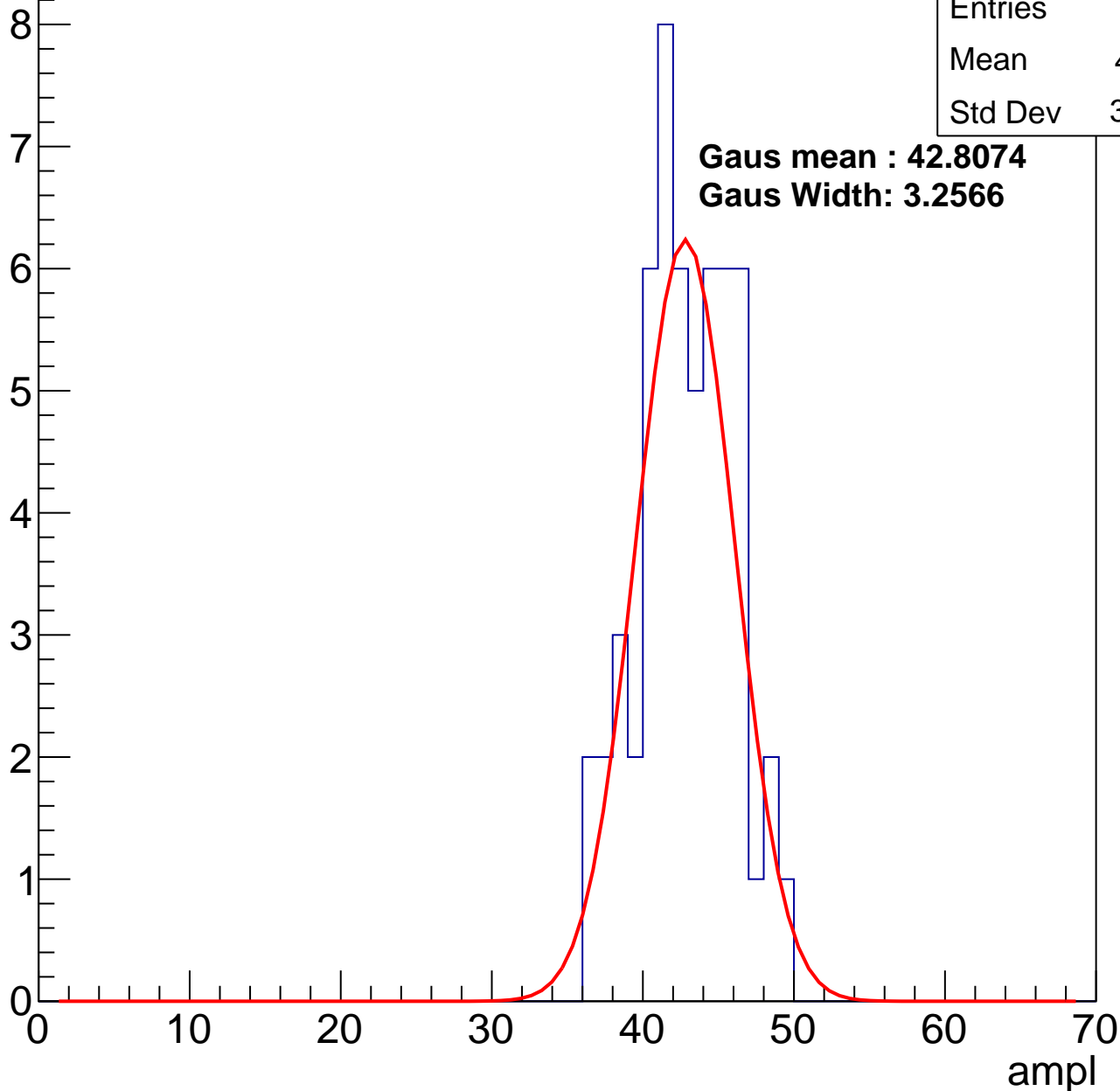
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	42.41
Std Dev	3.092

Gaus mean : 42.8074

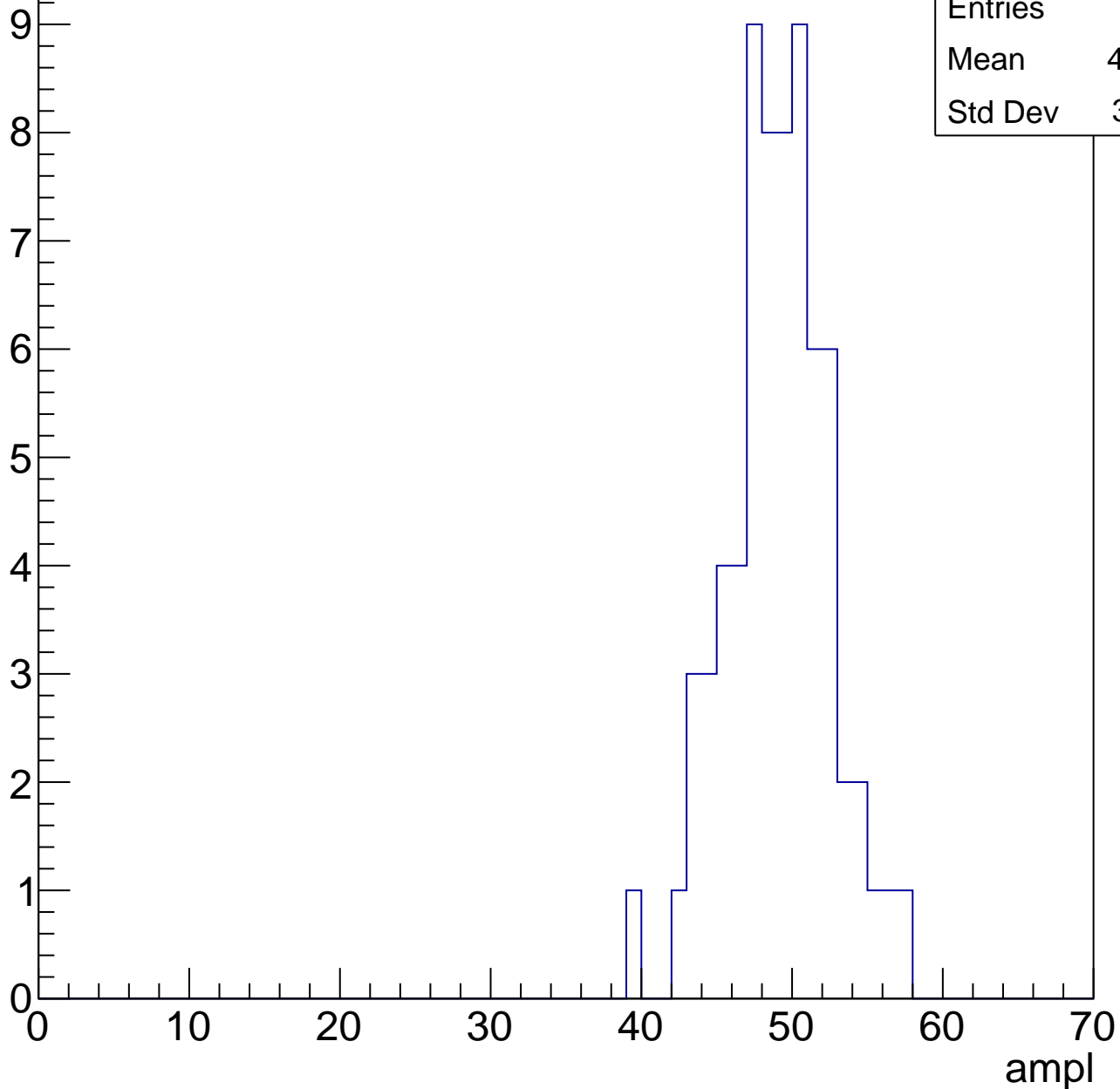
Gaus Width: 3.2566



B1L002S, U7-ch20, adc3

calib_packv5_042523_0143.root, FC#10, port B3

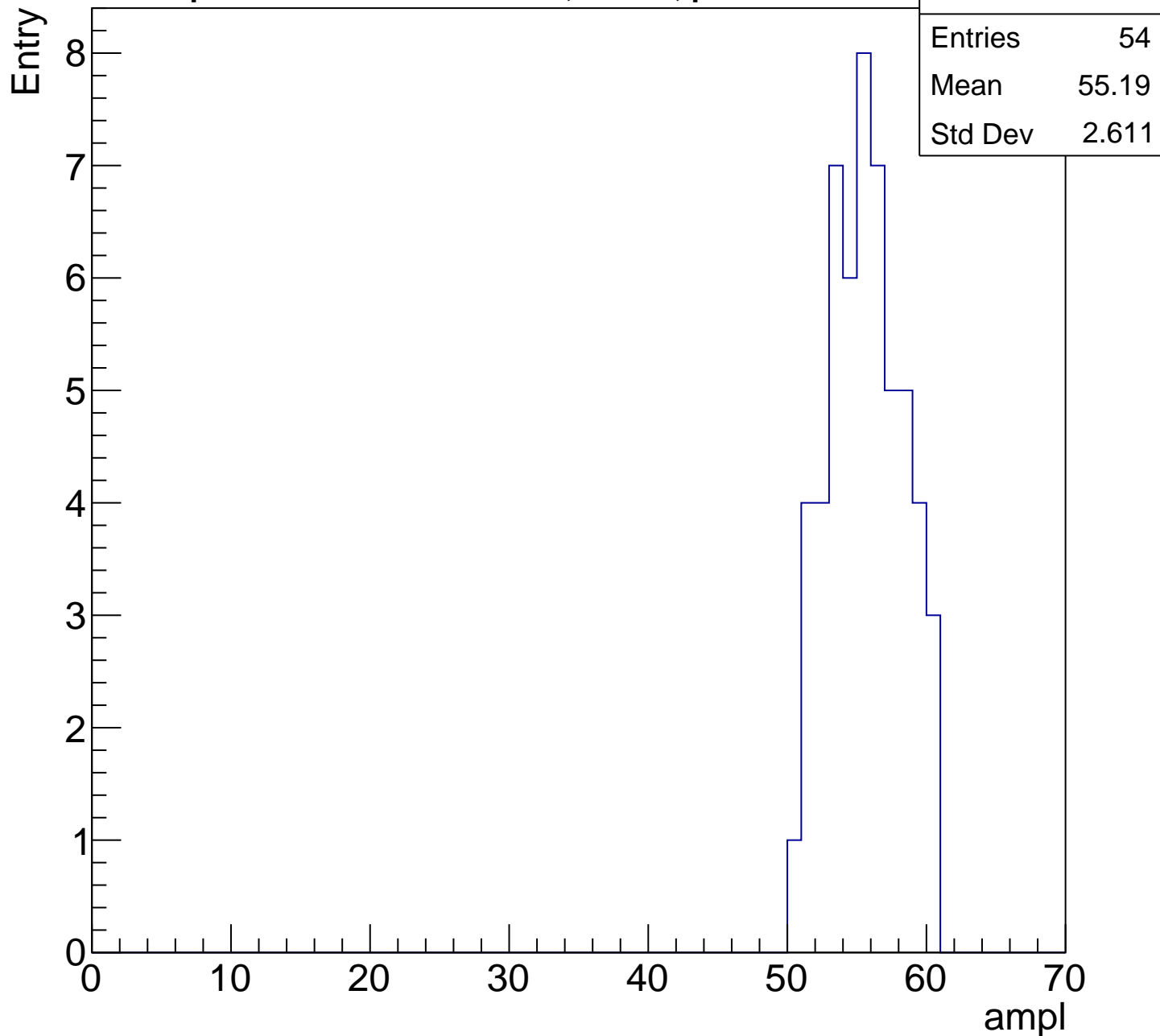
Entry



Entries	69
Mean	48.62
Std Dev	3.371

B1L002S, U7-ch20, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch20, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	53
Mean	58.36
Std Dev	8.594

Entry

10

8

6

4

2

0

0

10

20

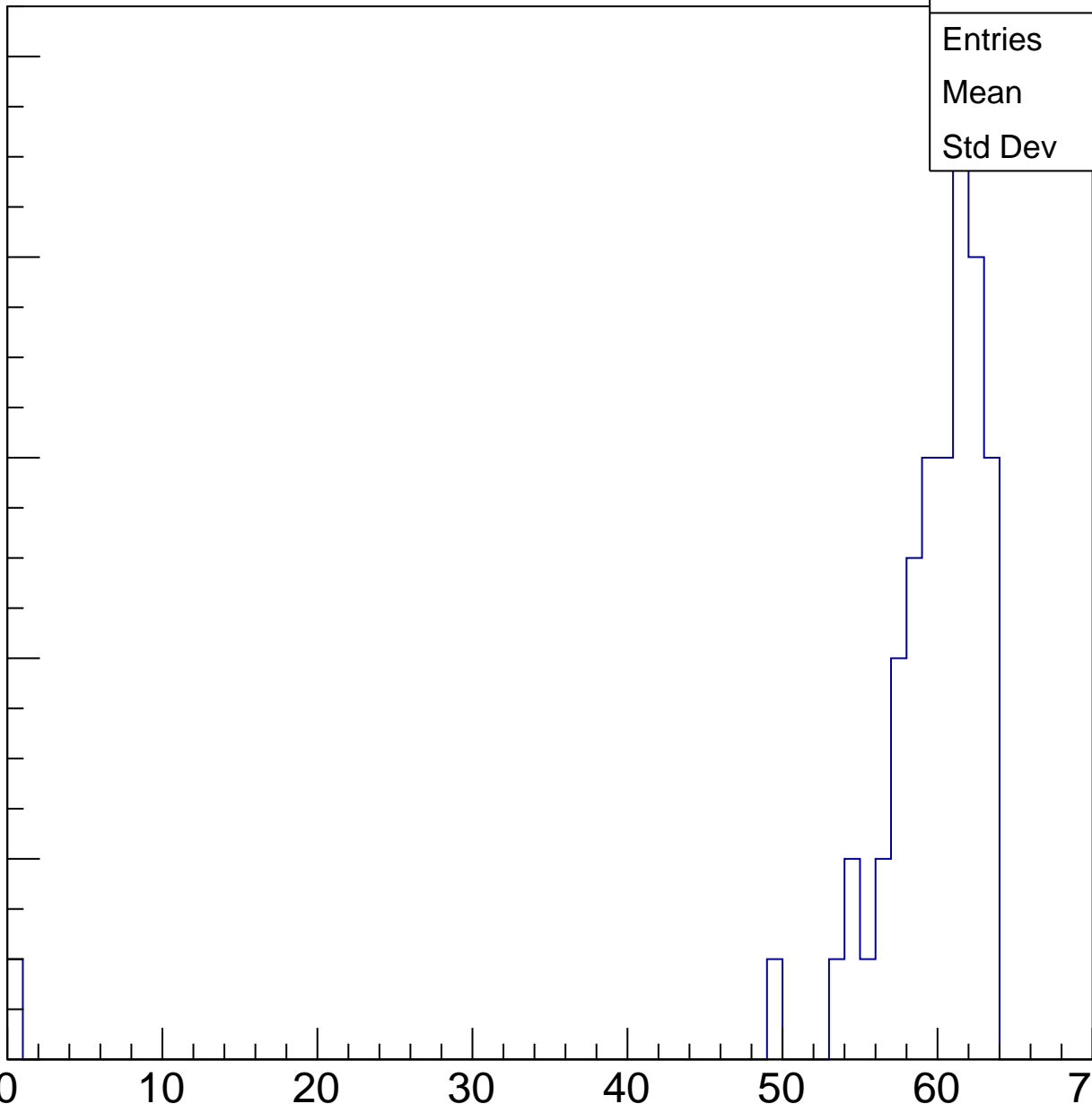
30

40

50

60

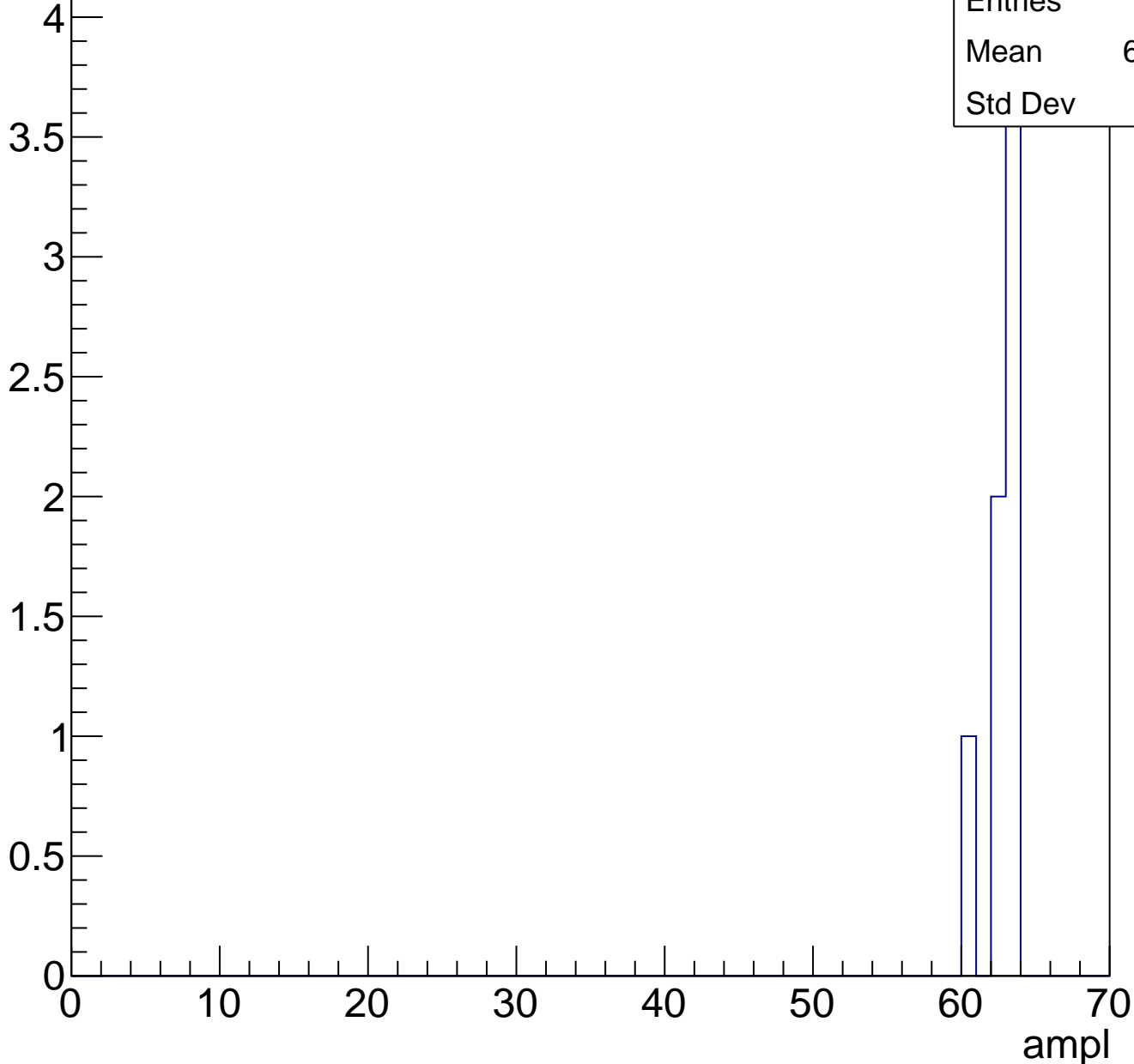
ampl



B1L002S, U7-ch20, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch20, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch21, adc0

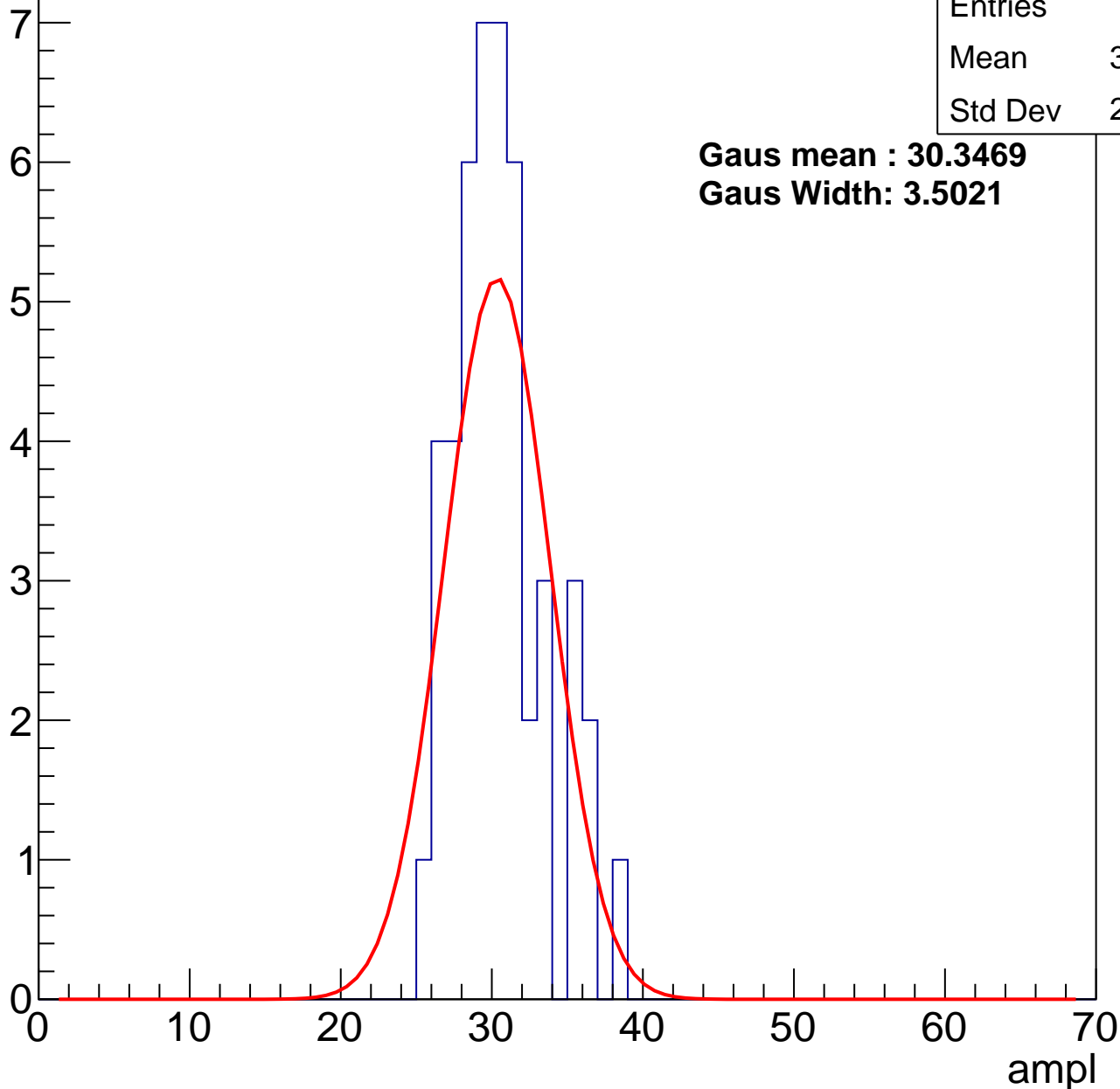
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	30.04
Std Dev	2.978

Gaus mean : 30.3469

Gaus Width: 3.5021



B1L002S, U7-ch21, adc1

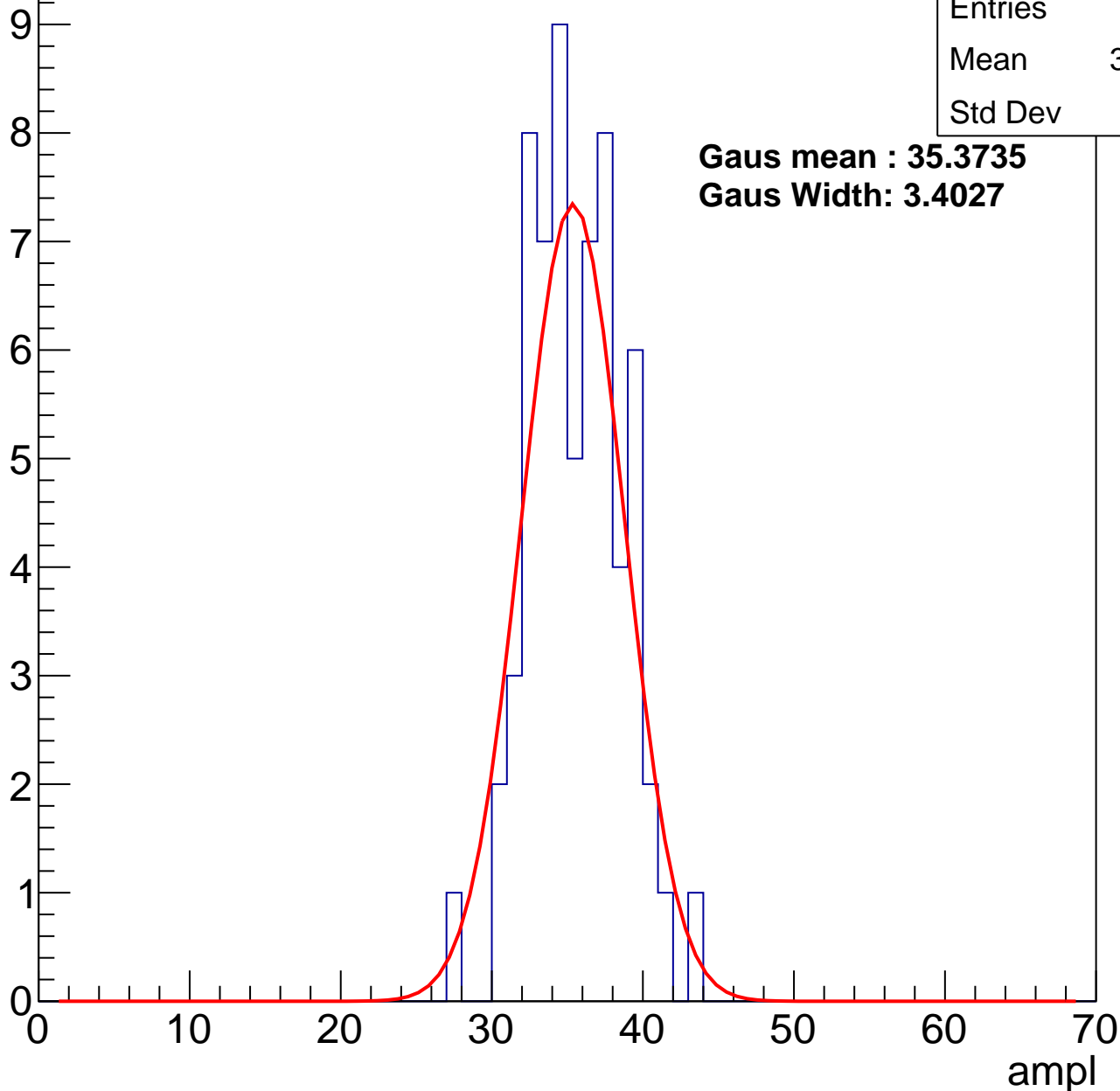
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.09
Std Dev	3.04

Gaus mean : 35.3735

Gaus Width: 3.4027



B1L002S, U7-ch21, adc2

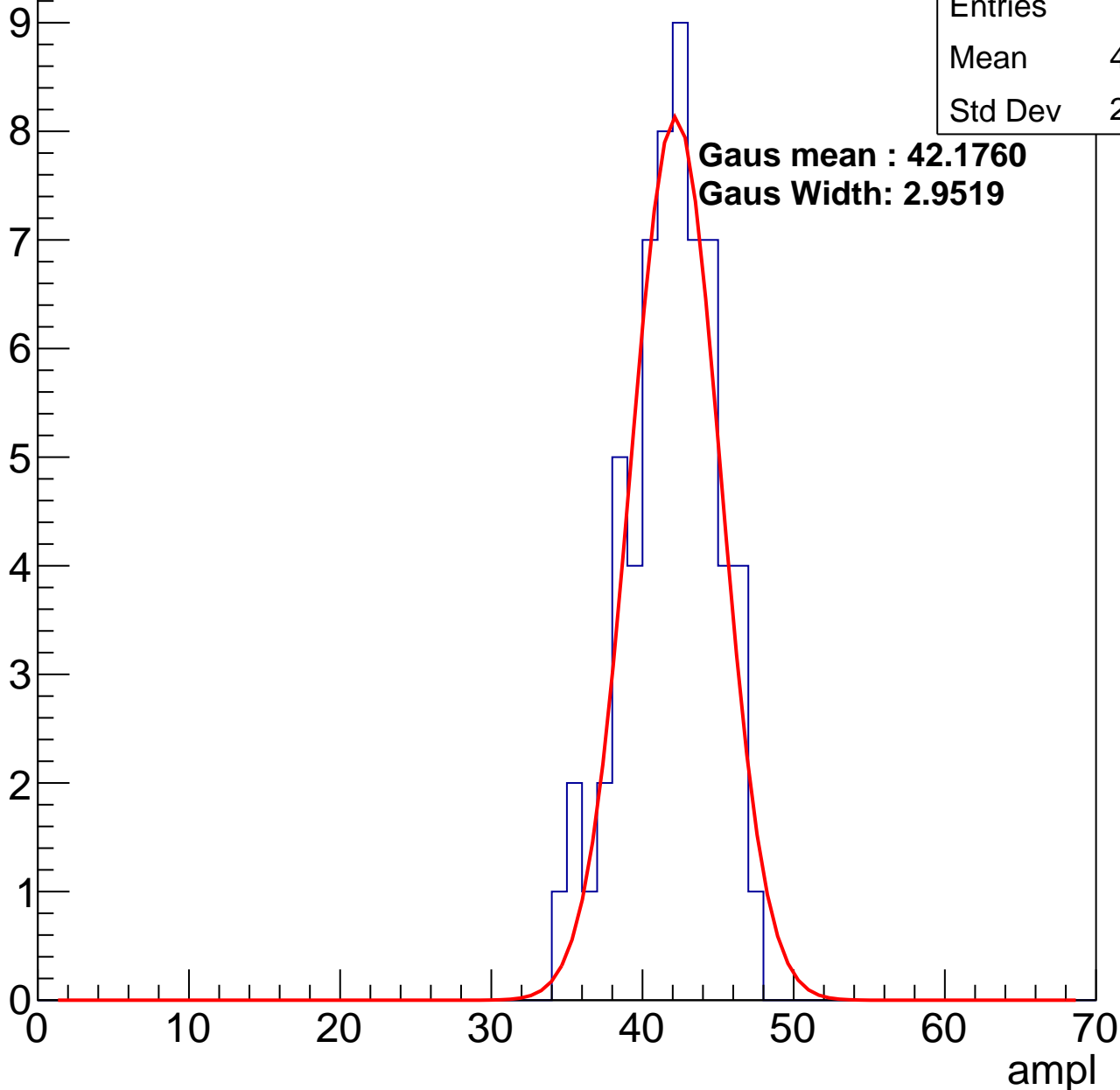
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.39
Std Dev	2.942

Gaus mean : 42.1760

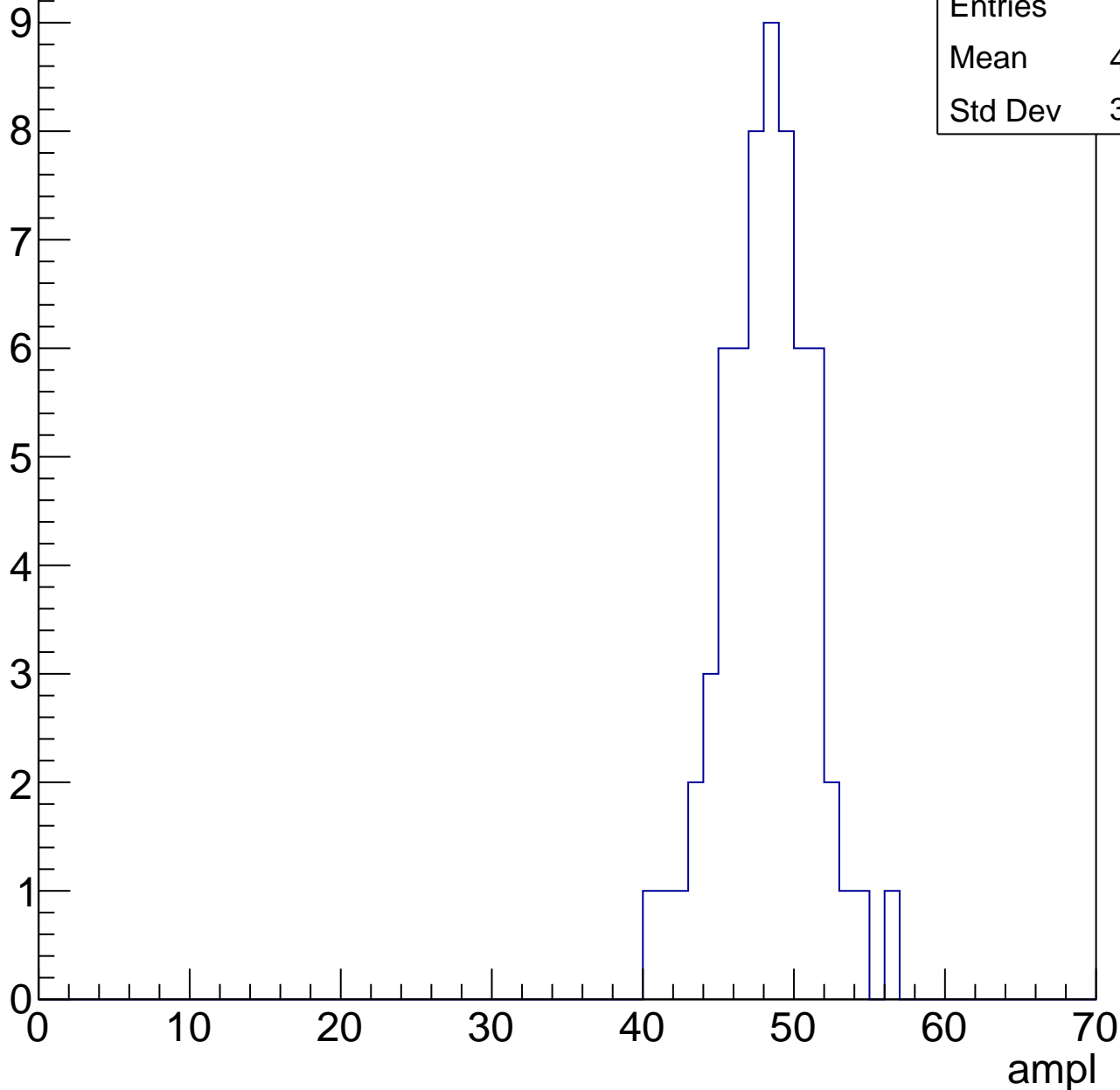
Gaus Width: 2.9519



B1L002S, U7-ch21, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



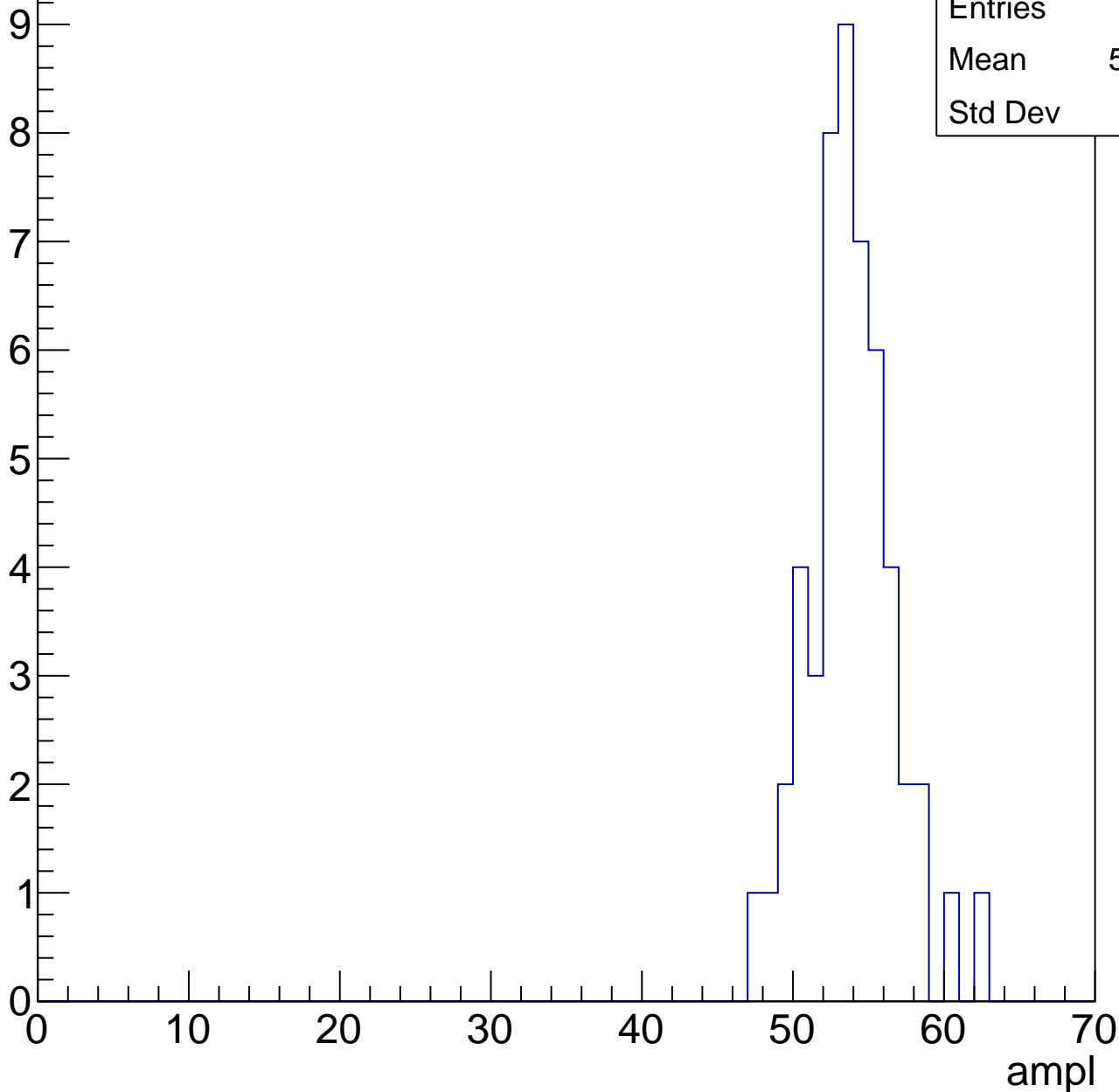
Entries	62
Mean	47.74
Std Dev	3.037

B1L002S, U7-ch21, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	53.39
Std Dev	2.87



B1L002S, U7-ch21, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	60
Mean	58.65
Std Dev	2.568

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

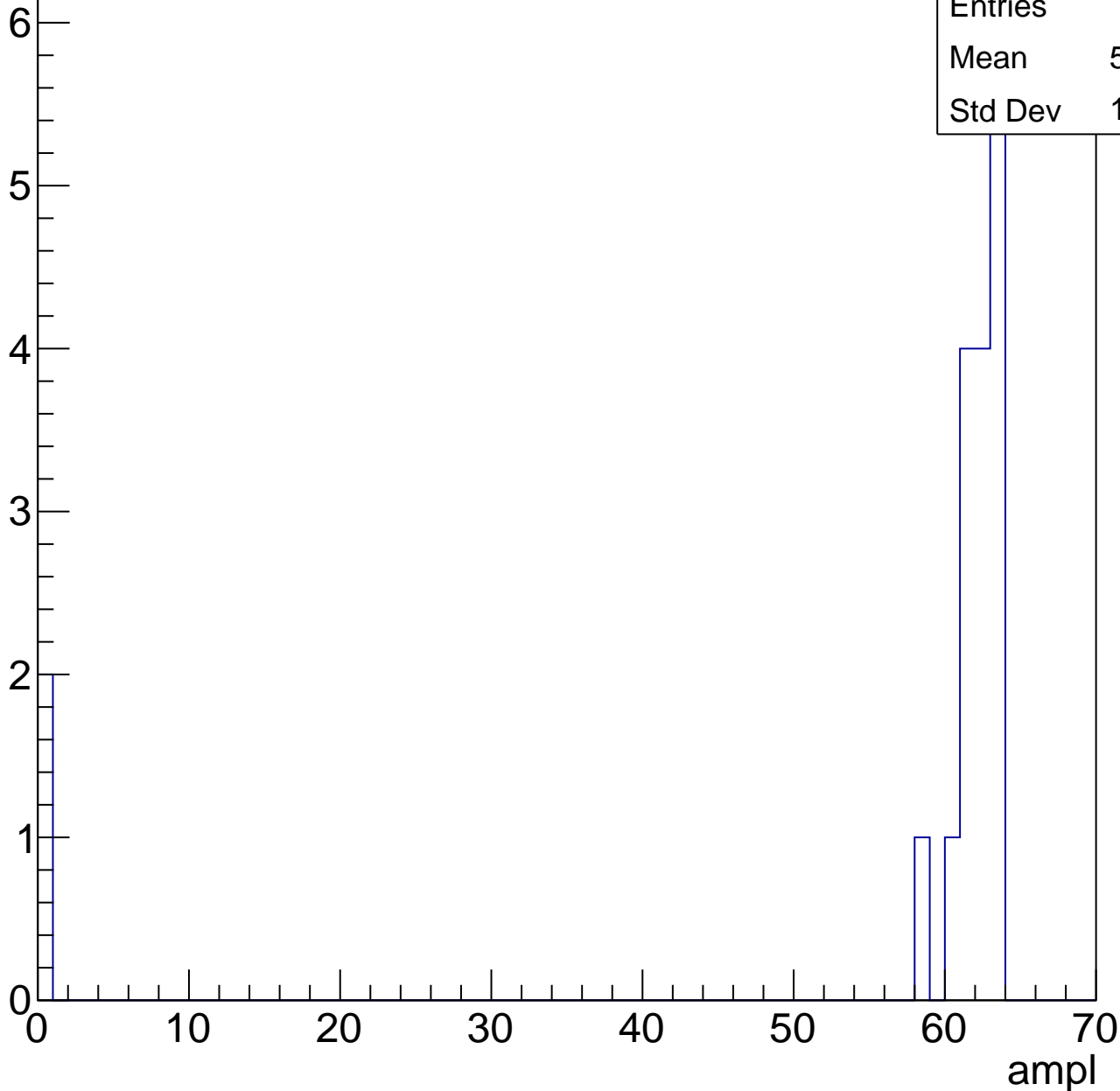
10

B1L002S, U7-ch21, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

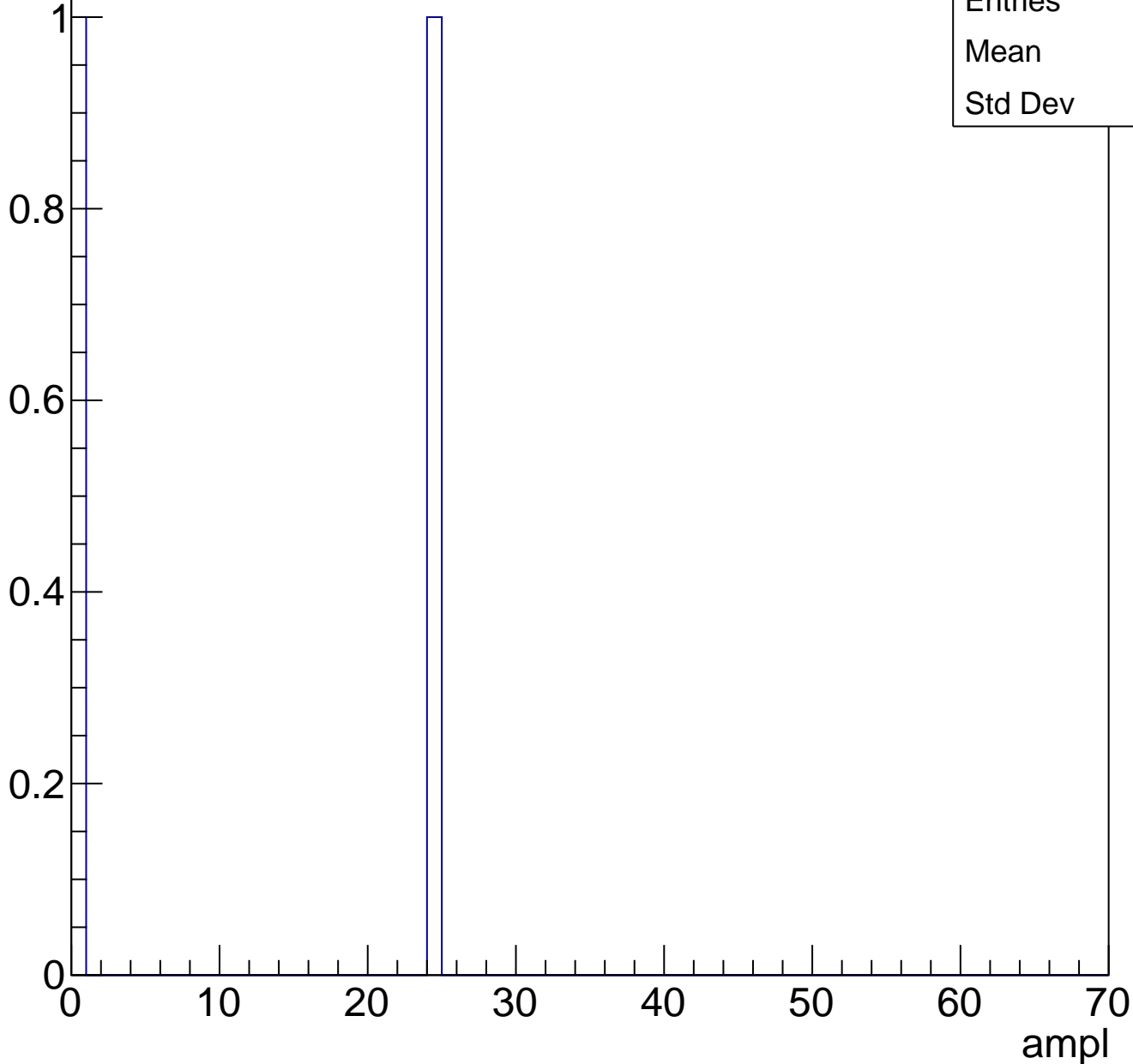
Entries	18
Mean	54.89
Std Dev	19.45



B1L002S, U7-ch21, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch22, adc0

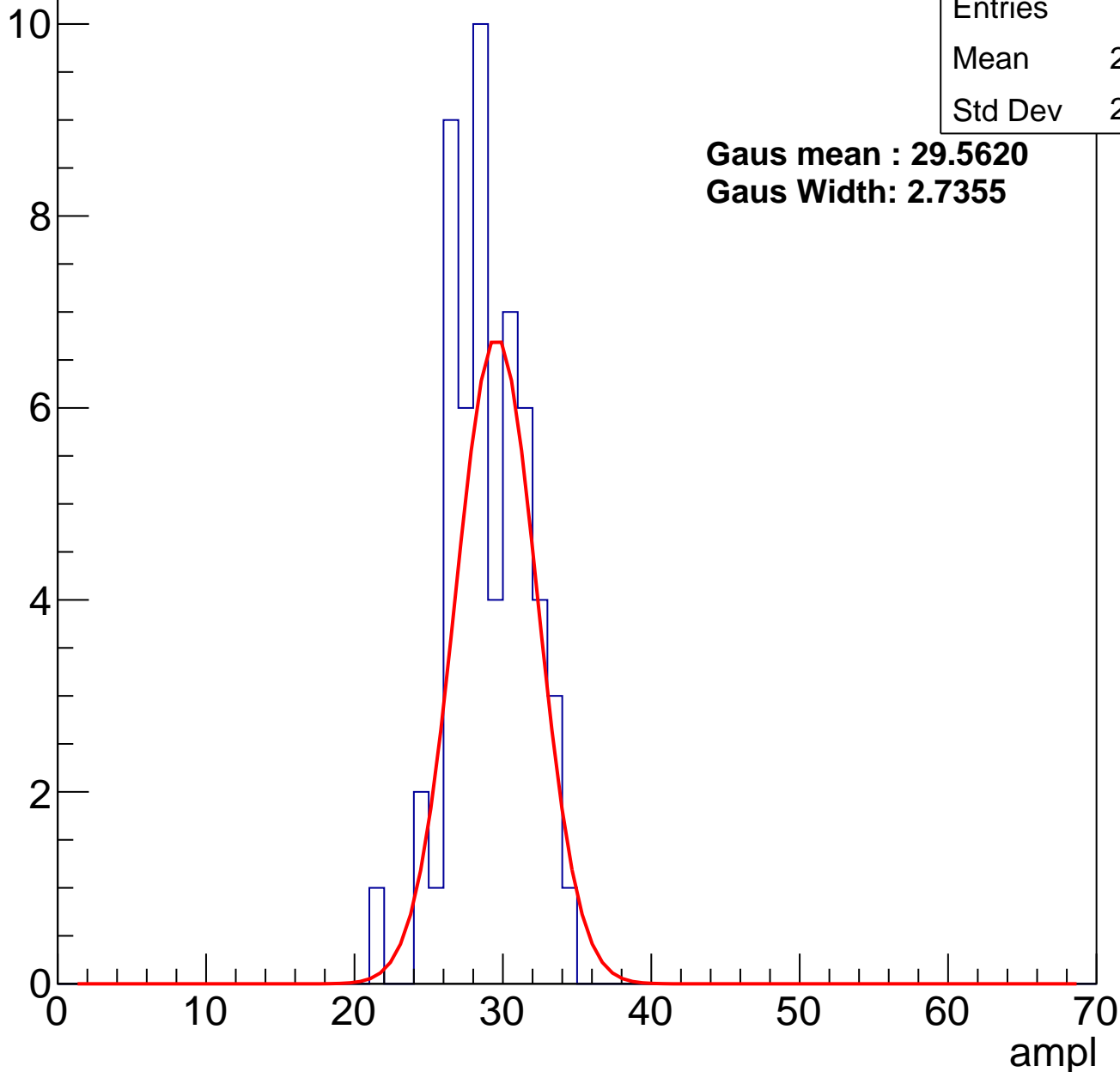
calib_packv5_042523_0143.root, FC#10, port B3

Entries	54
Mean	28.57
Std Dev	2.636

Gaus mean : 29.5620

Gaus Width: 2.7355

Entry



B1L002S, U7-ch22, adc1

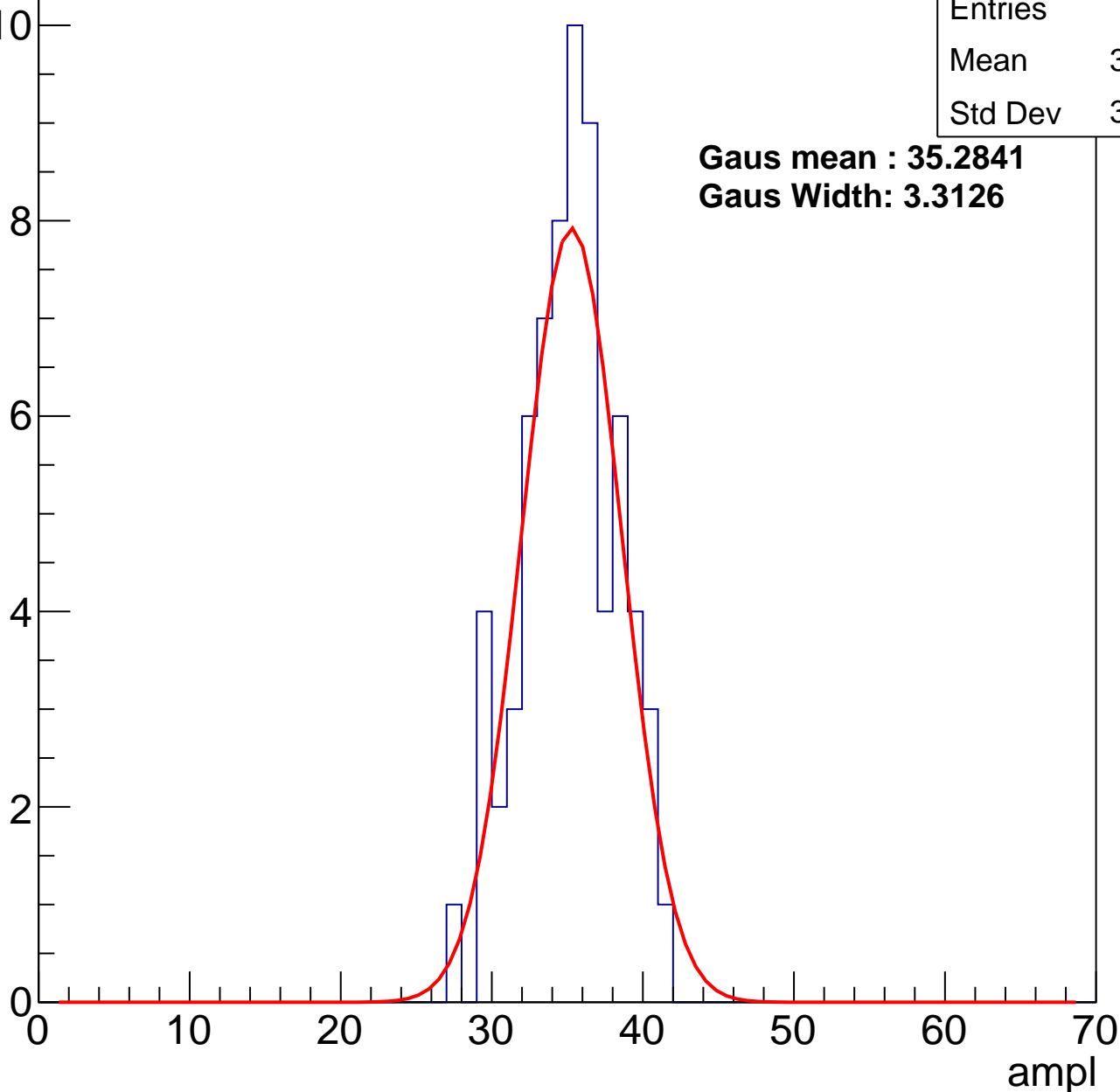
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.68
Std Dev	3.075

Gaus mean : 35.2841

Gaus Width: 3.3126



B1L002S, U7-ch22, adc2

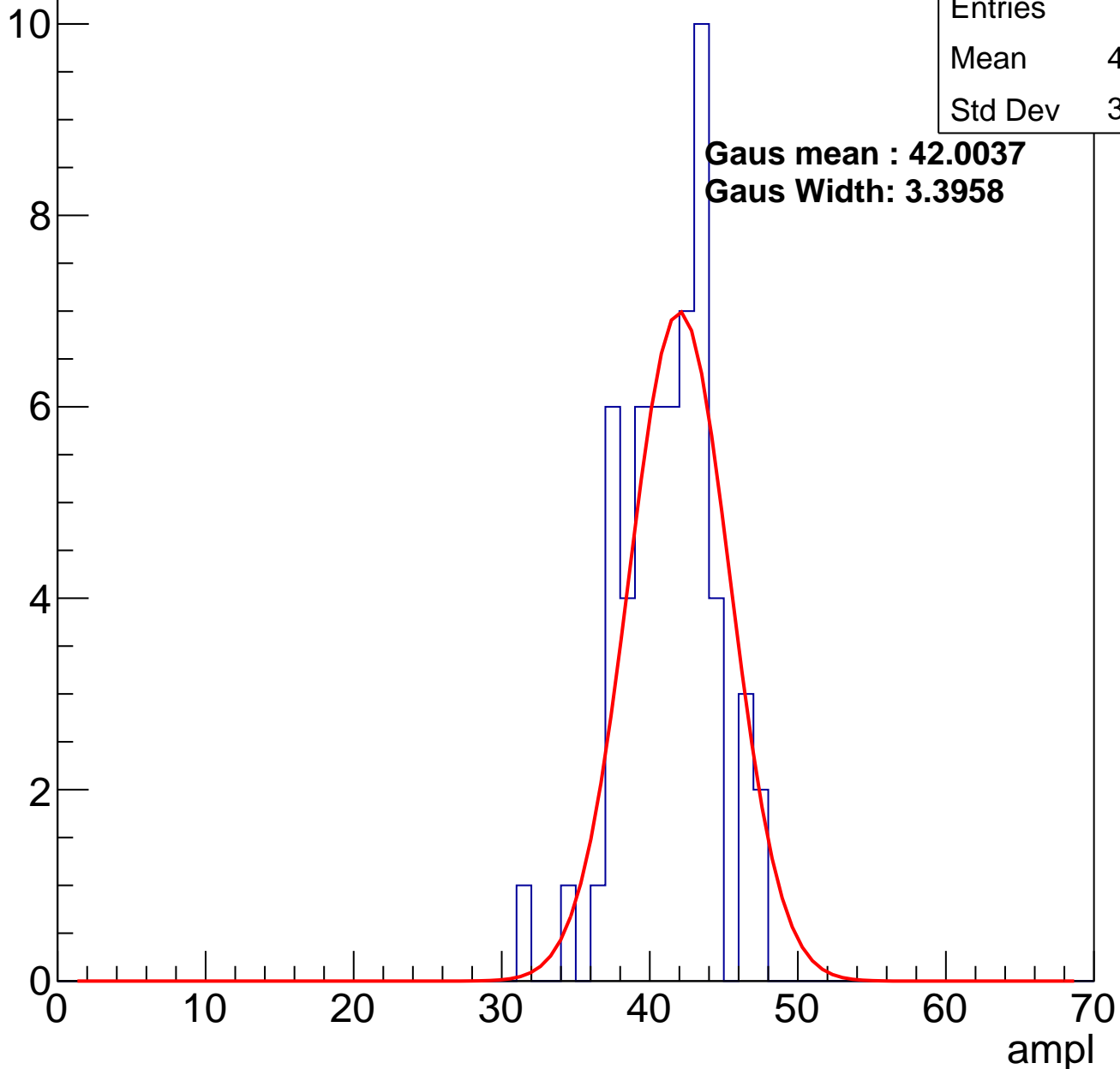
calib_packv5_042523_0143.root, FC#10, port B3

Entries	57
Mean	40.82
Std Dev	3.146

Gaus mean : 42.0037

Gaus Width: 3.3958

Entry

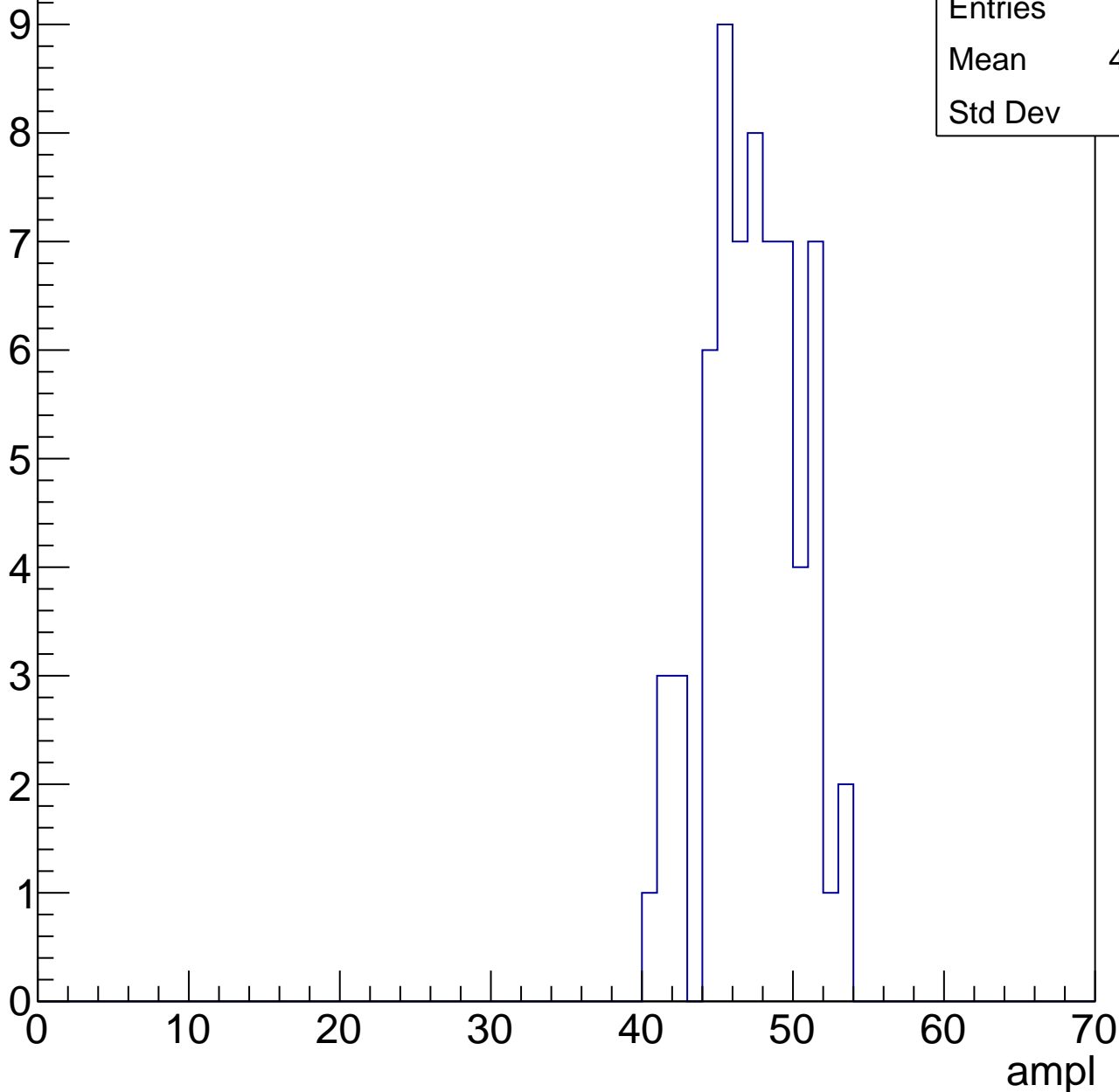


B1L002S, U7-ch22, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.92
Std Dev	3.06

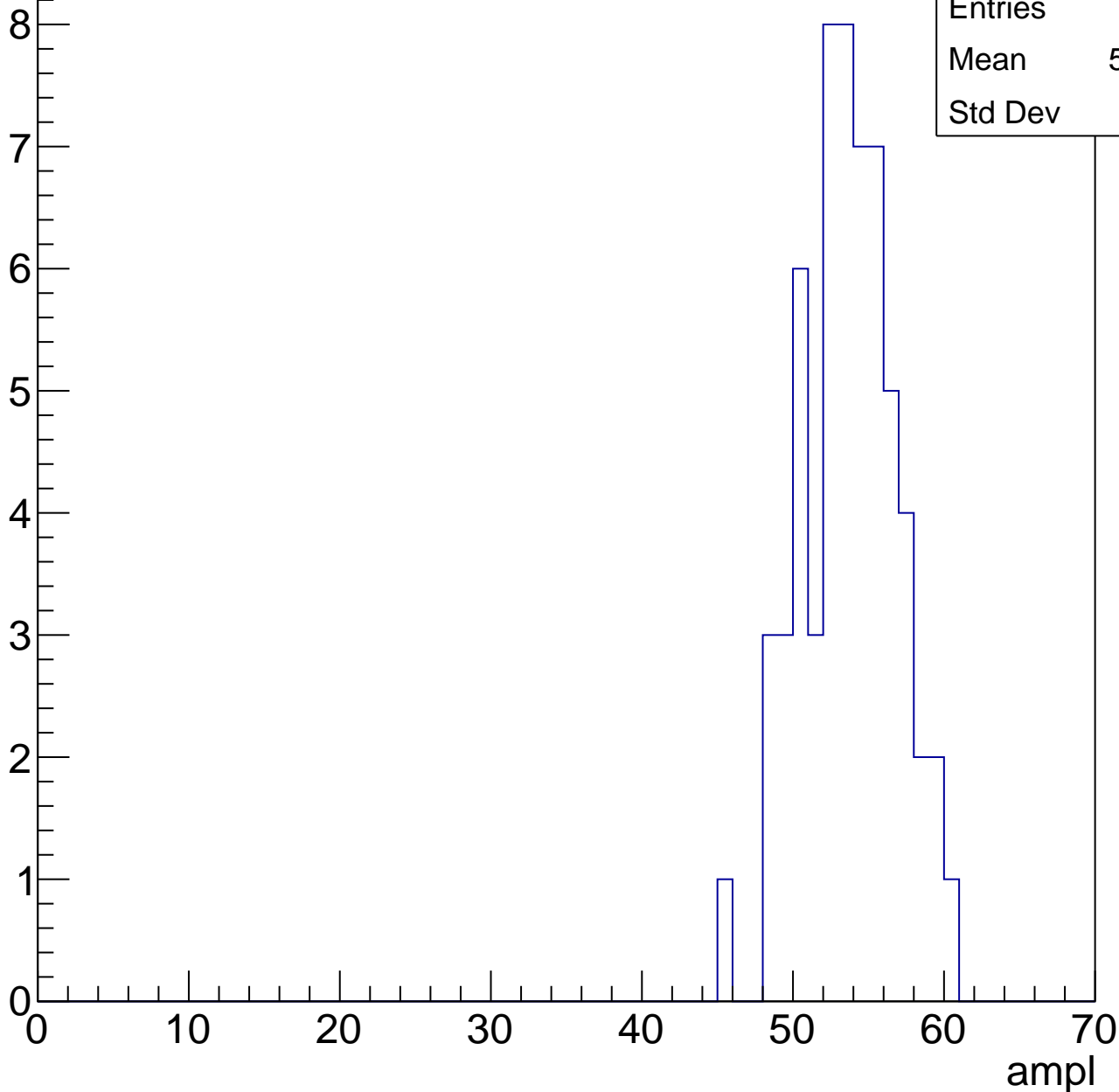


B1L002S, U7-ch22, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	53.23
Std Dev	3.09



B1L002S, U7-ch22, adc5

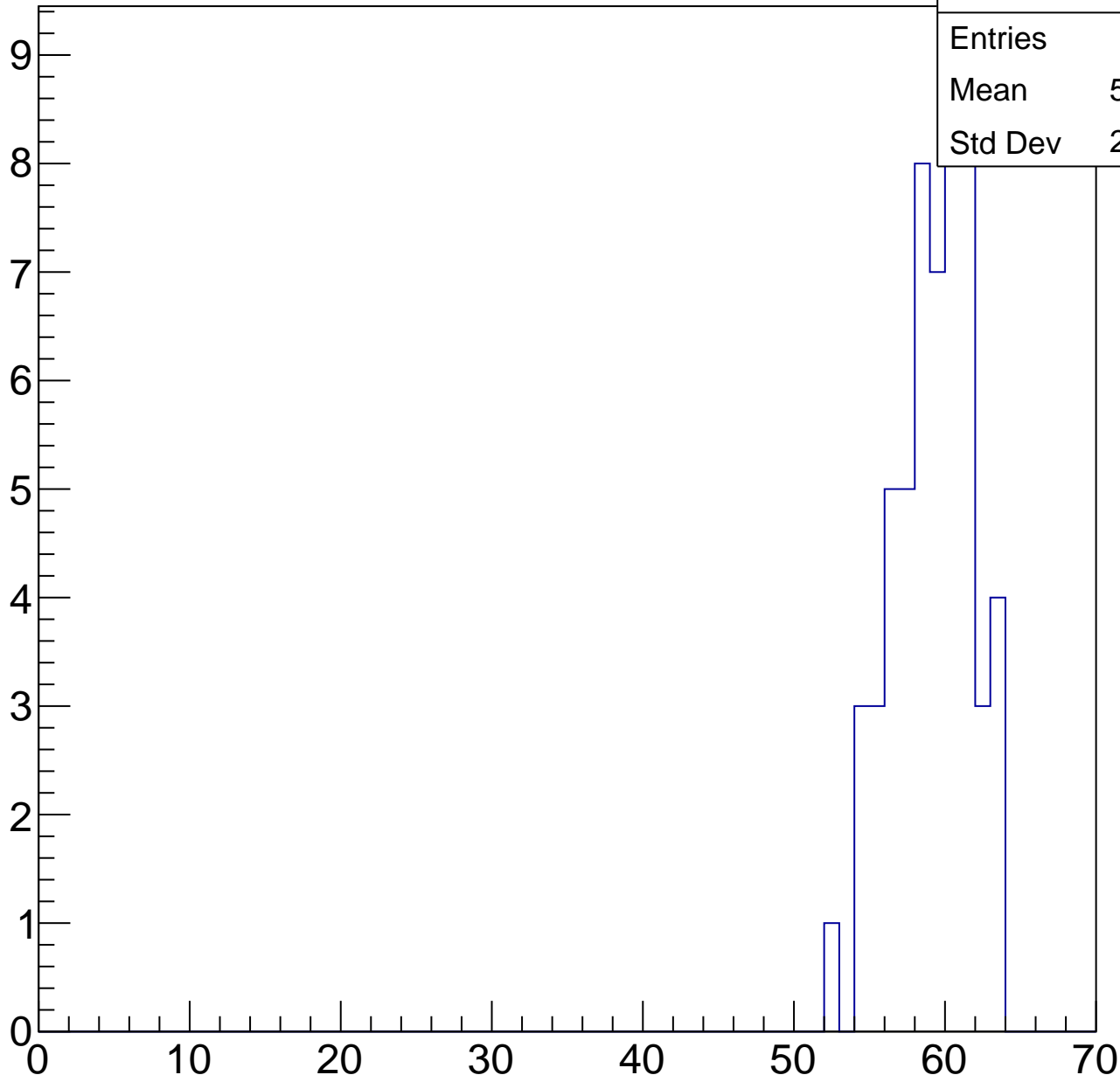
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.74
Std Dev	2.572

ampl

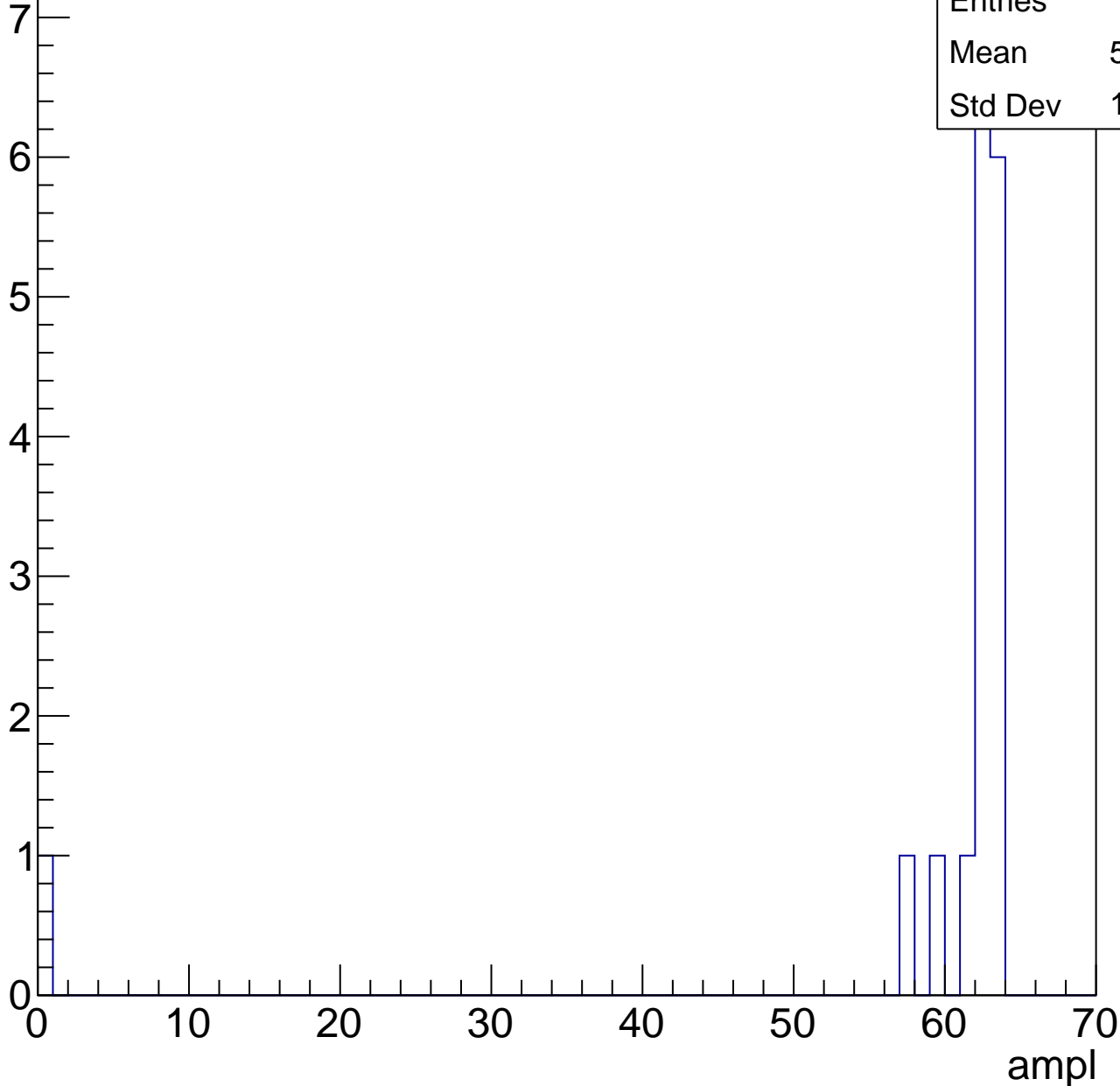


B1L002S, U7-ch22, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

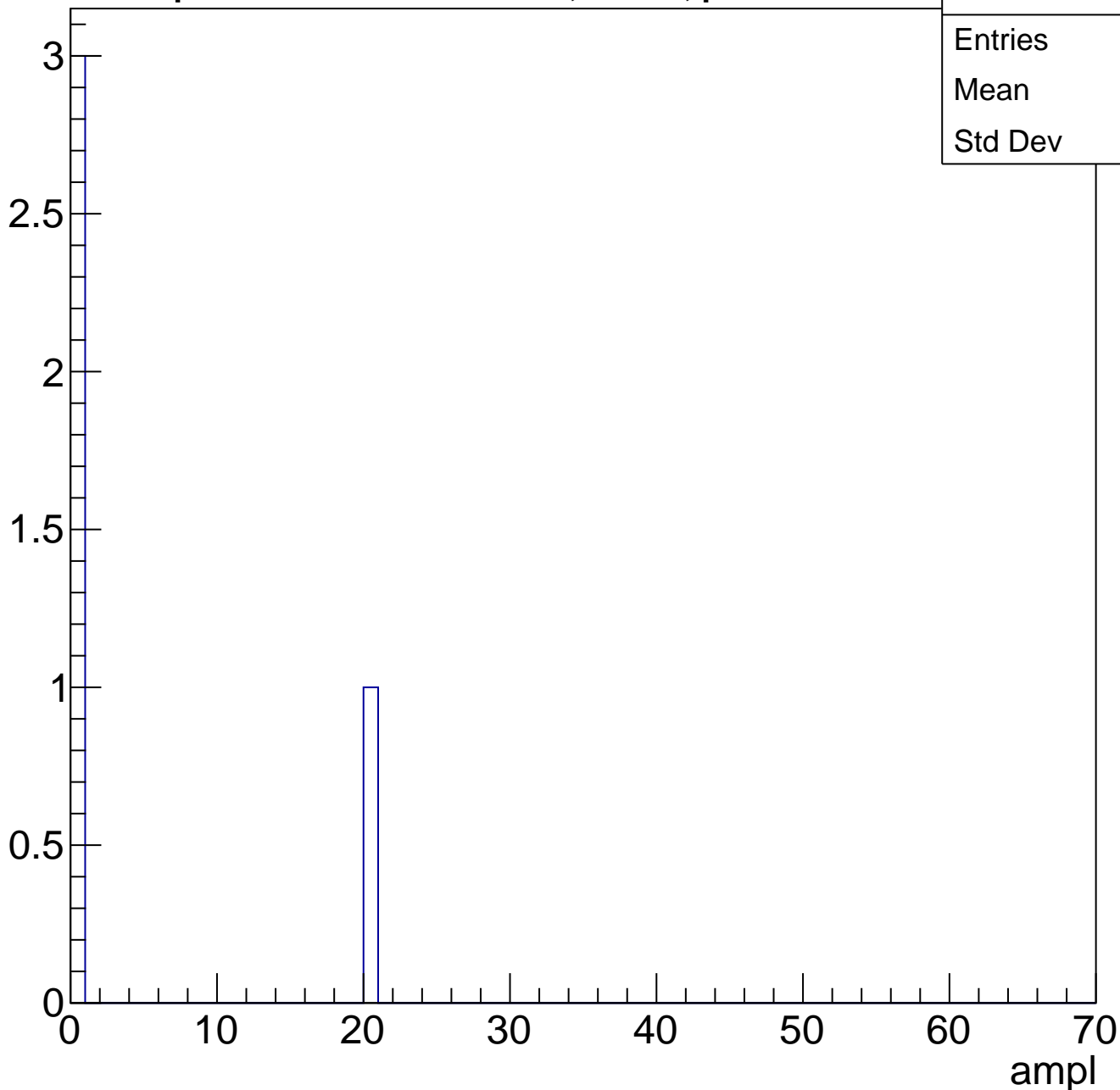
Entries	17
Mean	58.18
Std Dev	14.63



B1L002S, U7-ch22, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	5
Std Dev	8.66

B1L002S, U7-ch23, adc0

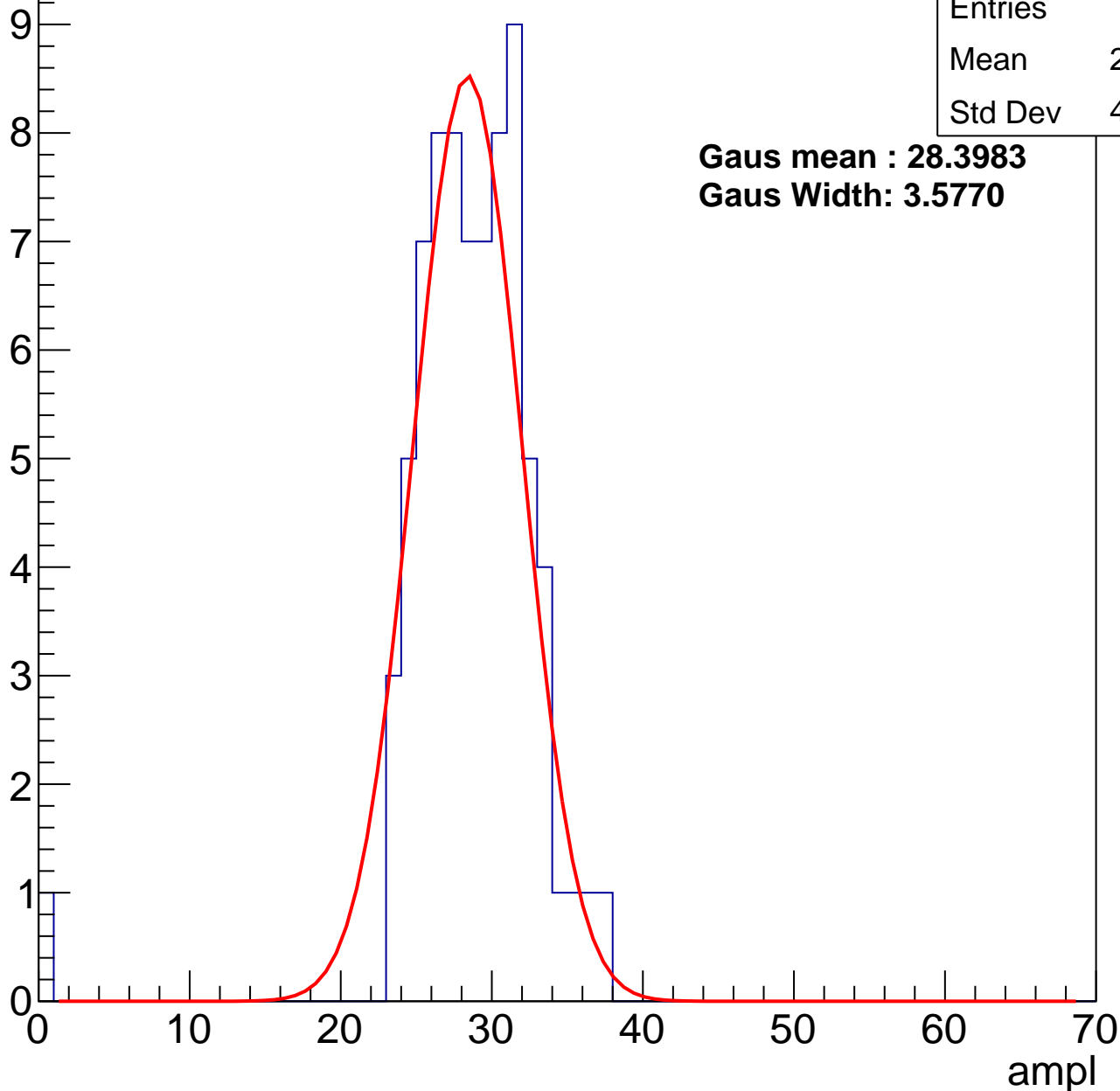
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	76
Mean	28.16
Std Dev	4.545

Gaus mean : 28.3983

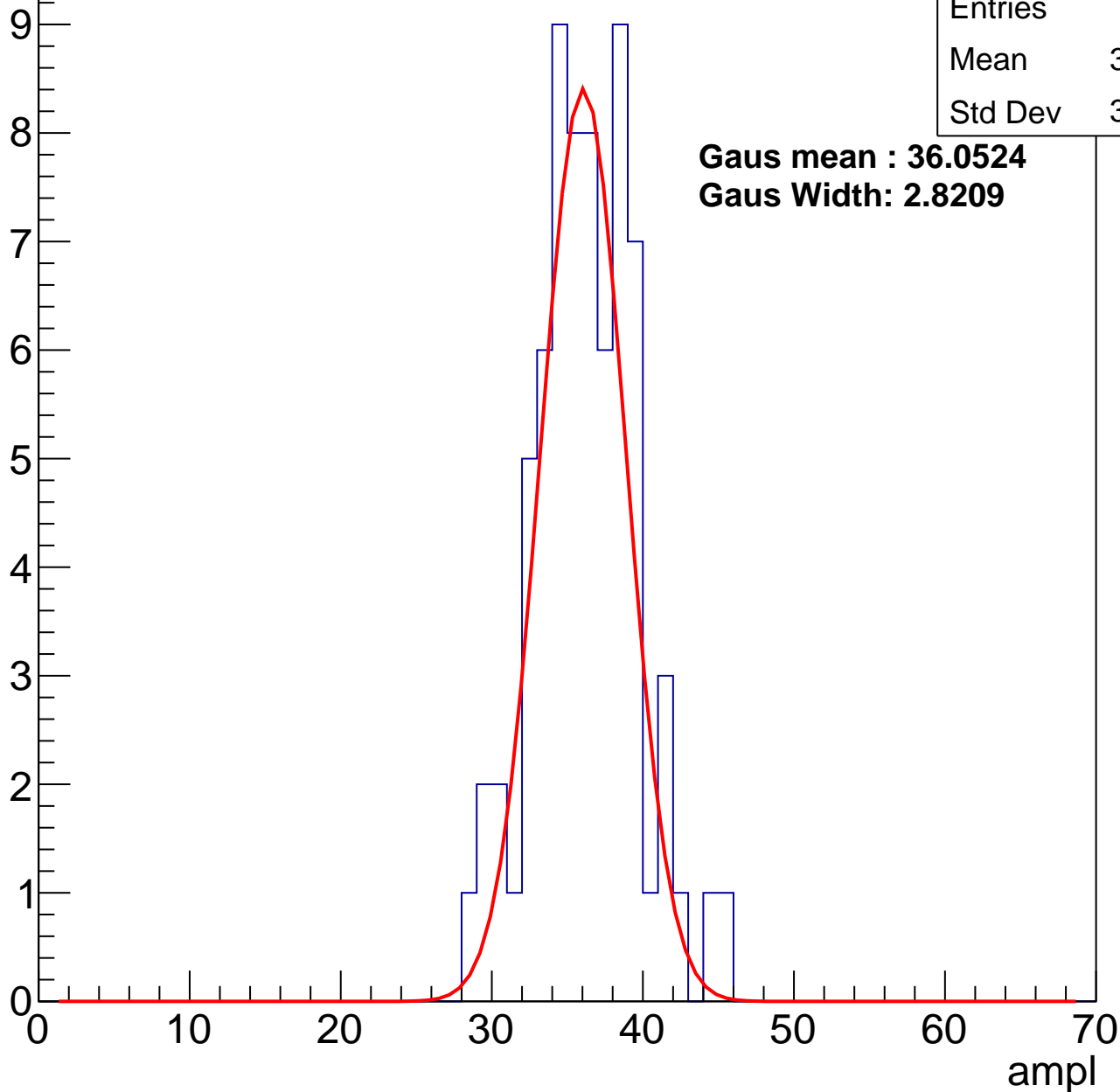
Gaus Width: 3.5770



B1L002S, U7-ch23, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch23, adc2

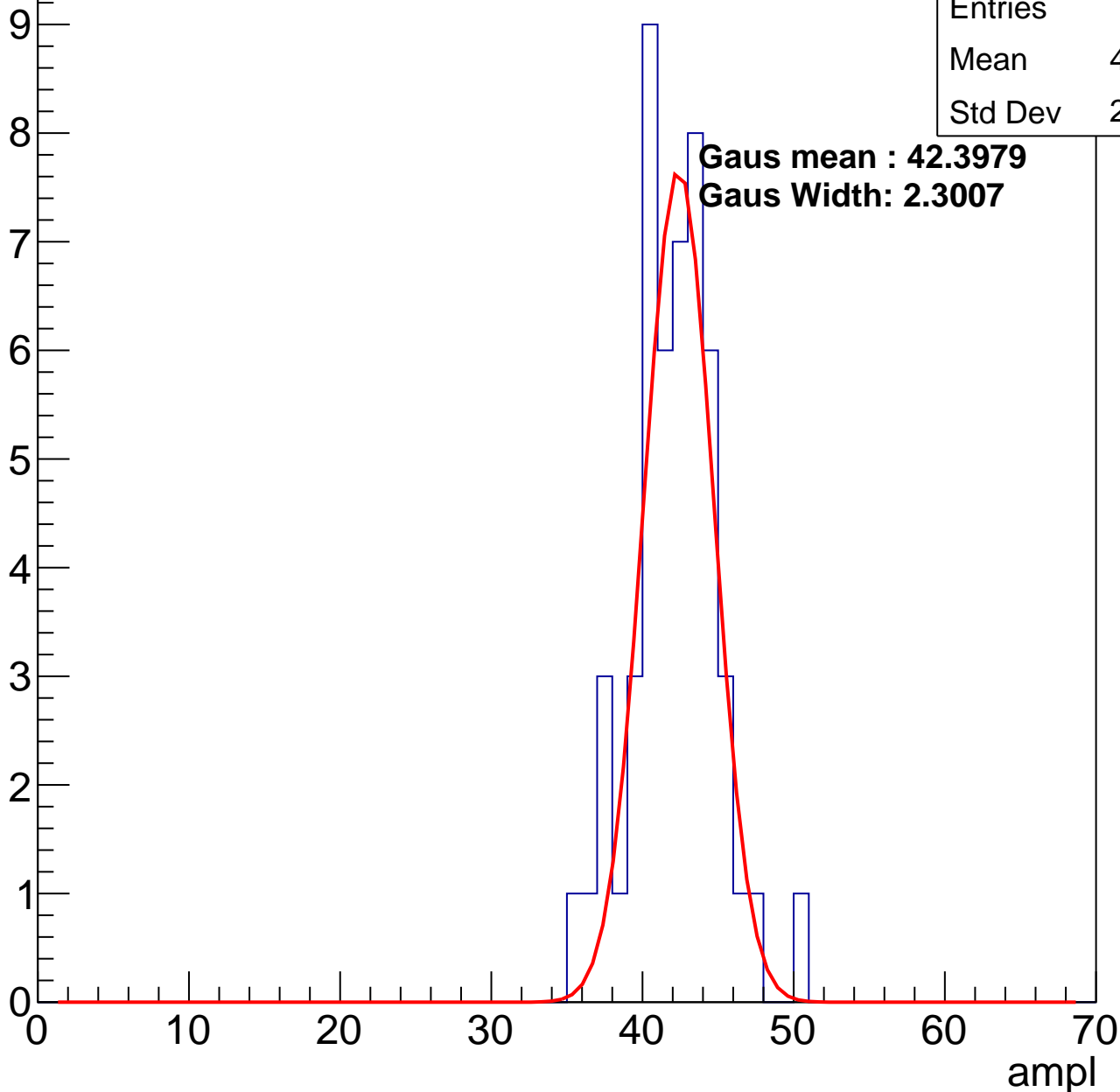
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.63
Std Dev	2.807

Gaus mean : 42.3979

Gaus Width: 2.3007



B1L002S, U7-ch23, adc3

calib_packv5_042523_0143.root, FC#10, port B3

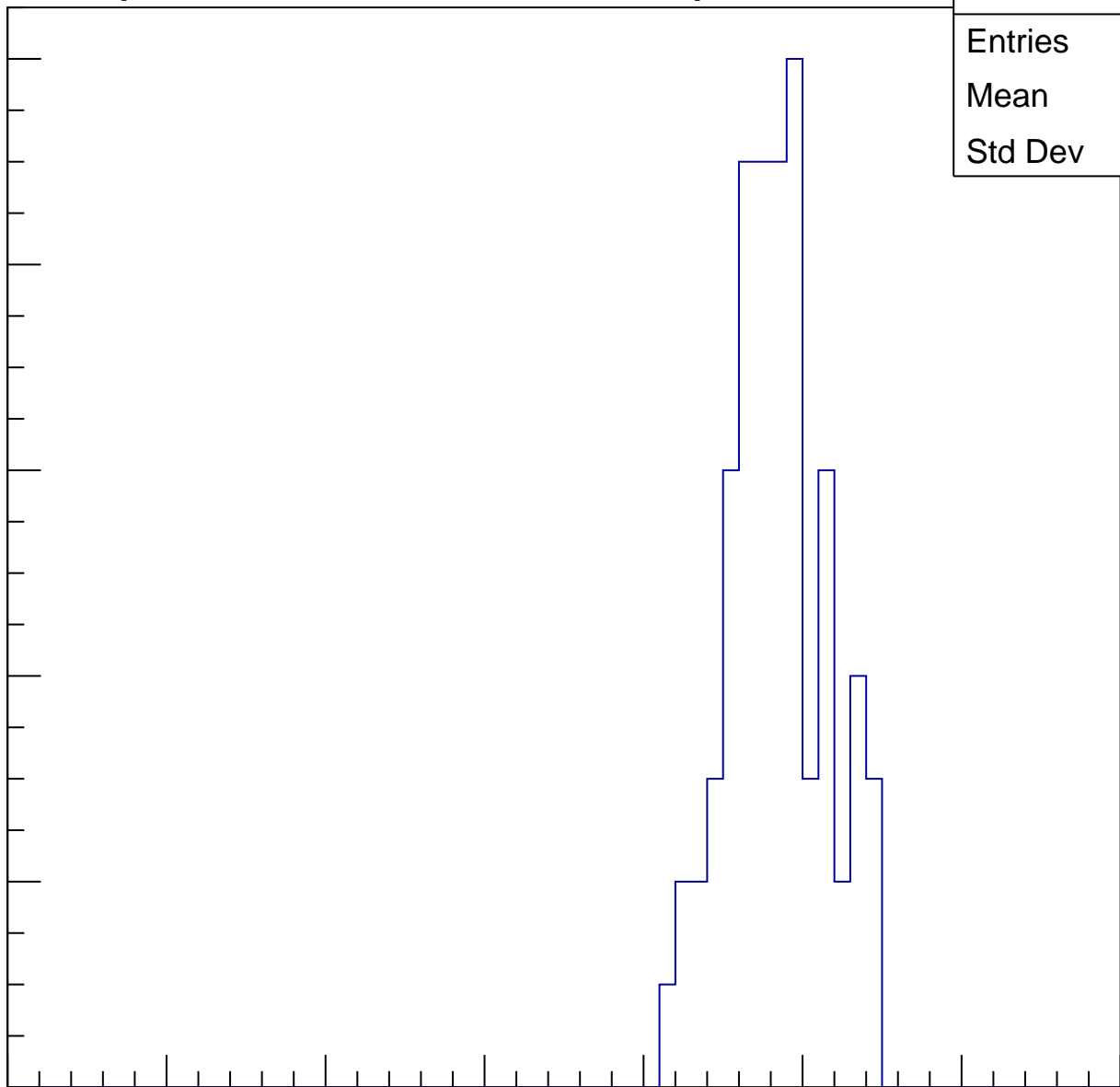
Entries	69
Mean	47.91
Std Dev	3.03

Entry

10
8
6
4
2
0

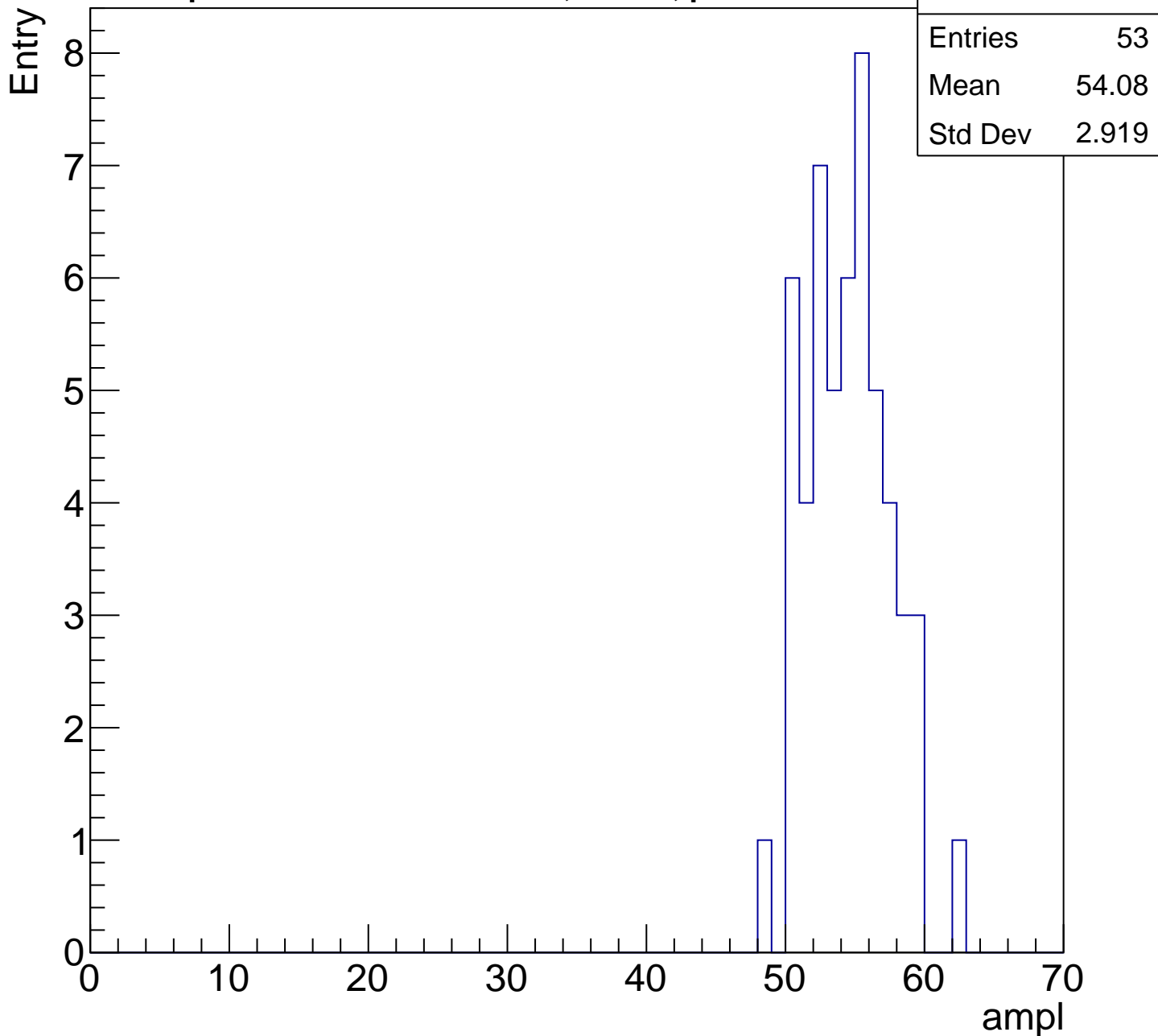
0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch23, adc4

calib_packv5_042523_0143.root, FC#10, port B3

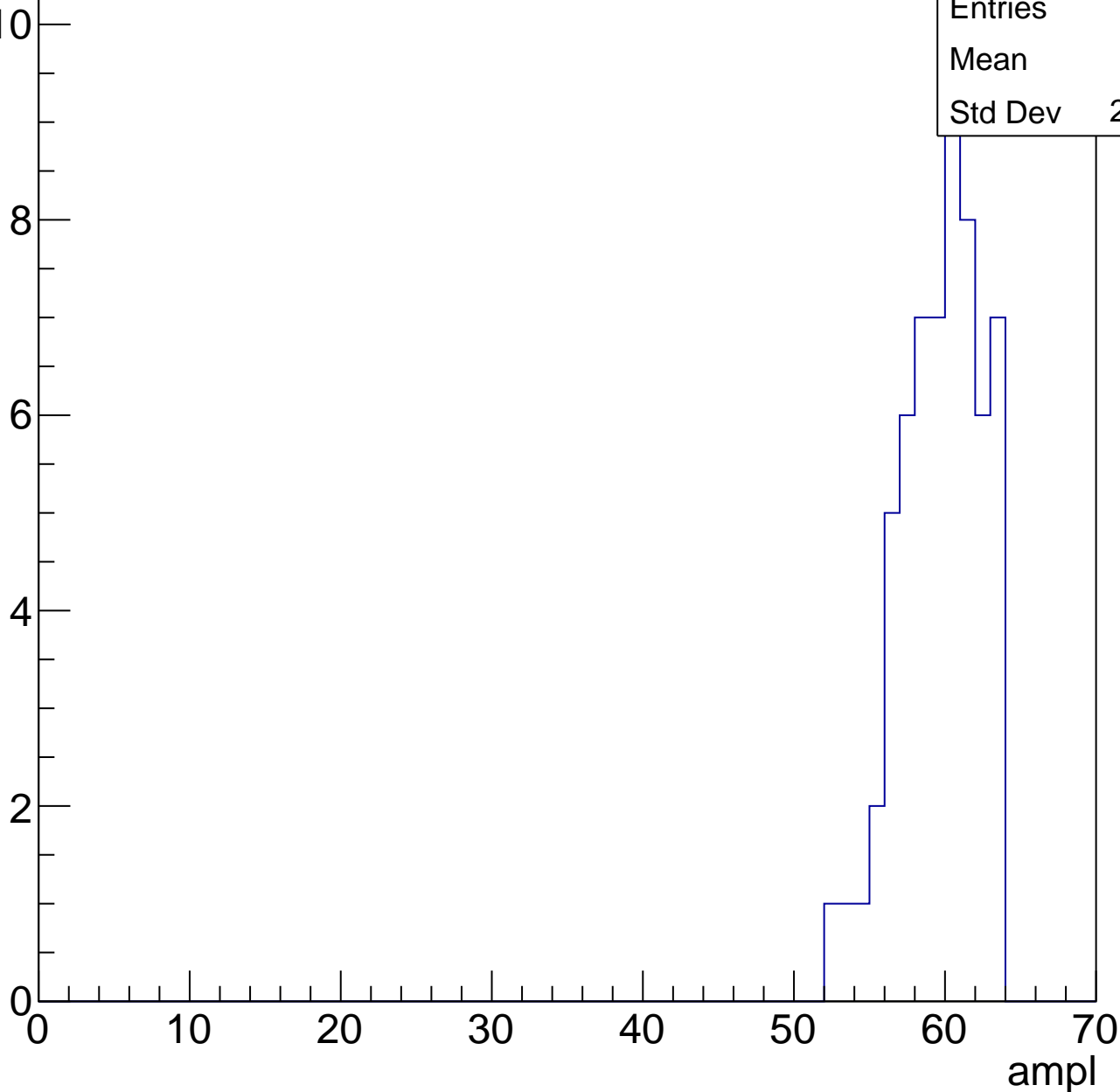


B1L002S, U7-ch23, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

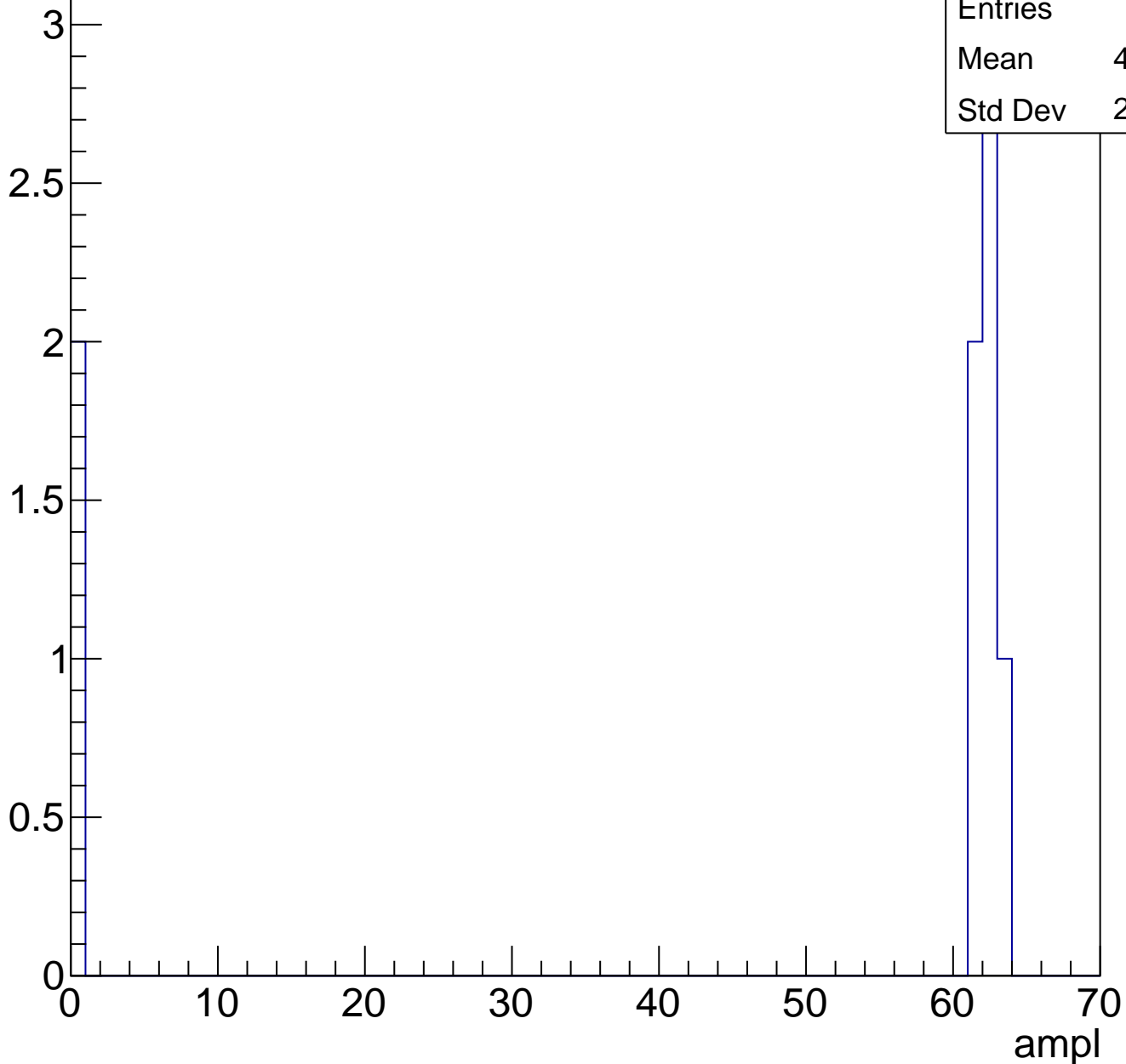
Entries	61
Mean	59.2
Std Dev	2.635



B1L002S, U7-ch23, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch23, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch24, adc0

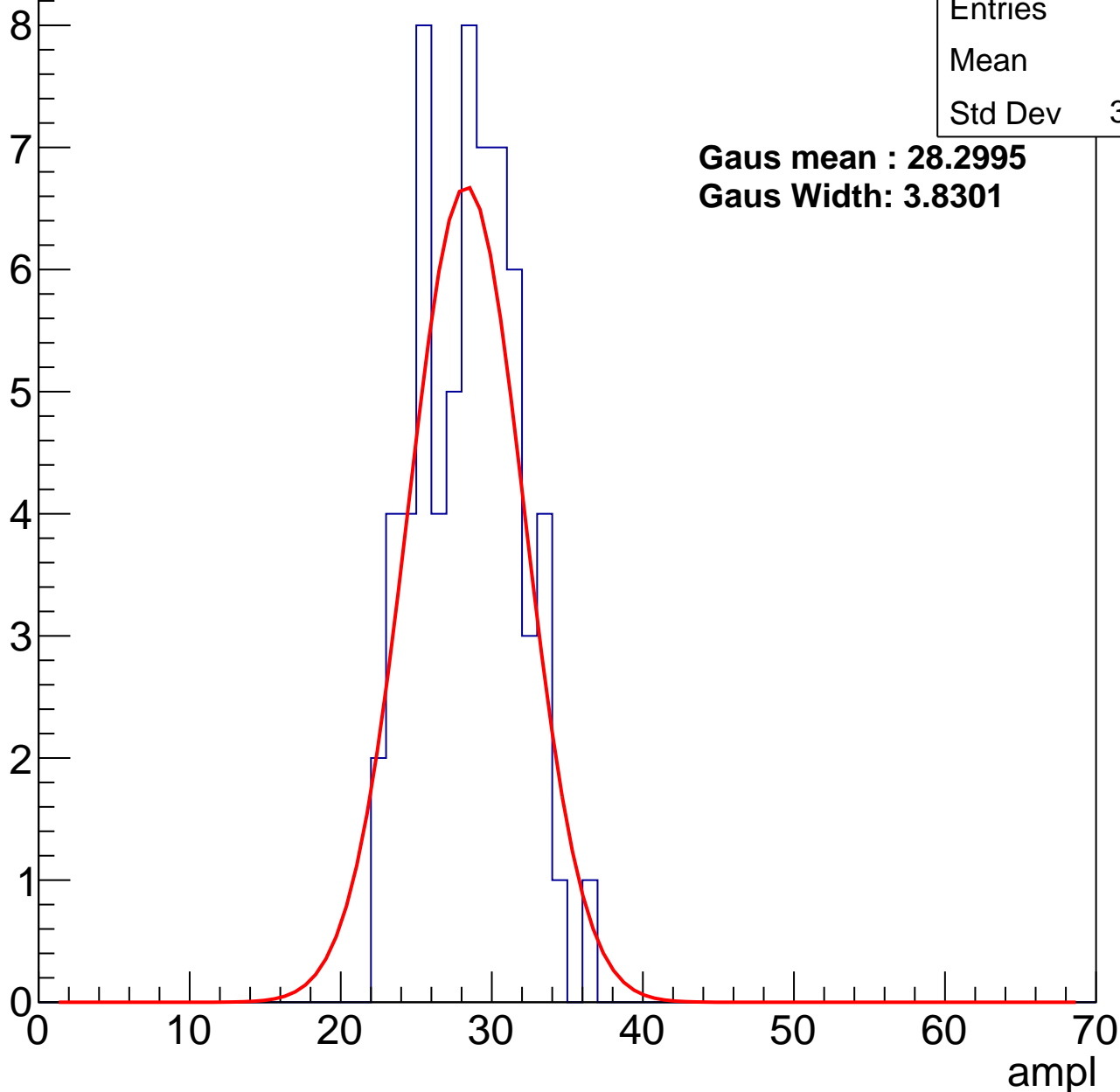
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28
Std Dev	3.226

Gaus mean : 28.2995

Gaus Width: 3.8301



B1L002S, U7-ch24, adc1

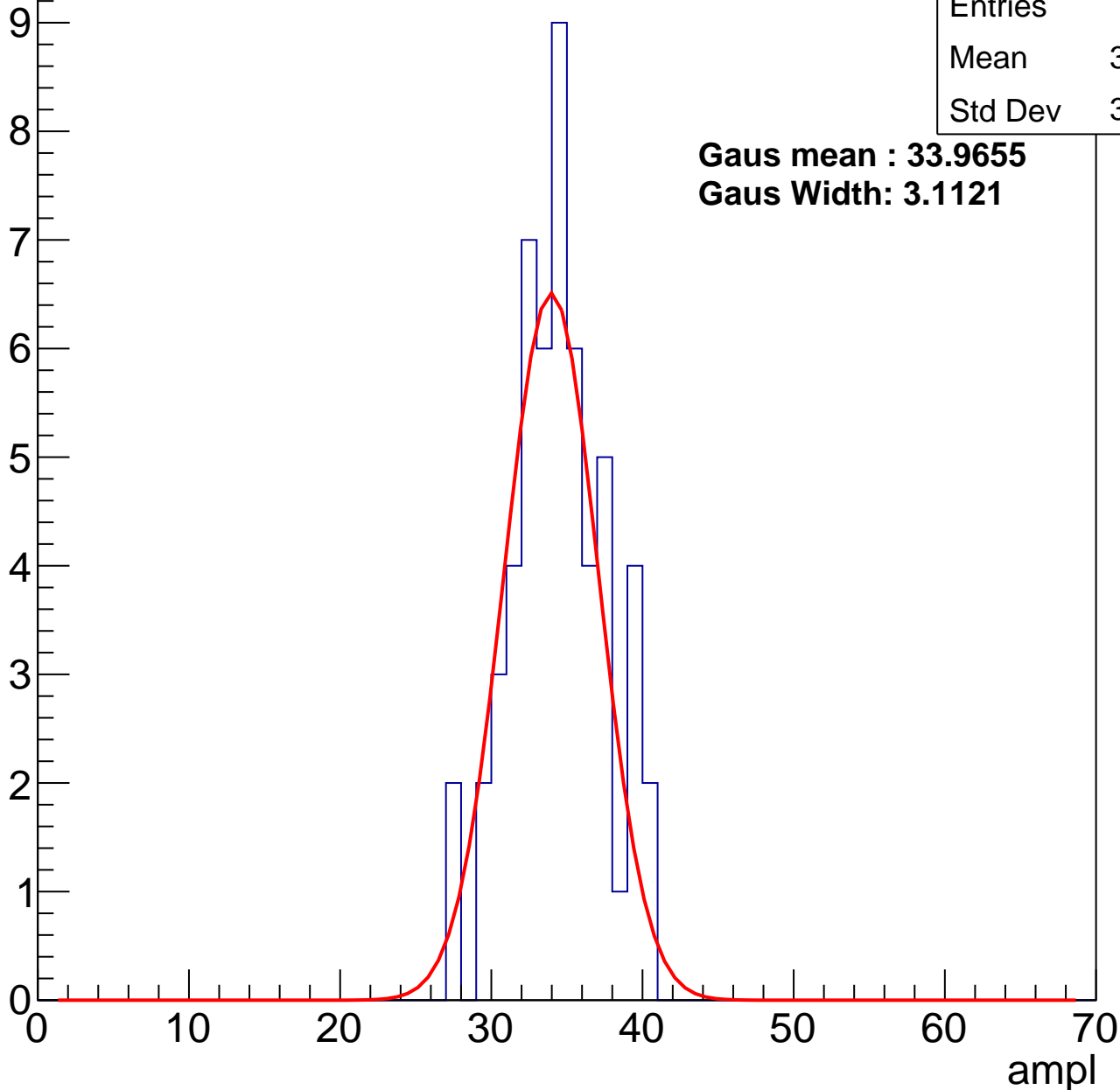
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	33.95
Std Dev	3.077

Gaus mean : 33.9655

Gaus Width: 3.1121



B1L002S, U7-ch24, adc2

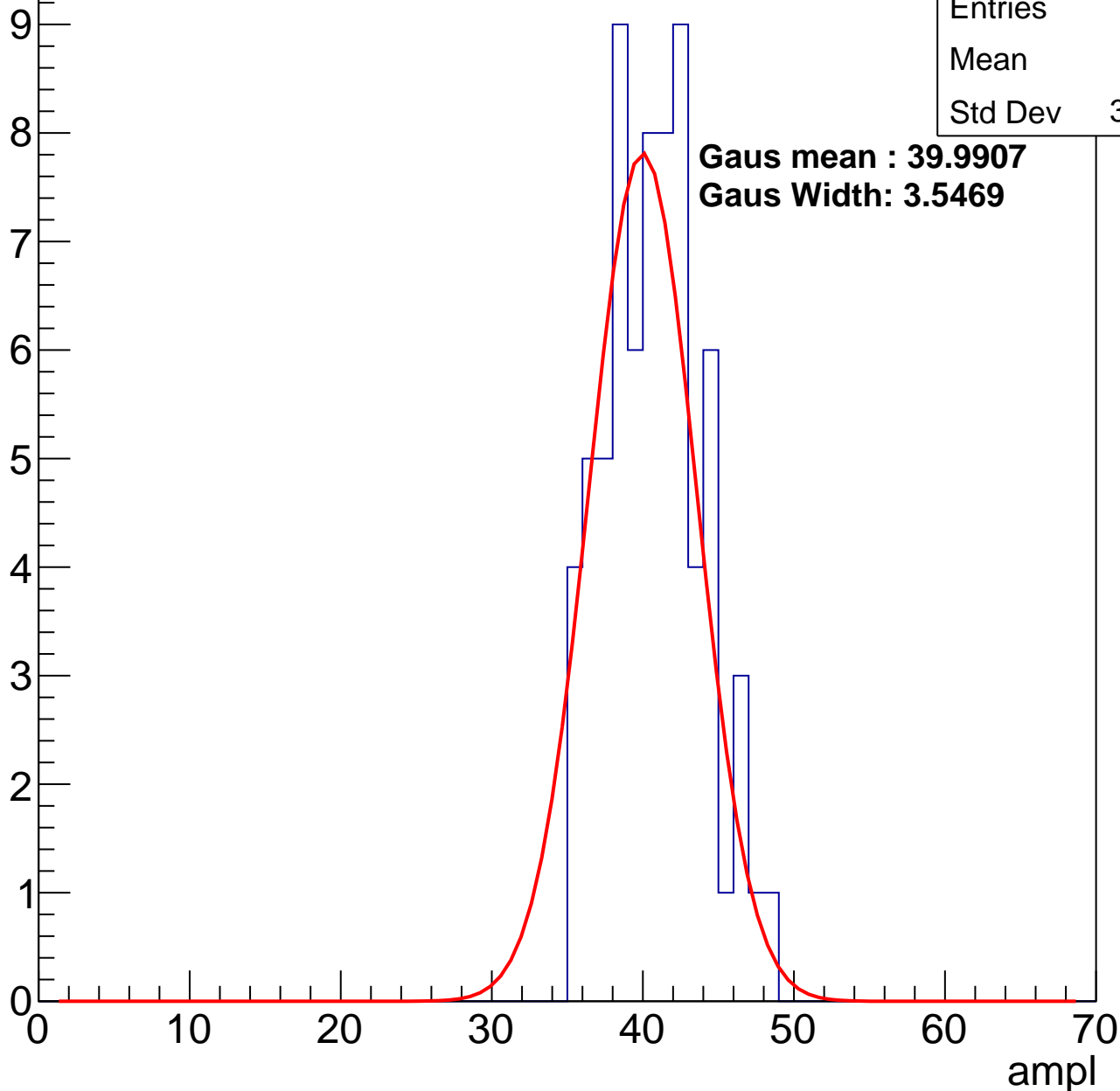
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.3
Std Dev	3.123

Gaus mean : 39.9907

Gaus Width: 3.5469



B1L002S, U7-ch24, adc3

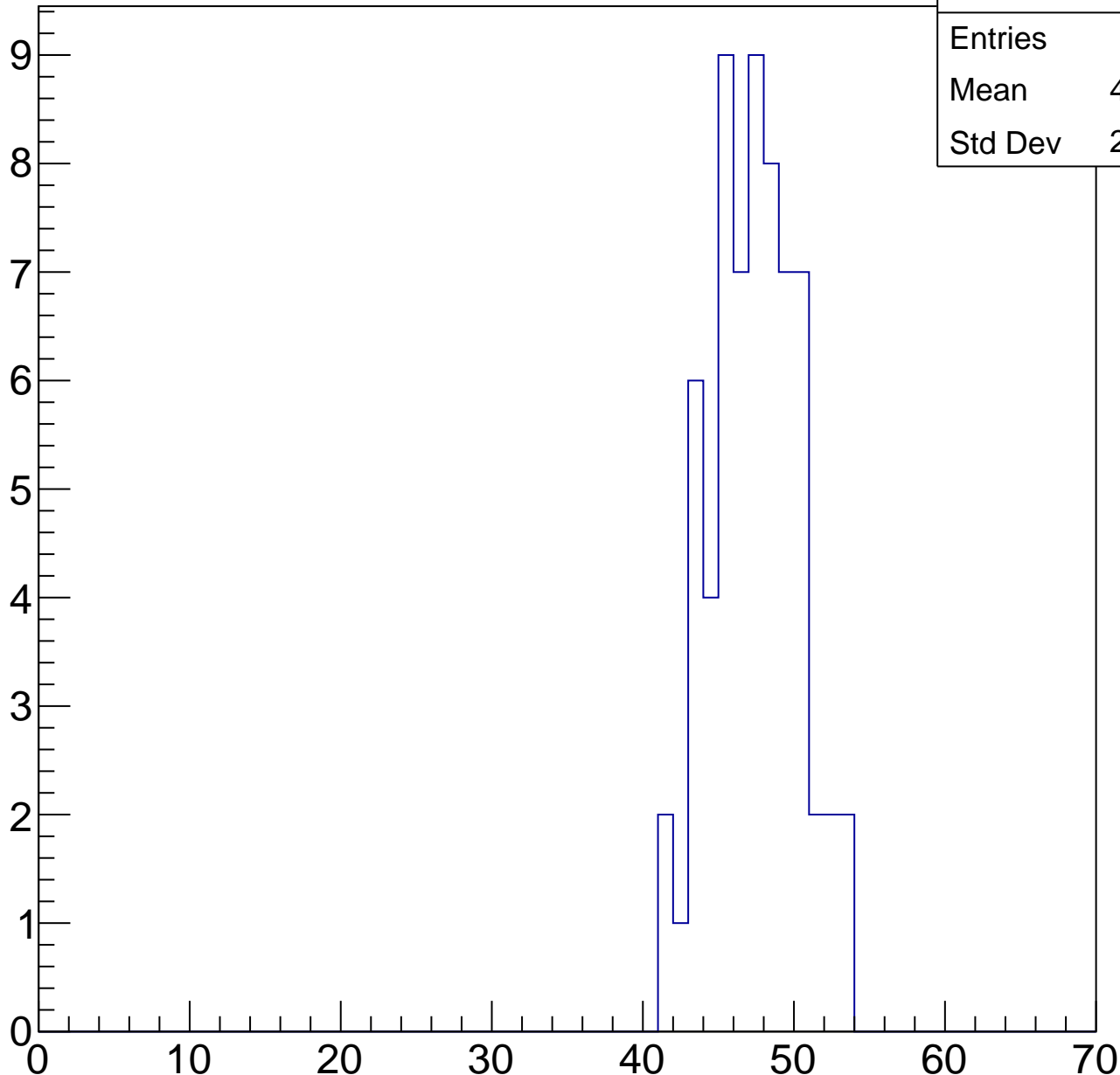
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	46.92
Std Dev	2.819

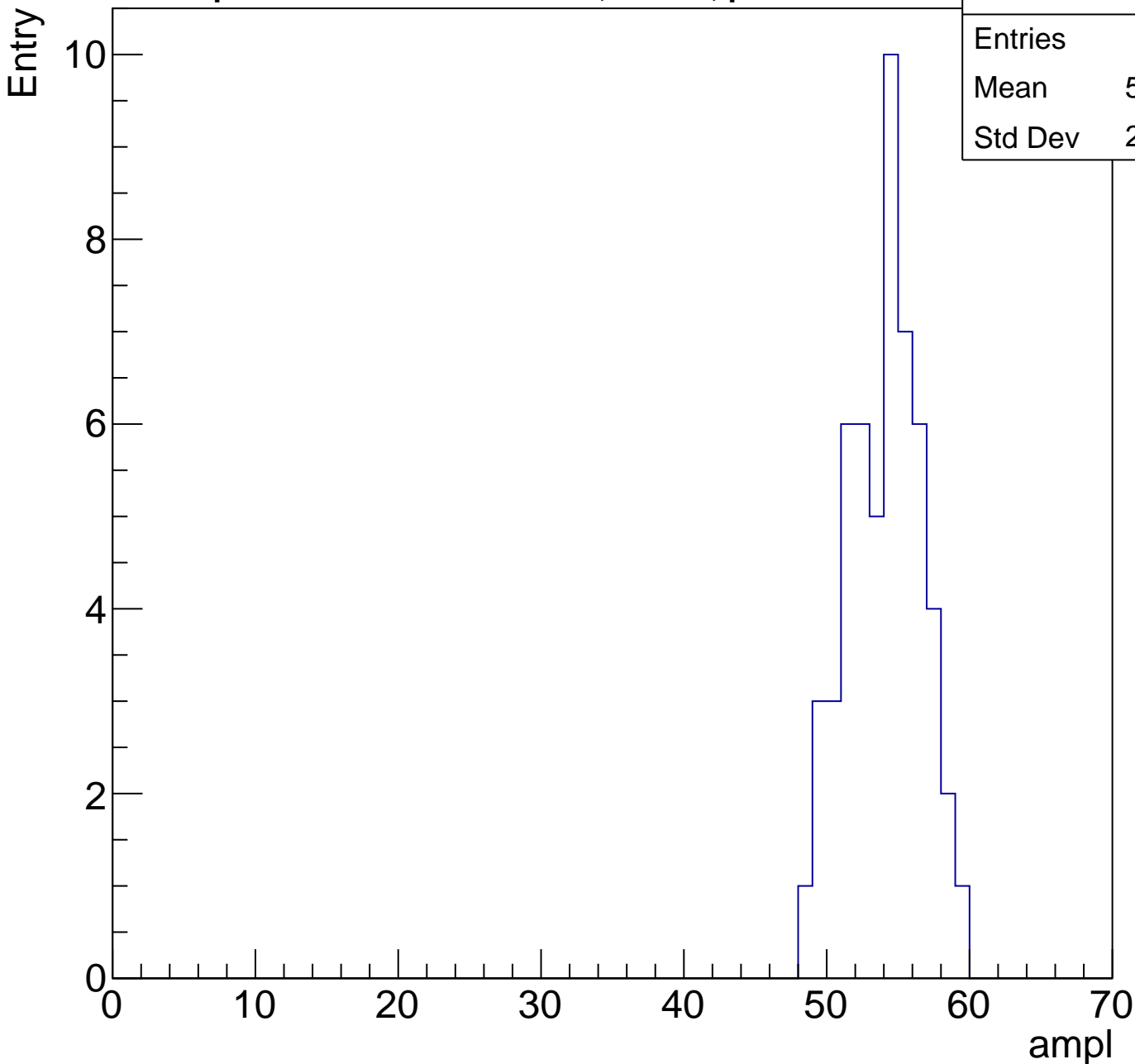
ampl



B1L002S, U7-ch24, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	54
Mean	53.56
Std Dev	2.565

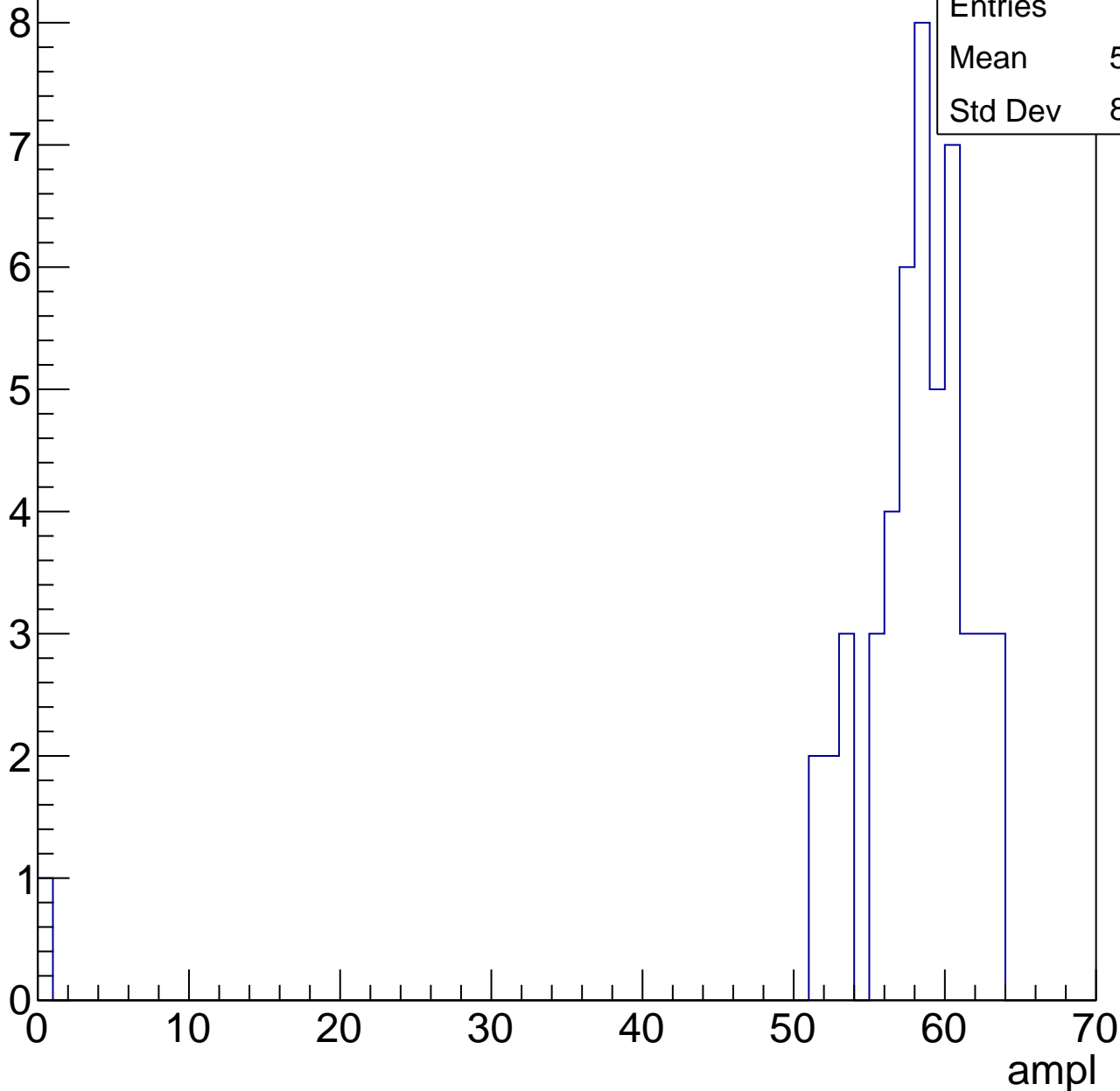


B1L002S, U7-ch24, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

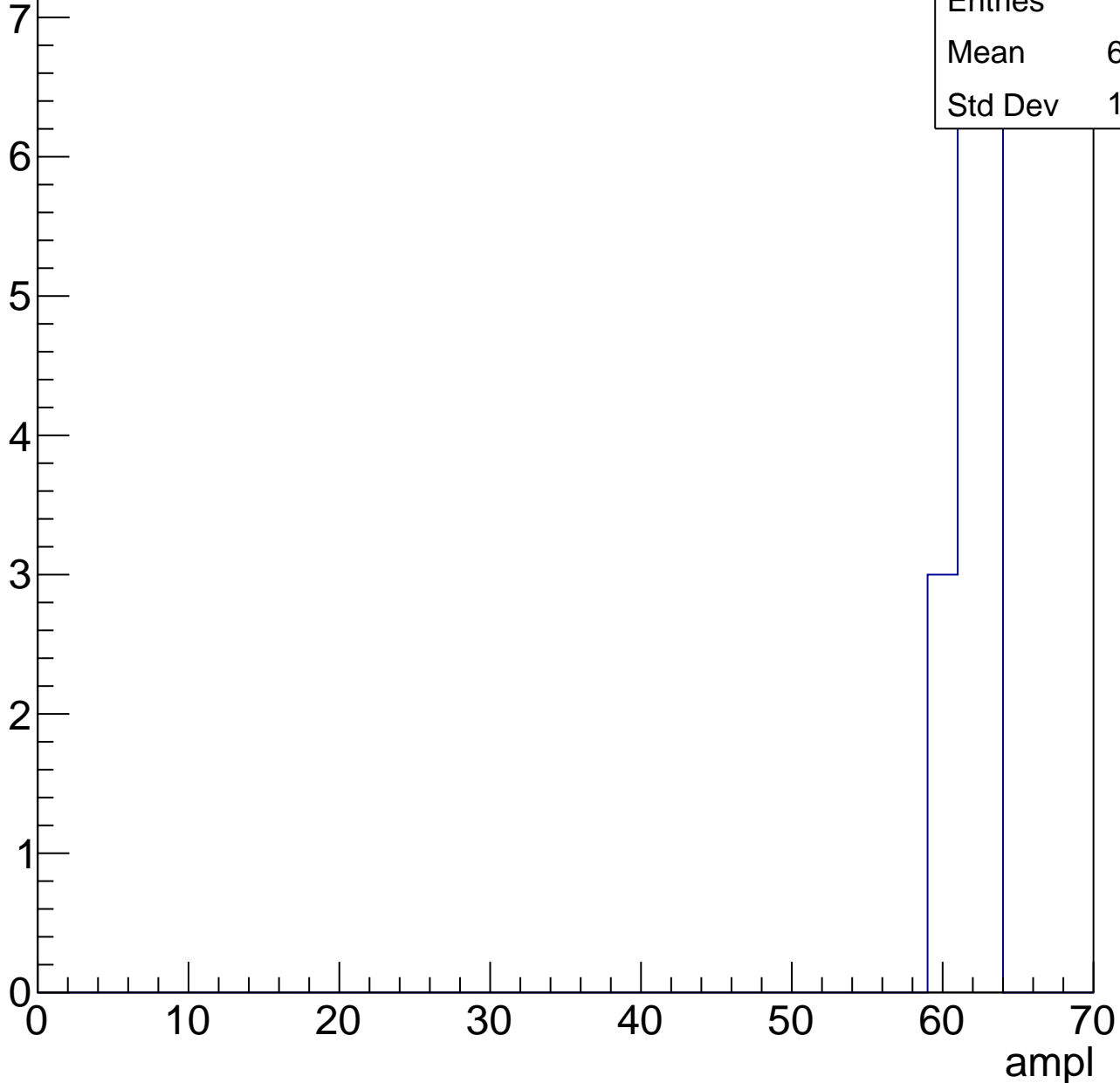
Entries	50
Mean	56.66
Std Dev	8.662



B1L002S, U7-ch24, adc6

calib_packv5_042523_0143.root, FC#10, port B3

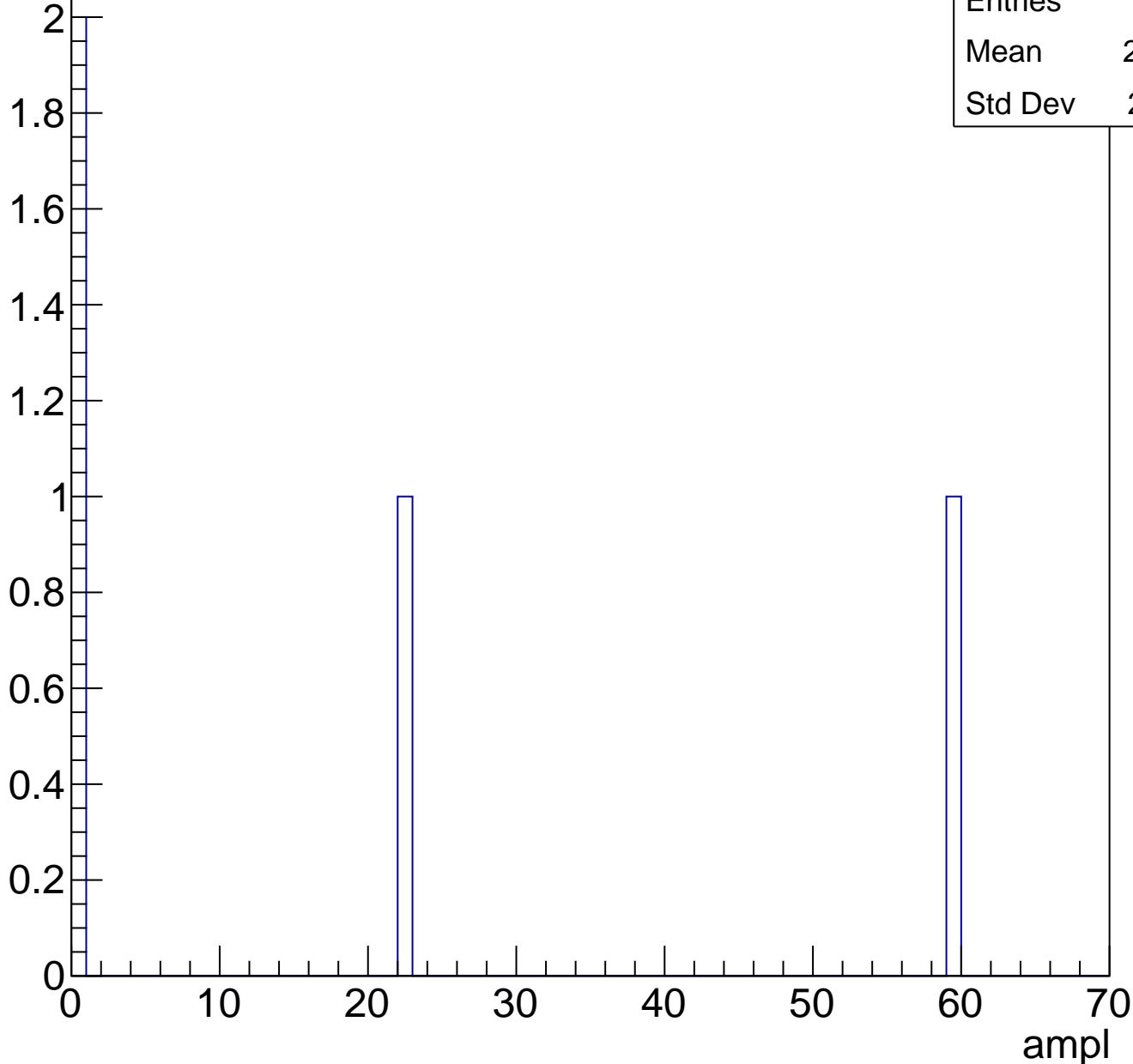
Entry



B1L002S, U7-ch24, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.25
Std Dev	24.11

B1L002S, U7-ch25, adc0

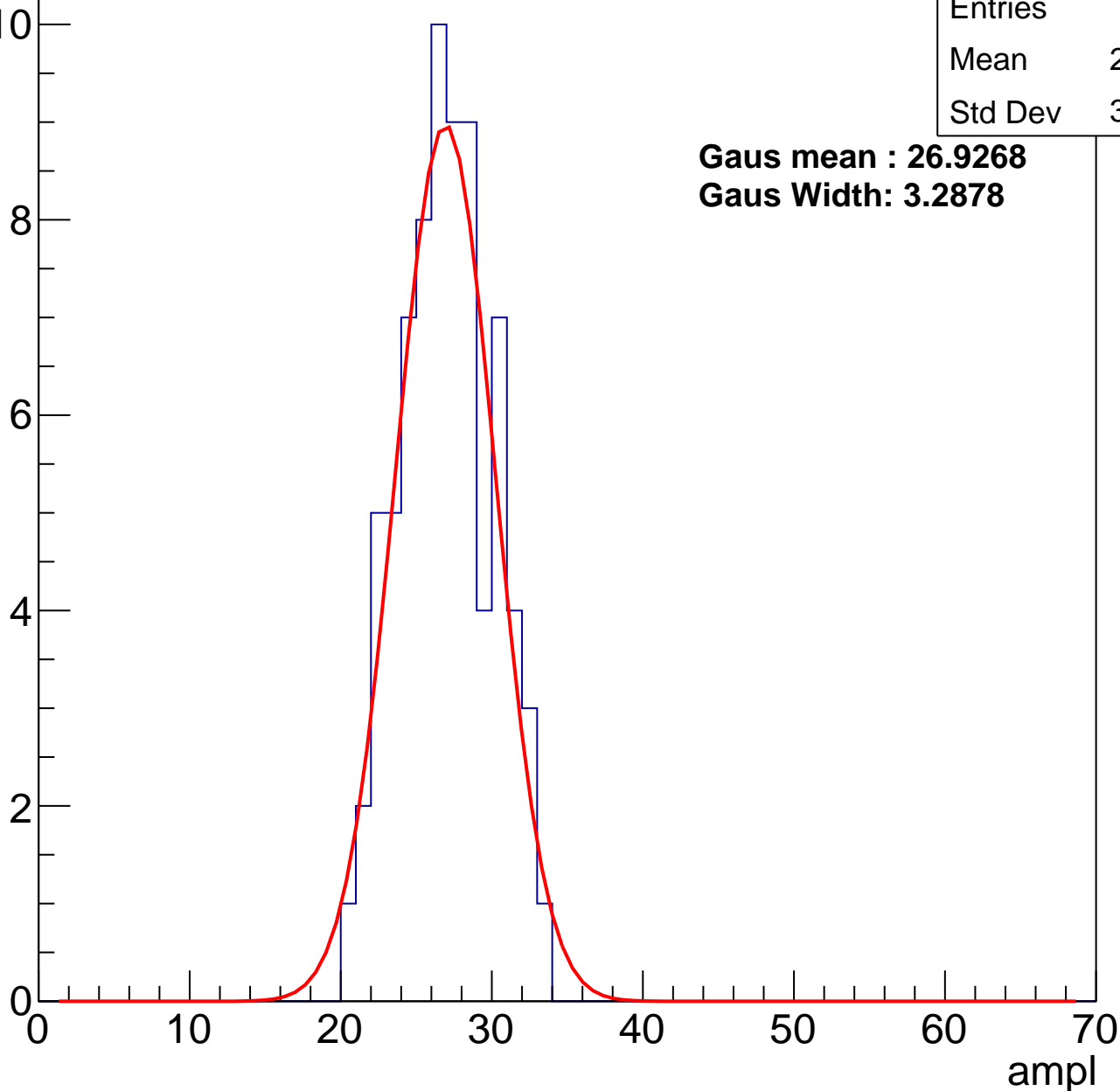
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	26.52
Std Dev	3.004

Gaus mean : 26.9268

Gaus Width: 3.2878



B1L002S, U7-ch25, adc1

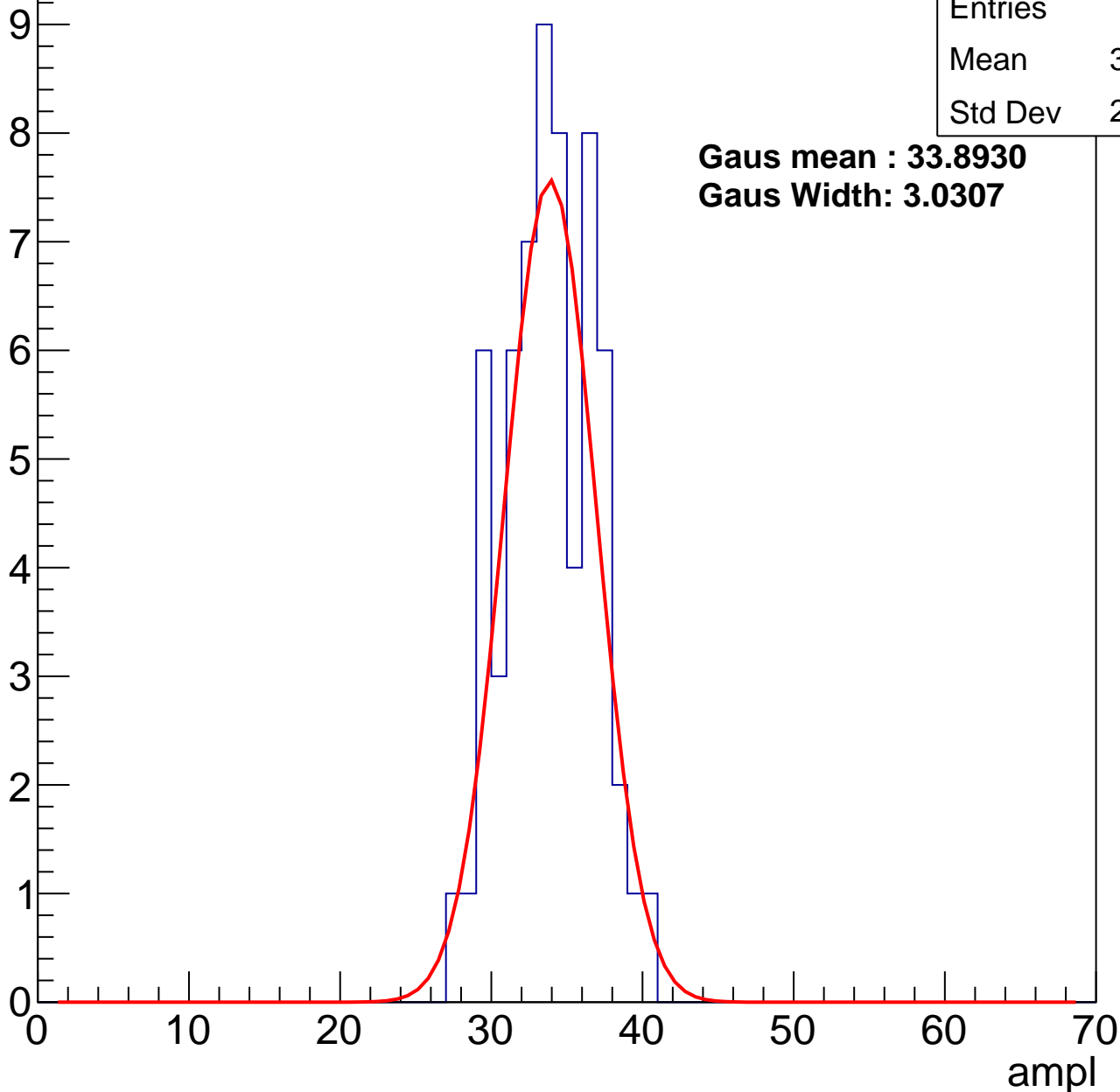
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.38
Std Dev	2.908

Gaus mean : 33.8930

Gaus Width: 3.0307



B1L002S, U7-ch25, adc2

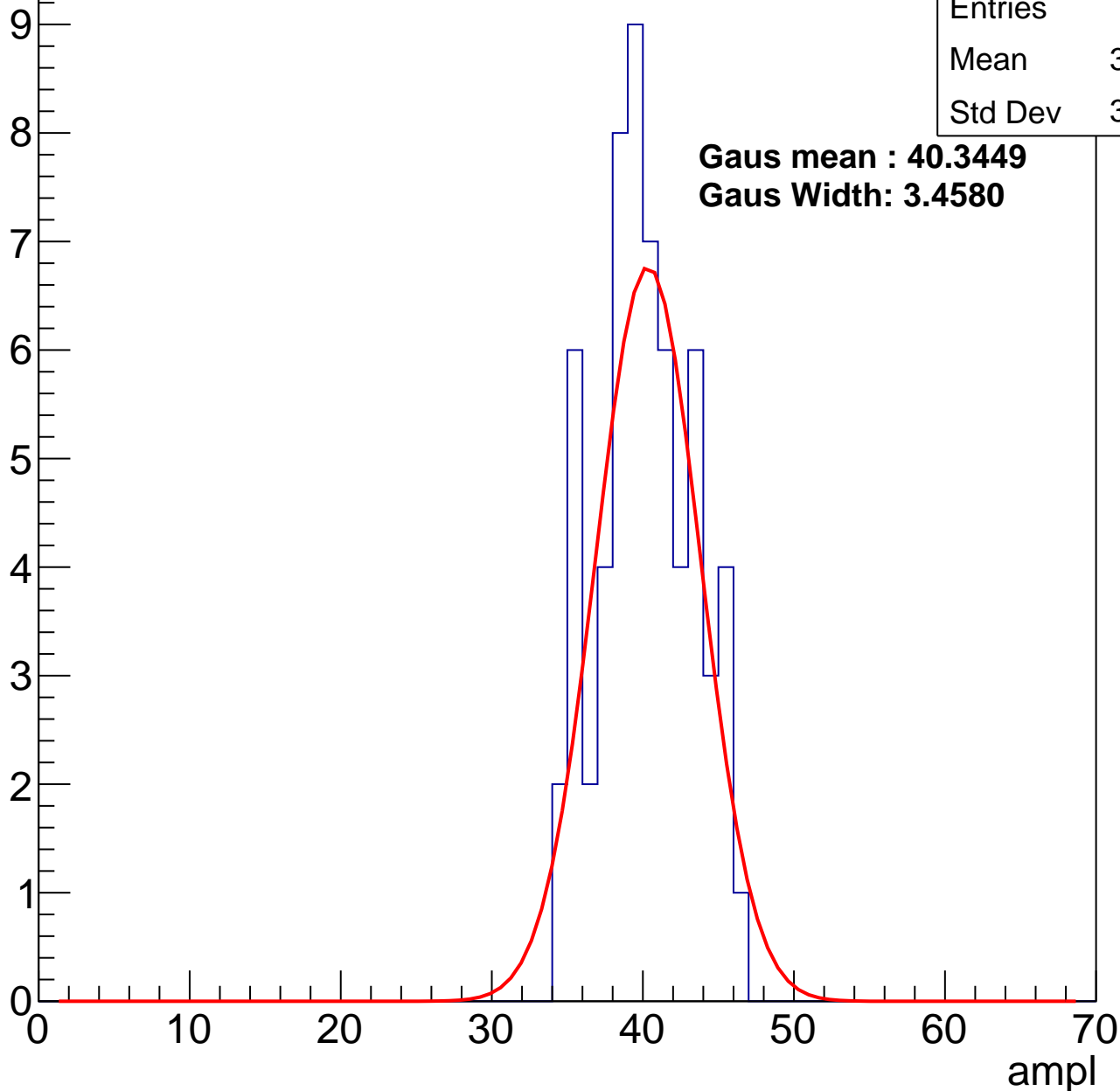
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.73
Std Dev	3.075

Gaus mean : 40.3449

Gaus Width: 3.4580



B1L002S, U7-ch25, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	63
Mean	45.62
Std Dev	2.925

Entry

10

8

6

4

2

0

0

10

20

30

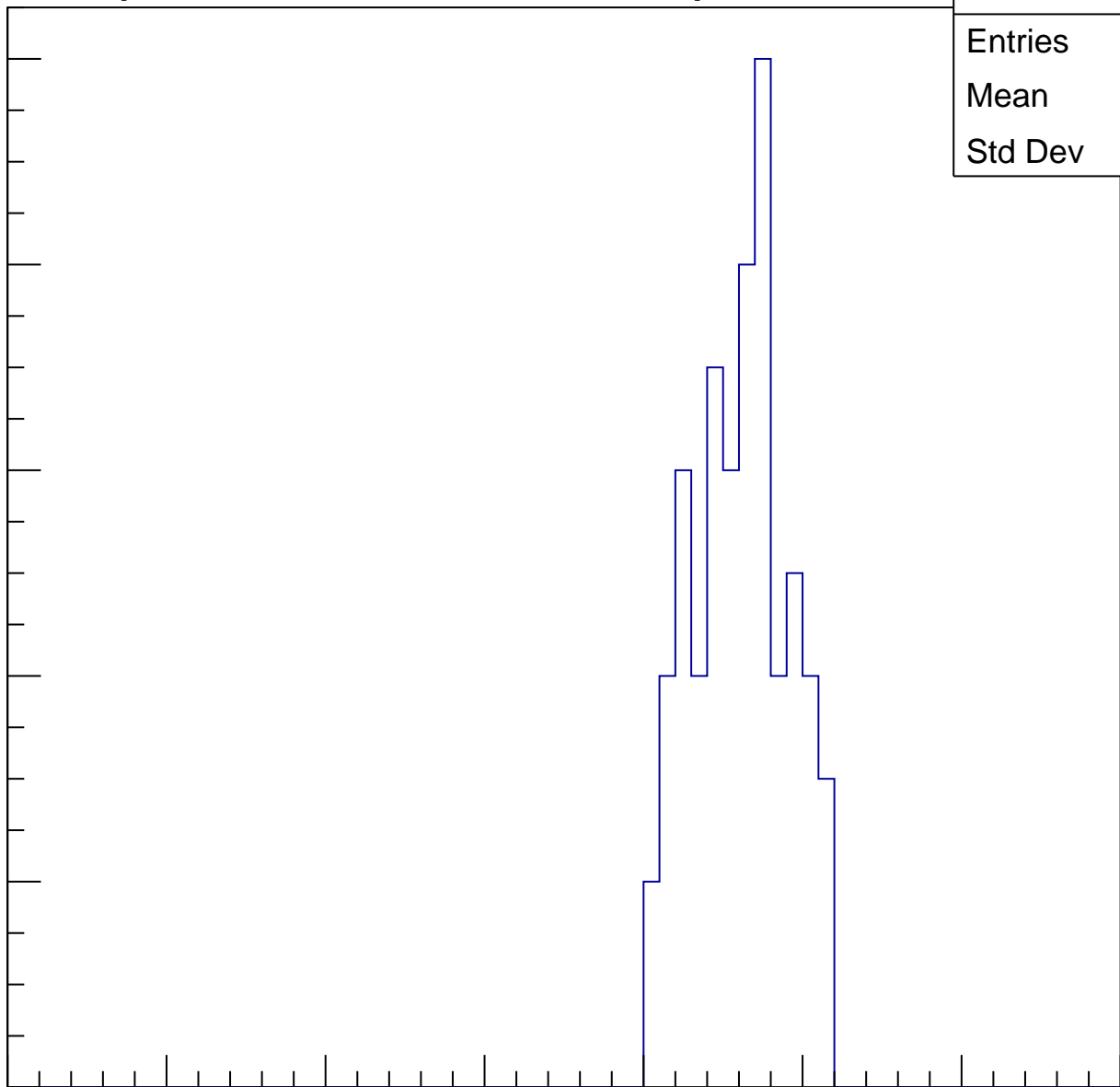
40

50

60

70

ampl

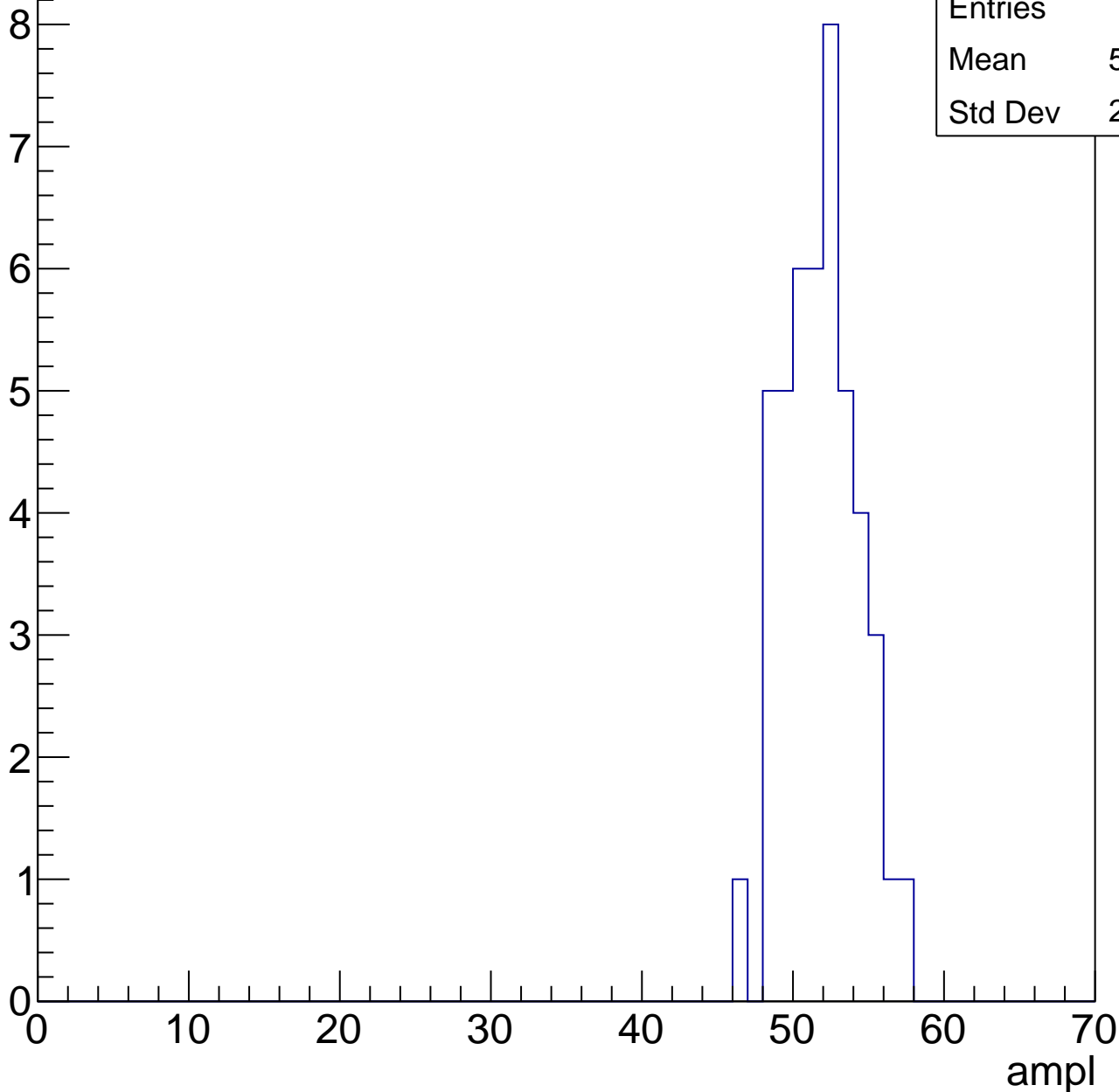


B1L002S, U7-ch25, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	51.38
Std Dev	2.416



B1L002S, U7-ch25, adc5

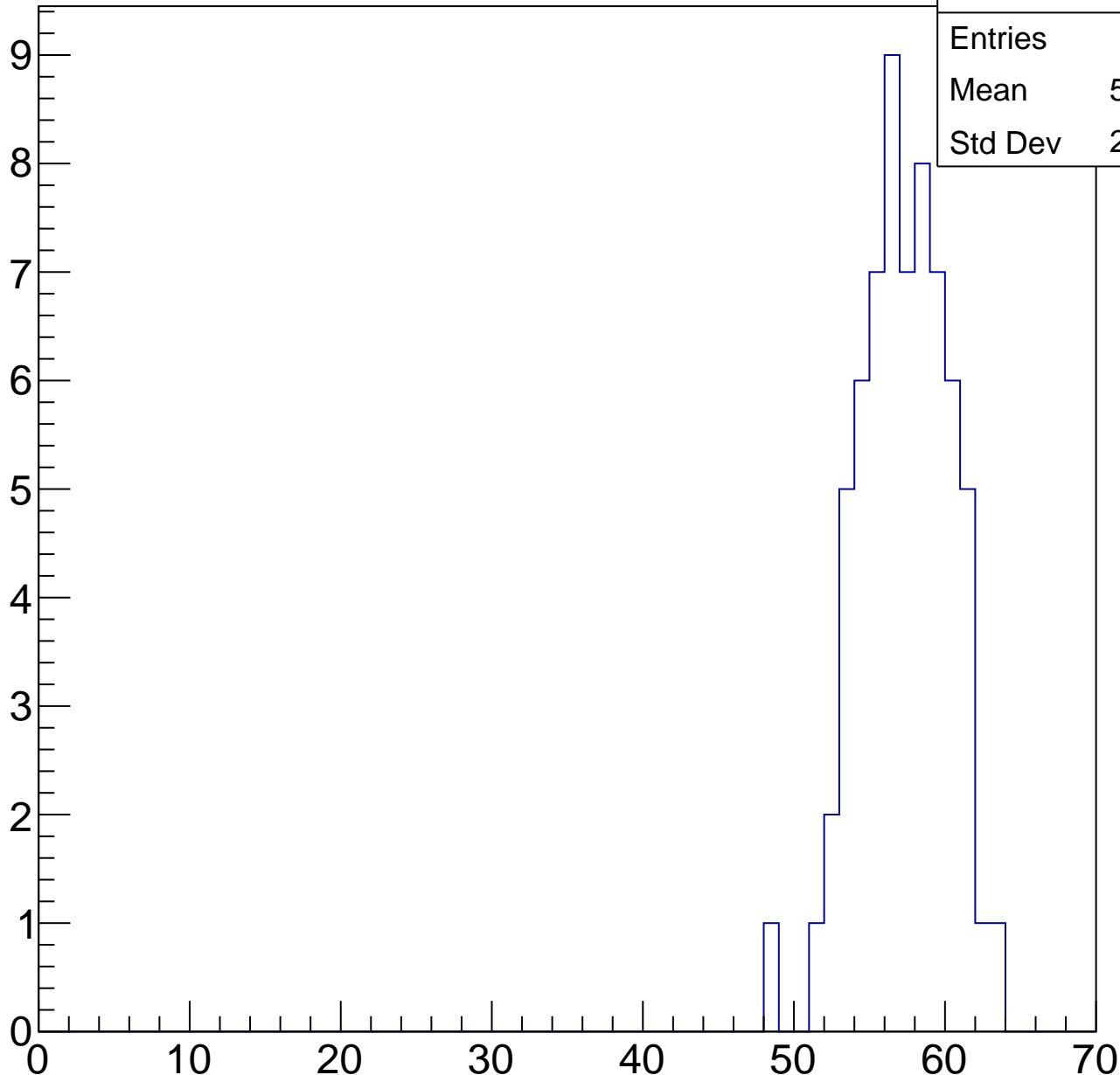
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	56.77
Std Dev	2.927

ampl



B1L002S, U7-ch25, adc6

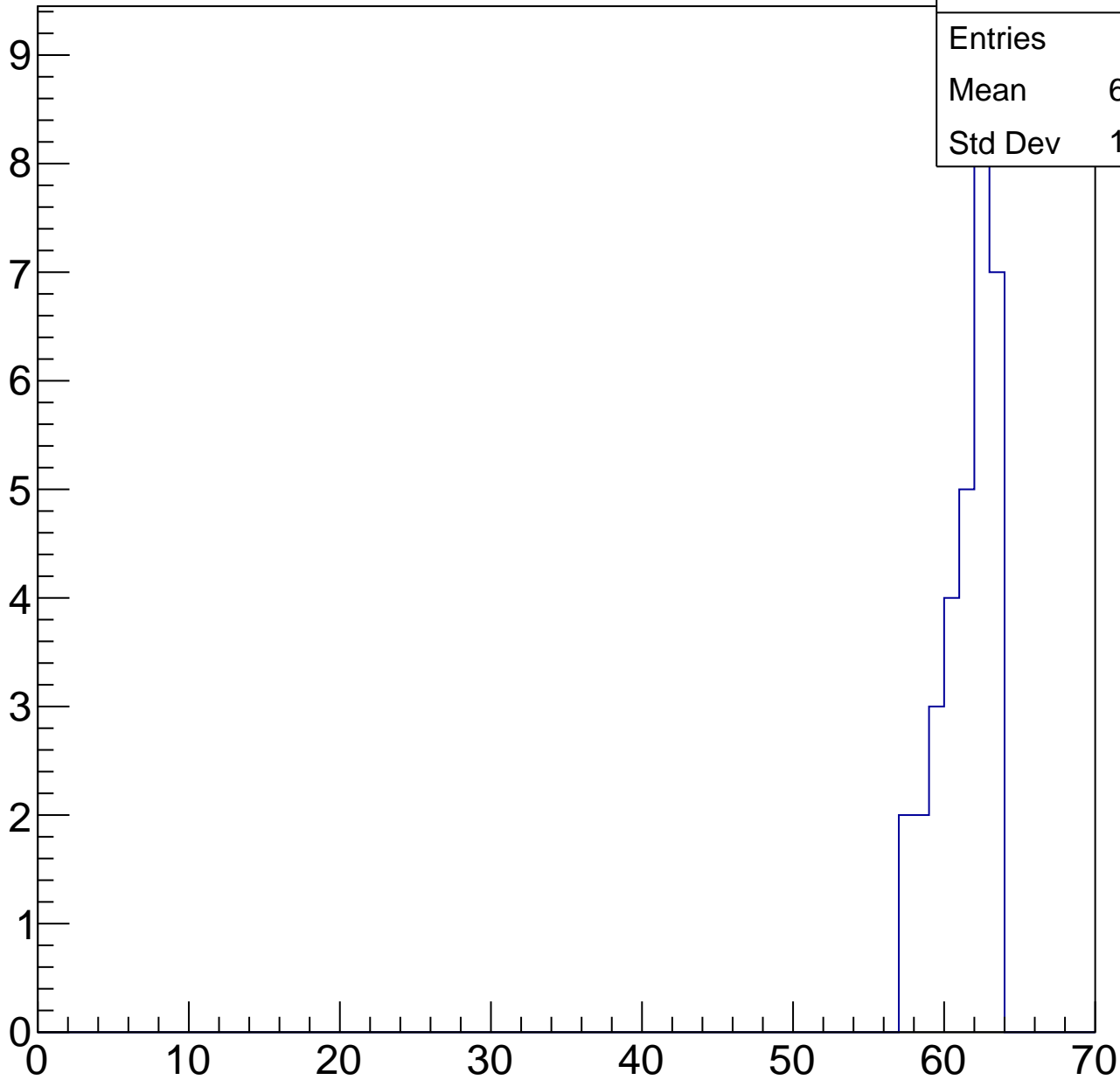
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.97
Std Dev	1.794

ampl



B1L002S, U7-ch25, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch26, adc0

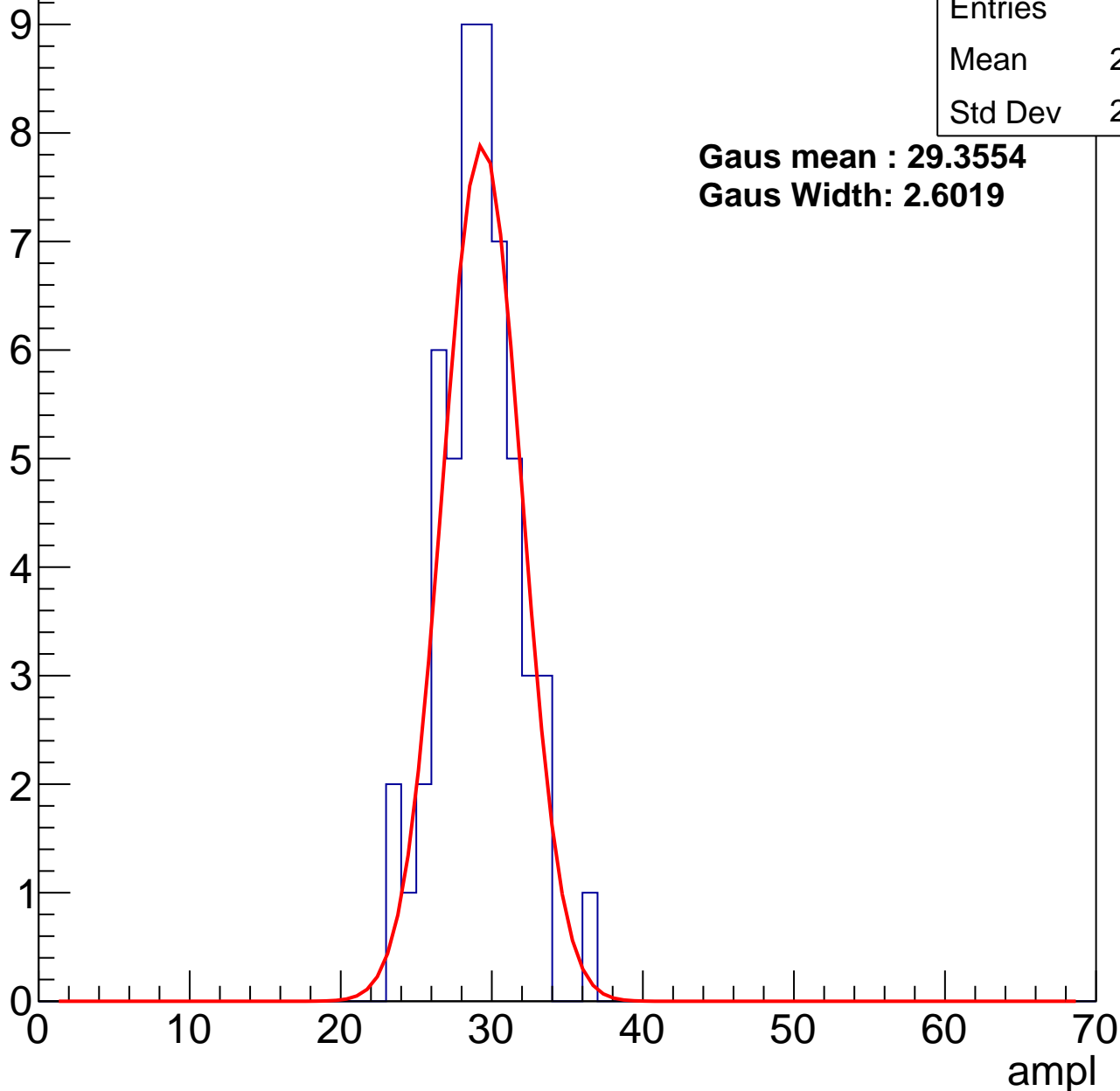
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.68
Std Dev	2.597

Gaus mean : 29.3554

Gaus Width: 2.6019



B1L002S, U7-ch26, adc1

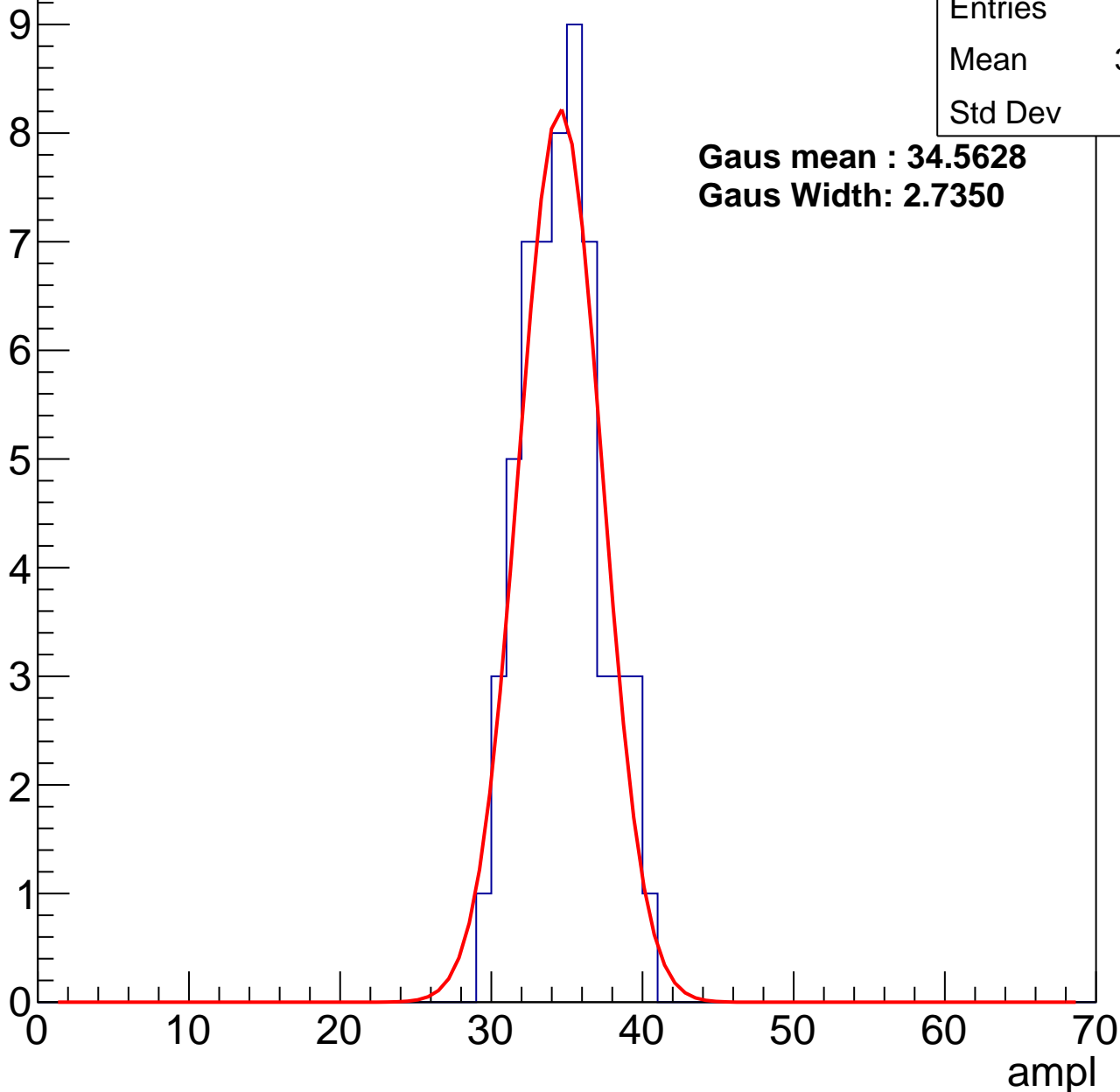
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.21
Std Dev	2.56

Gaus mean : 34.5628

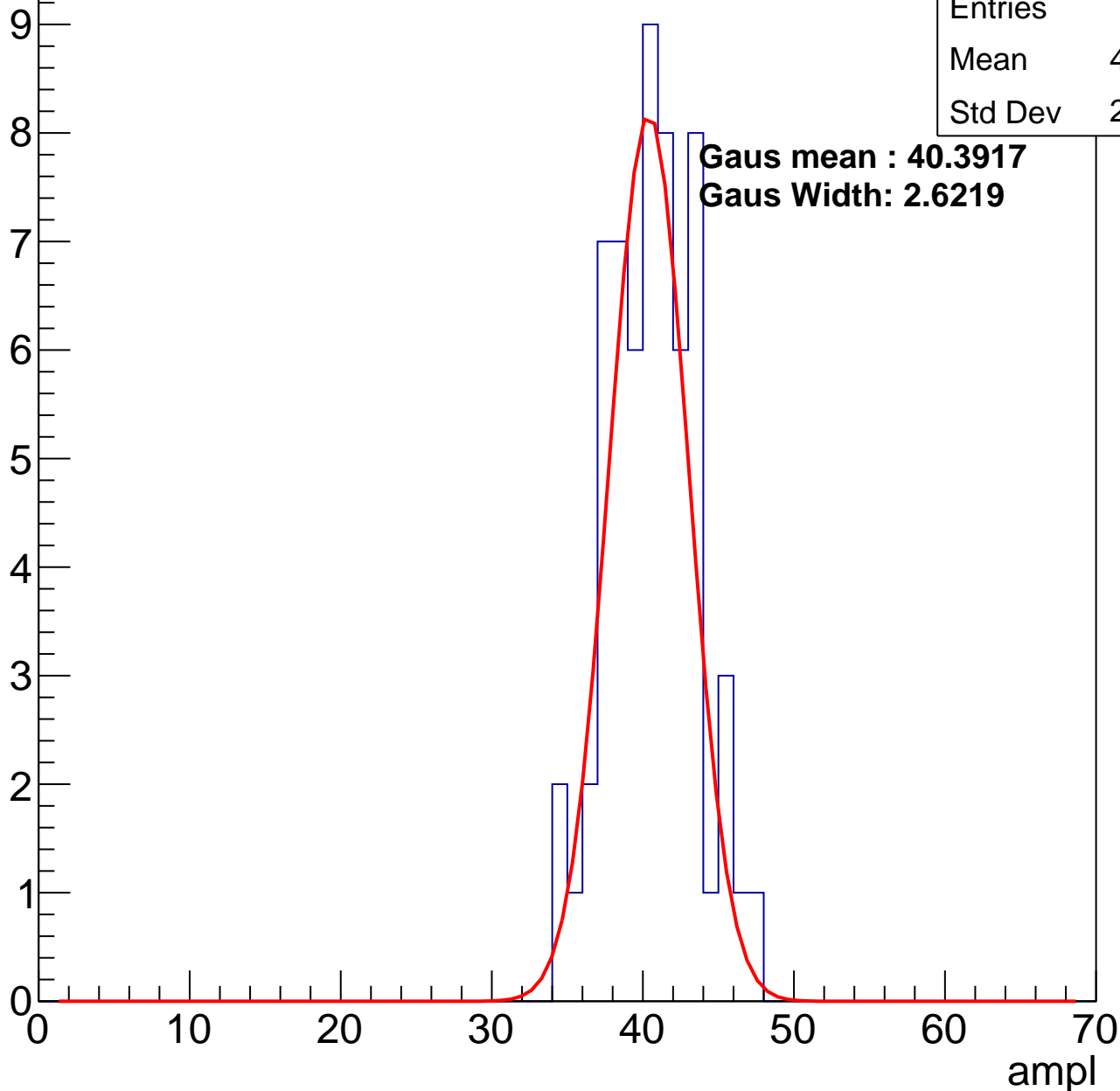
Gaus Width: 2.7350



B1L002S, U7-ch26, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

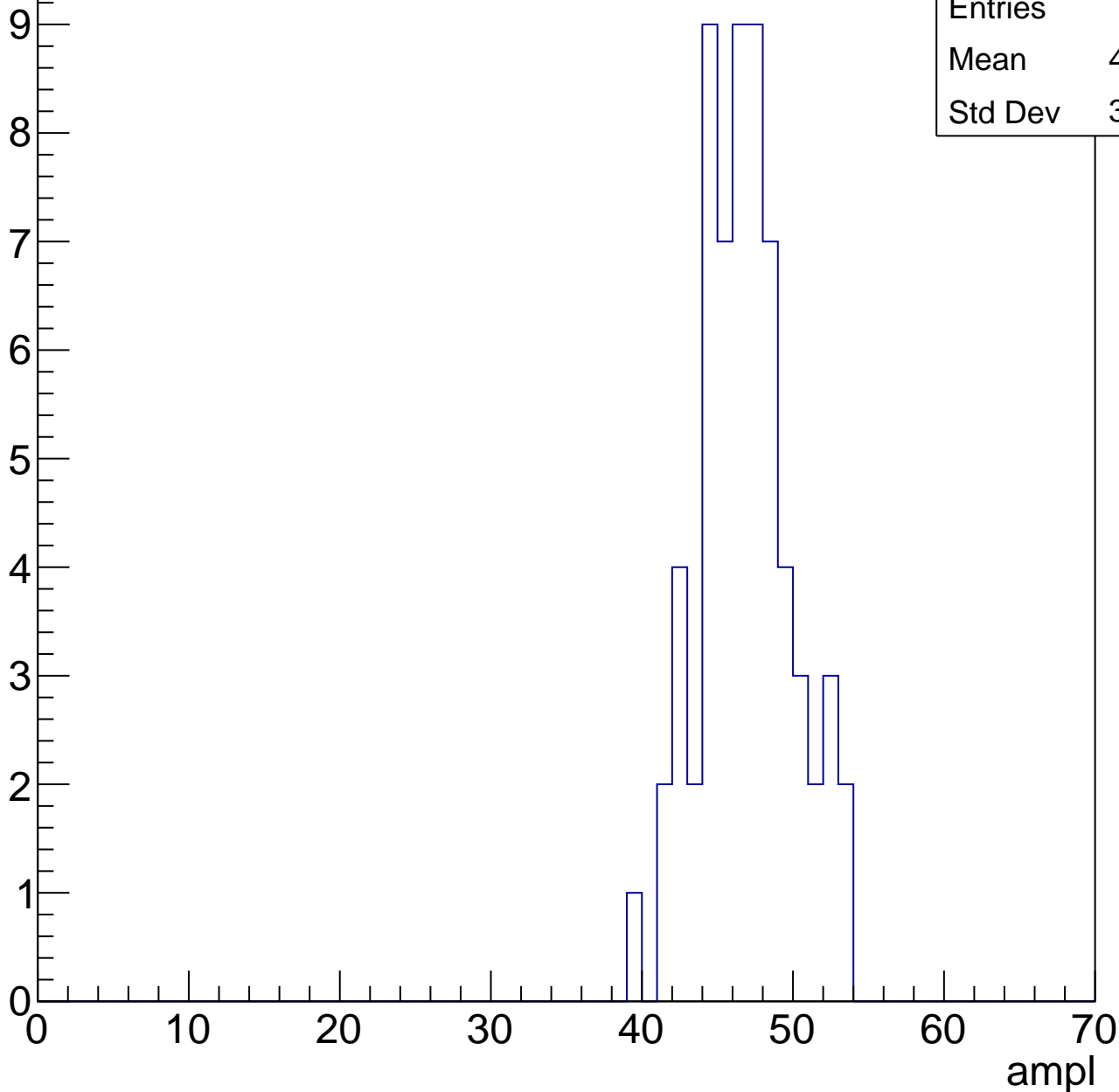


B1L002S, U7-ch26, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.39
Std Dev	3.039

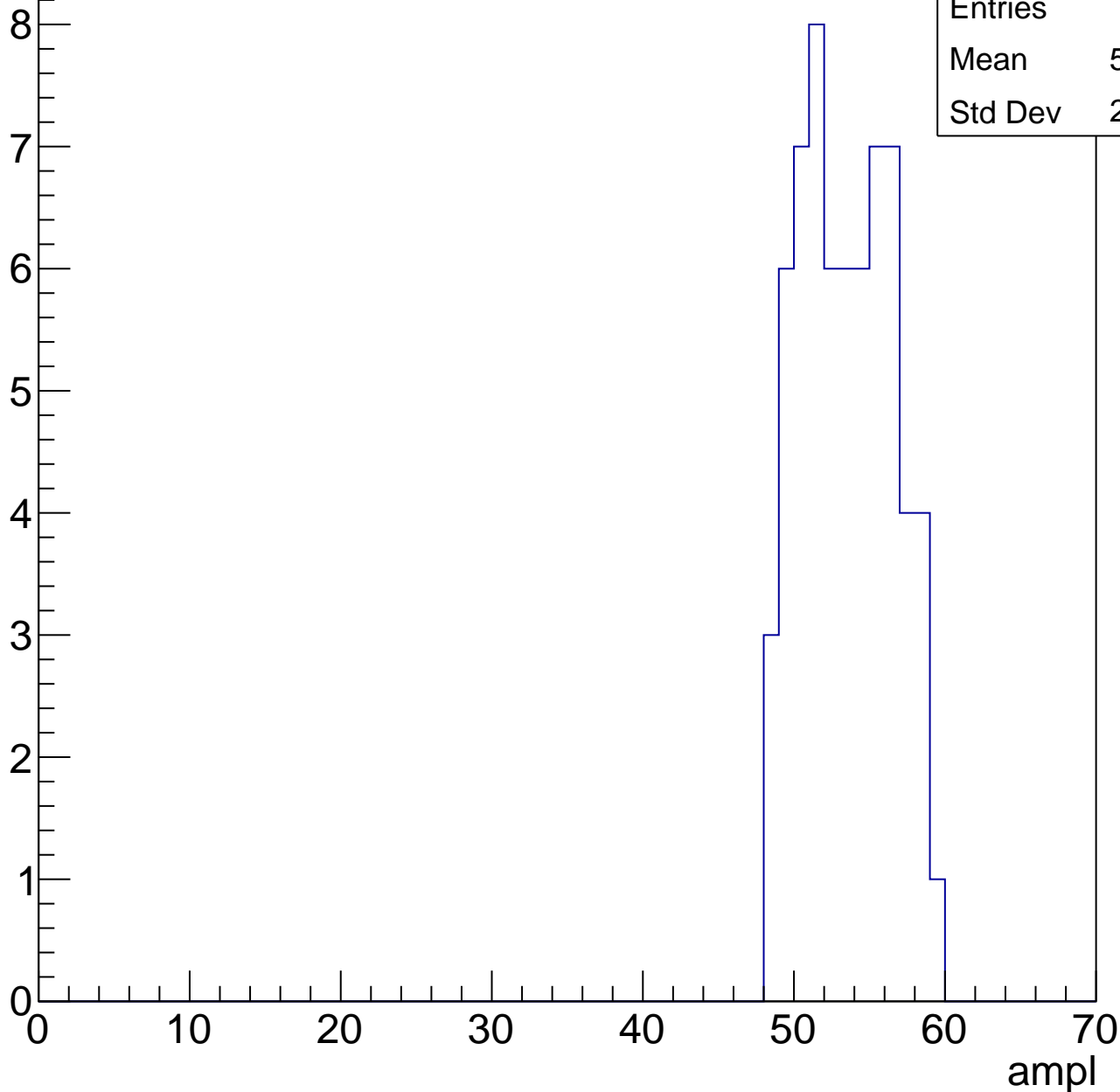


B1L002S, U7-ch26, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	53.02
Std Dev	2.959

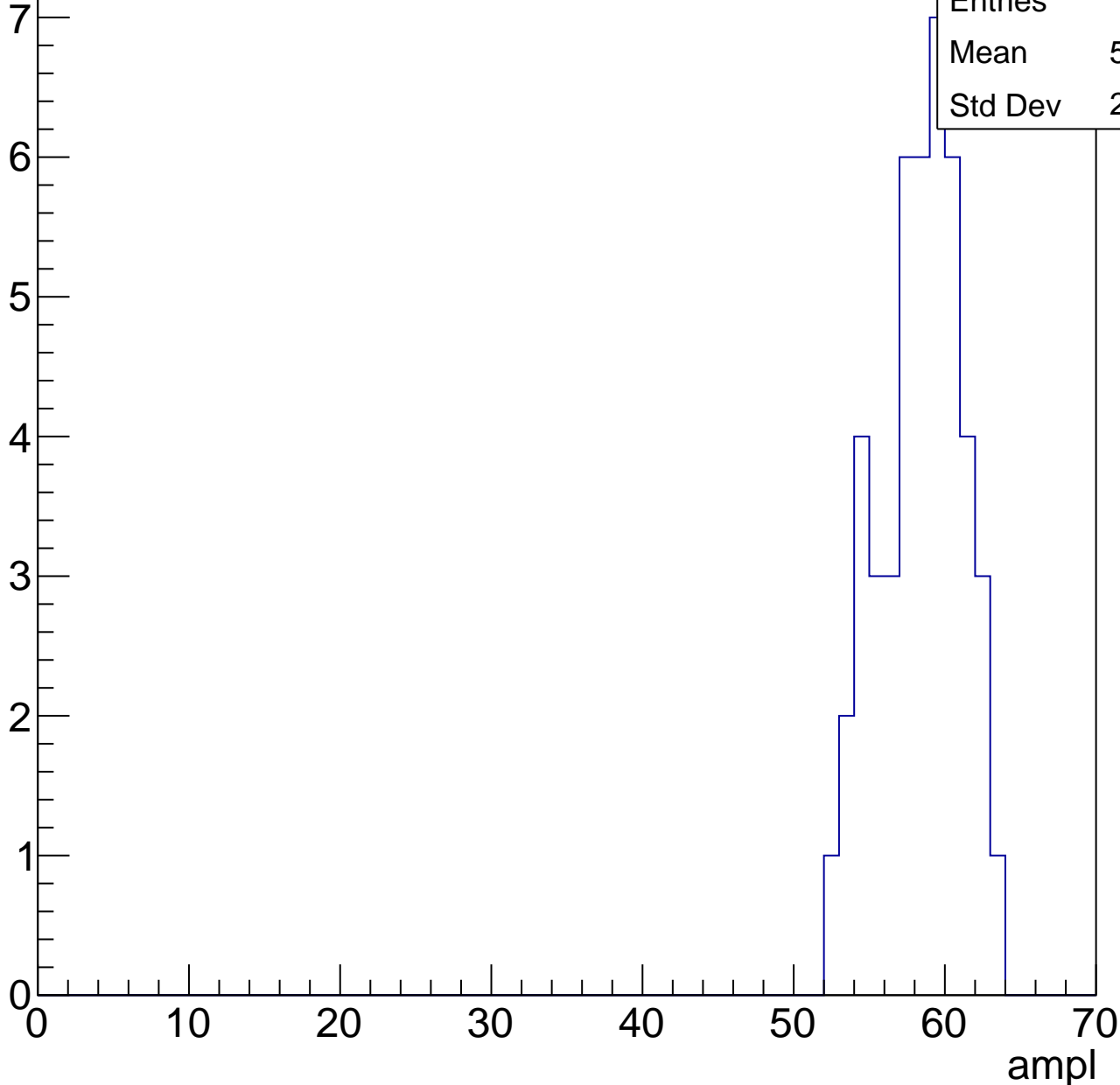


B1L002S, U7-ch26, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

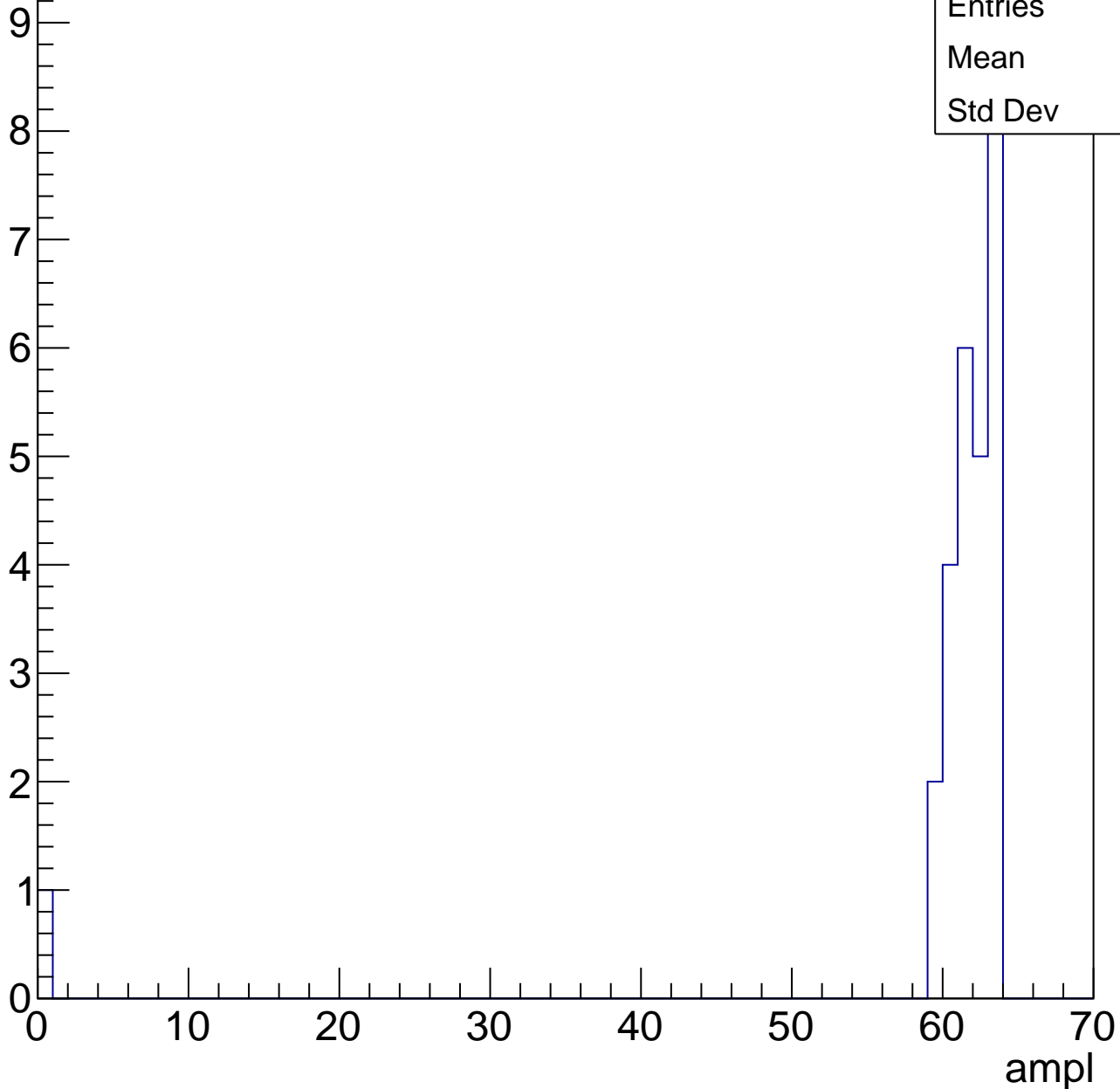
Entries	46
Mean	57.89
Std Dev	2.696



B1L002S, U7-ch26, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

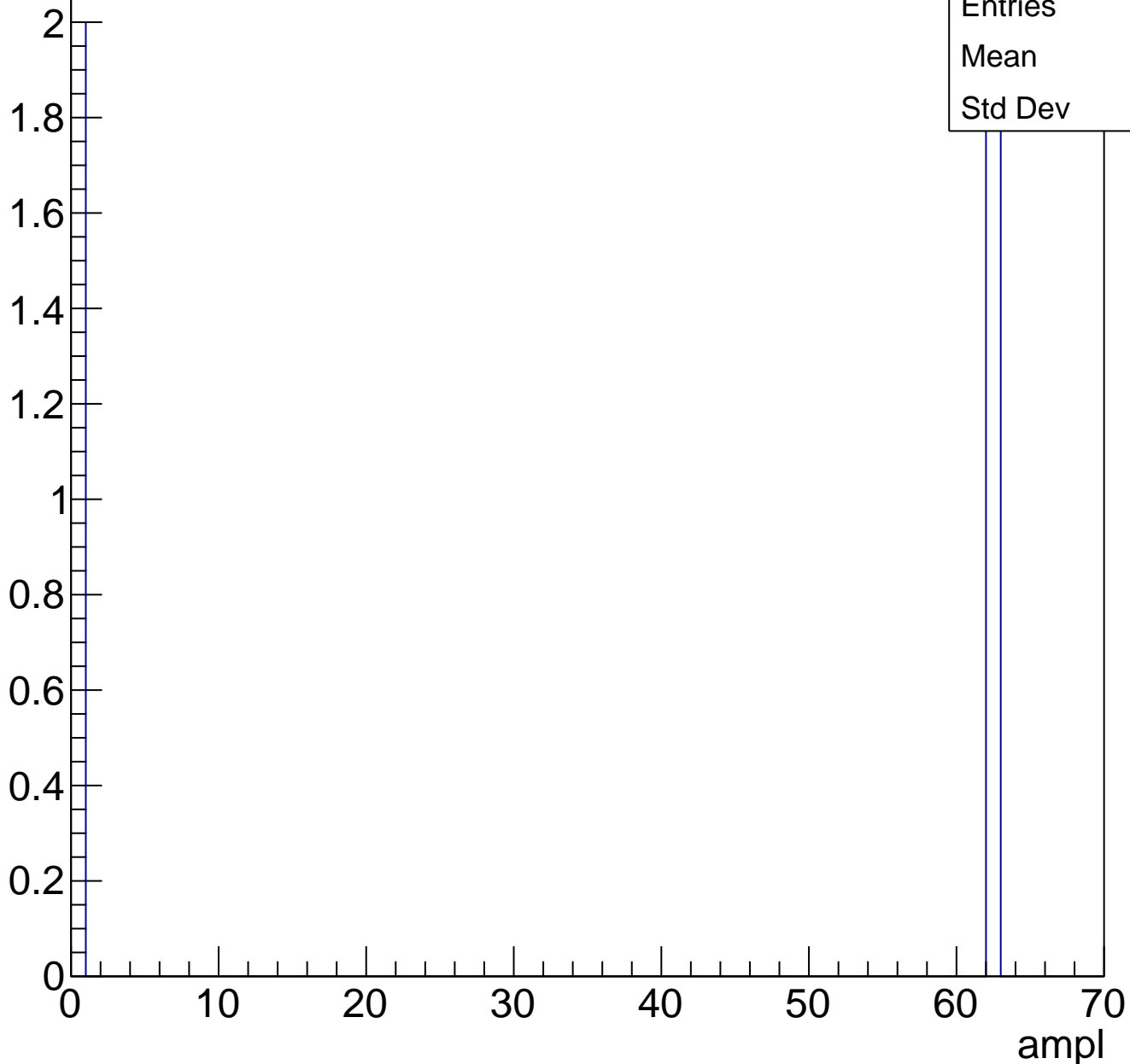


Entries	27
Mean	59.3
Std Dev	11.7

B1L002S, U7-ch26, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch27, adc0

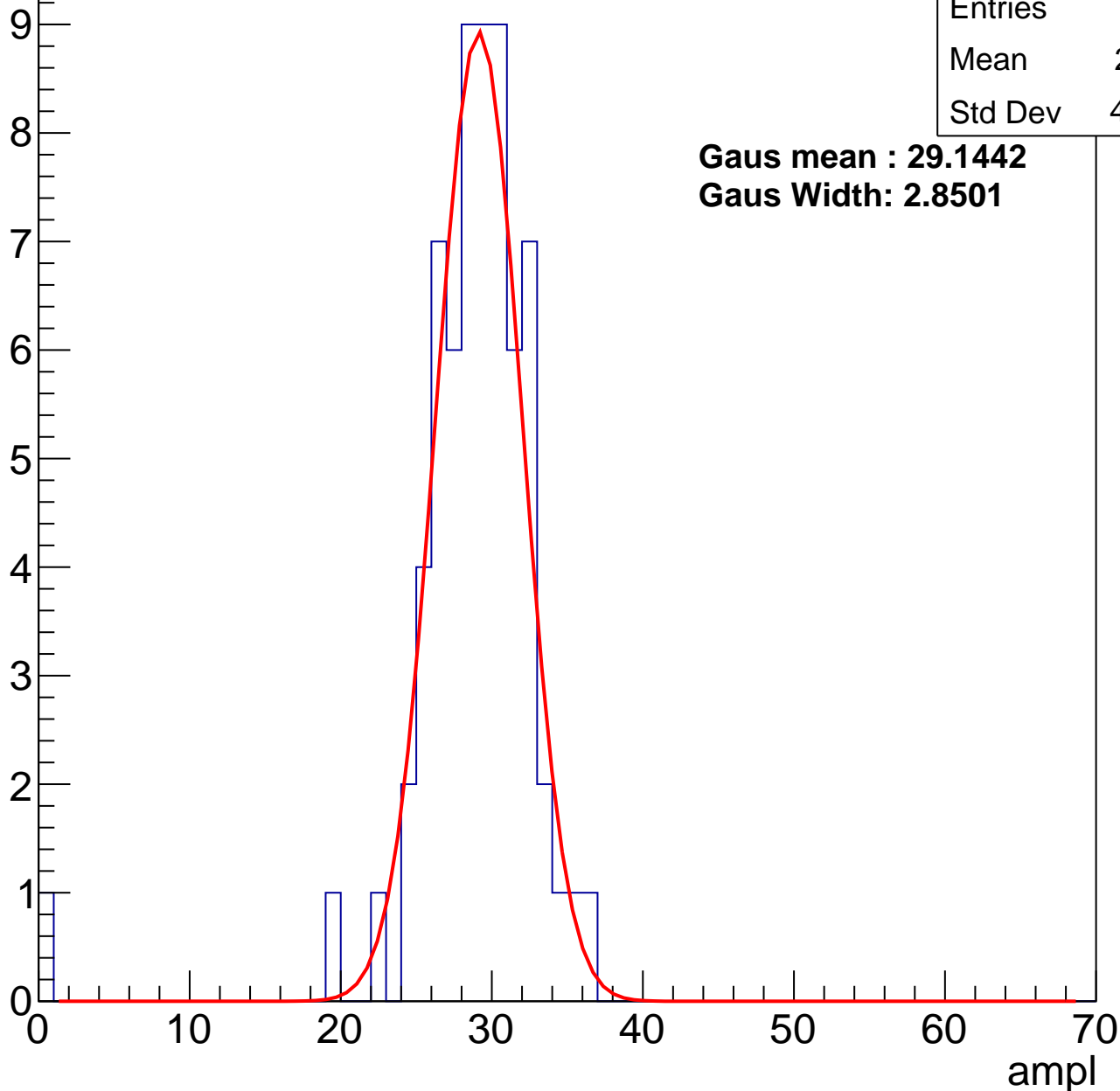
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.31
Std Dev	4.582

Gaus mean : 29.1442

Gaus Width: 2.8501



B1L002S, U7-ch27, adc1

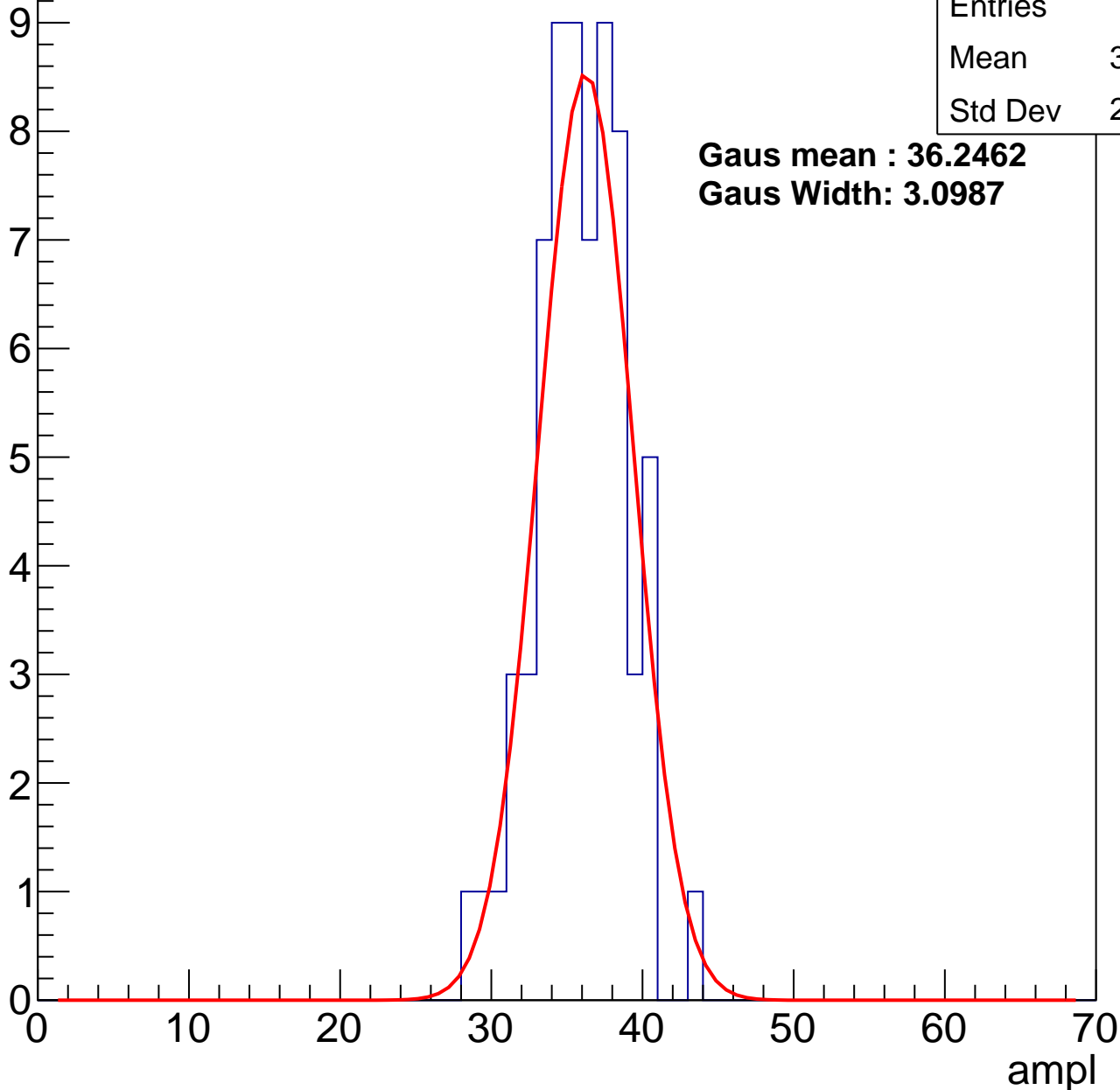
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	35.48
Std Dev	2.888

Gaus mean : 36.2462

Gaus Width: 3.0987



B1L002S, U7-ch27, adc2

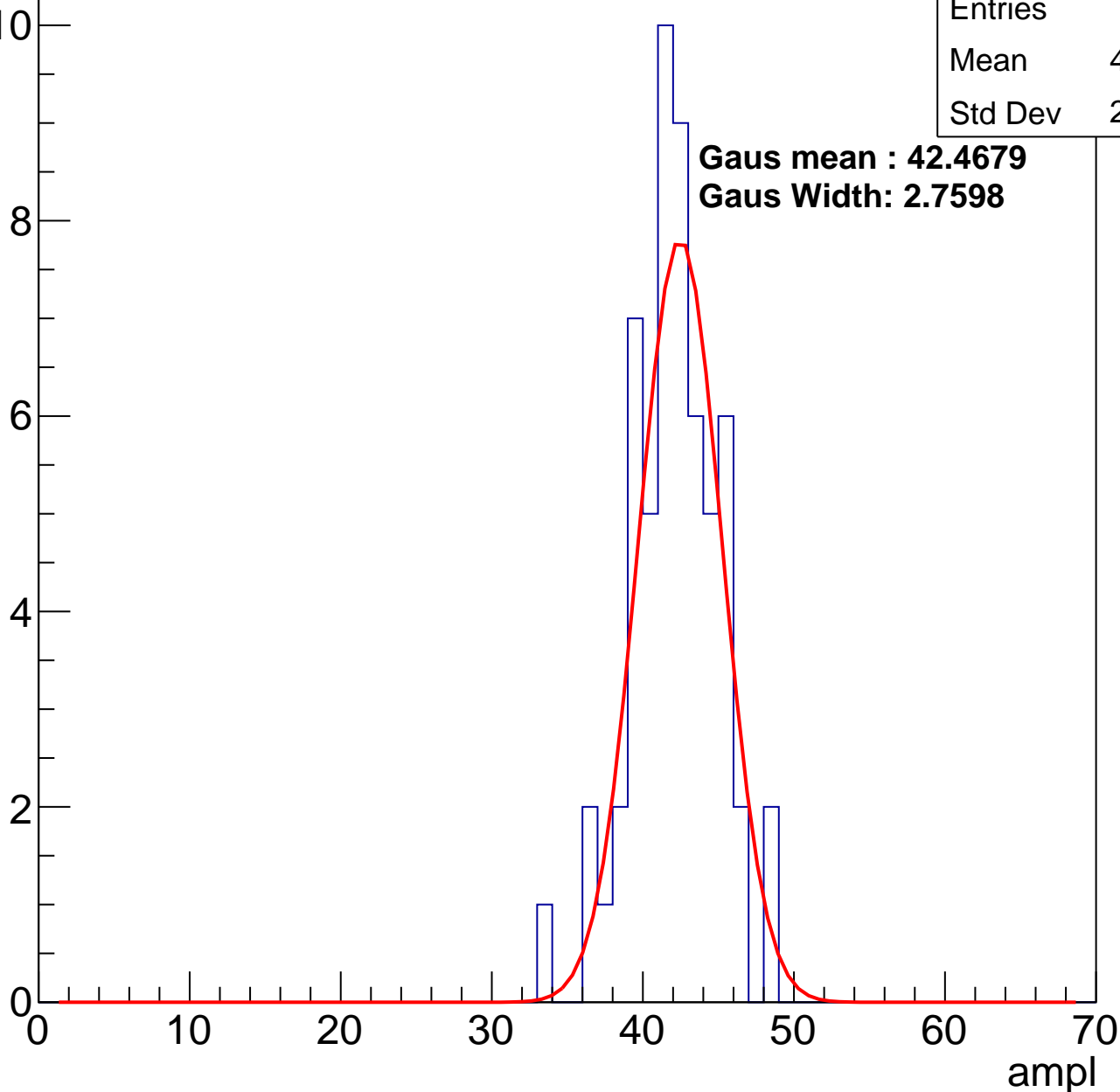
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.64
Std Dev	2.875

Gaus mean : 42.4679

Gaus Width: 2.7598

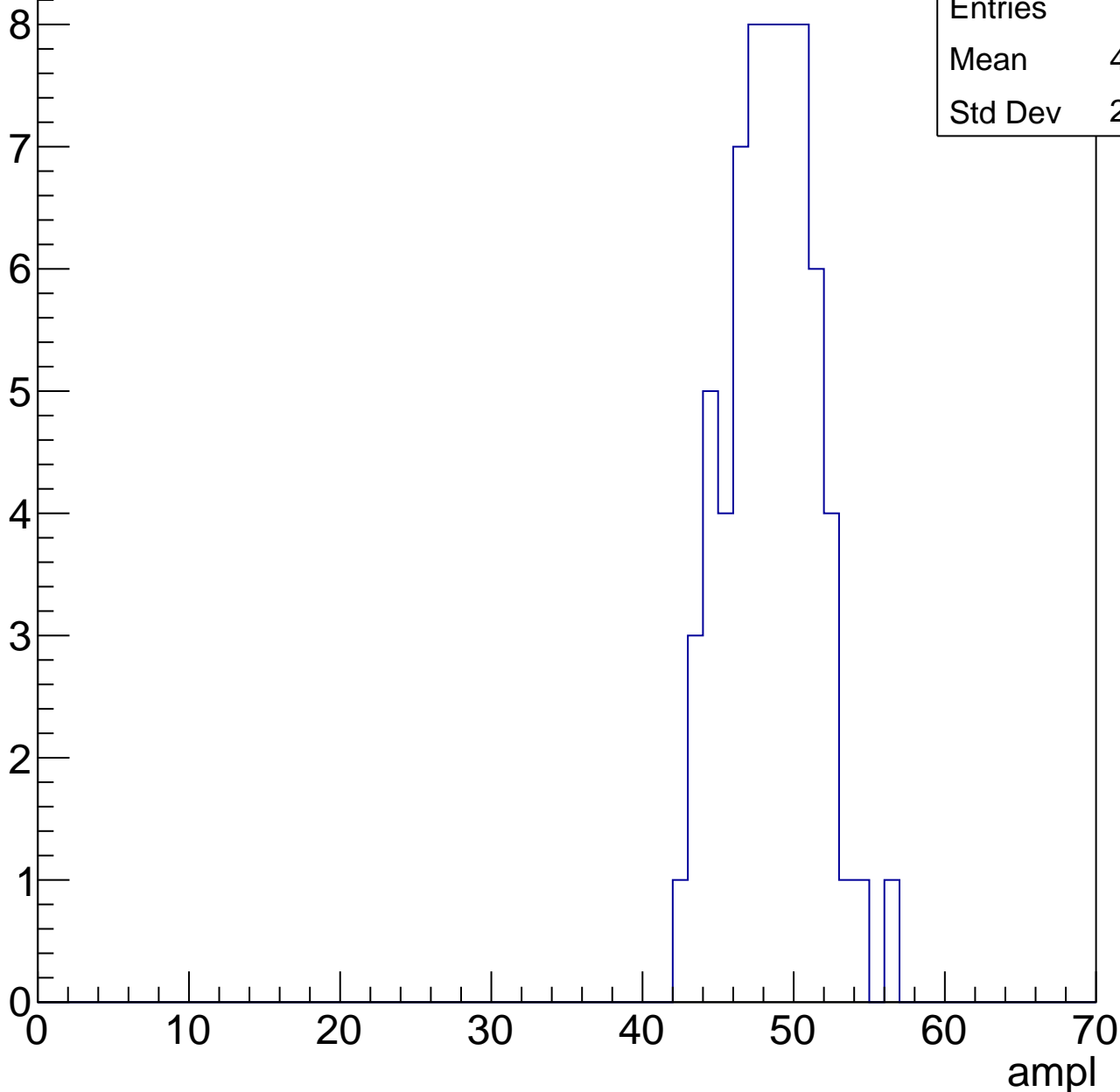


B1L002S, U7-ch27, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	48.03
Std Dev	2.898

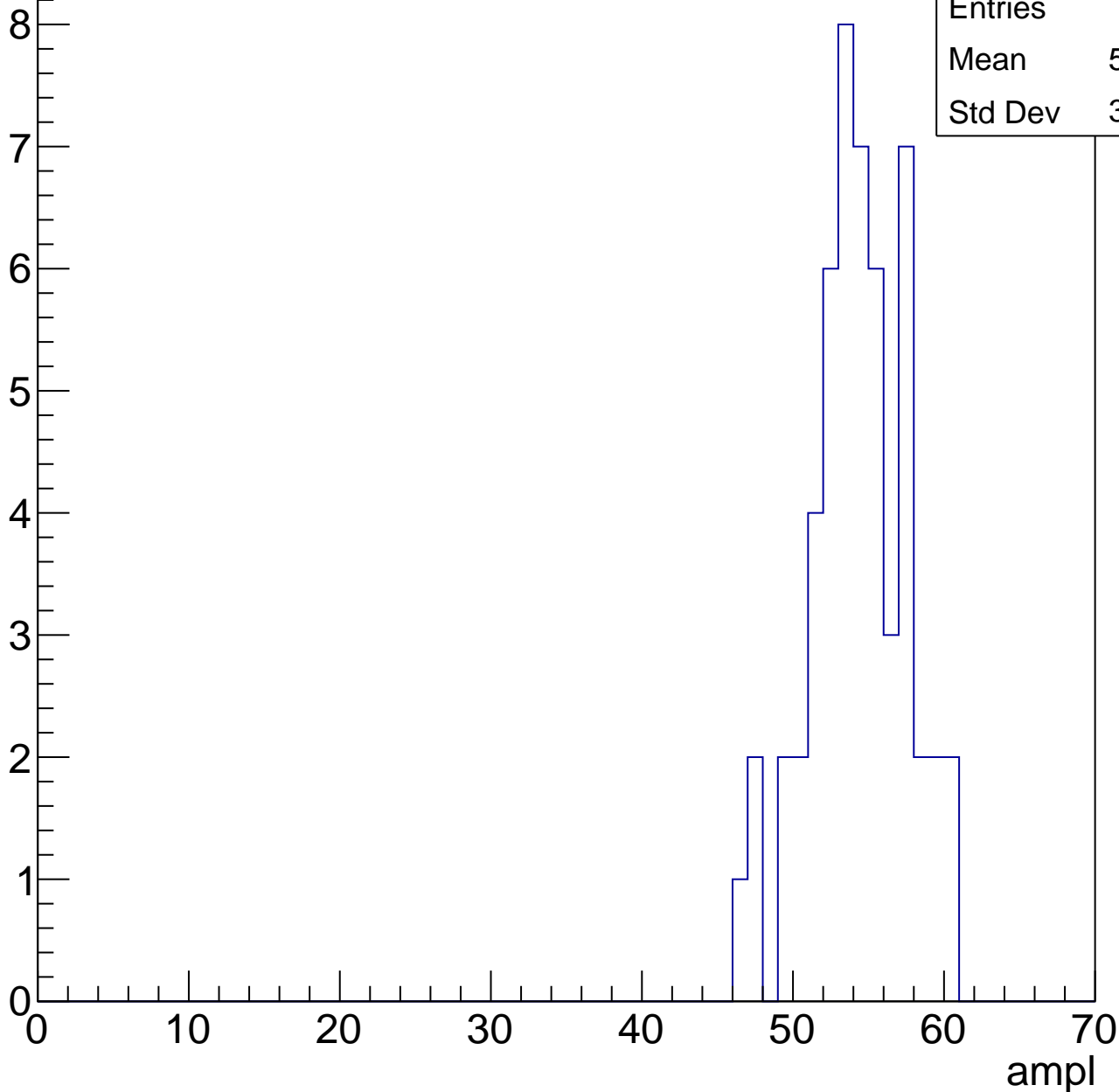


B1L002S, U7-ch27, adc4

calib_packv5_042523_0143.root, FC#10, port B3

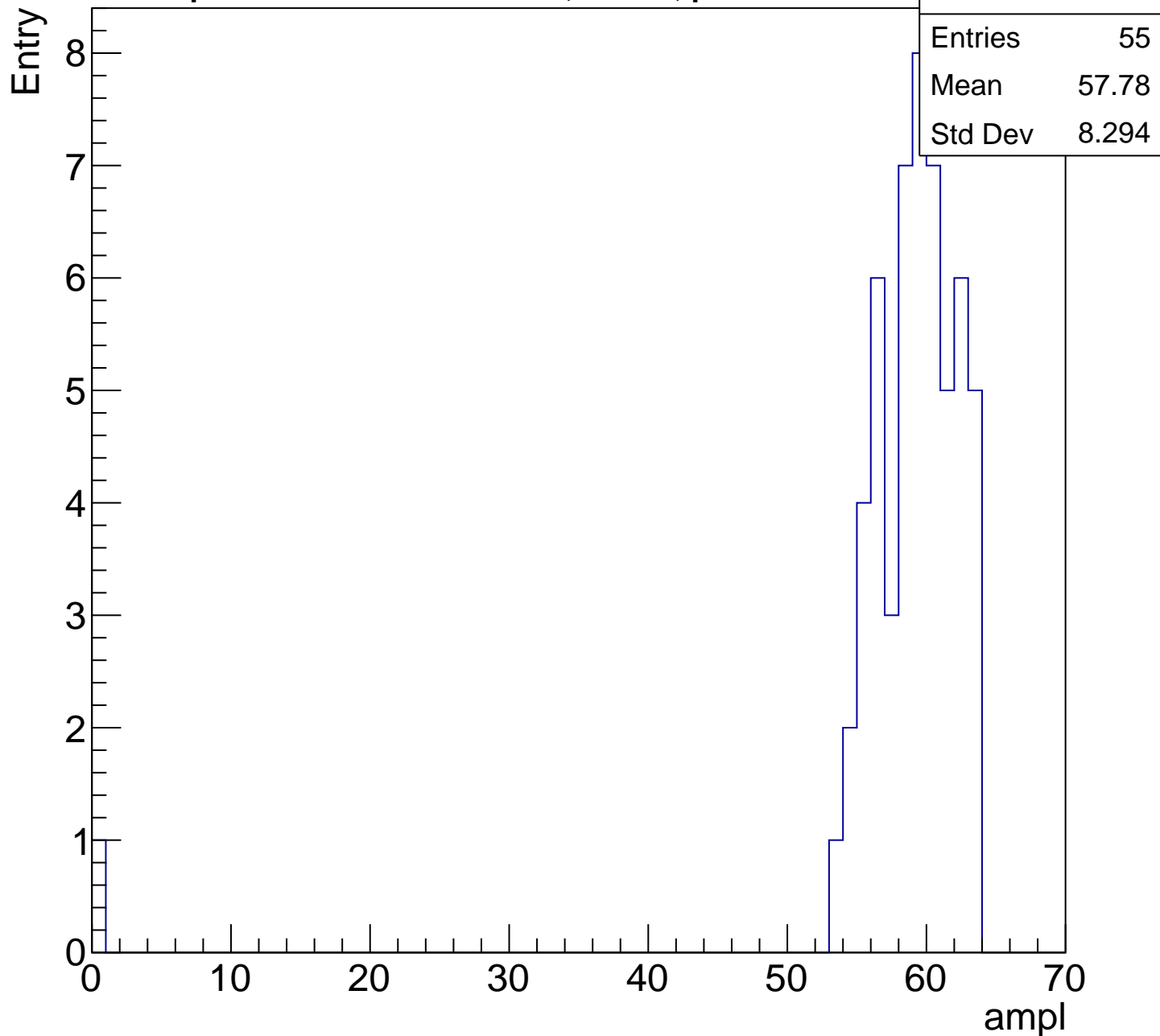
Entry

Entries	54
Mean	53.83
Std Dev	3.178



B1L002S, U7-ch27, adc5

calib_packv5_042523_0143.root, FC#10, port B3

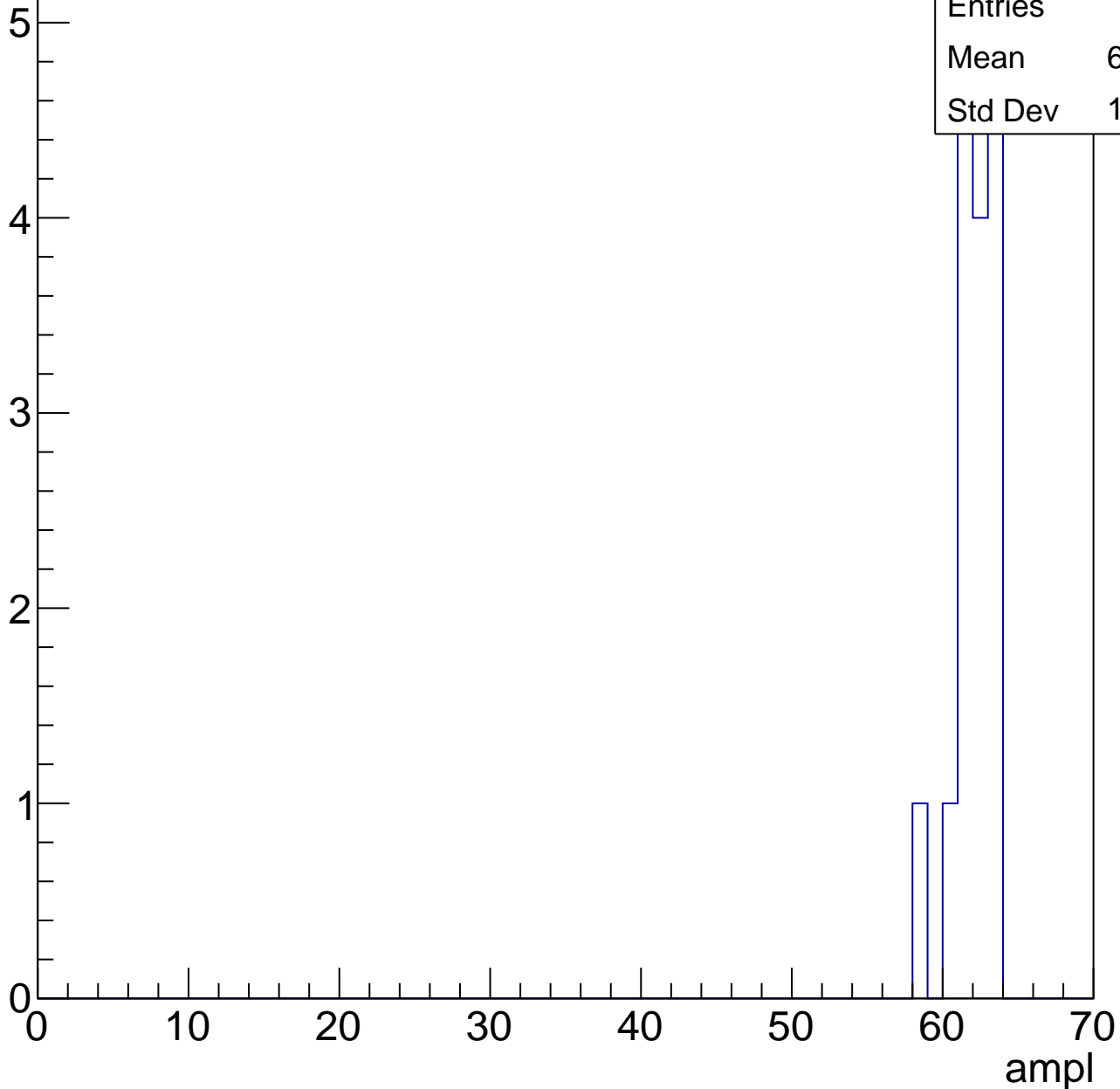


B1L002S, U7-ch27, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	16
Mean	61.62
Std Dev	1.317



B1L002S, U7-ch27, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch28, adc0

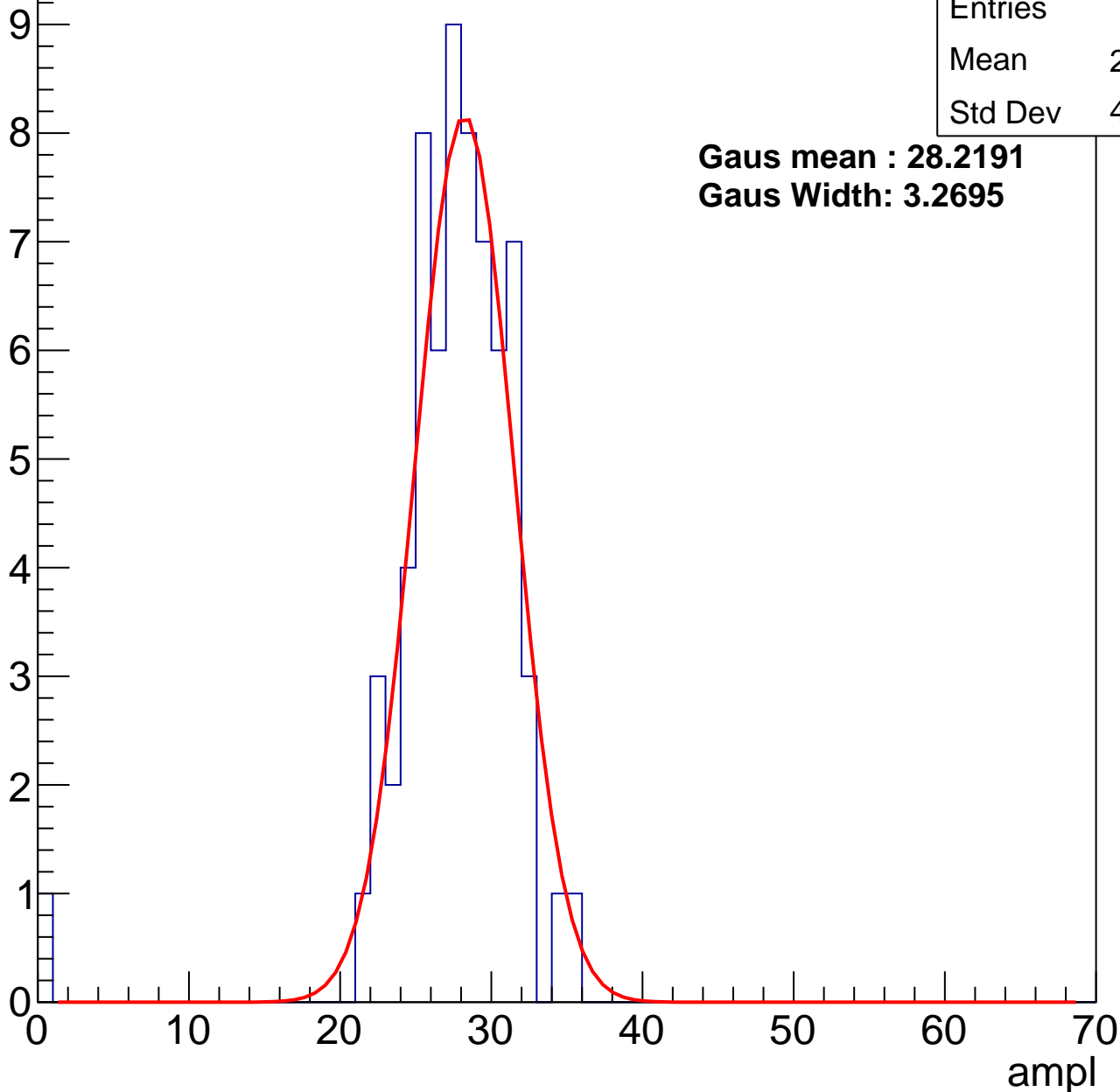
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.12
Std Dev	4.464

Gaus mean : 28.2191

Gaus Width: 3.2695



B1L002S, U7-ch28, adc1

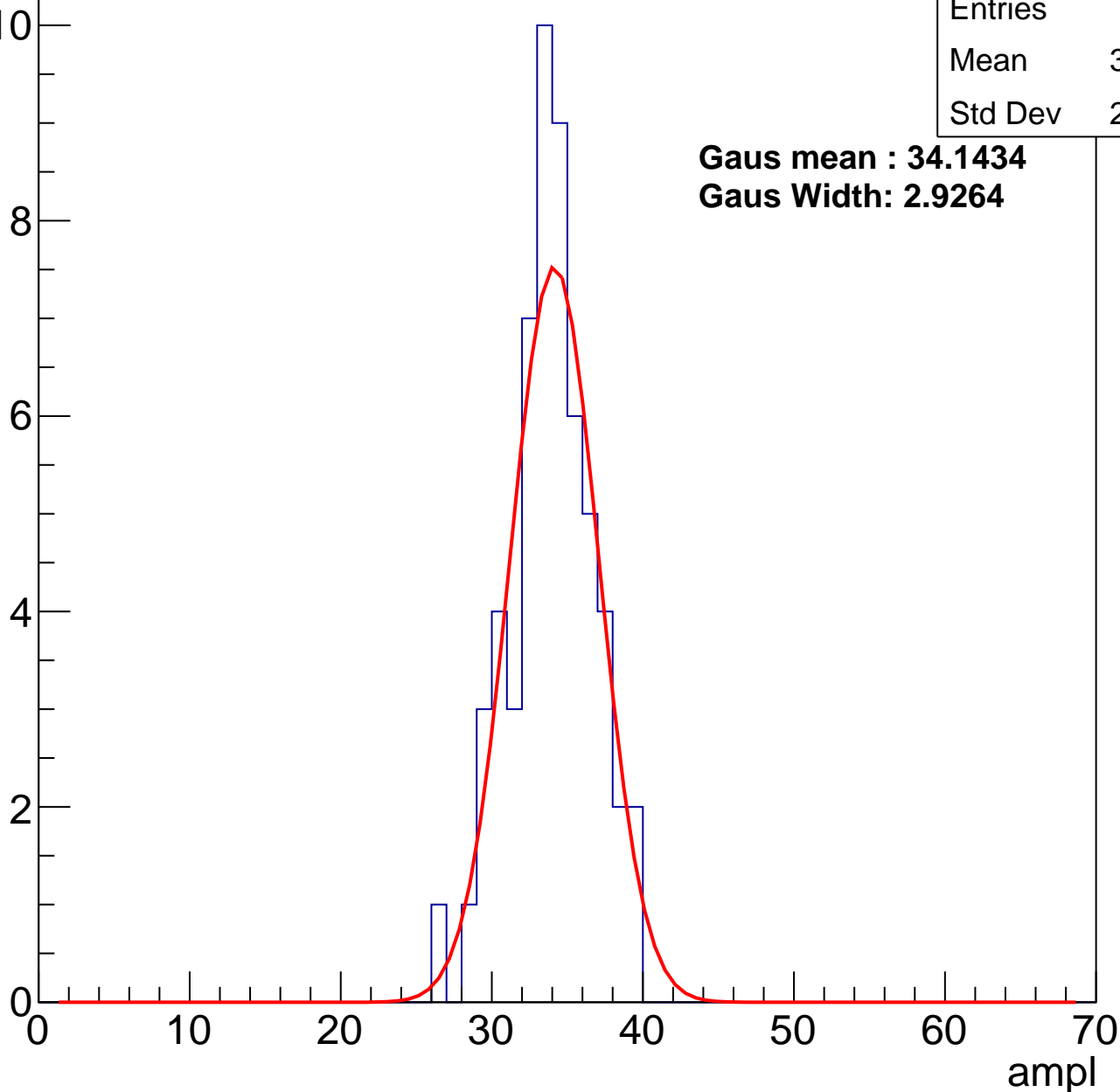
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.44
Std Dev	2.747

Gaus mean : 34.1434

Gaus Width: 2.9264



B1L002S, U7-ch28, adc2

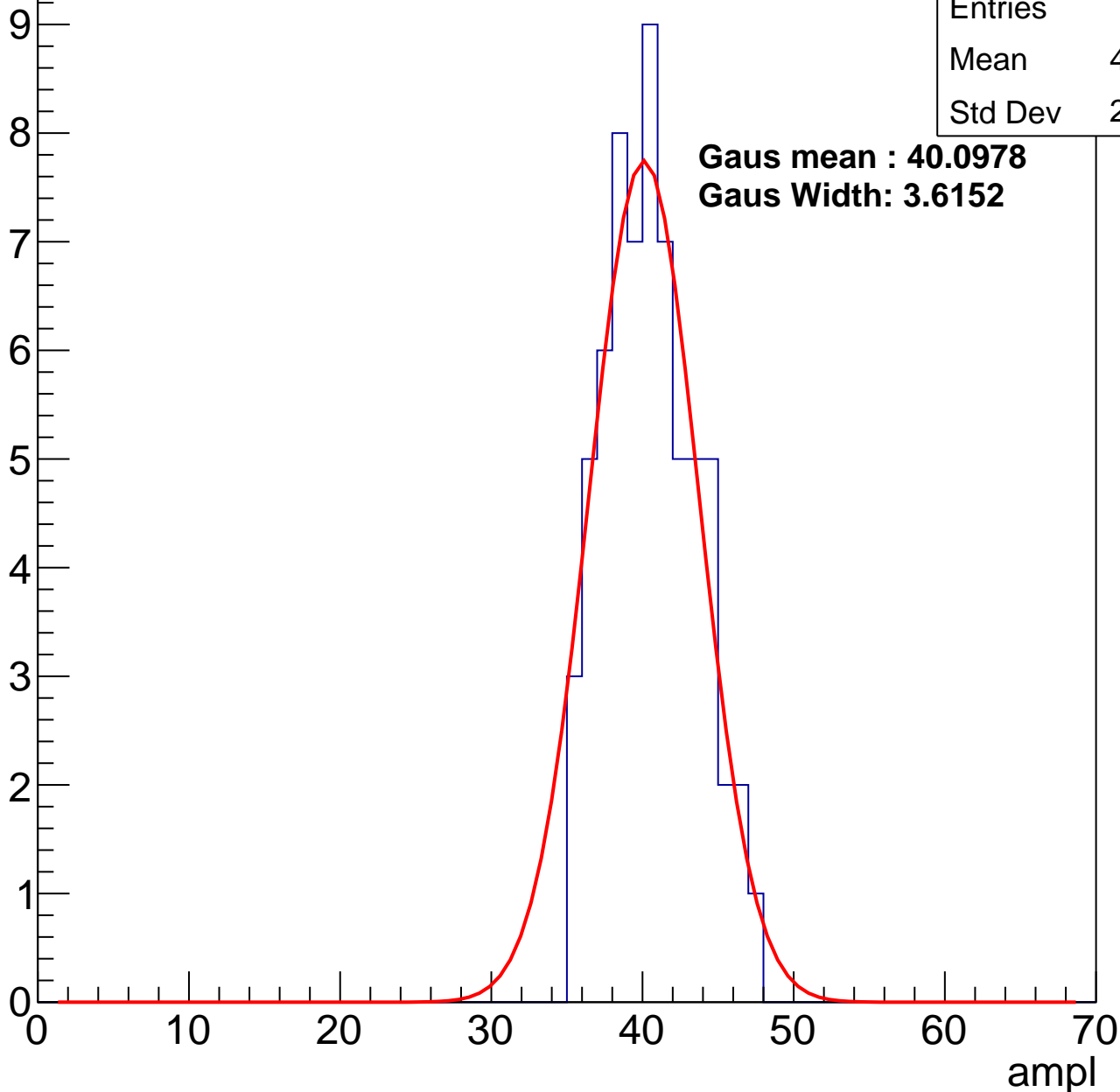
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.08
Std Dev	2.963

Gaus mean : 40.0978

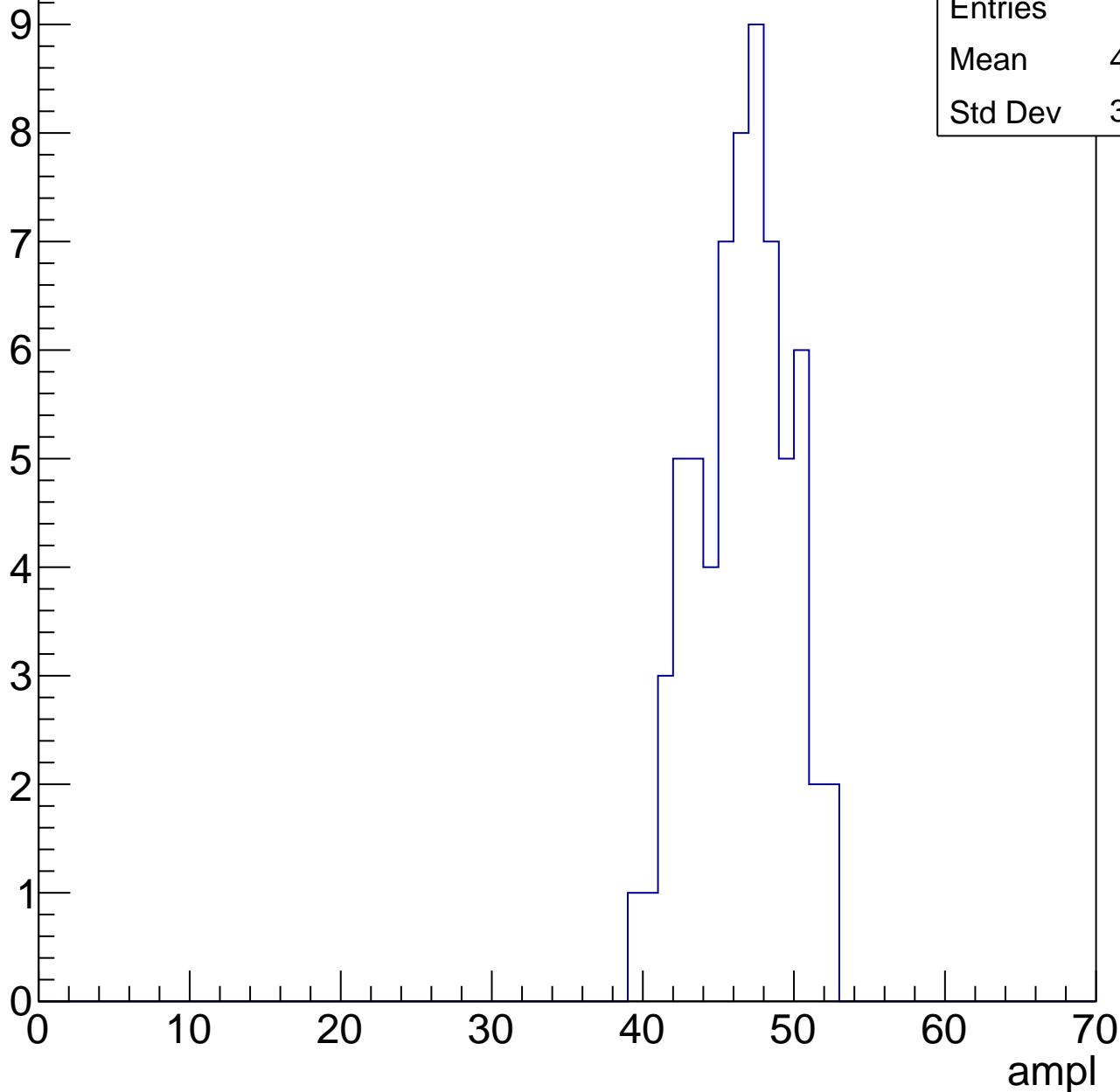
Gaus Width: 3.6152



B1L002S, U7-ch28, adc3

calib_packv5_042523_0143.root, FC#10, port B3

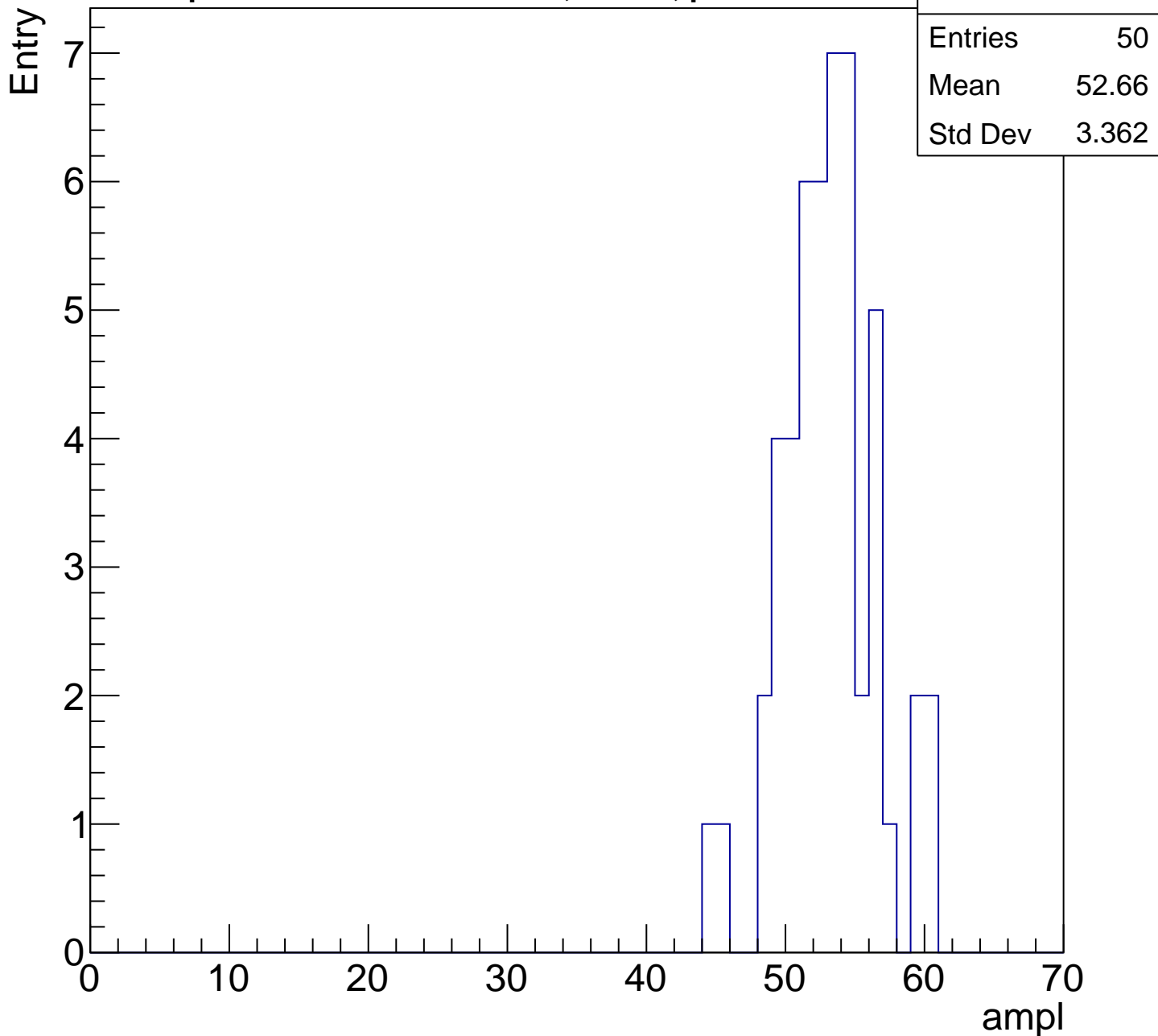
Entry



Entries	65
Mean	46.09
Std Dev	3.057

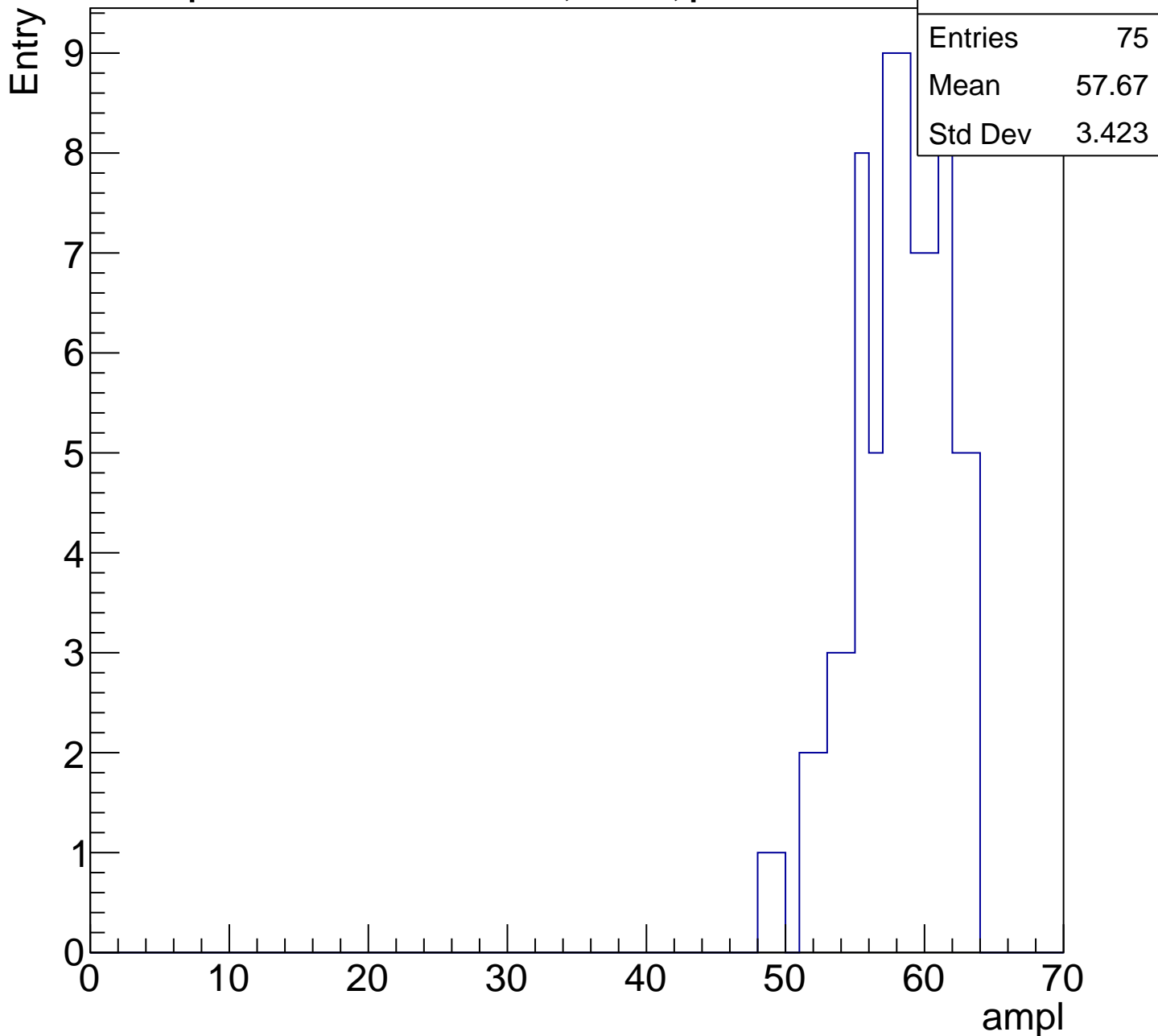
B1L002S, U7-ch28, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch28, adc5

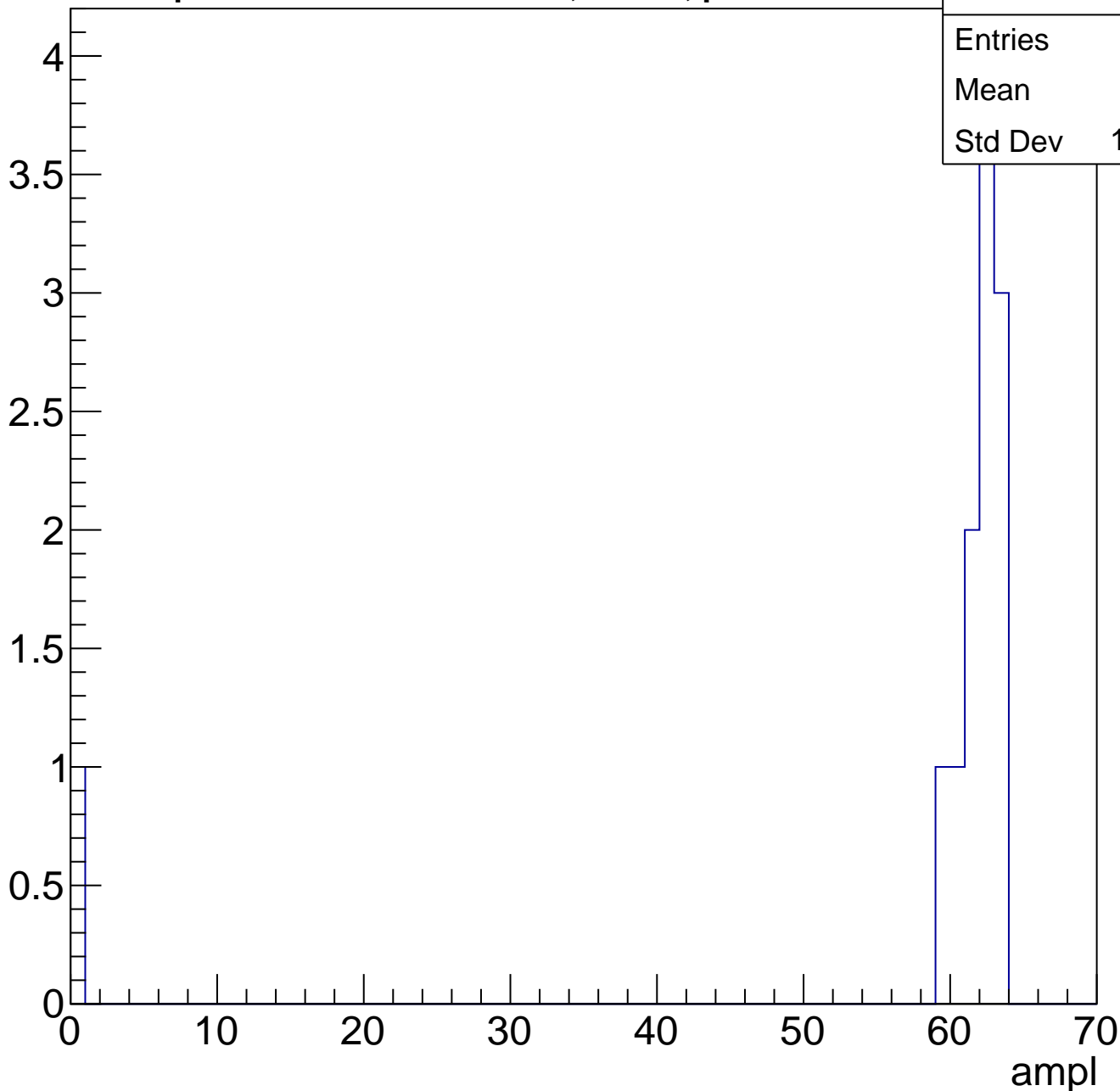
calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch28, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

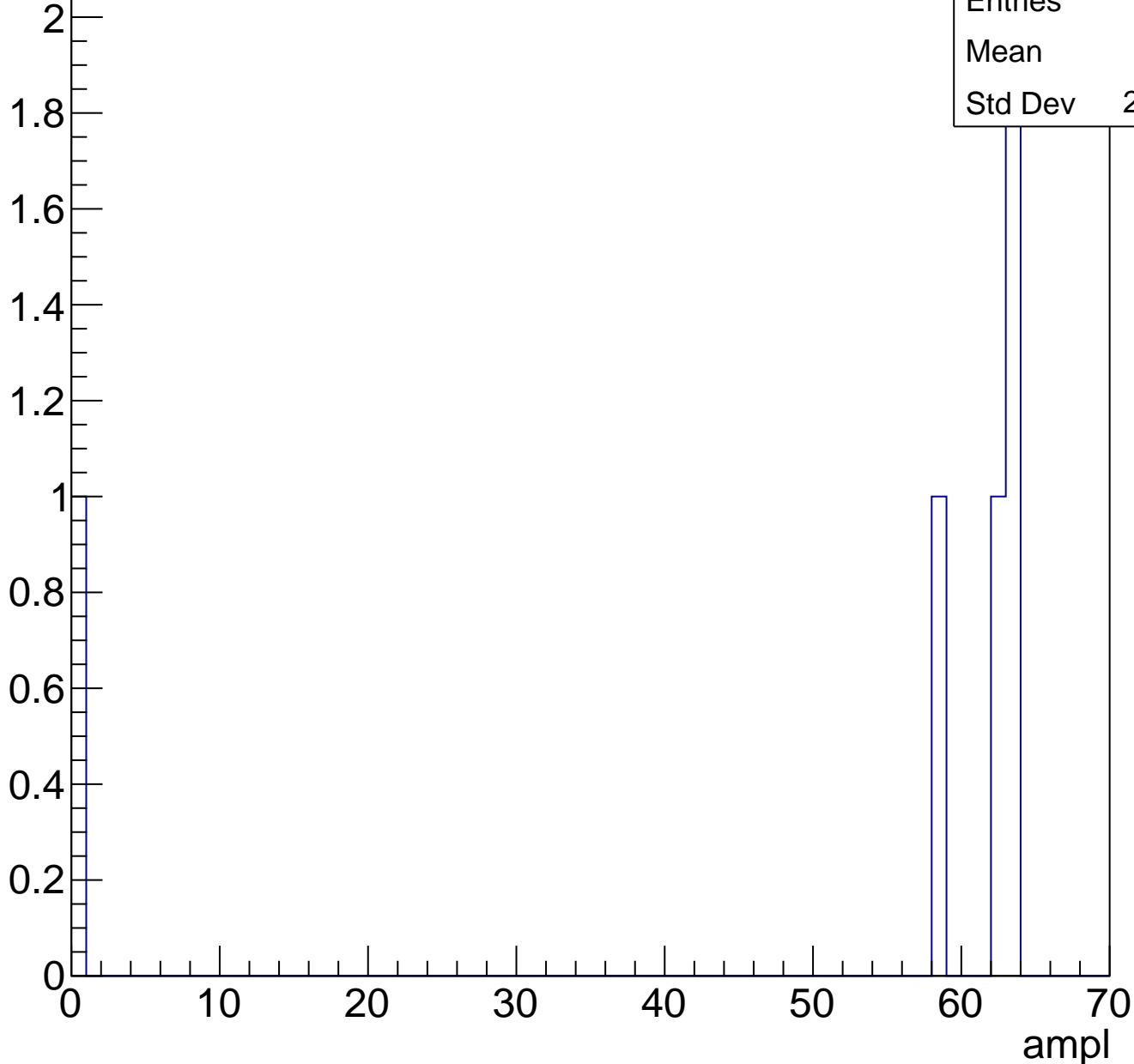


Entries	12
Mean	56.5
Std Dev	17.08

B1L002S, U7-ch28, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch29, adc0

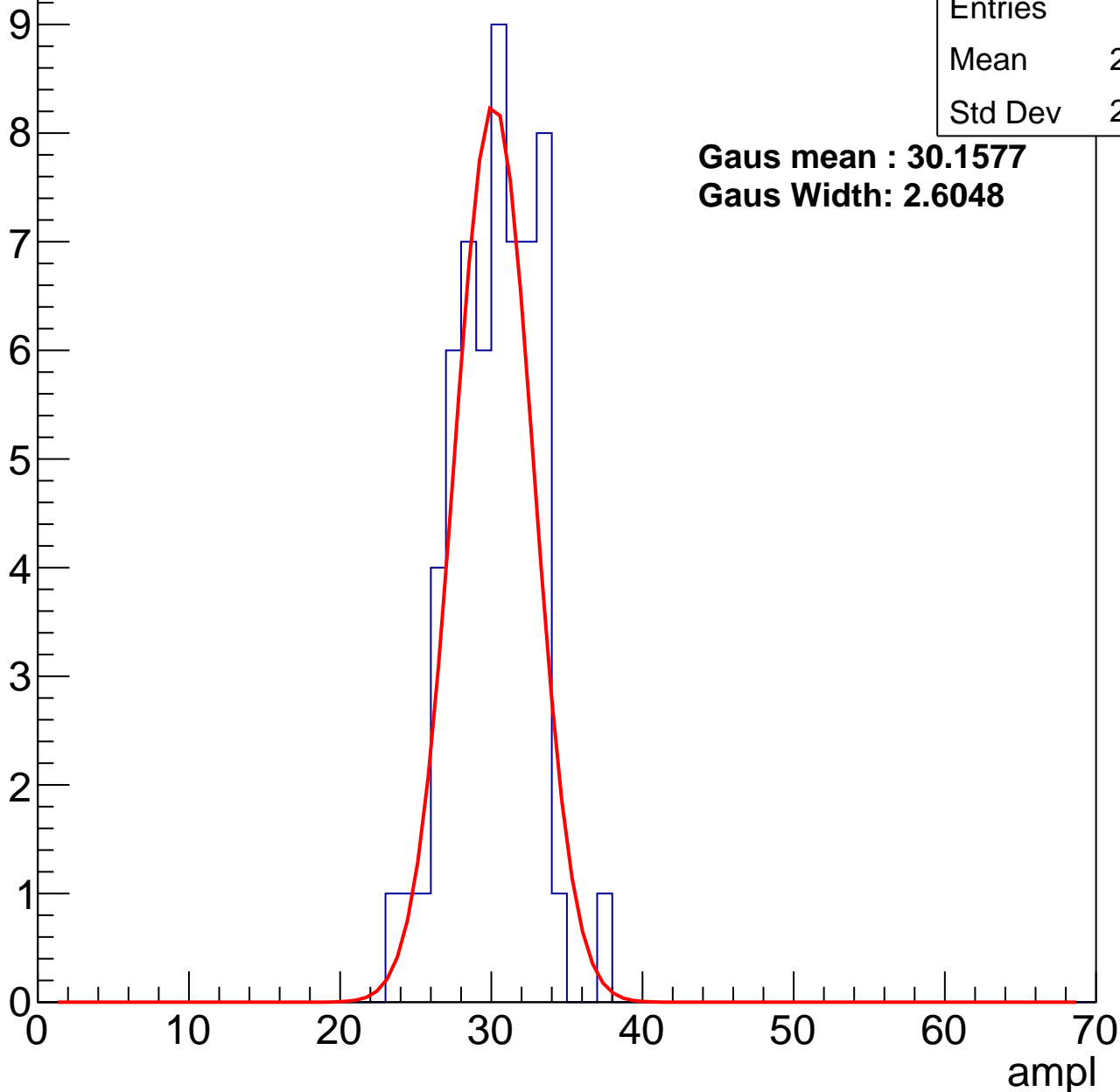
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	29.73
Std Dev	2.699

Gaus mean : 30.1577

Gaus Width: 2.6048



B1L002S, U7-ch29, adc1

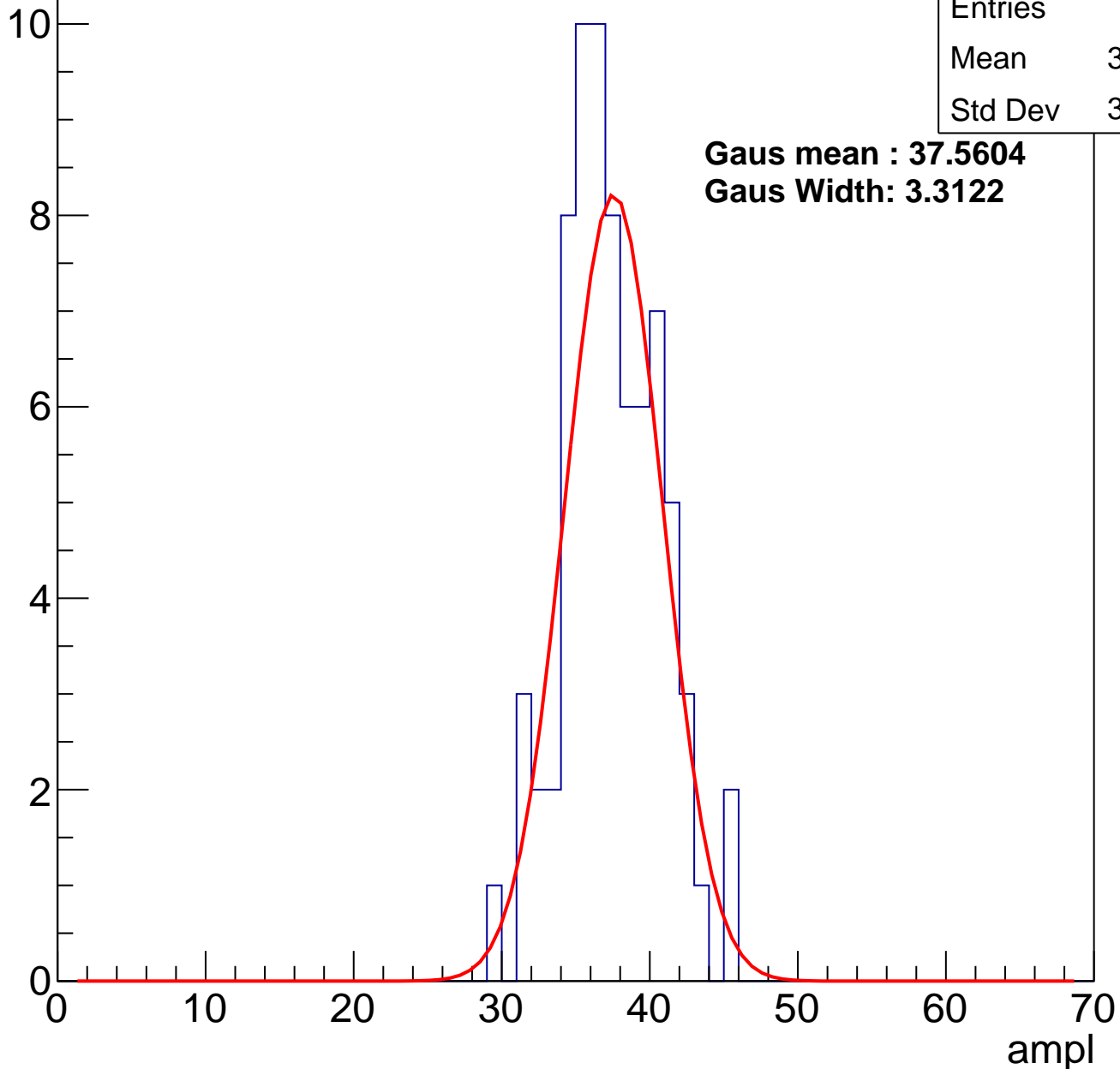
calib_packv5_042523_0143.root, FC#10, port B3

Entries	74
Mean	36.97
Std Dev	3.263

Gaus mean : 37.5604

Gaus Width: 3.3122

Entry



B1L002S, U7-ch29, adc2

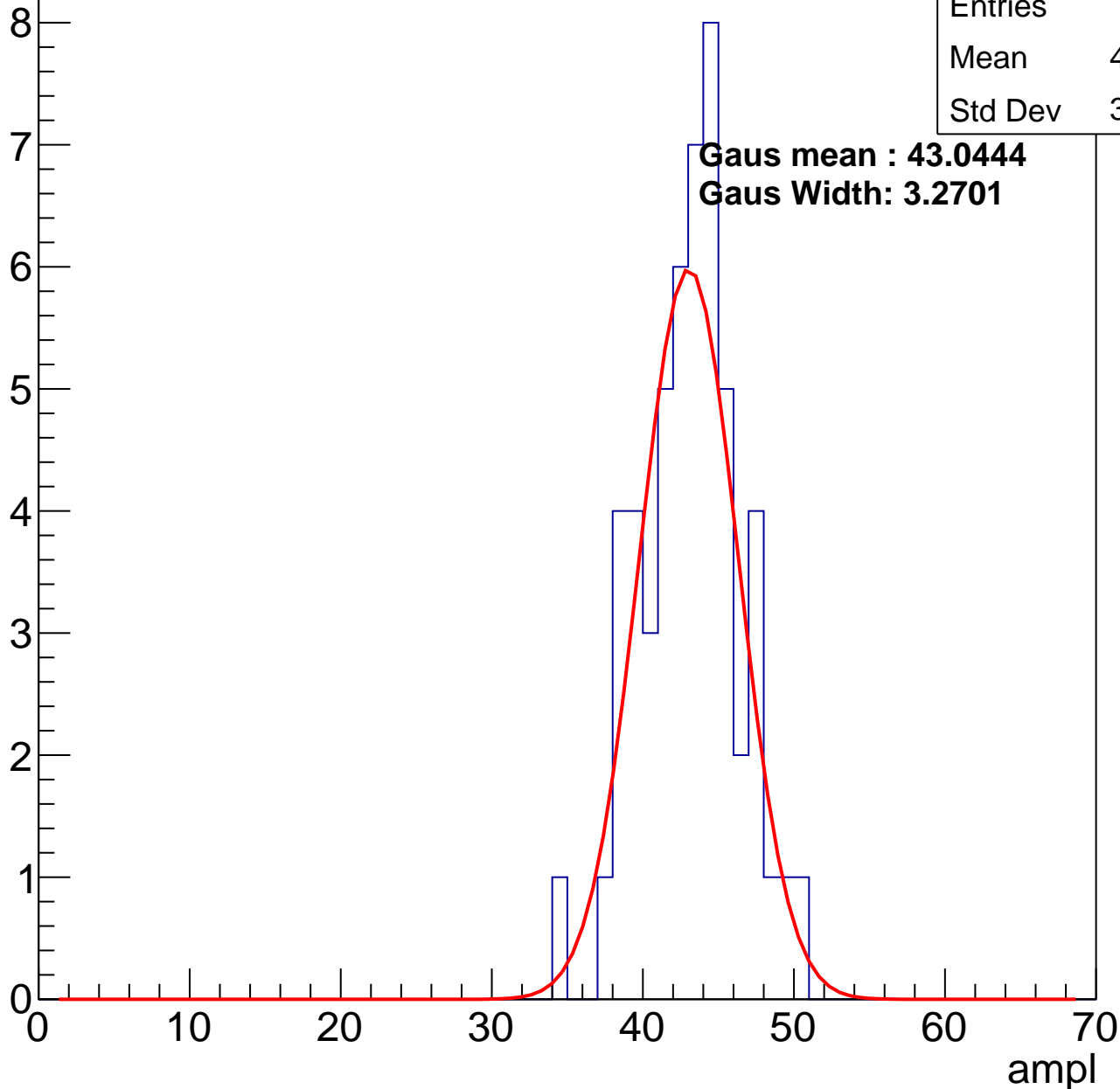
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	42.66
Std Dev	3.215

Gaus mean : 43.0444

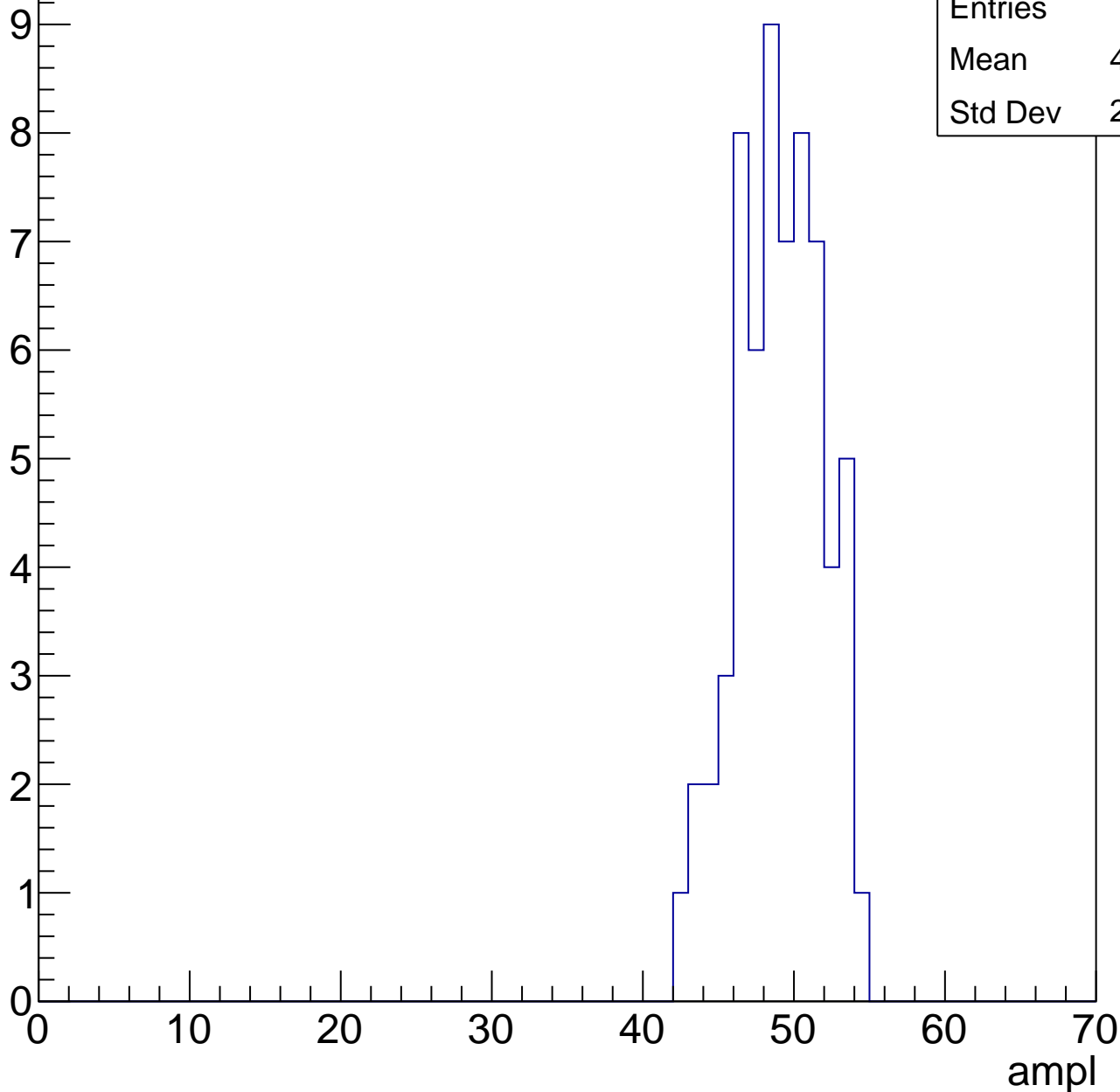
Gaus Width: 3.2701



B1L002S, U7-ch29, adc3

calib_packv5_042523_0143.root, FC#10, port B3

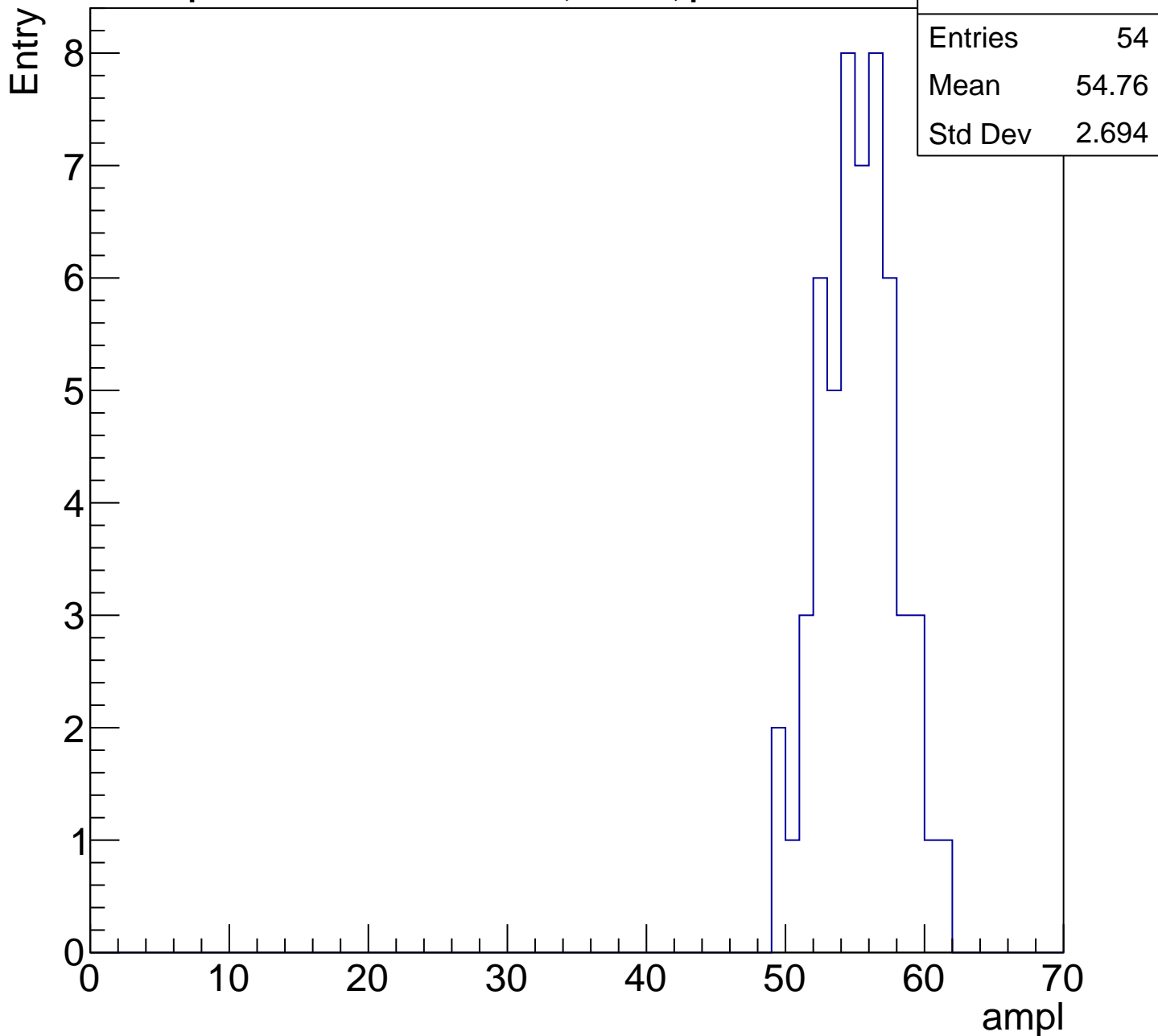
Entry



Entries	63
Mean	48.57
Std Dev	2.787

B1L002S, U7-ch29, adc4

calib_packv5_042523_0143.root, FC#10, port B3

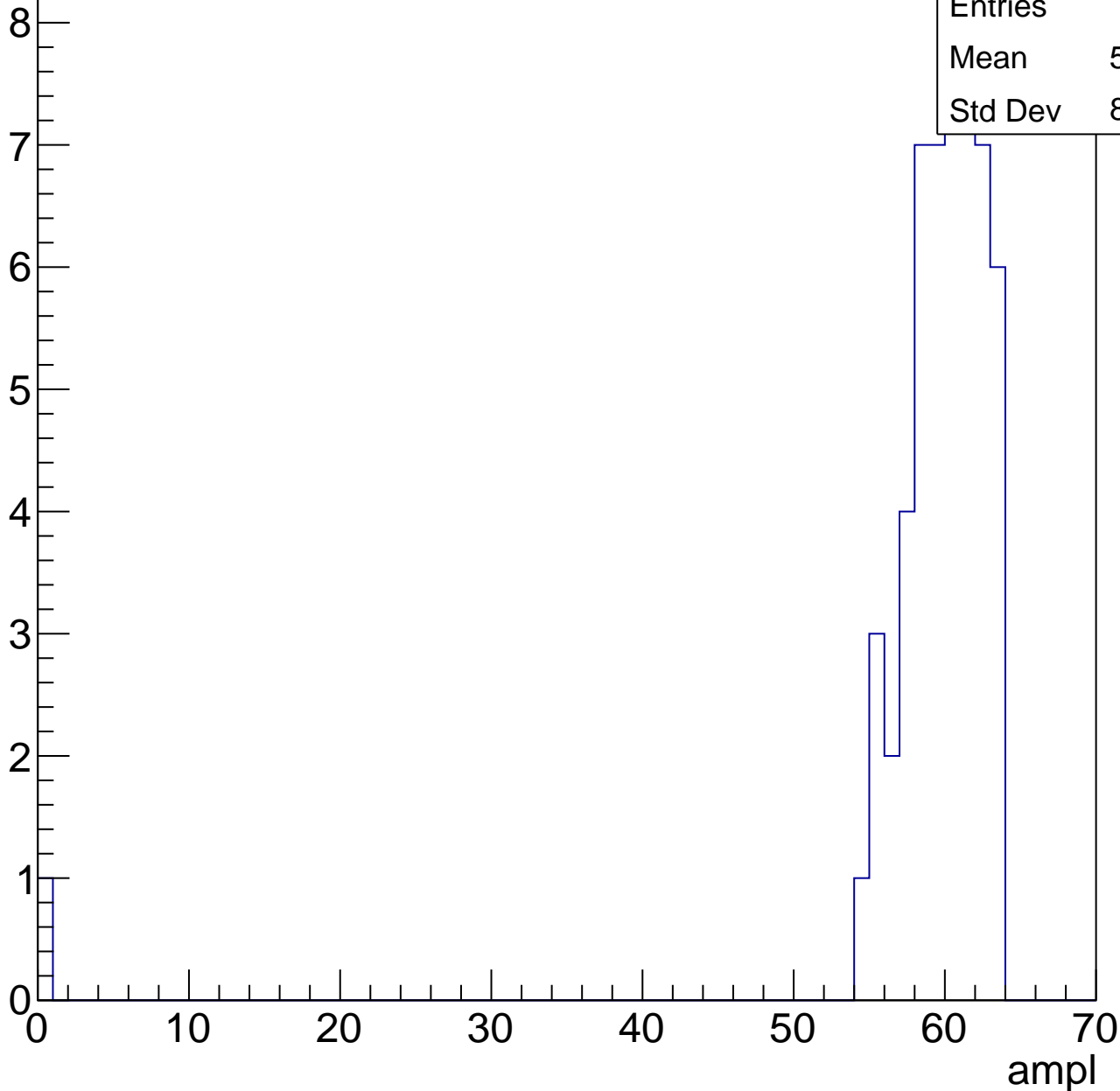


B1L002S, U7-ch29, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

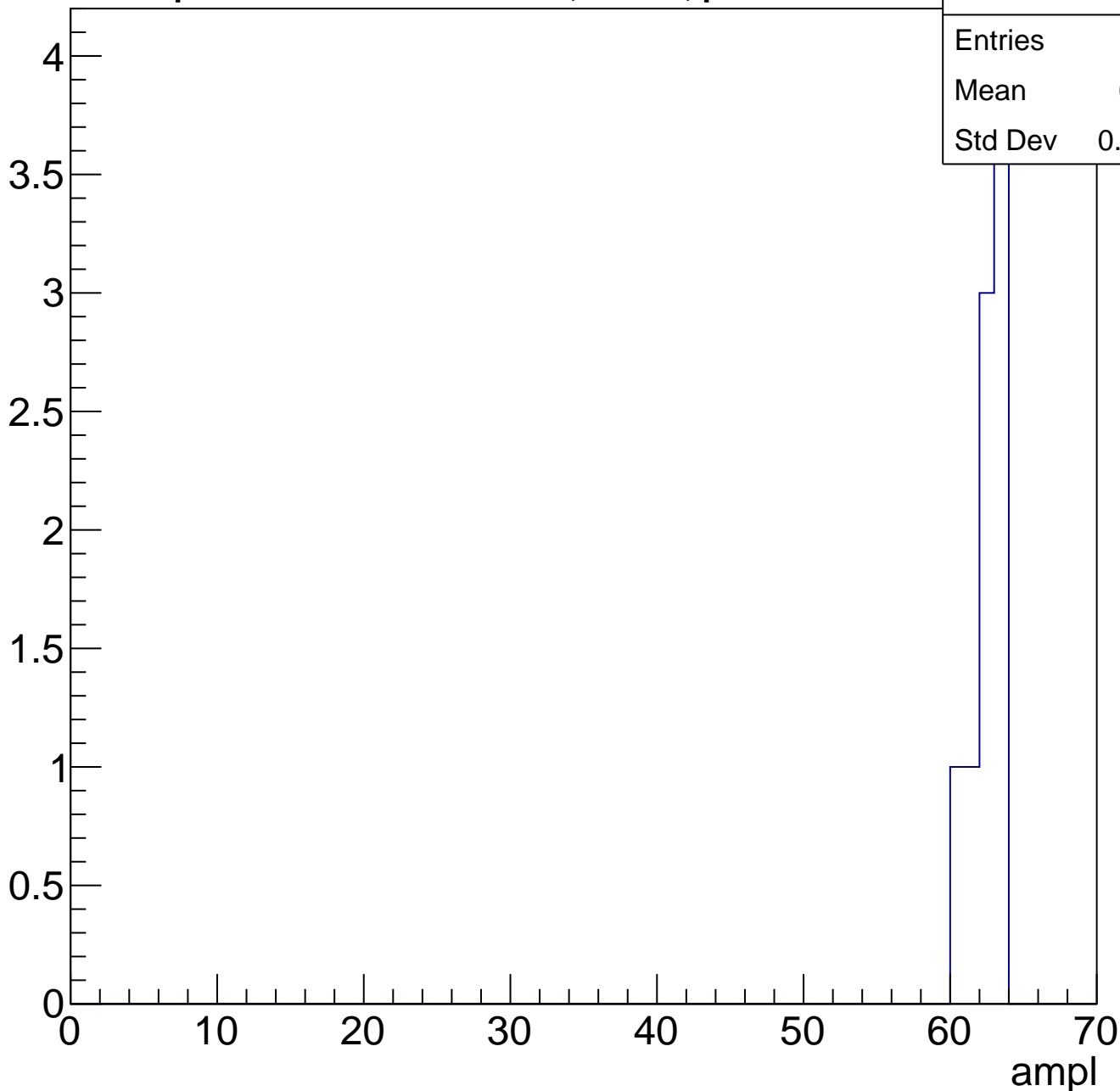
Entries	54
Mean	58.48
Std Dev	8.366



B1L002S, U7-ch29, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch29, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch30, adc0

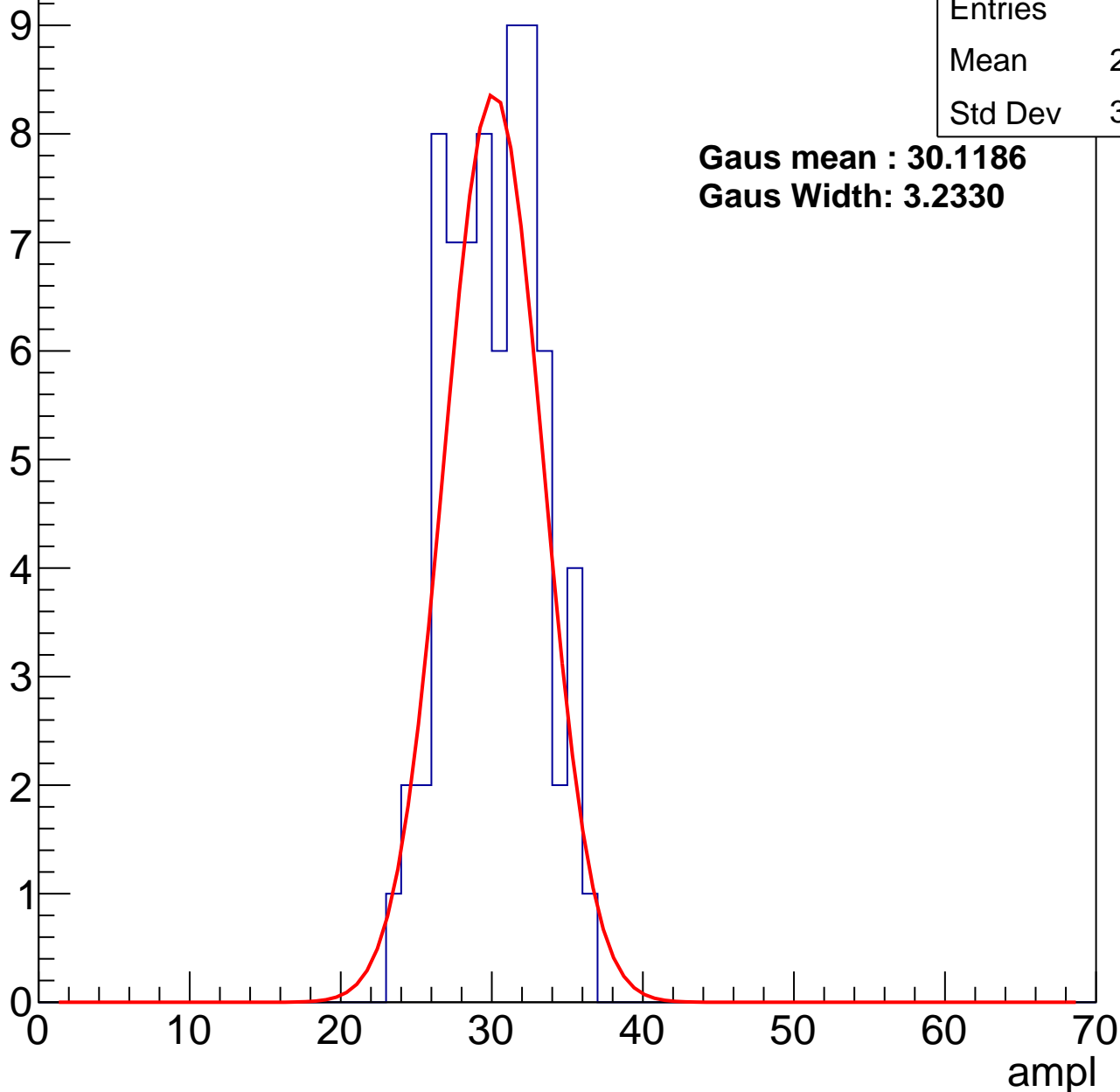
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	29.65
Std Dev	3.019

Gaus mean : 30.1186

Gaus Width: 3.2330



B1L002S, U7-ch30, adc1

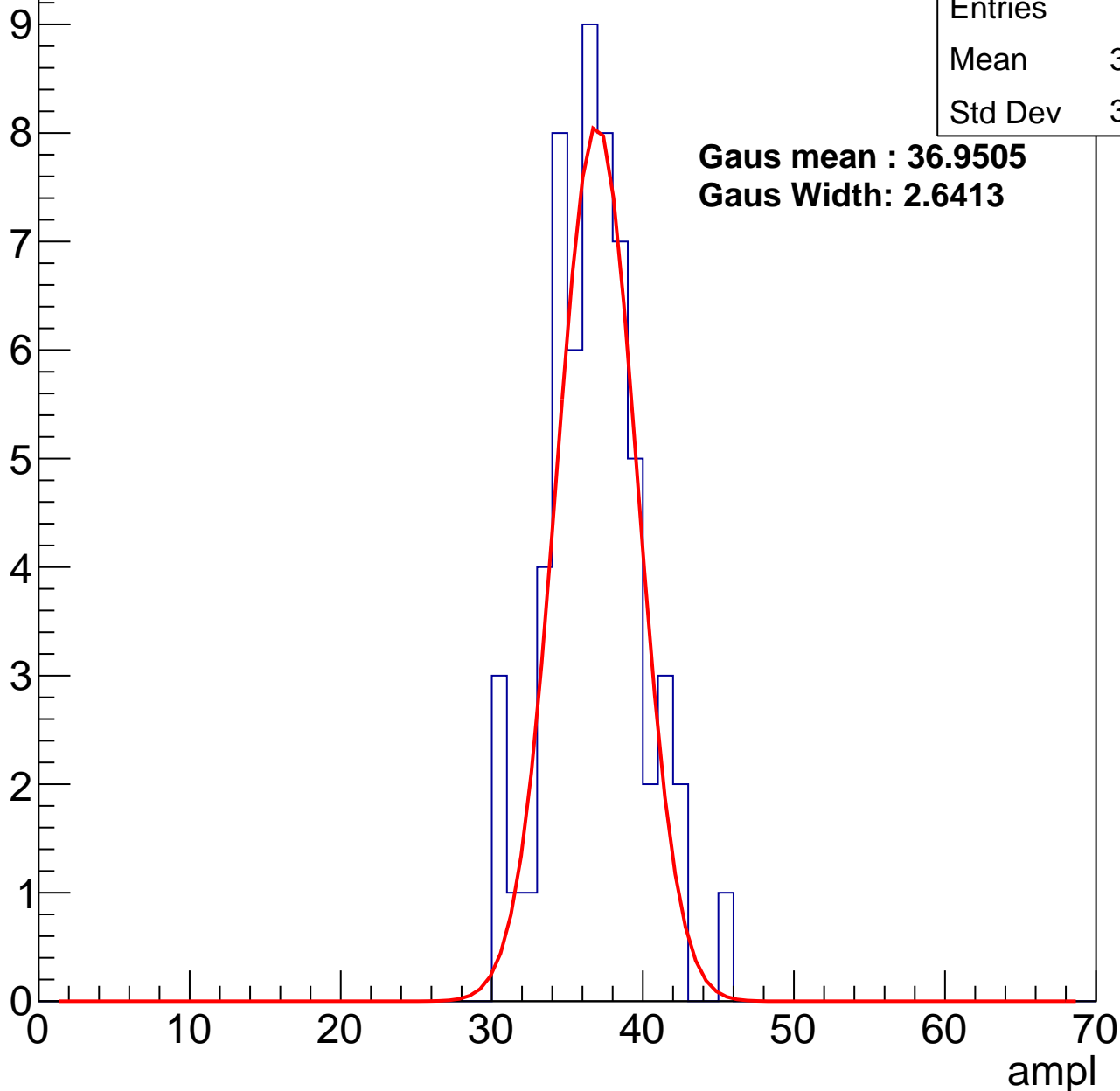
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	36.33
Std Dev	3.048

Gaus mean : 36.9505

Gaus Width: 2.6413



B1L002S, U7-ch30, adc2

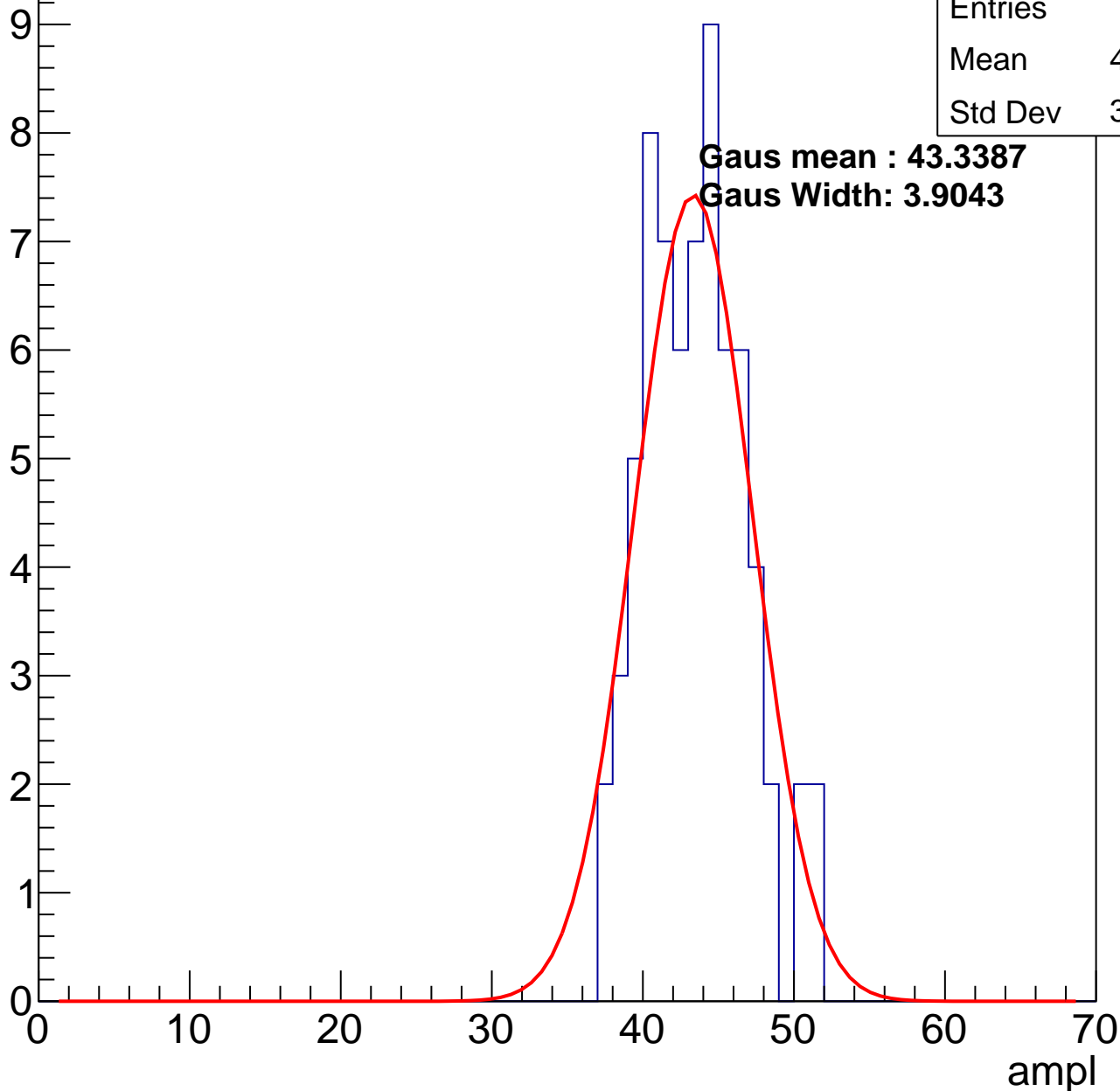
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	43.06
Std Dev	3.318

Gaus mean : 43.3387

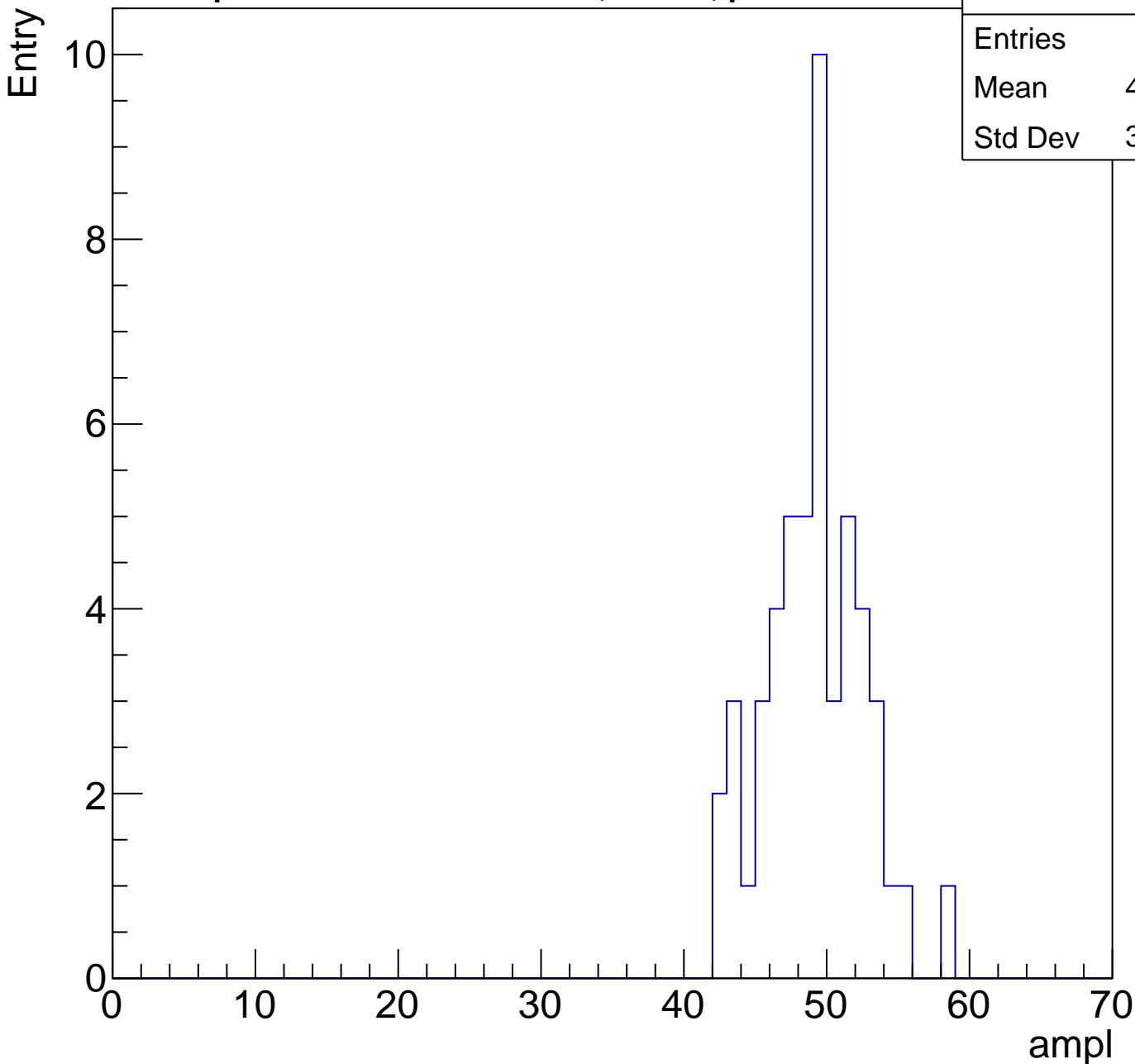
Gaus Width: 3.9043



B1L002S, U7-ch30, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	51
Mean	48.63
Std Dev	3.378

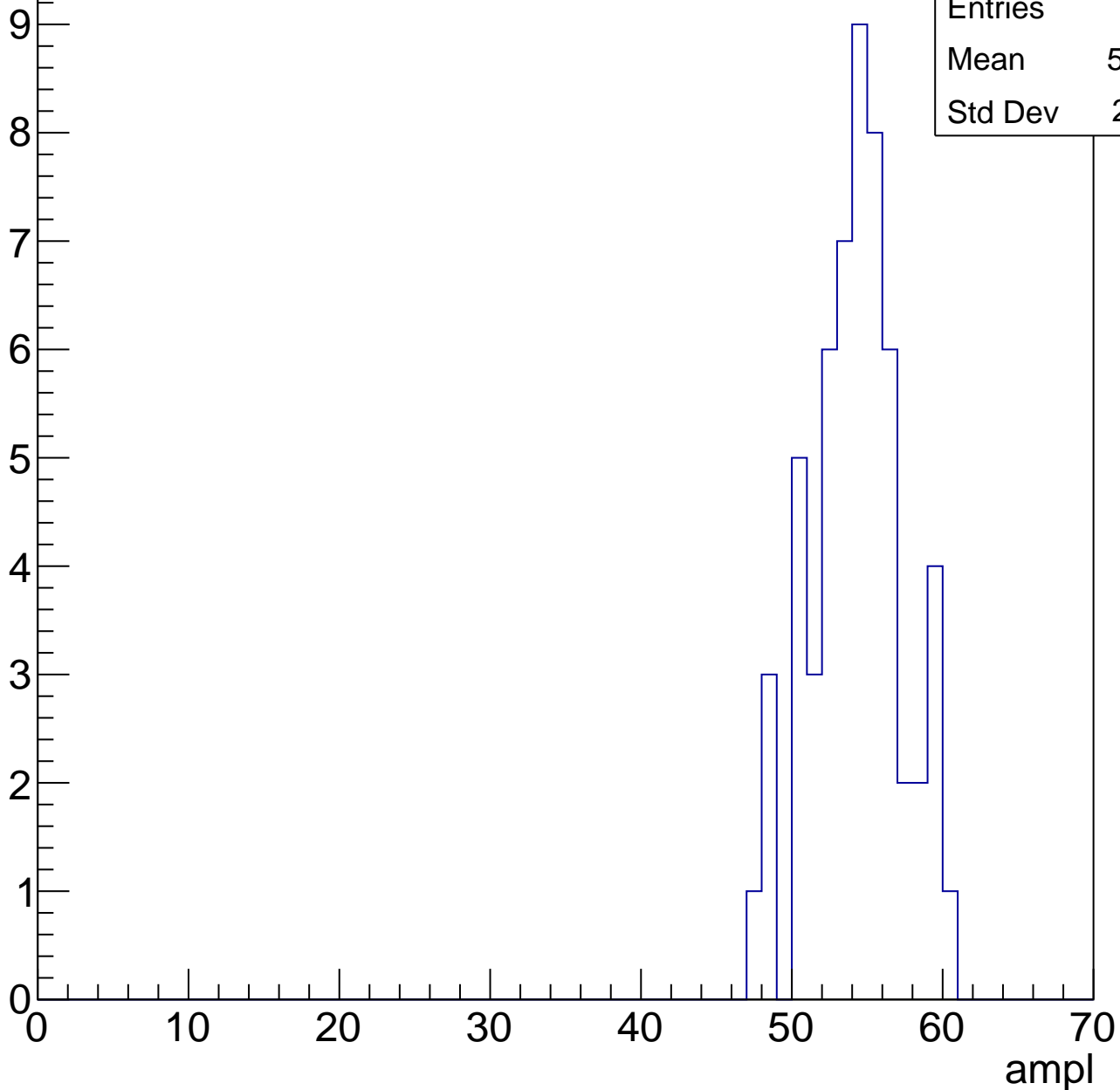


B1L002S, U7-ch30, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.77
Std Dev	2.991



B1L002S, U7-ch30, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.27
Std Dev	8.548

ampl

0

10

20

30

40

50

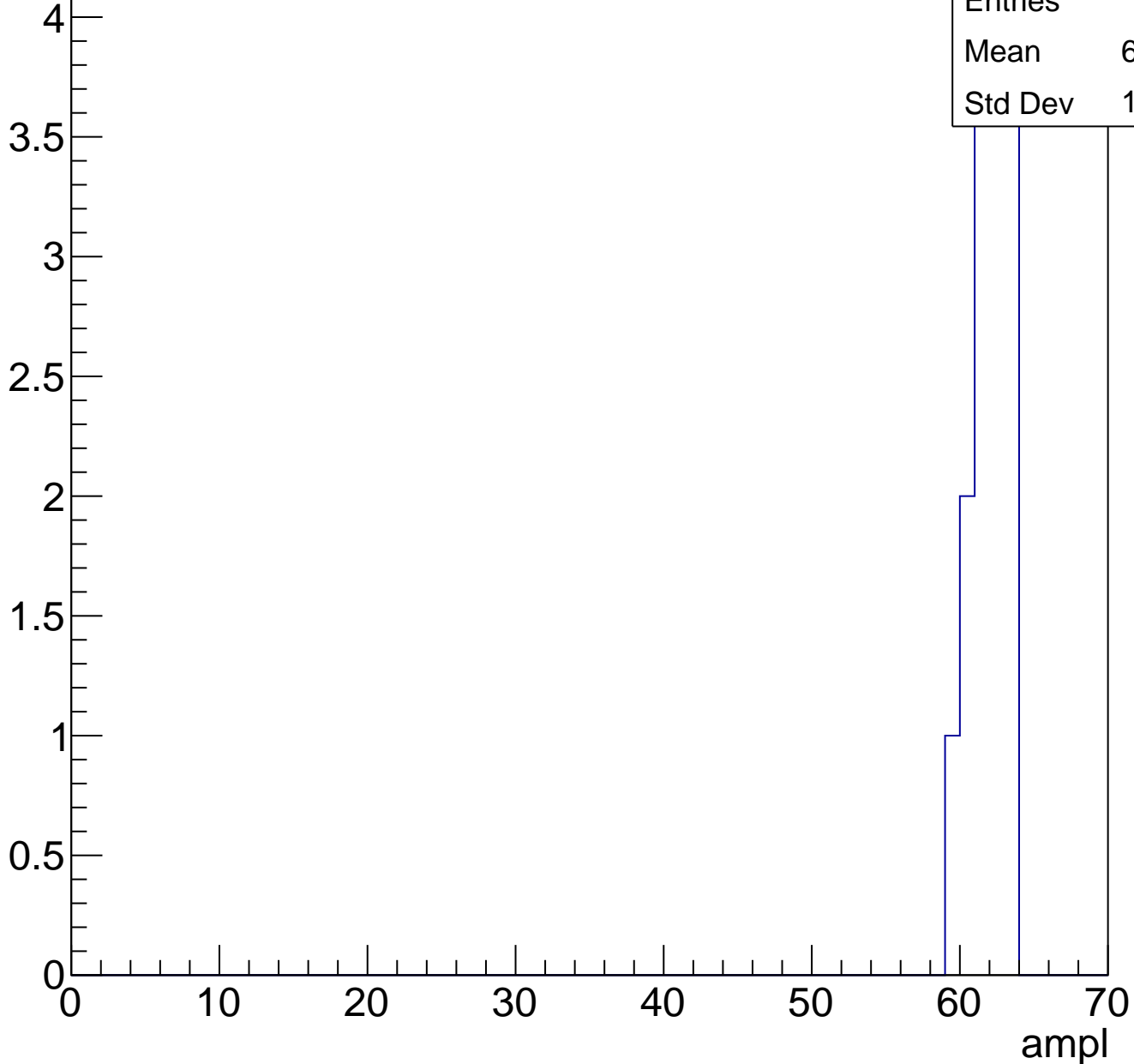
60

70

B1L002S, U7-ch30, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch30, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch31, adc0

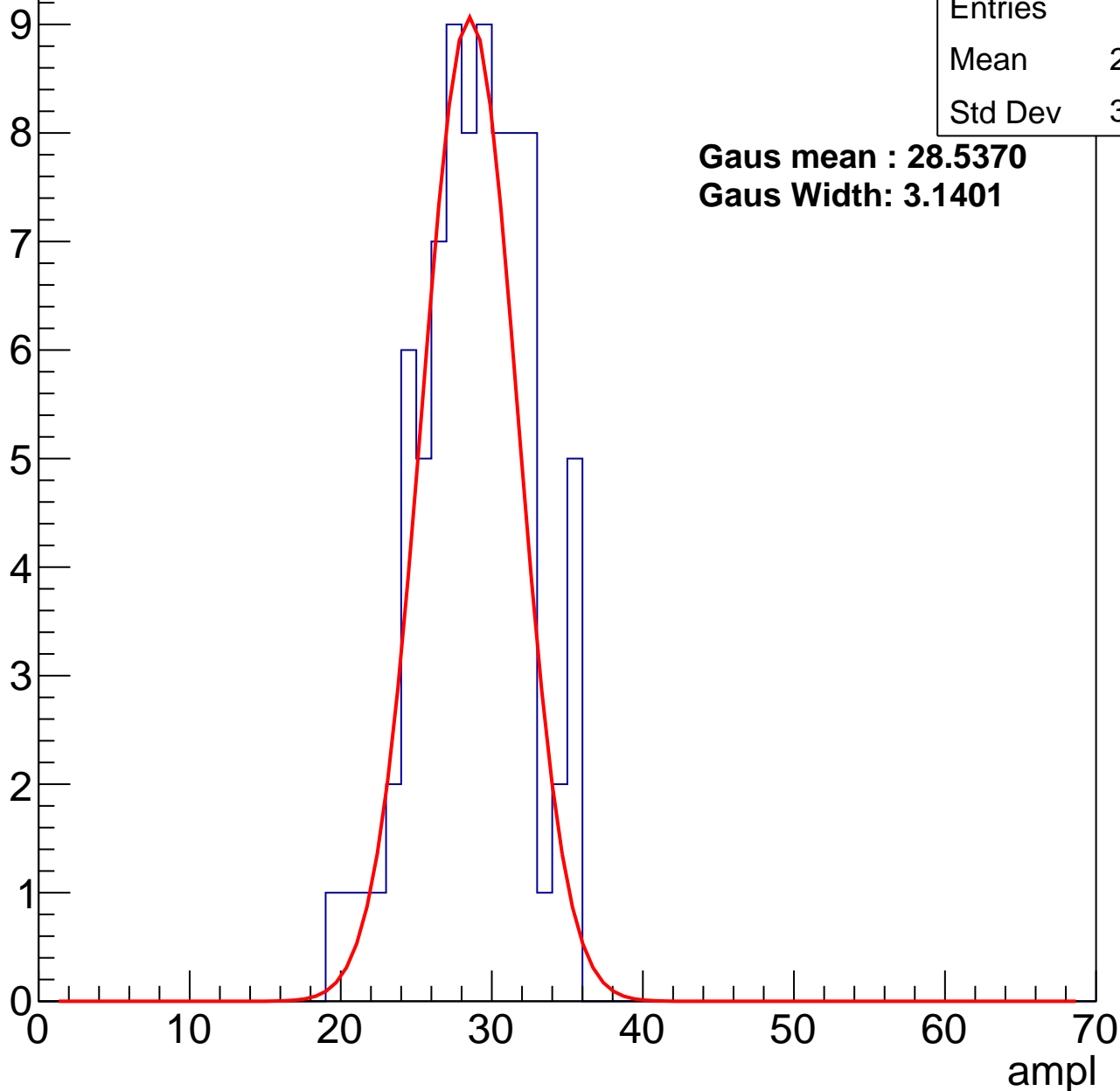
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	82
Mean	28.38
Std Dev	3.543

Gaus mean : 28.5370

Gaus Width: 3.1401



B1L002S, U7-ch31, adc1

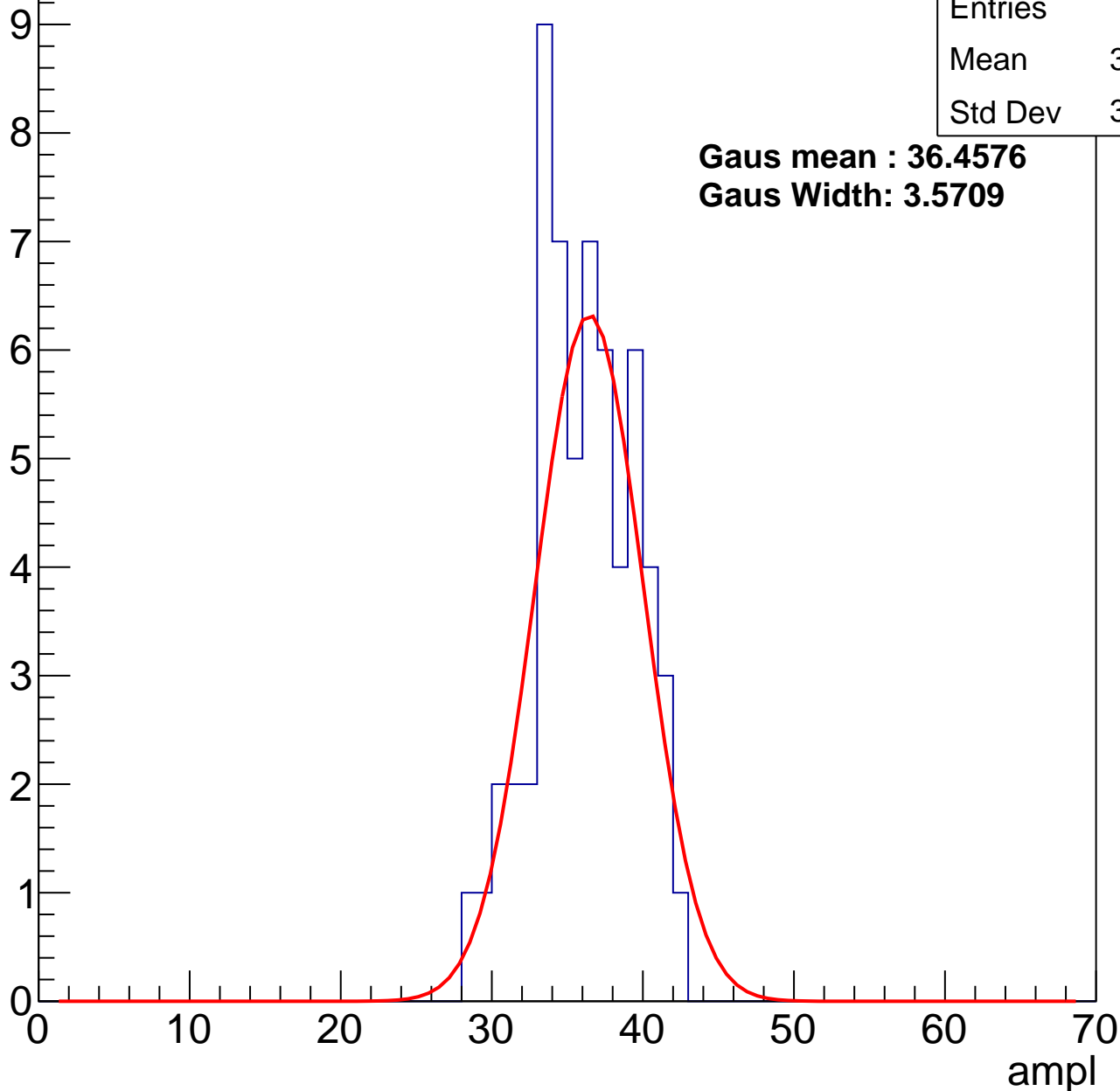
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.63
Std Dev	3.225

Gaus mean : 36.4576

Gaus Width: 3.5709



B1L002S, U7-ch31, adc2

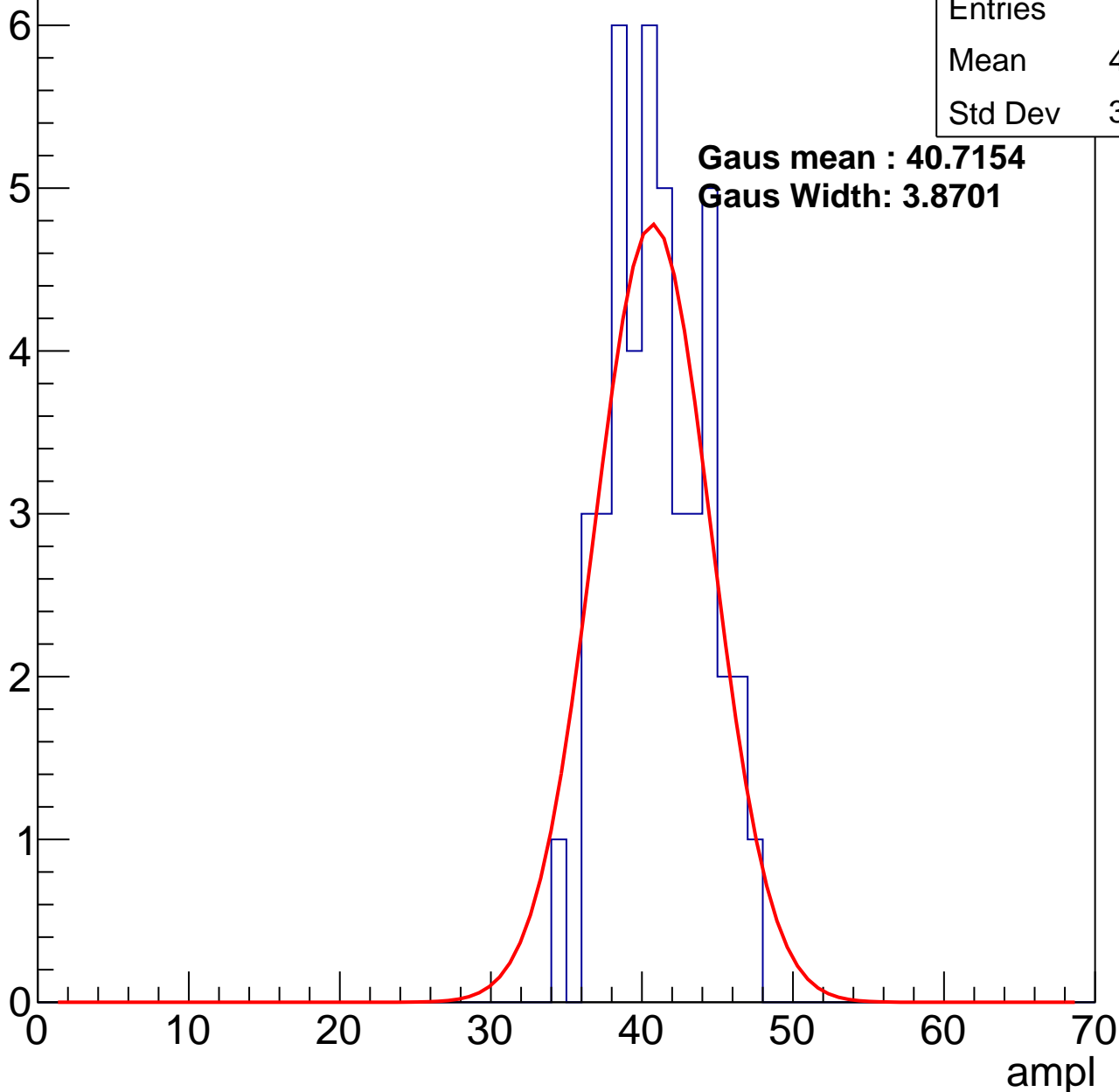
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	44
Mean	40.59
Std Dev	3.085

Gaus mean : 40.7154

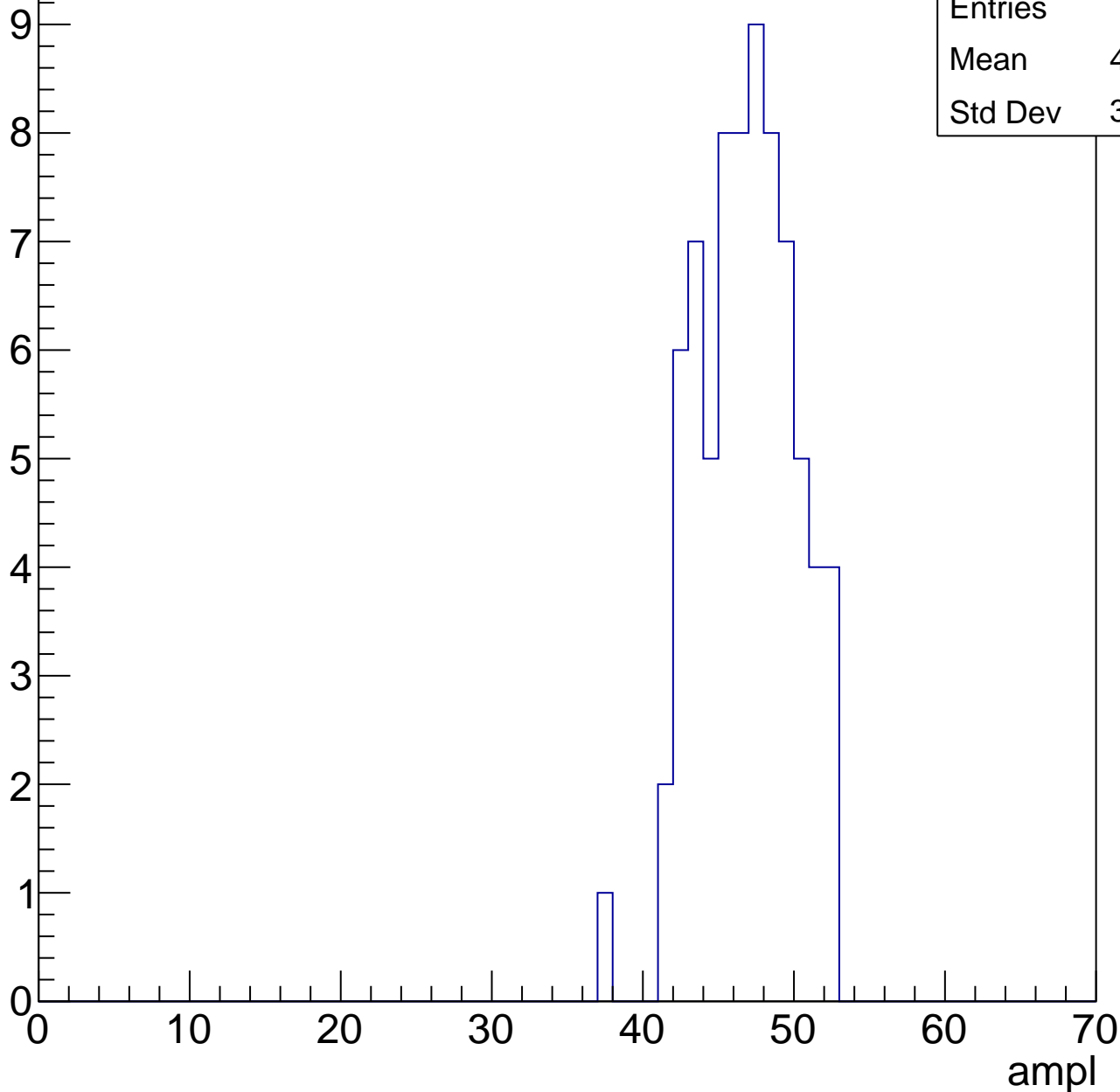
Gaus Width: 3.8701



B1L002S, U7-ch31, adc3

calib_packv5_042523_0143.root, FC#10, port B3

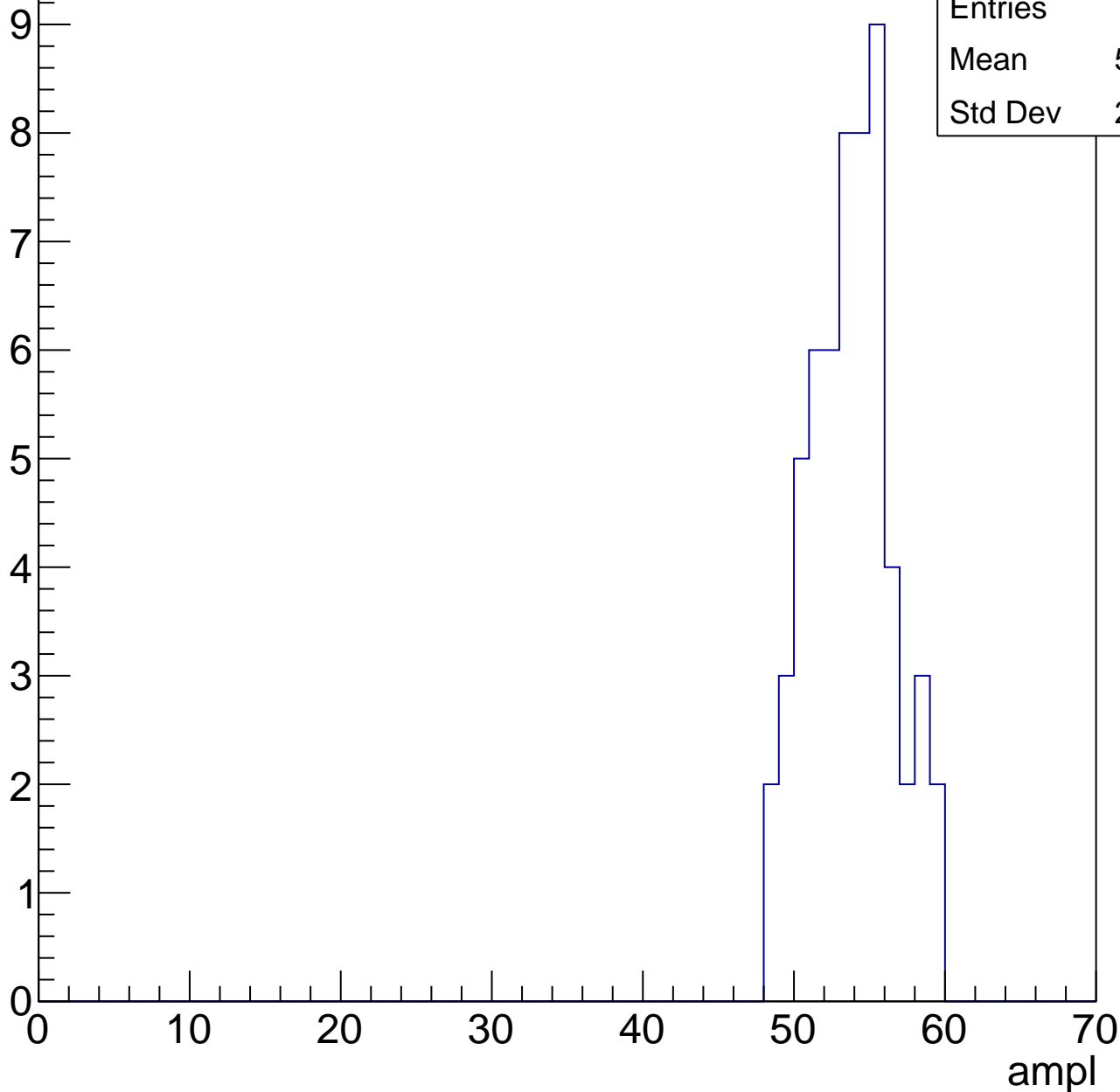
Entry



B1L002S, U7-ch31, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

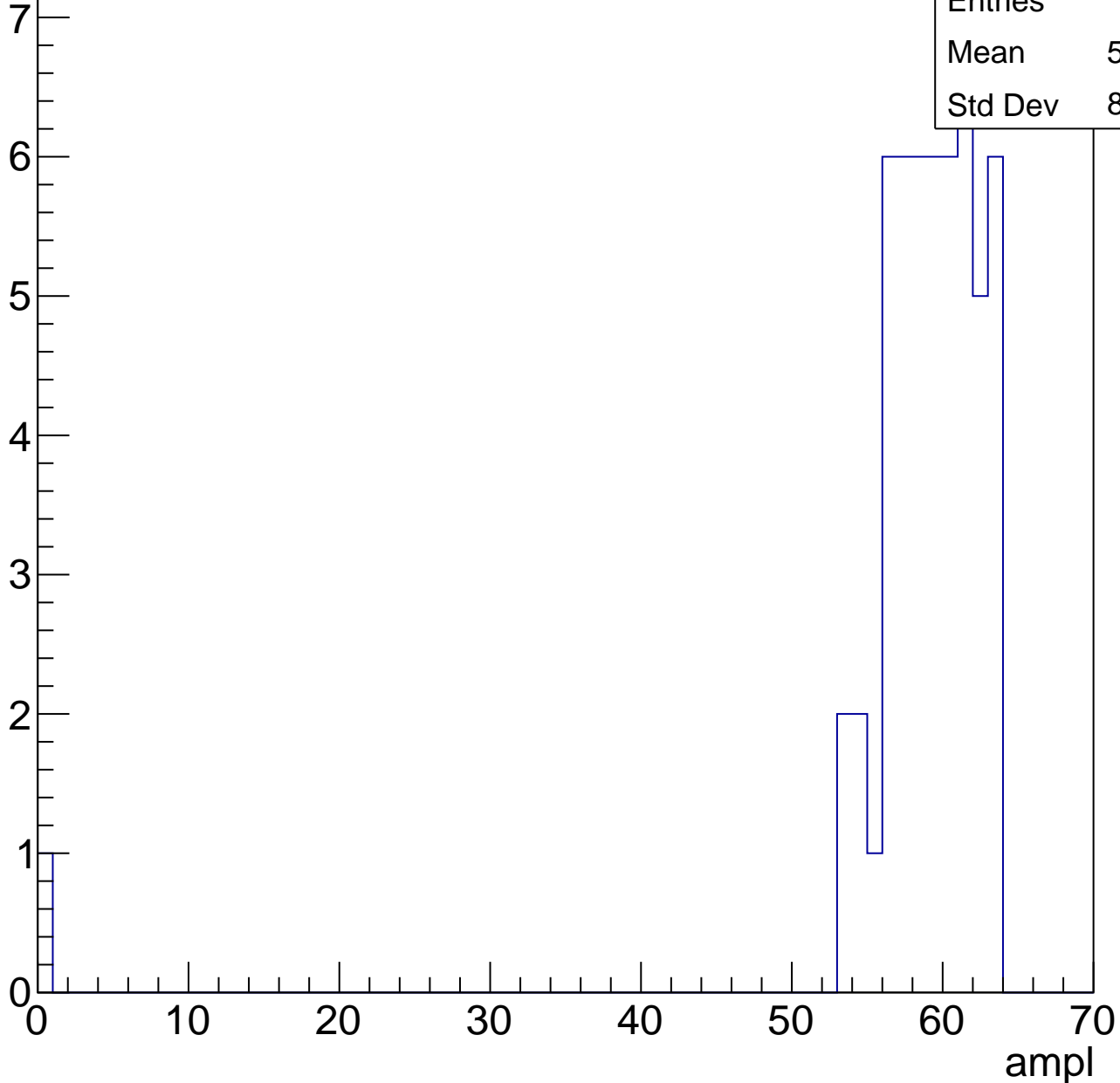


B1L002S, U7-ch31, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.85
Std Dev	8.396



B1L002S, U7-ch31, adc6

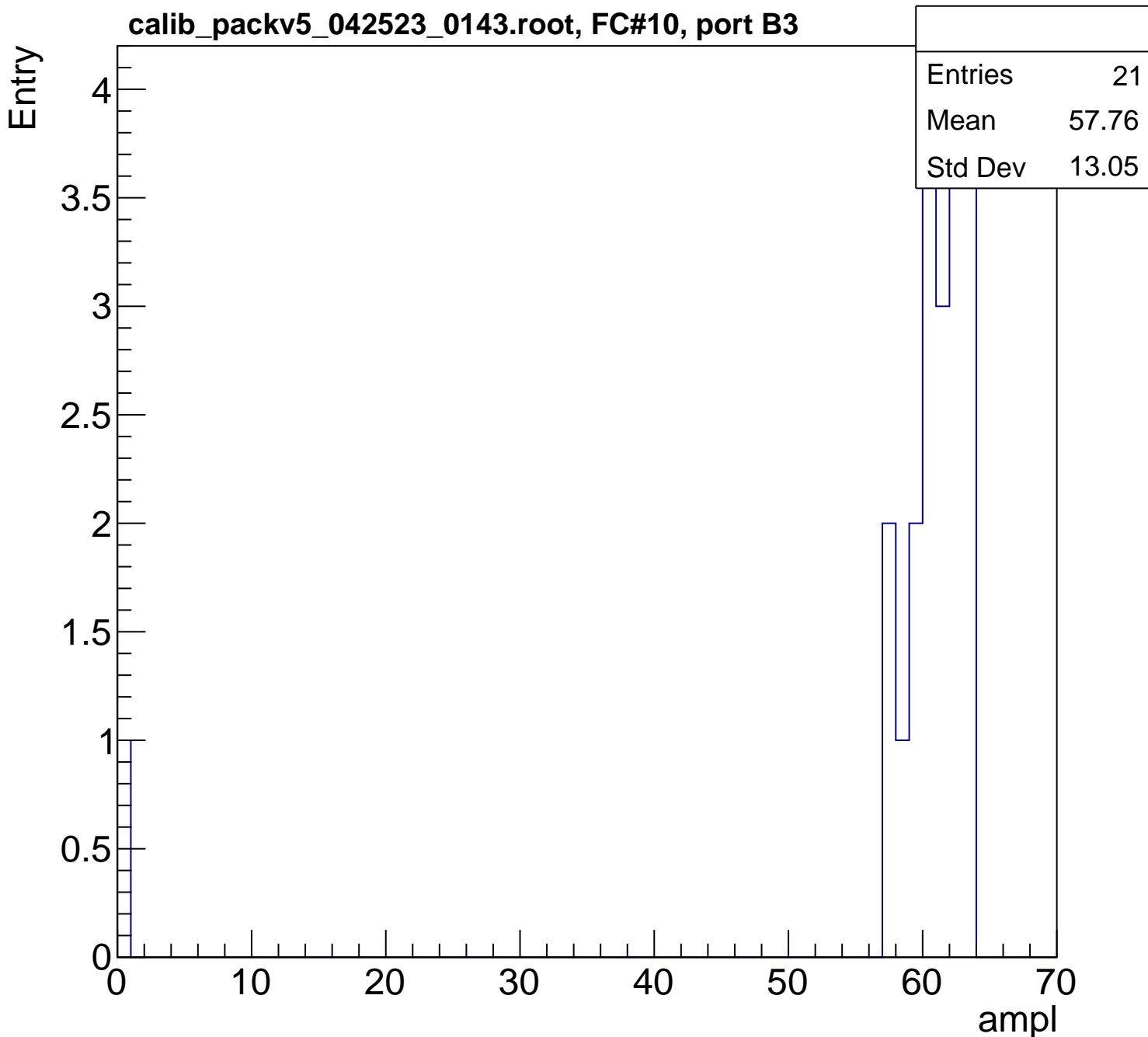
calib_packv5_042523_0143.root, FC#10, port B3

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	21
Mean	57.76
Std Dev	13.05

ampl



B1L002S, U7-ch31, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch32, adc0

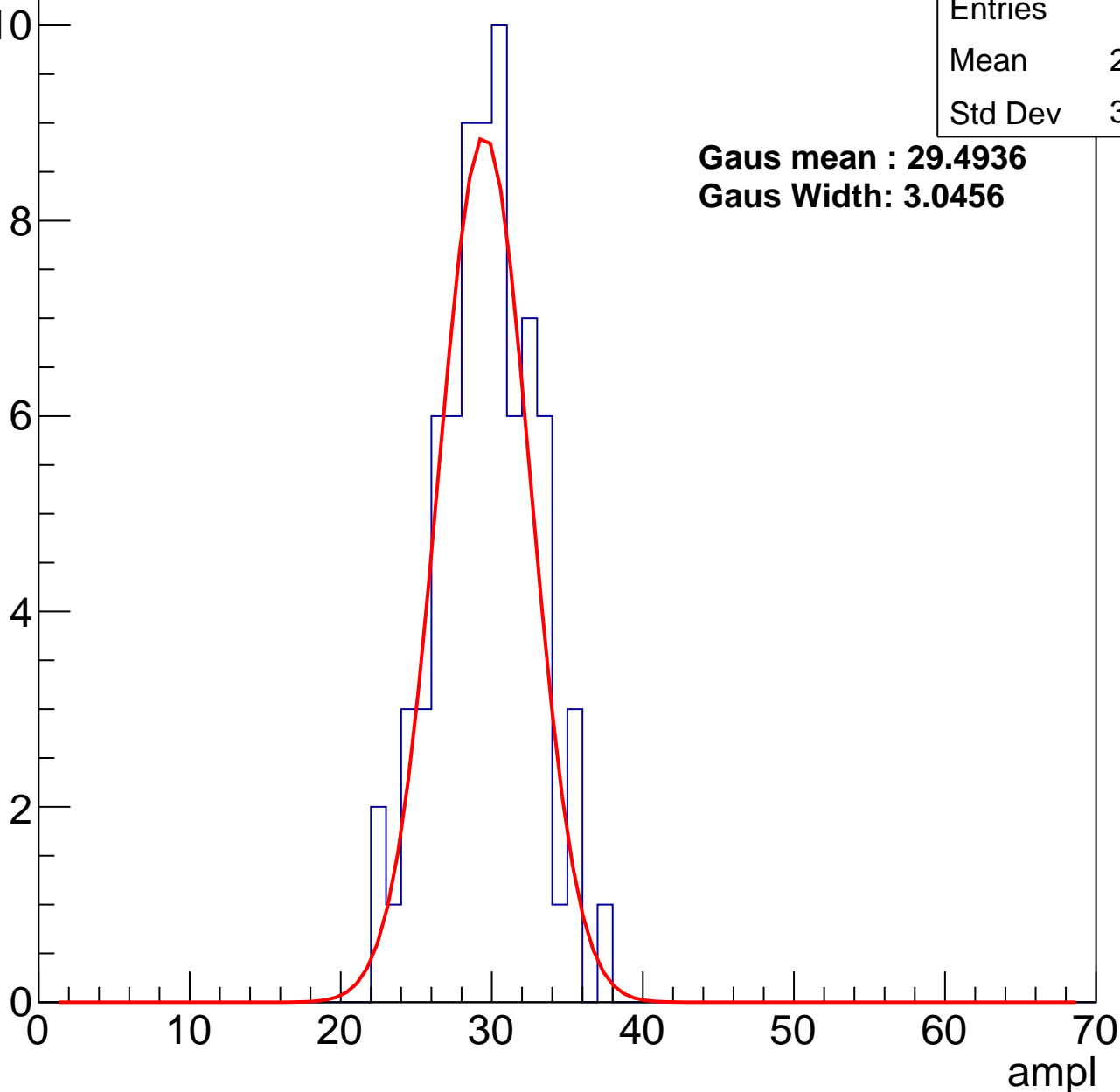
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	29.16
Std Dev	3.167

Gaus mean : 29.4936

Gaus Width: 3.0456



B1L002S, U7-ch32, adc1

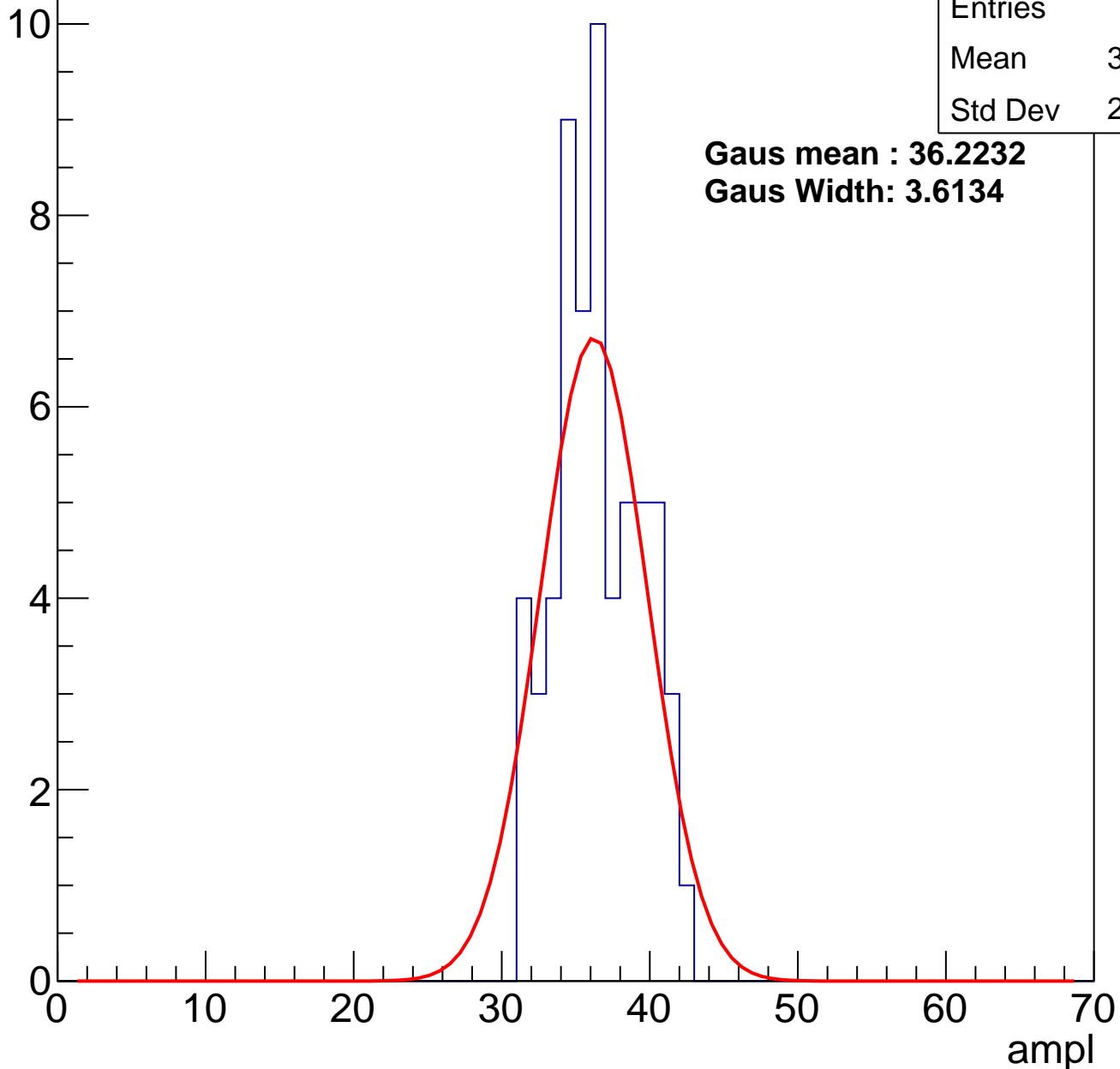
calib_packv5_042523_0143.root, FC#10, port B3

Entries	60
Mean	36.02
Std Dev	2.849

Gaus mean : 36.2232

Gaus Width: 3.6134

Entry



B1L002S, U7-ch32, adc2

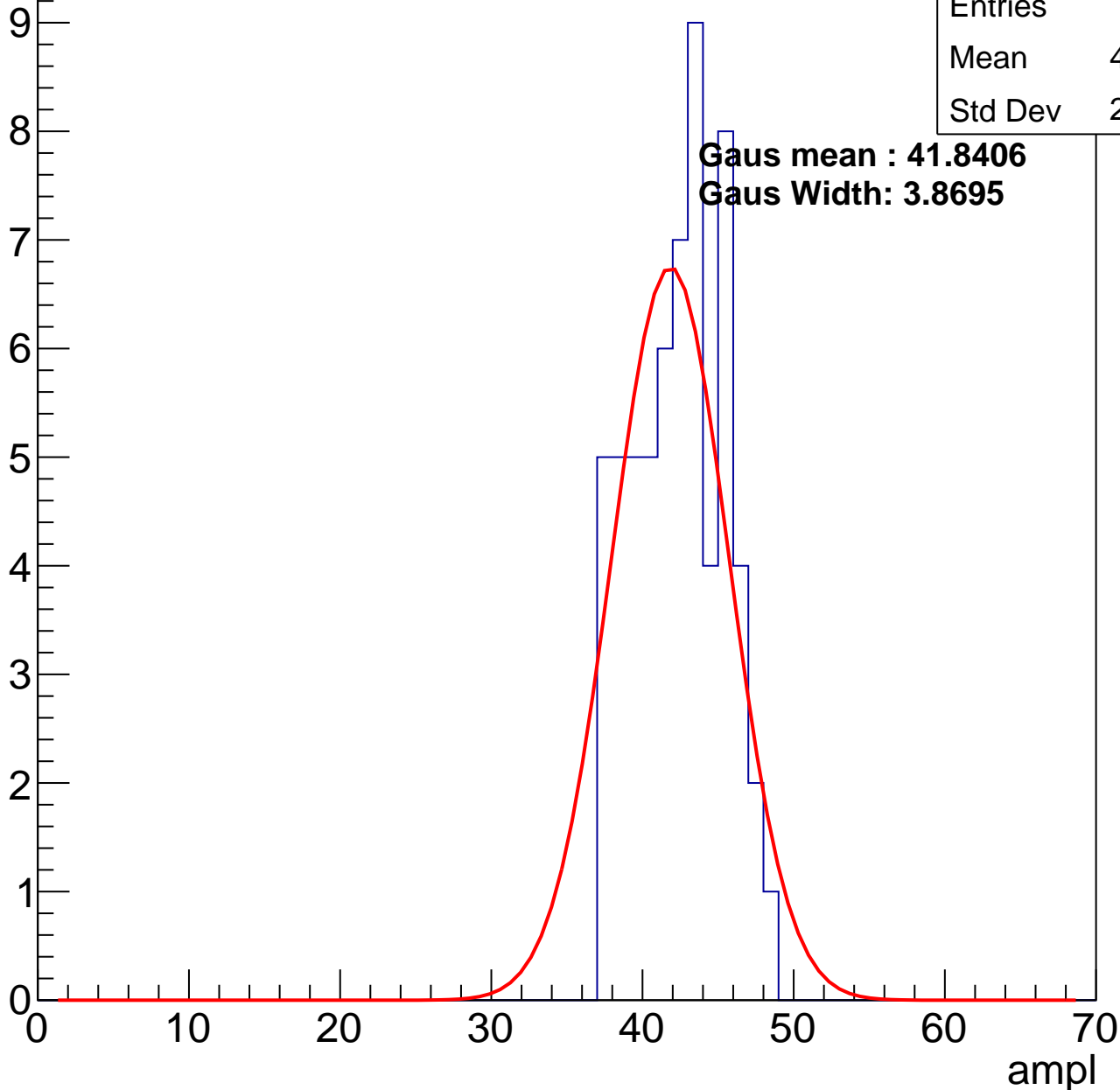
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.95
Std Dev	2.928

Gaus mean : 41.8406

Gaus Width: 3.8695

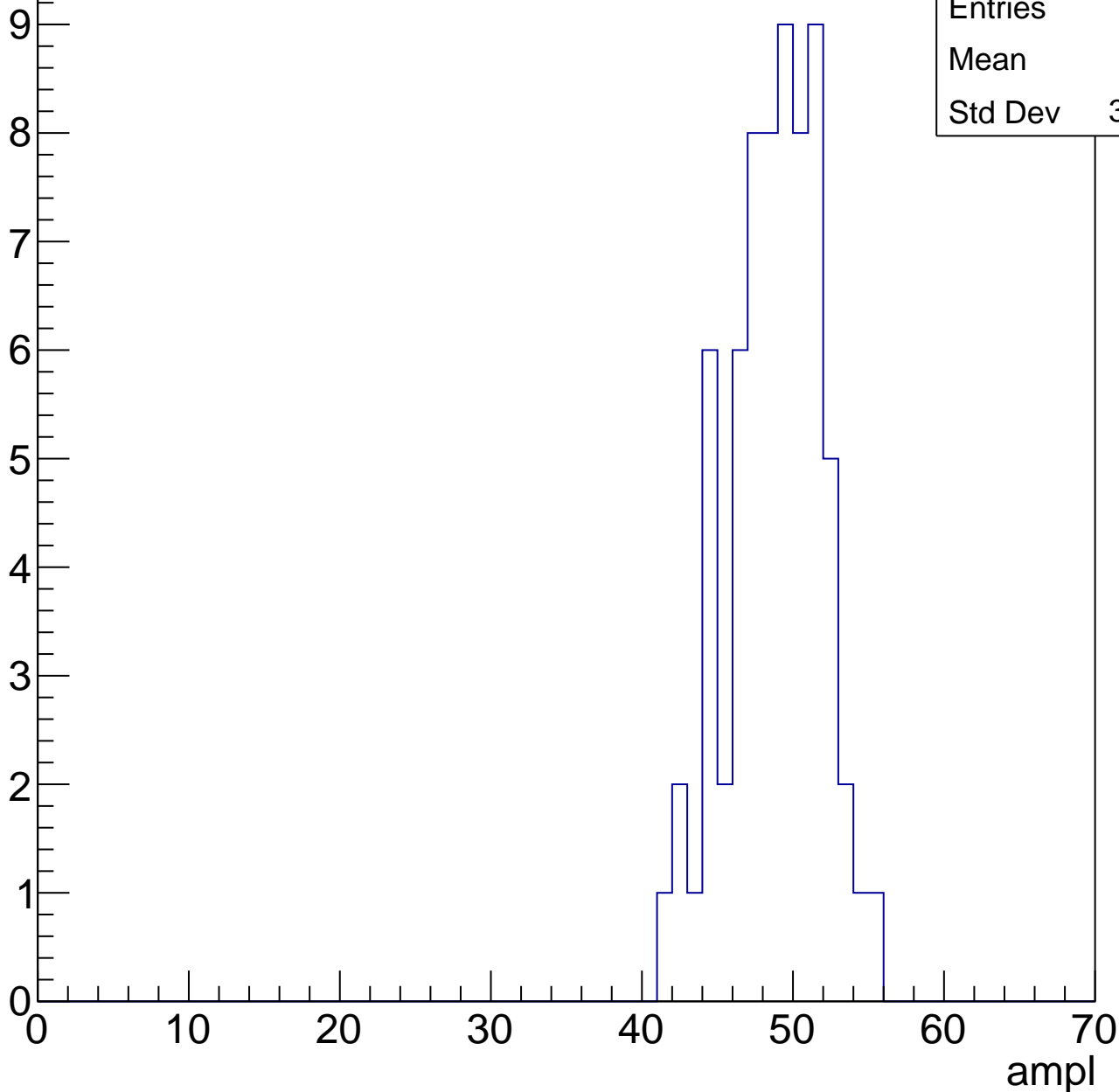


B1L002S, U7-ch32, adc3

calib_packv5_042523_0143.root, FC#10, port B3

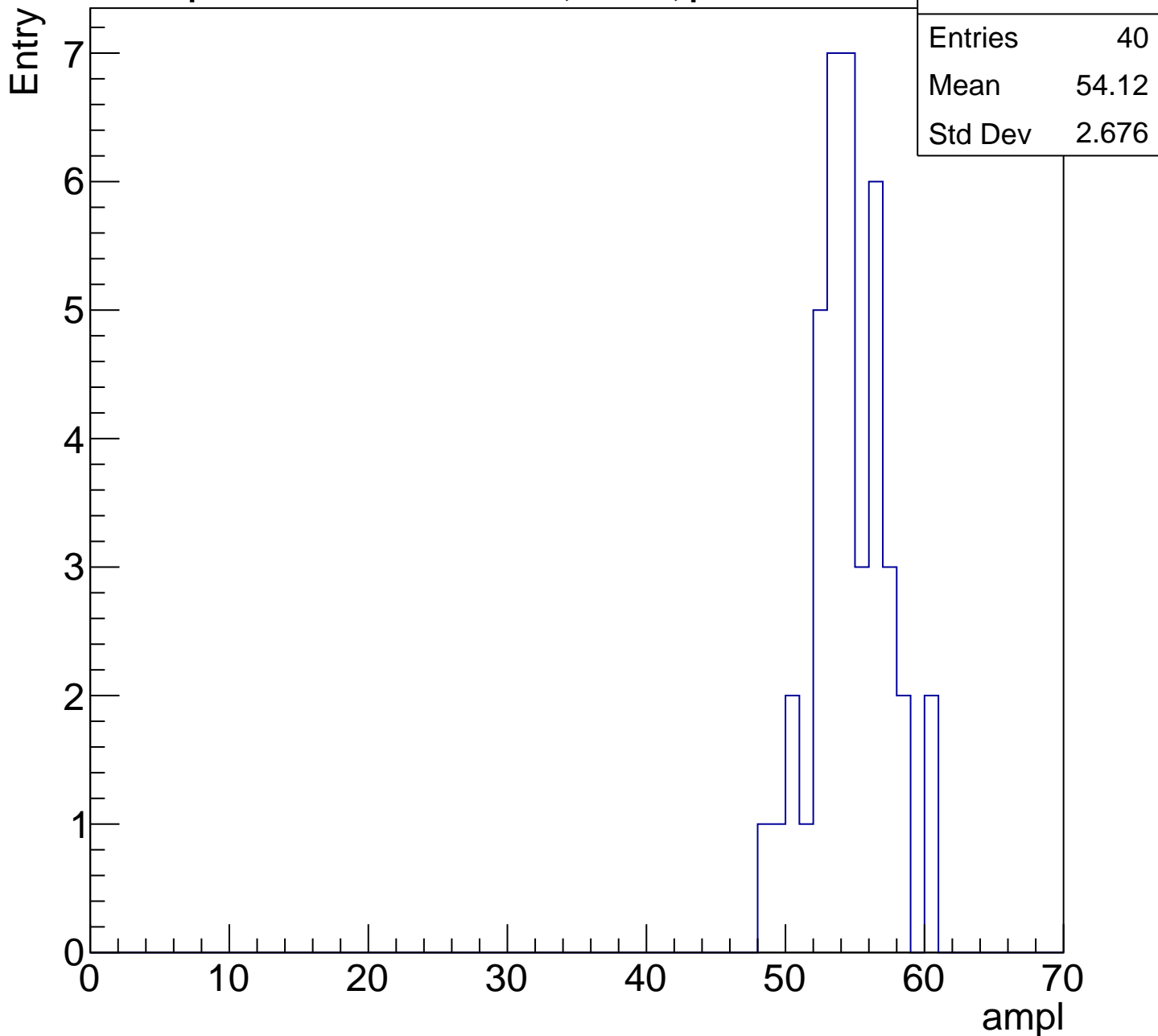
Entry

Entries	69
Mean	48.3
Std Dev	3.004



B1L002S, U7-ch32, adc4

calib_packv5_042523_0143.root, FC#10, port B3

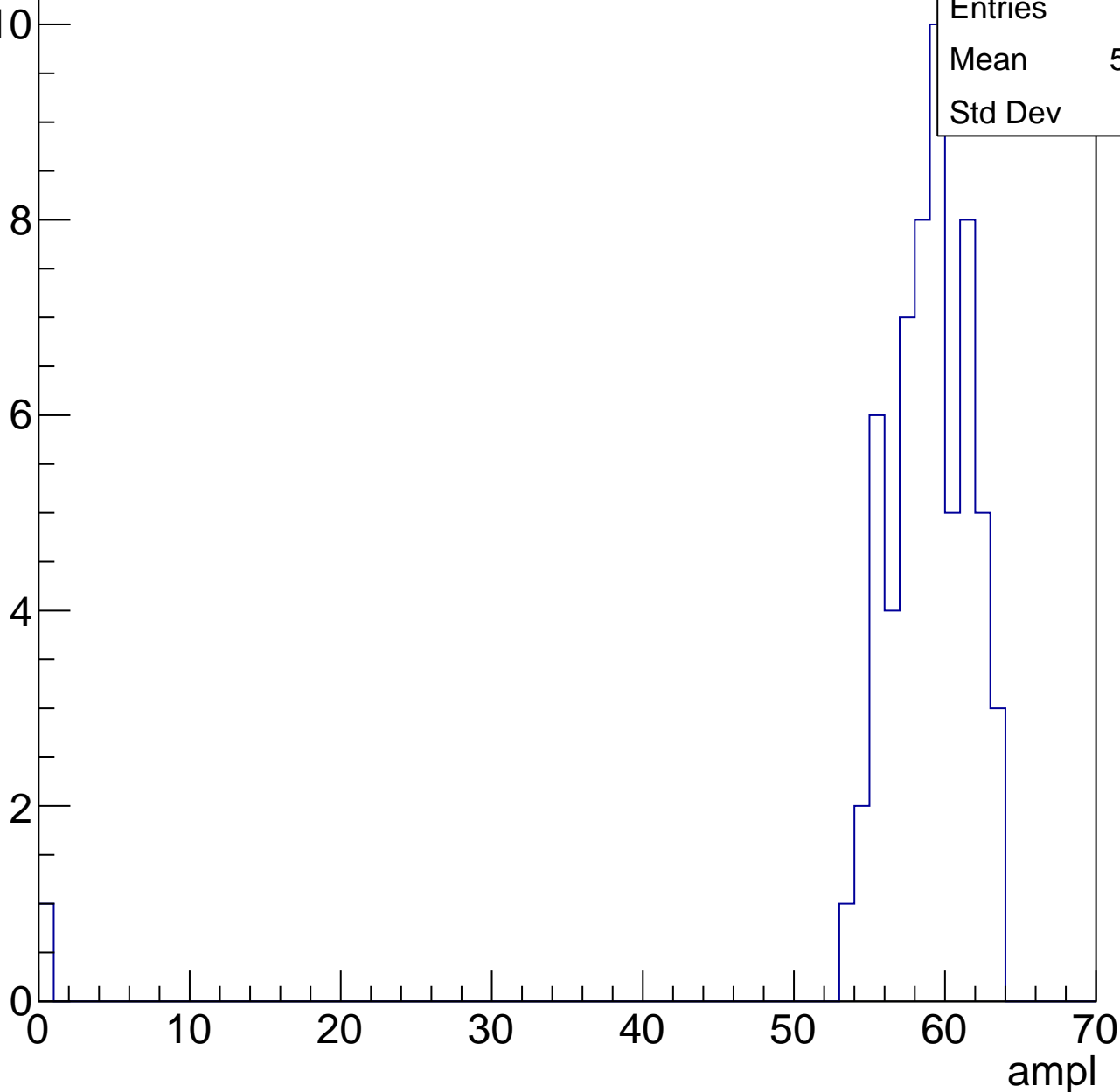


B1L002S, U7-ch32, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	57.58
Std Dev	7.9

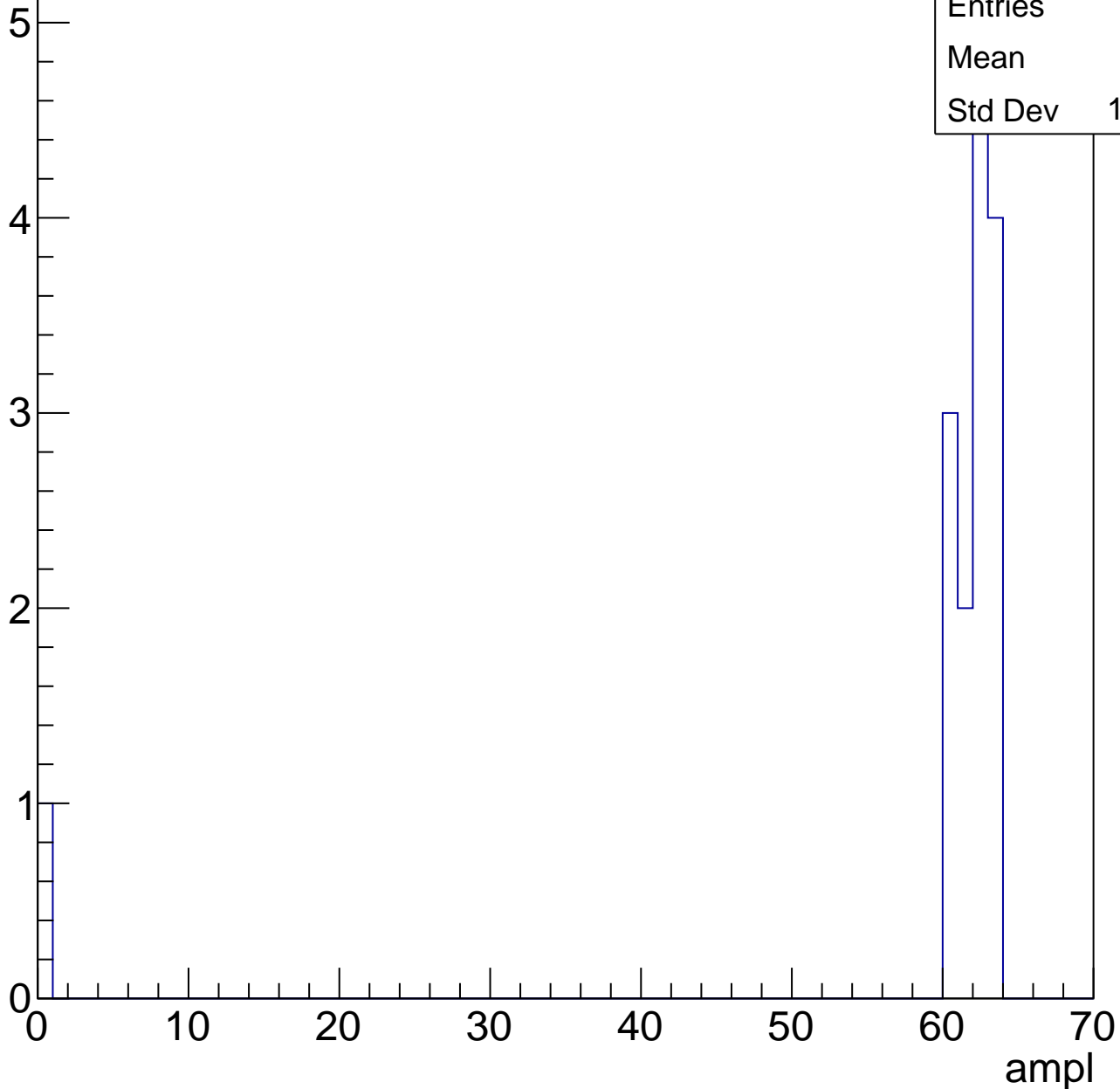


B1L002S, U7-ch32, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

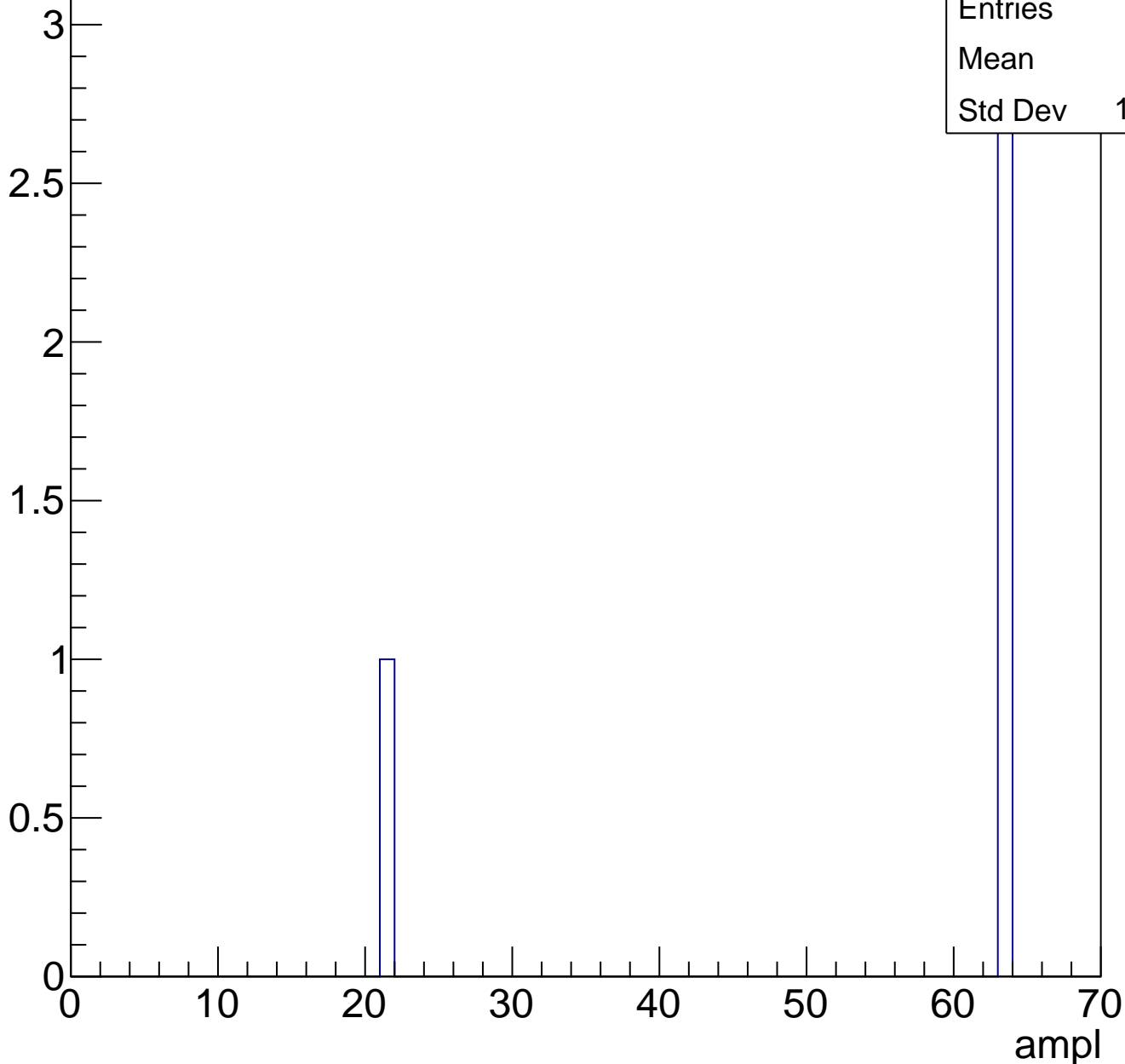
Entries	15
Mean	57.6
Std Dev	15.43



B1L002S, U7-ch32, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch33, adc0

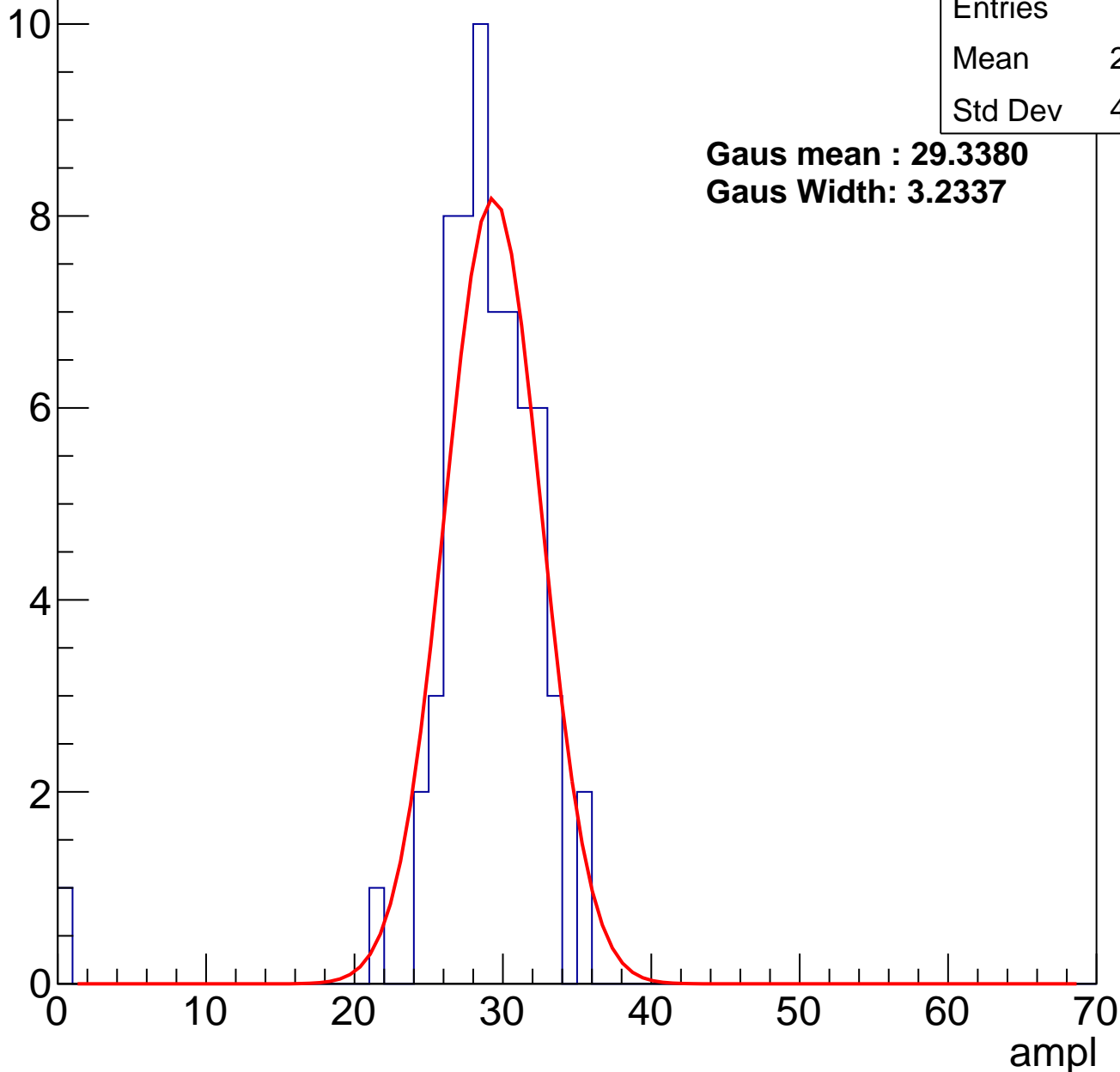
calib_packv5_042523_0143.root, FC#10, port B3

Entries	64
Mean	28.25
Std Dev	4.483

Gaus mean : 29.3380

Gaus Width: 3.2337

Entry



B1L002S, U7-ch33, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	75
Mean	35.41
Std Dev	3.15

Gaus mean : 36.0169

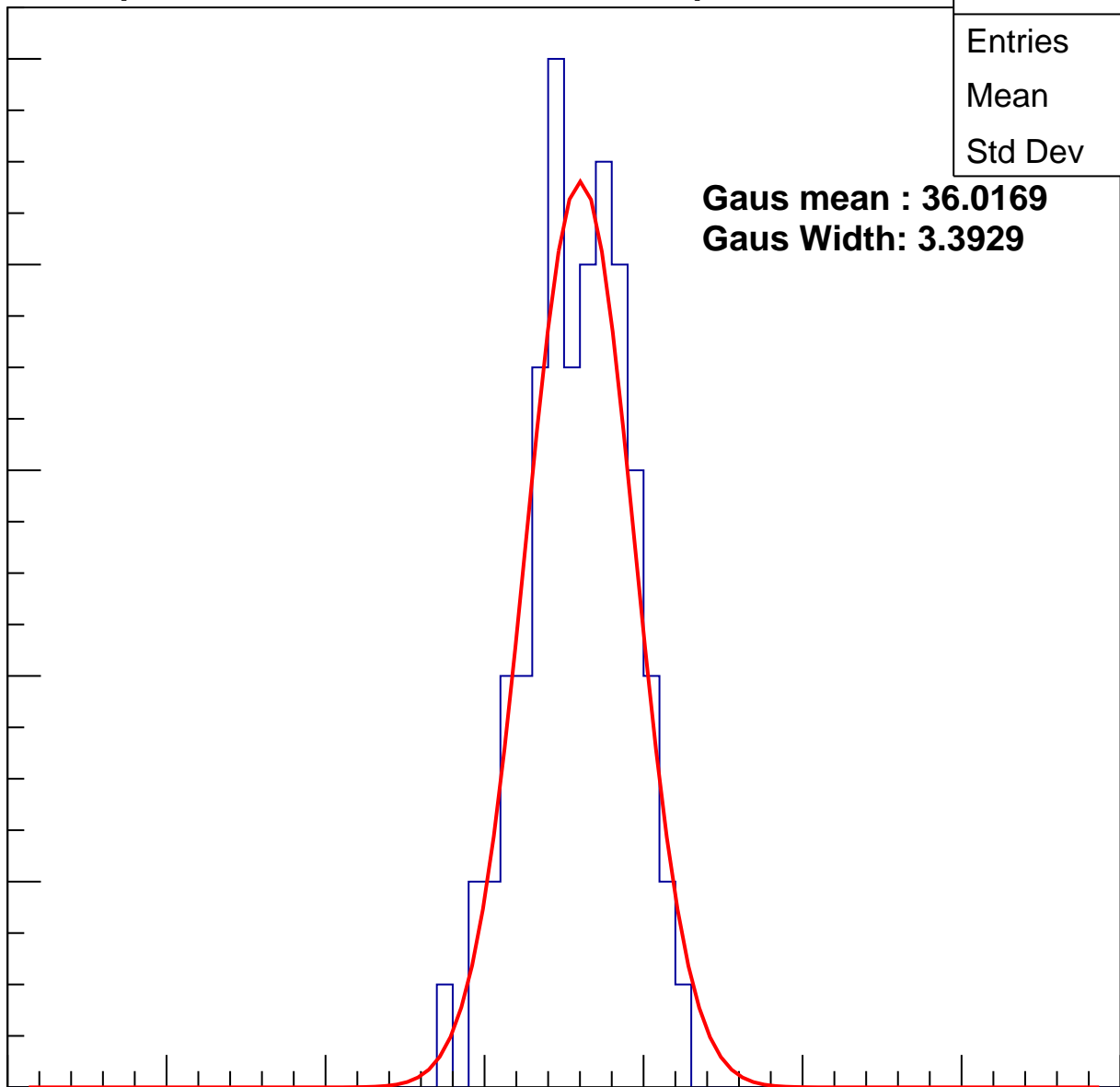
Gaus Width: 3.3929

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch33, adc2

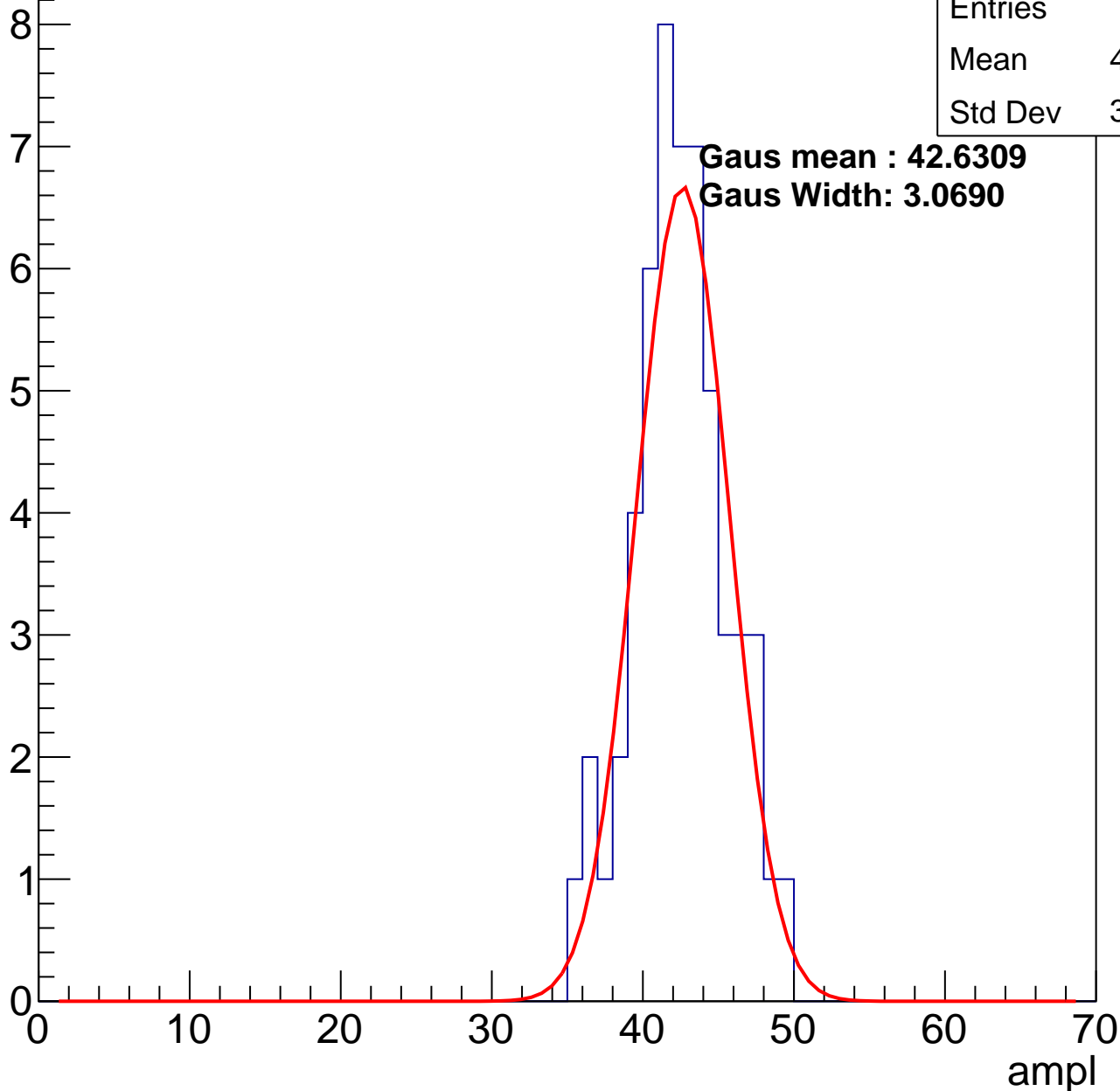
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	42.04
Std Dev	3.067

Gaus mean : 42.6309

Gaus Width: 3.0690

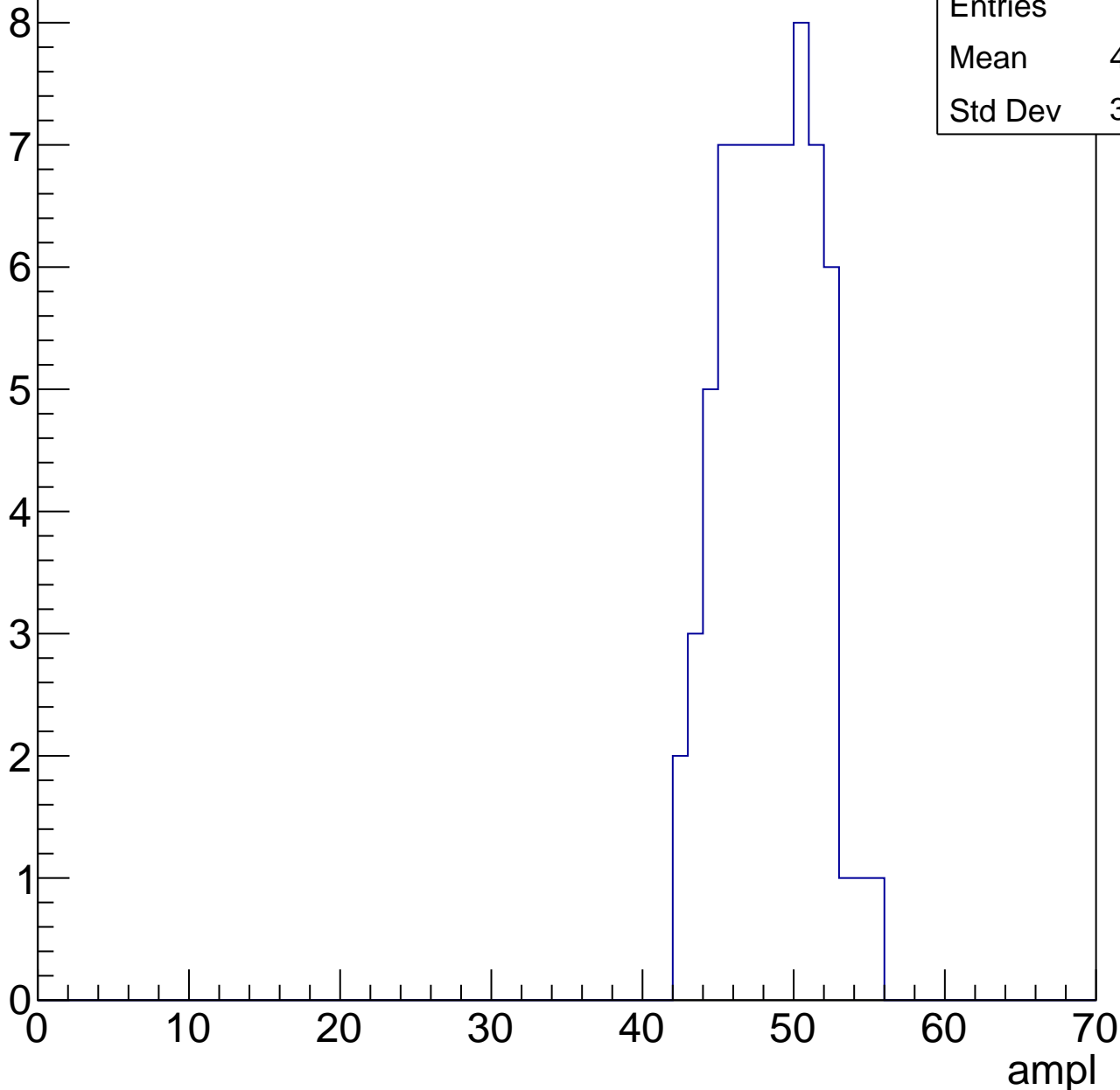


B1L002S, U7-ch33, adc3

calib_packv5_042523_0143.root, FC#10, port B3

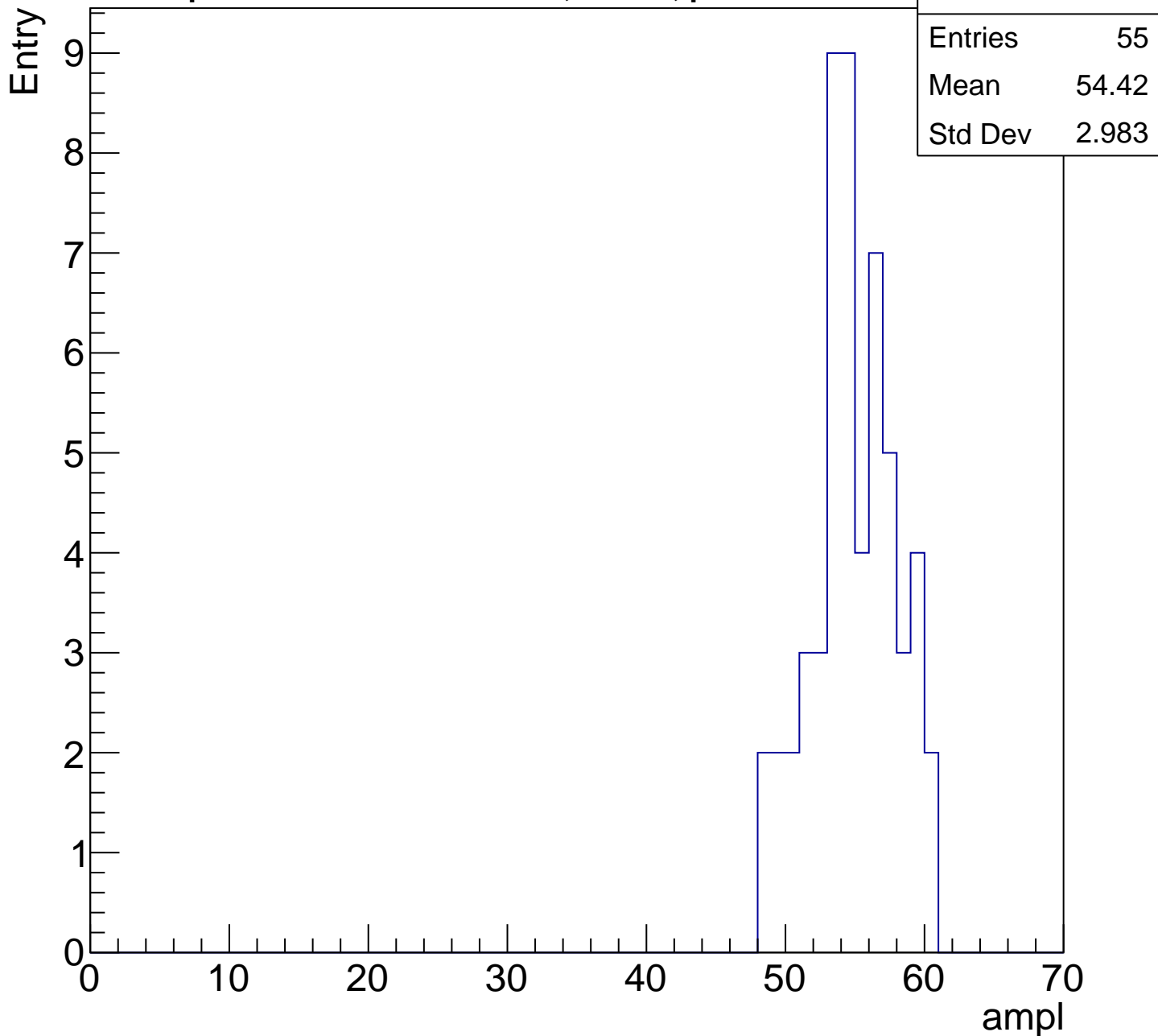
Entry

Entries	69
Mean	47.96
Std Dev	3.029



B1L002S, U7-ch33, adc4

calib_packv5_042523_0143.root, FC#10, port B3

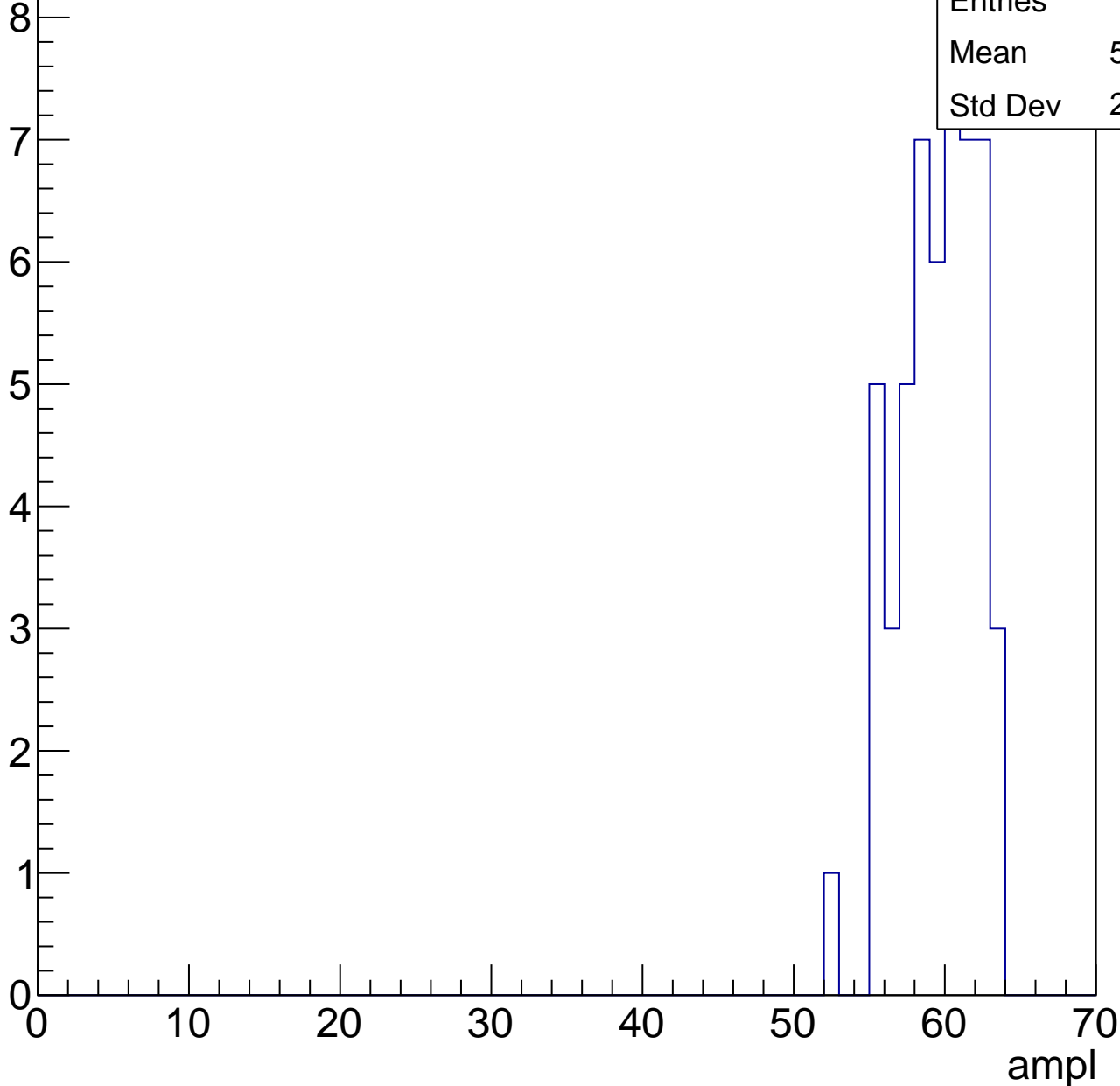


B1L002S, U7-ch33, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	59.04
Std Dev	2.519

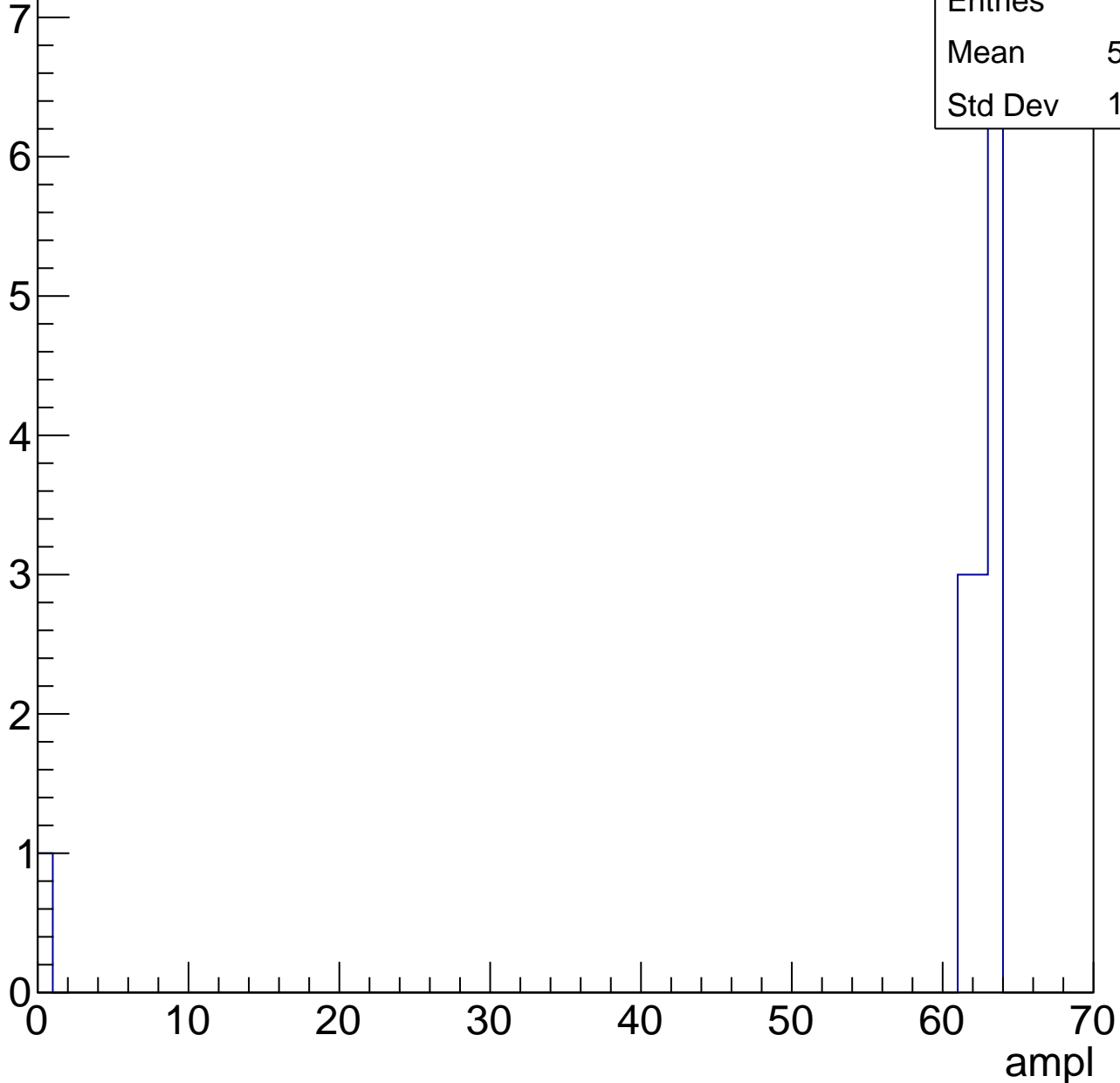


B1L002S, U7-ch33, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.86
Std Dev	16.07



B1L002S, U7-ch33, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch34, adc0

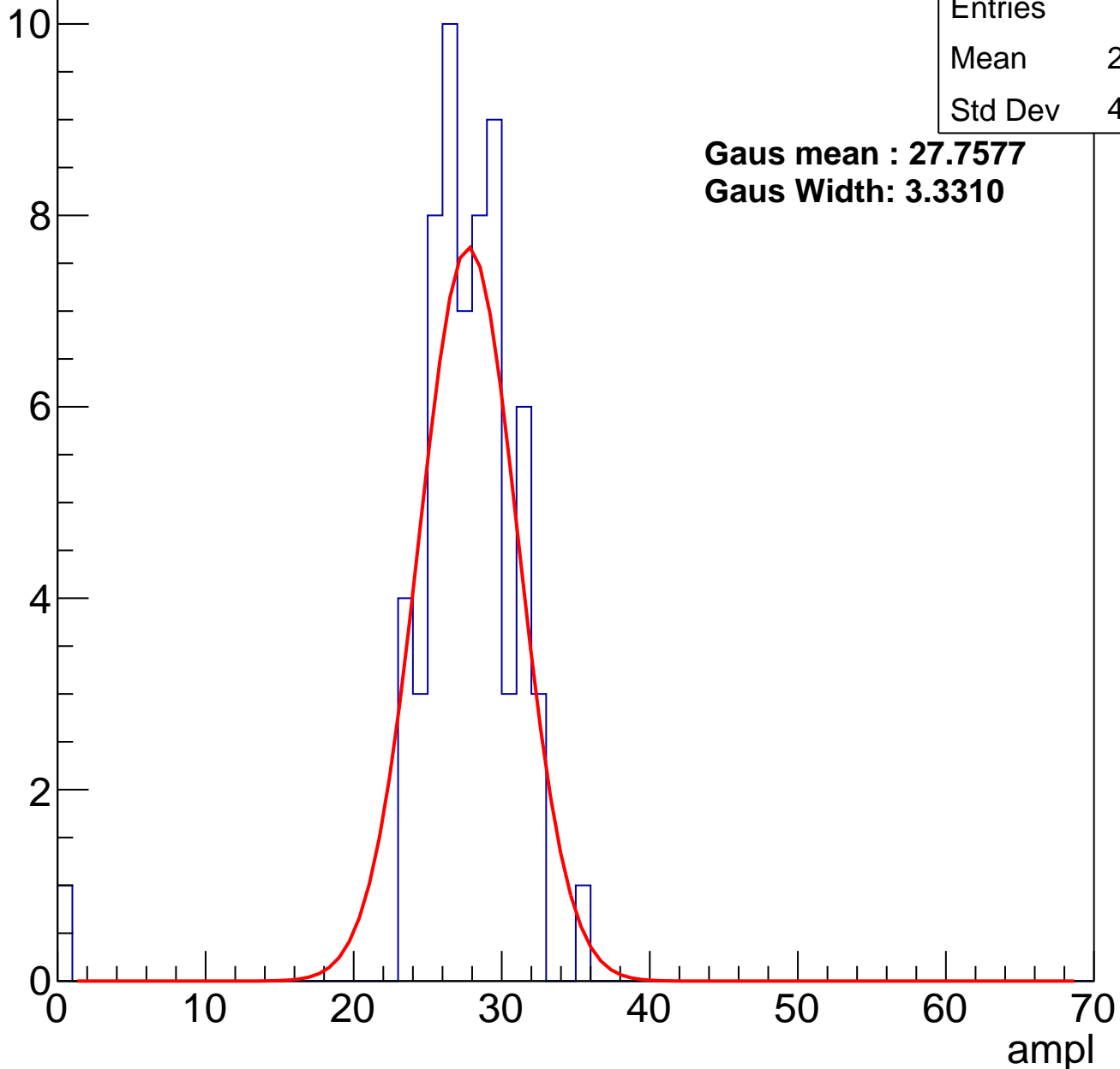
calib_packv5_042523_0143.root, FC#10, port B3

Entries	63
Mean	27.06
Std Dev	4.305

Gaus mean : 27.7577

Gaus Width: 3.3310

Entry



B1L002S, U7-ch34, adc1

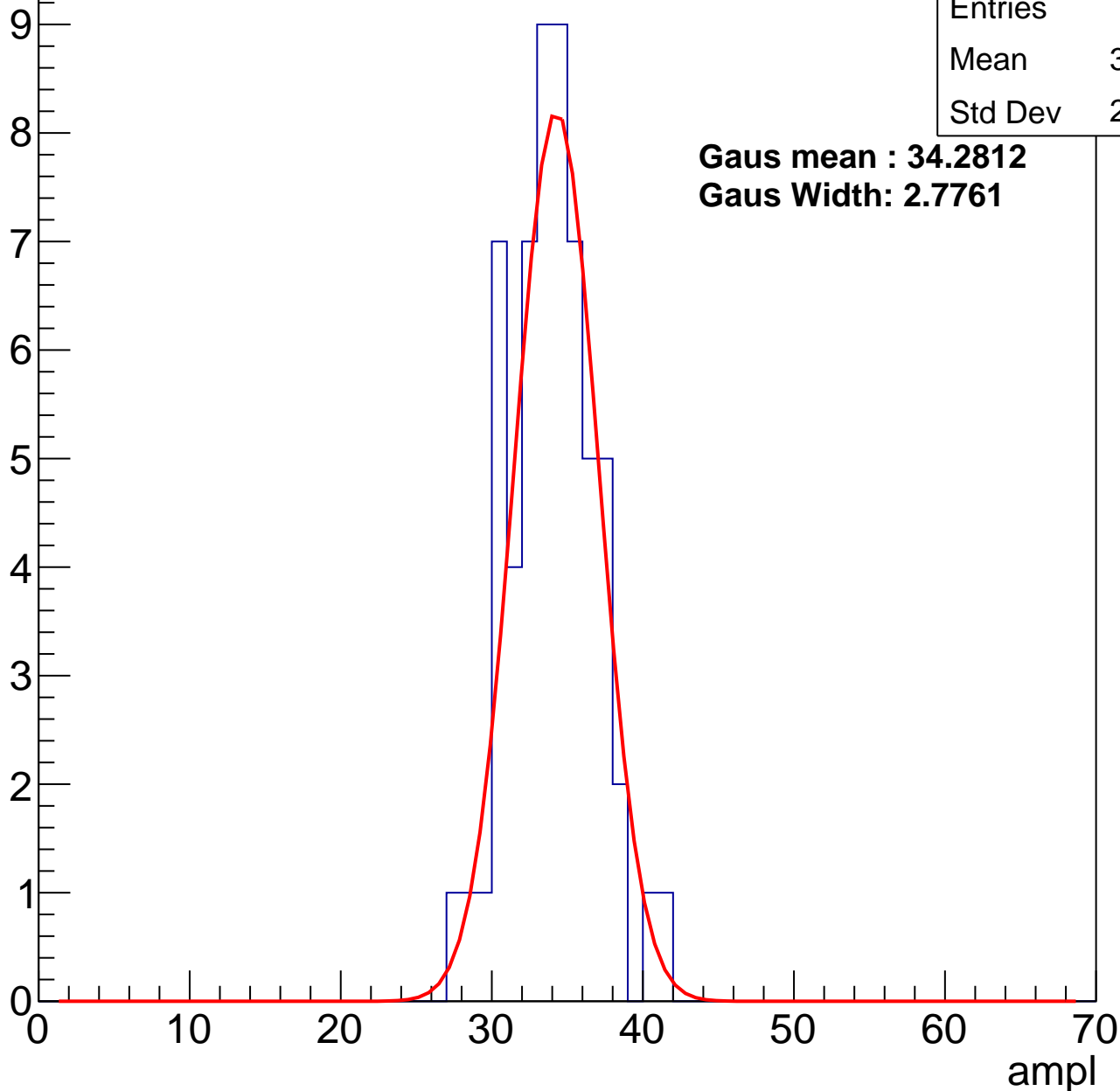
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.53
Std Dev	2.802

Gaus mean : 34.2812

Gaus Width: 2.7761



B1L002S, U7-ch34, adc2

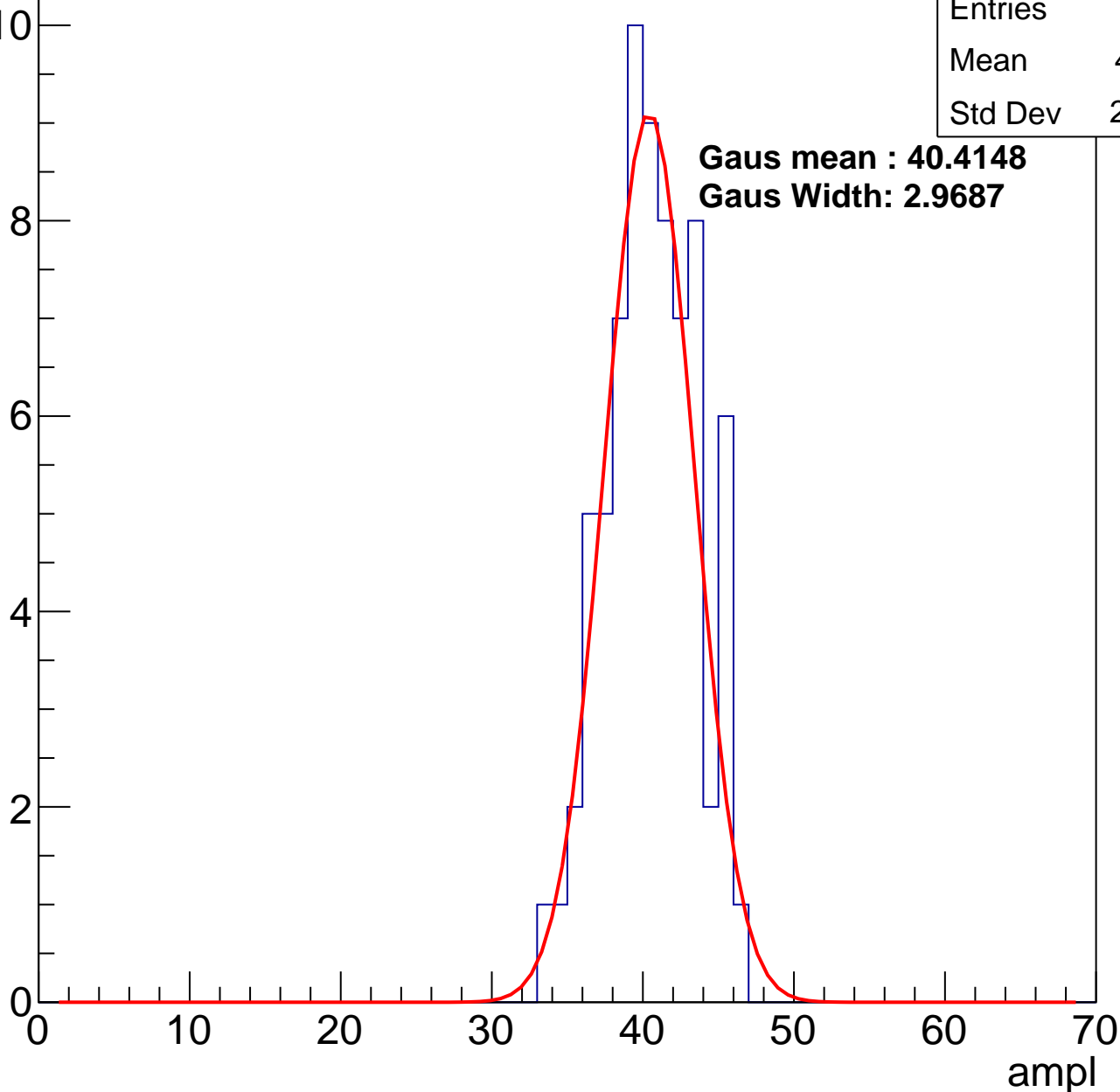
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	40.11
Std Dev	2.942

Gaus mean : 40.4148

Gaus Width: 2.9687

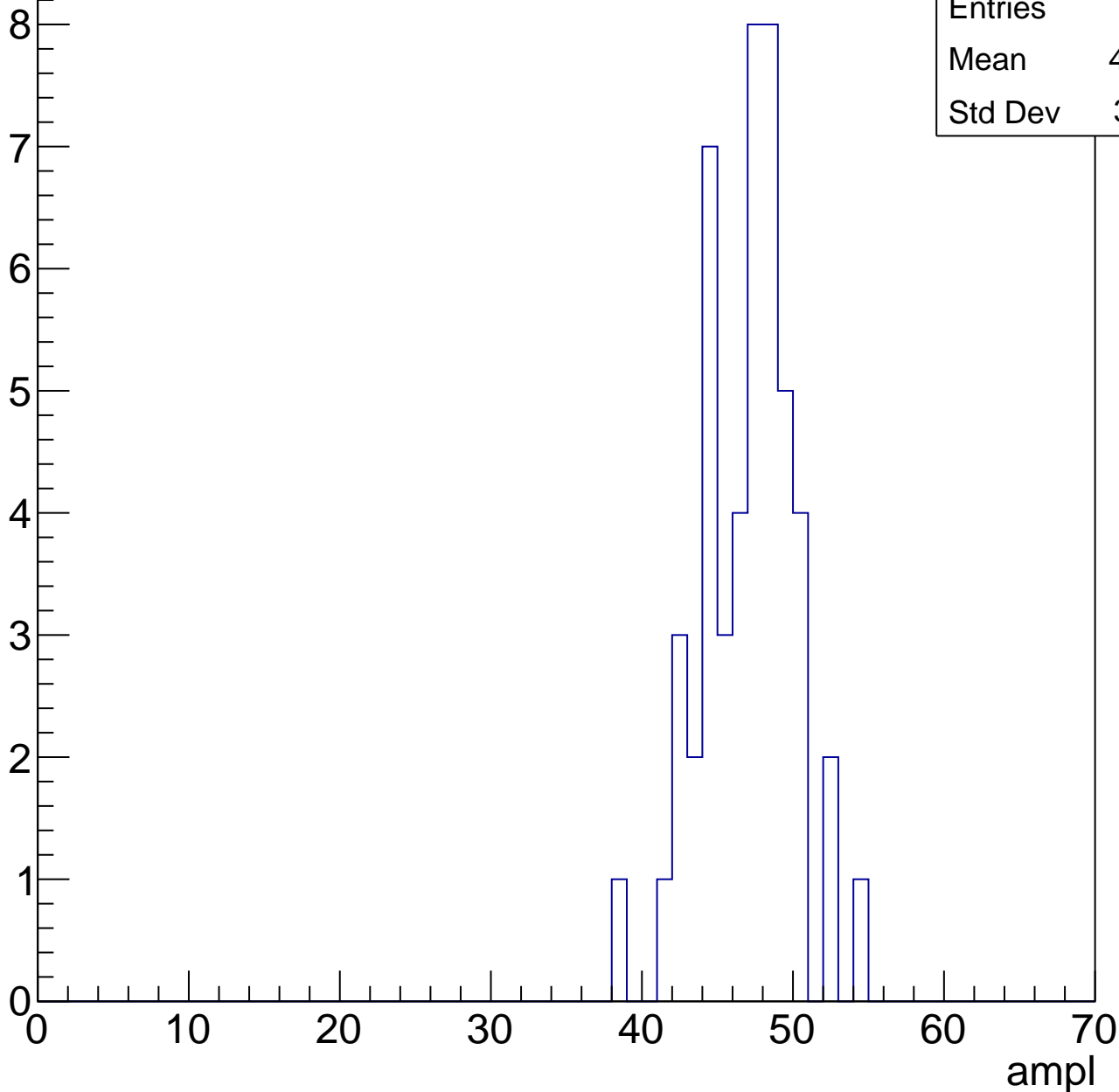


B1L002S, U7-ch34, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

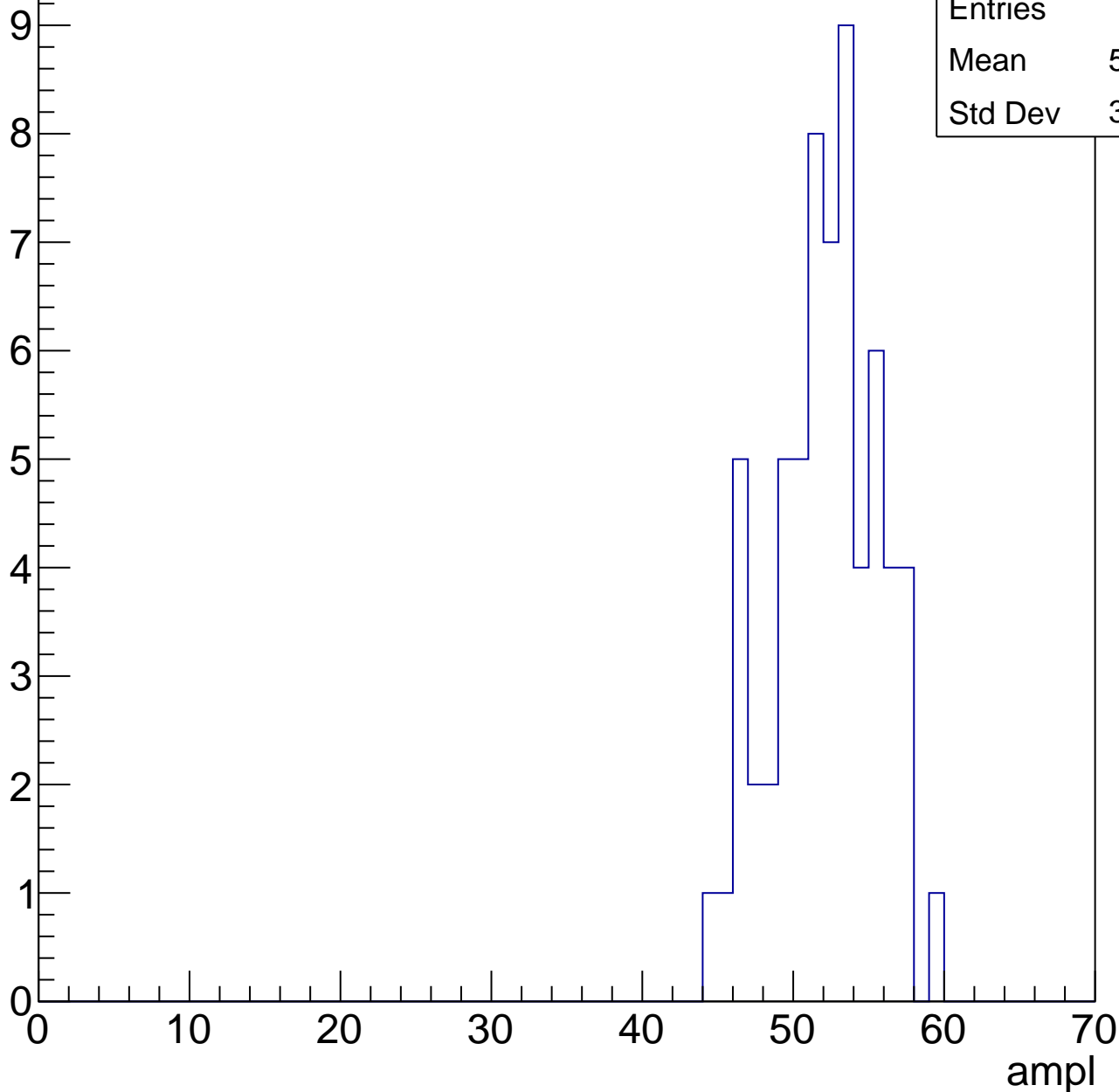
Entries	49
Mean	46.55
Std Dev	3.051



B1L002S, U7-ch34, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch34, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	57.78
Std Dev	2.974

Entry

10

8

6

4

2

0

0

10

20

30

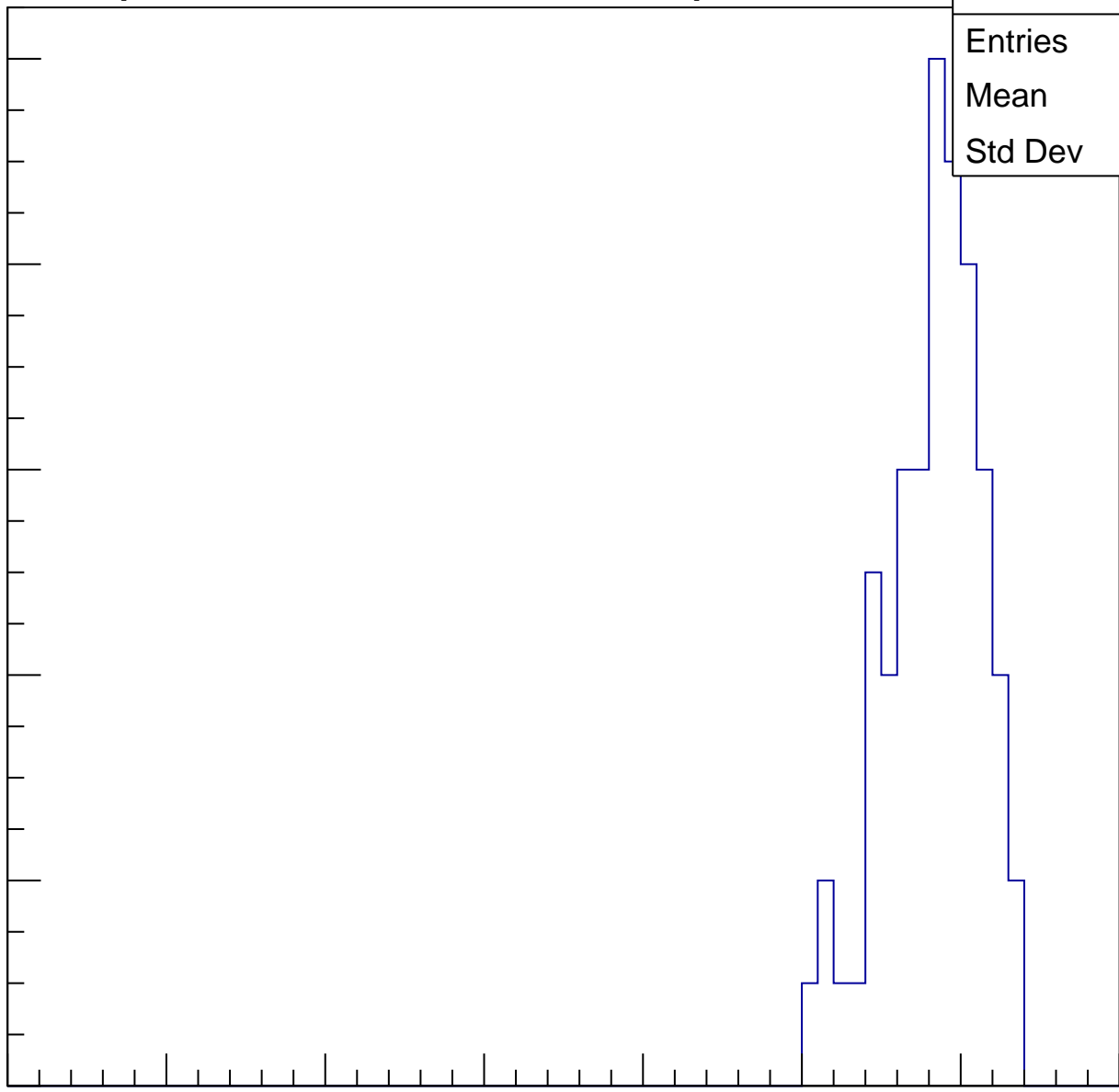
40

50

60

70

ampl

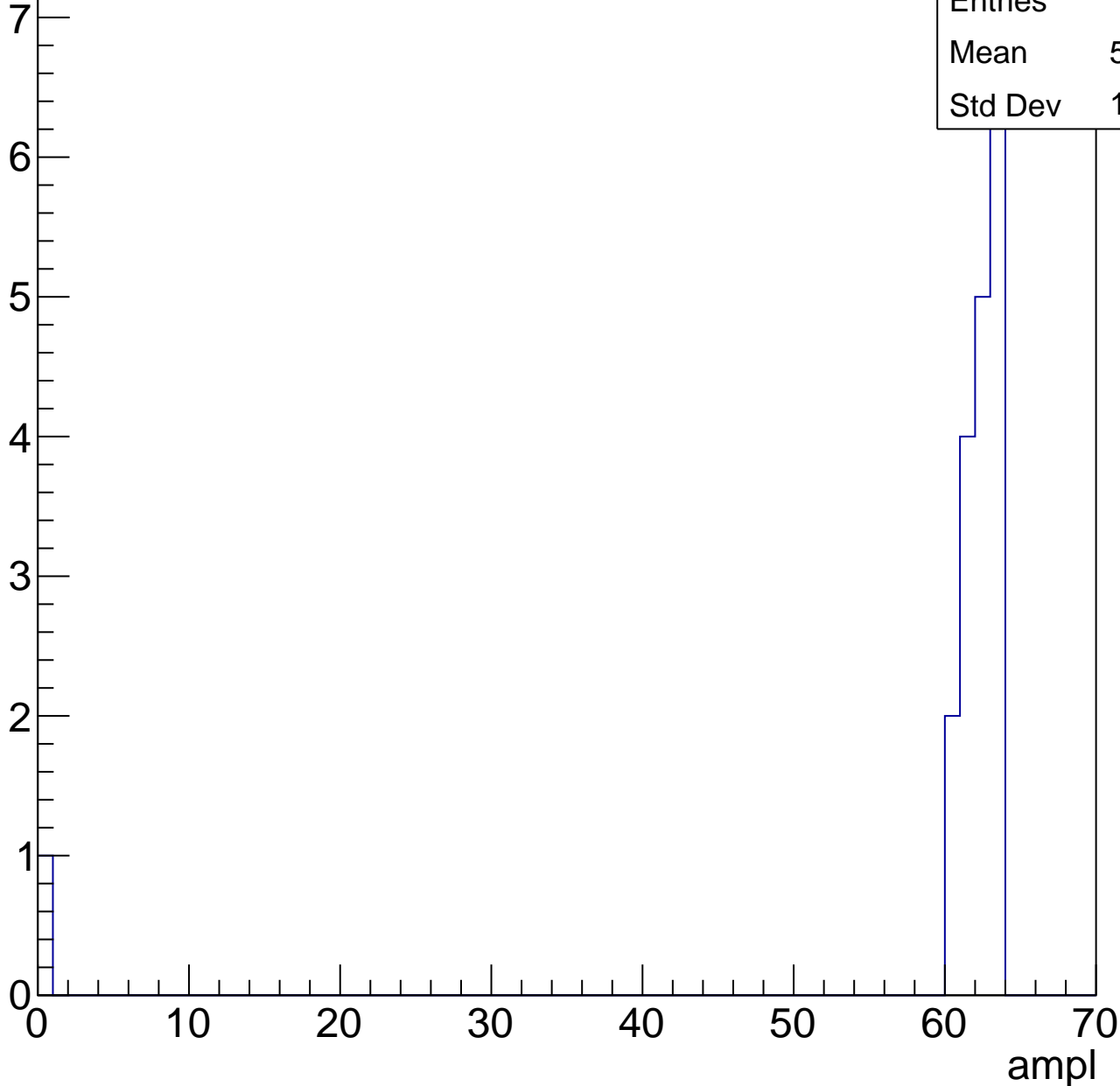


B1L002S, U7-ch34, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

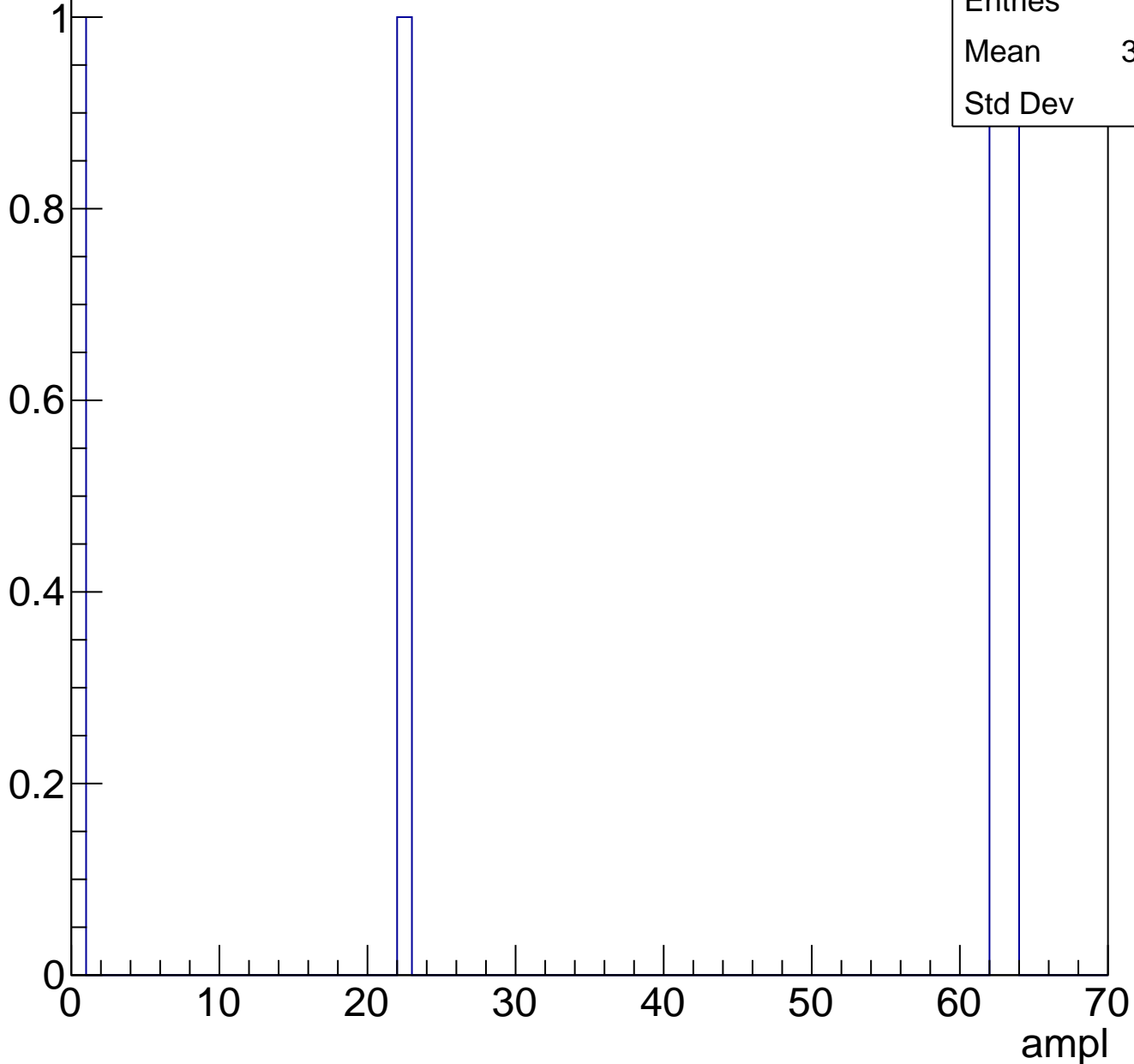
Entries	19
Mean	58.68
Std Dev	13.87



B1L002S, U7-ch34, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch35, adc0

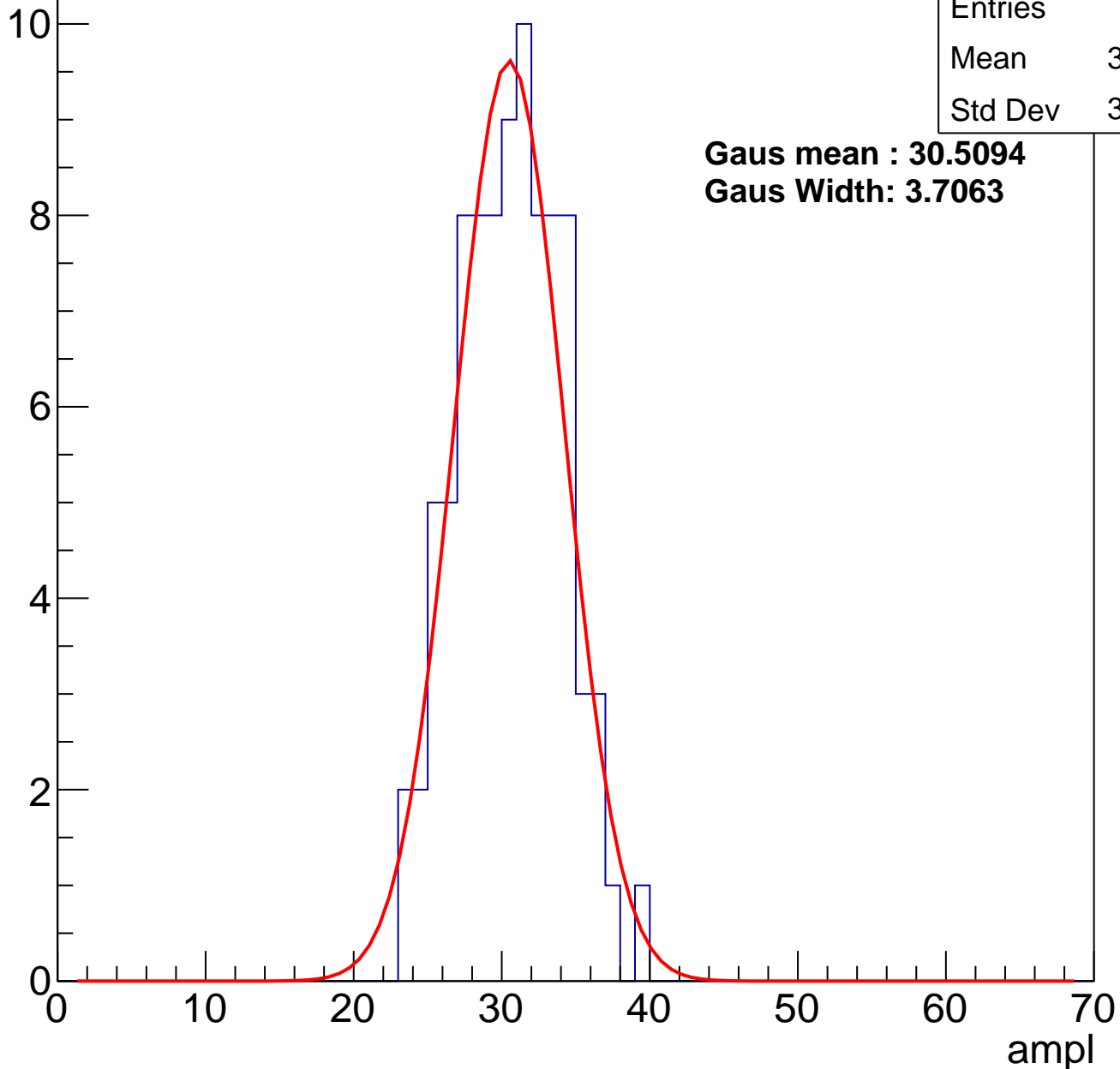
calib_packv5_042523_0143.root, FC#10, port B3

Entries	89
Mean	30.13
Std Dev	3.419

Gaus mean : 30.5094

Gaus Width: 3.7063

Entry



B1L002S, U7-ch35, adc1

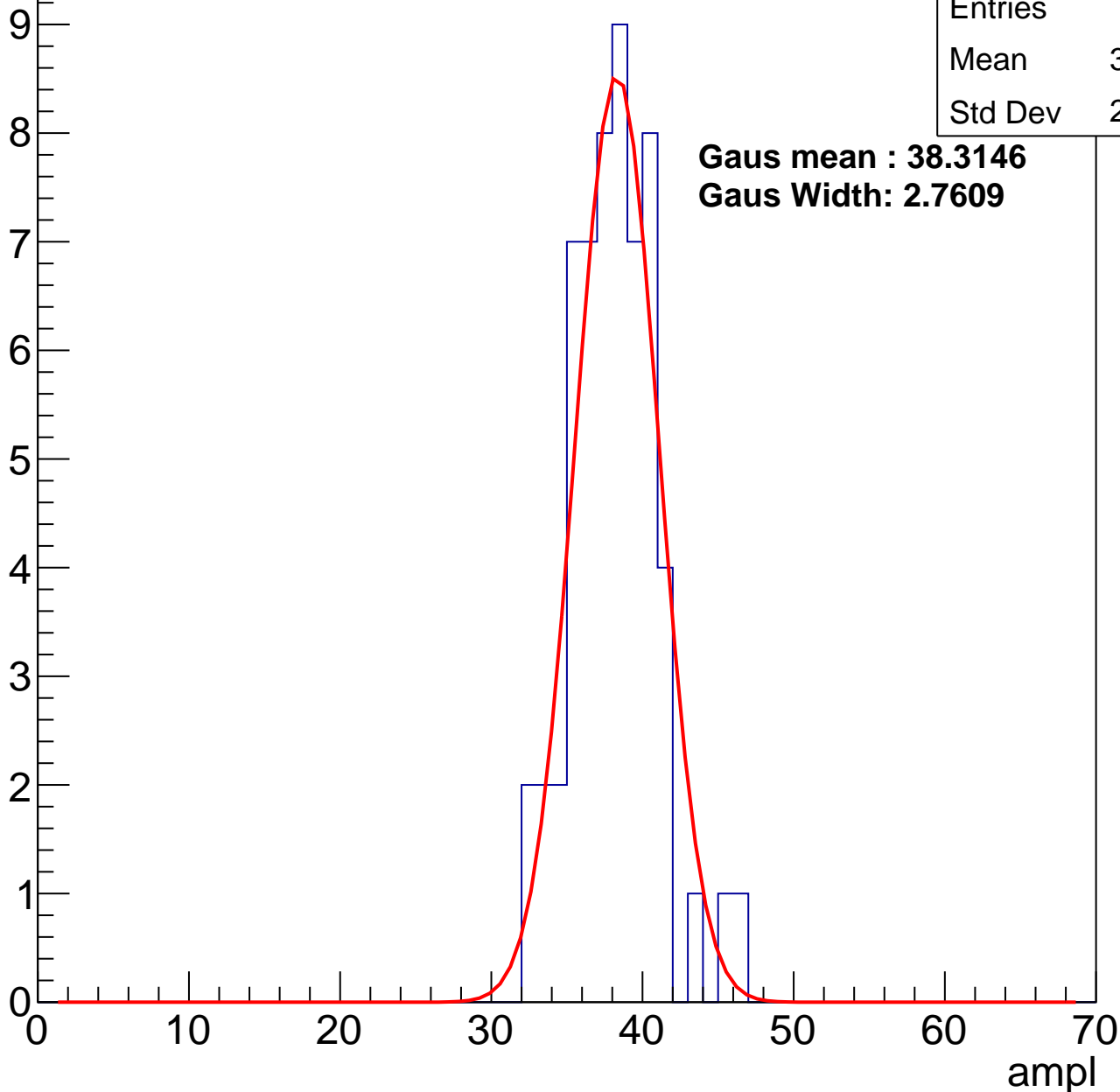
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	37.69
Std Dev	2.794

Gaus mean : 38.3146

Gaus Width: 2.7609



B1L002S, U7-ch35, adc2

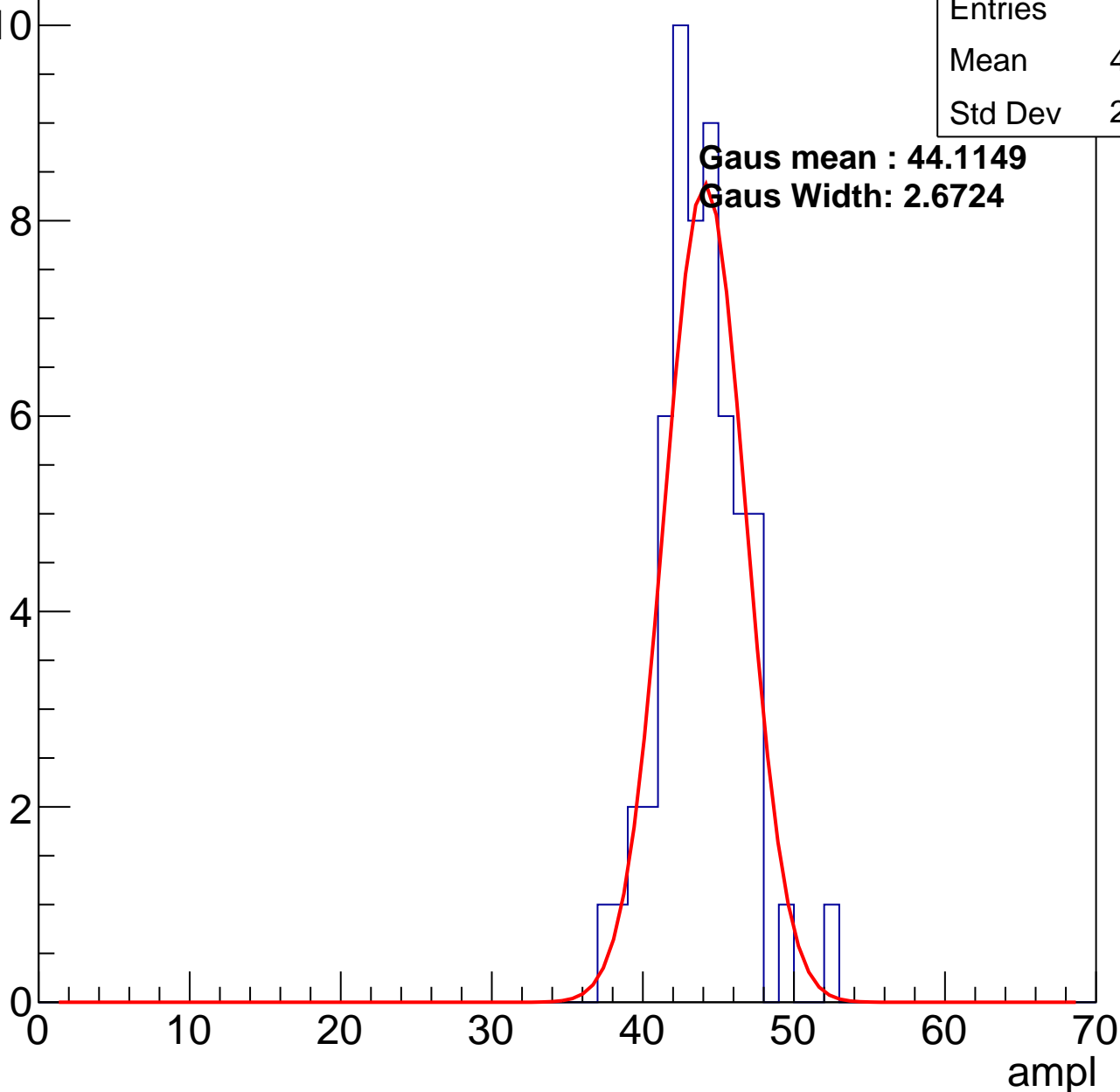
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	43.42
Std Dev	2.682

Gaus mean : 44.1149

Gaus Width: 2.6724

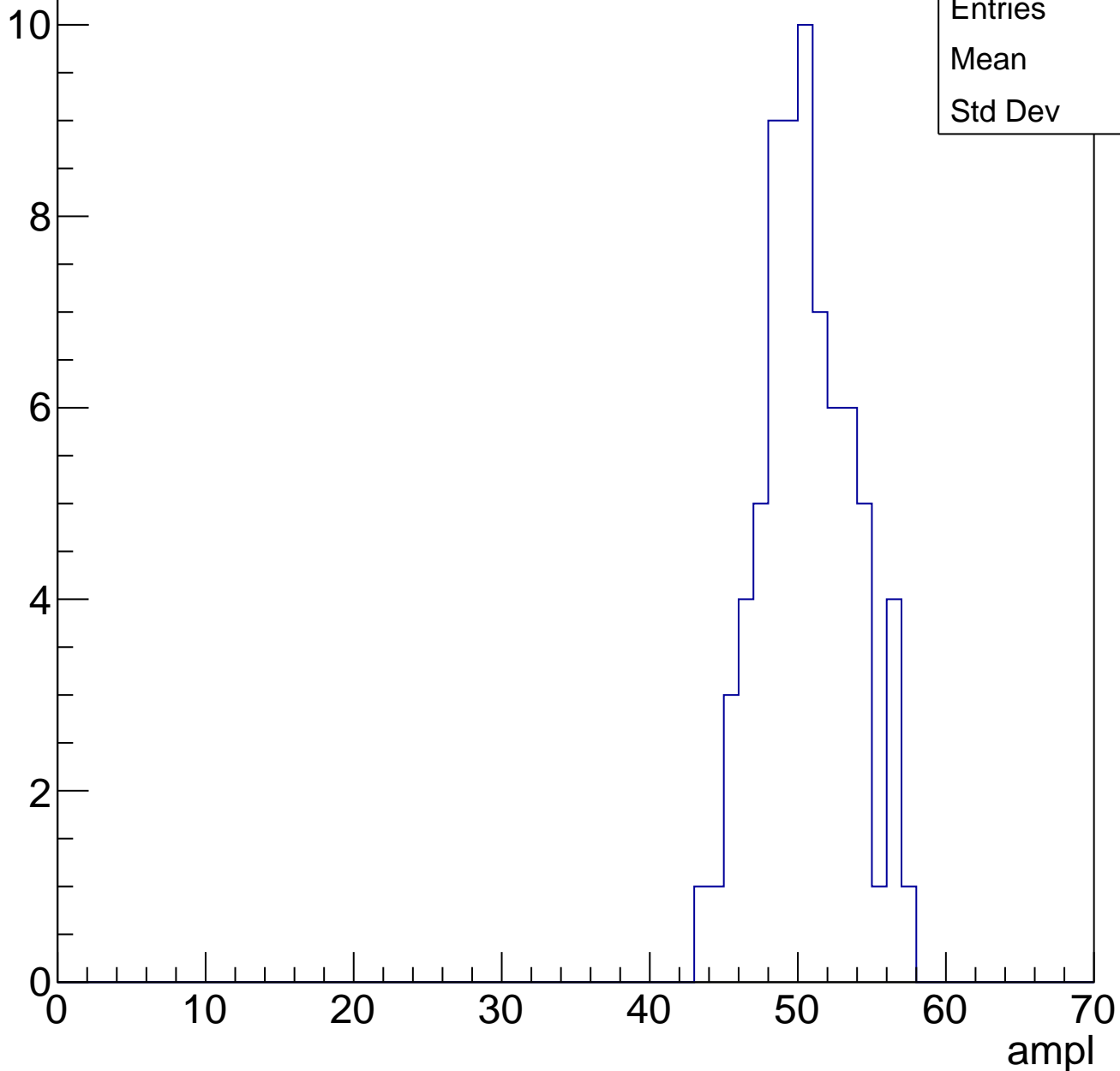


B1L002S, U7-ch35, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	72
Mean	50.1
Std Dev	3.11

Entry

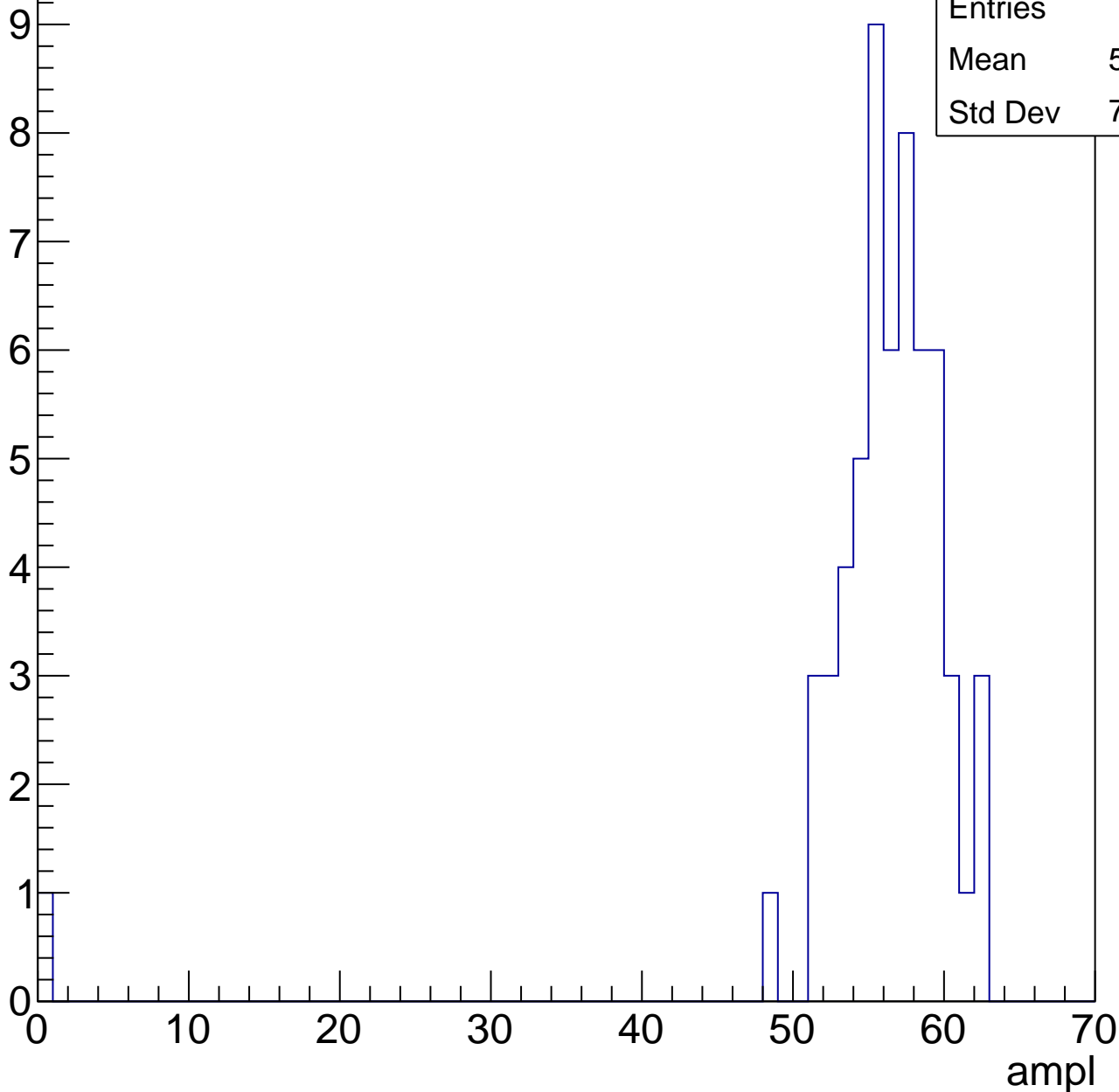


B1L002S, U7-ch35, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	55.17
Std Dev	7.827



B1L002S, U7-ch35, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries	31
Mean	60.42
Std Dev	1.642

ampl

10

20

30

40

50

60

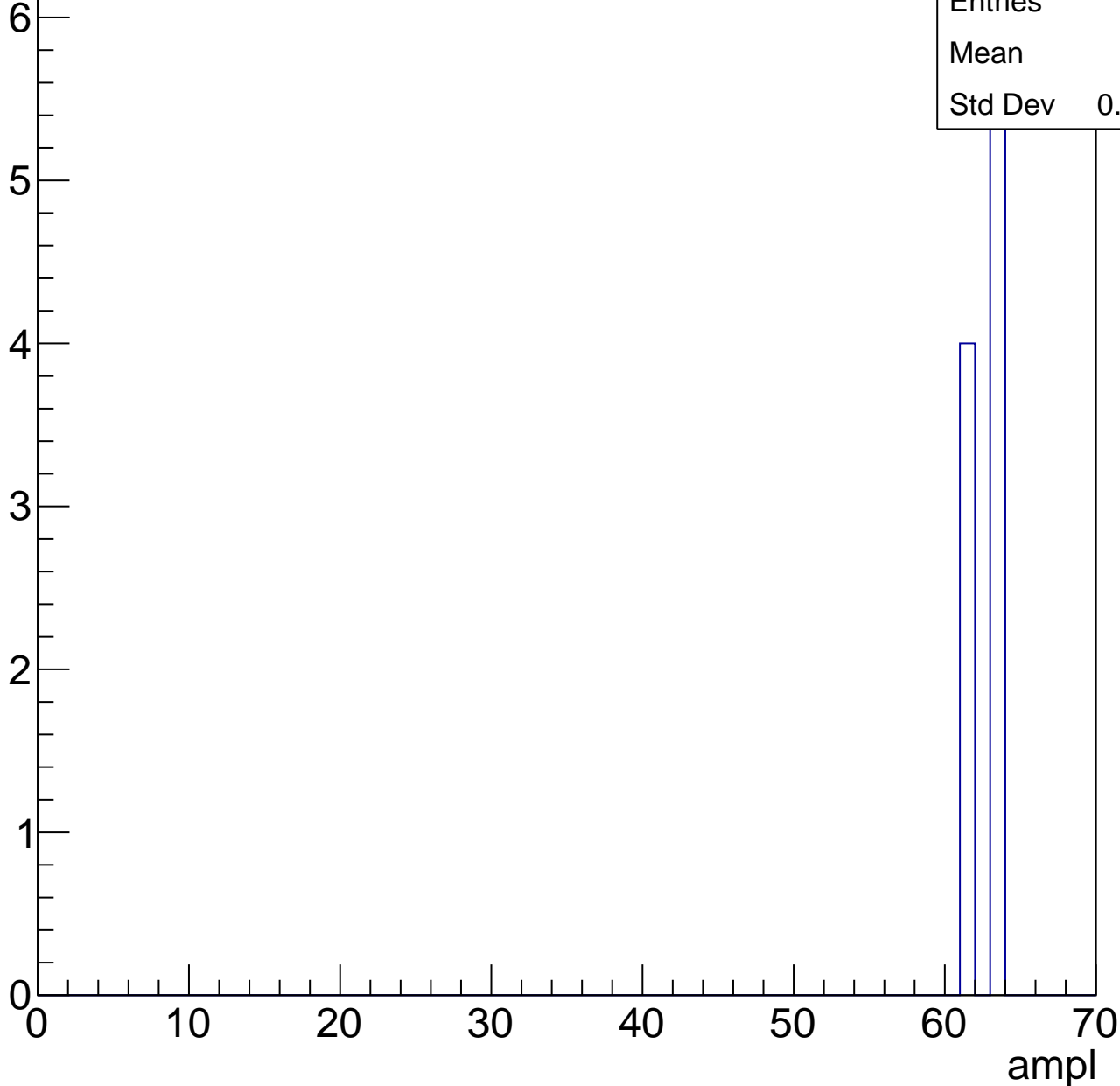
70

B1L002S, U7-ch35, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B1L002S, U7-ch35, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L002S, U7-ch36, adc0

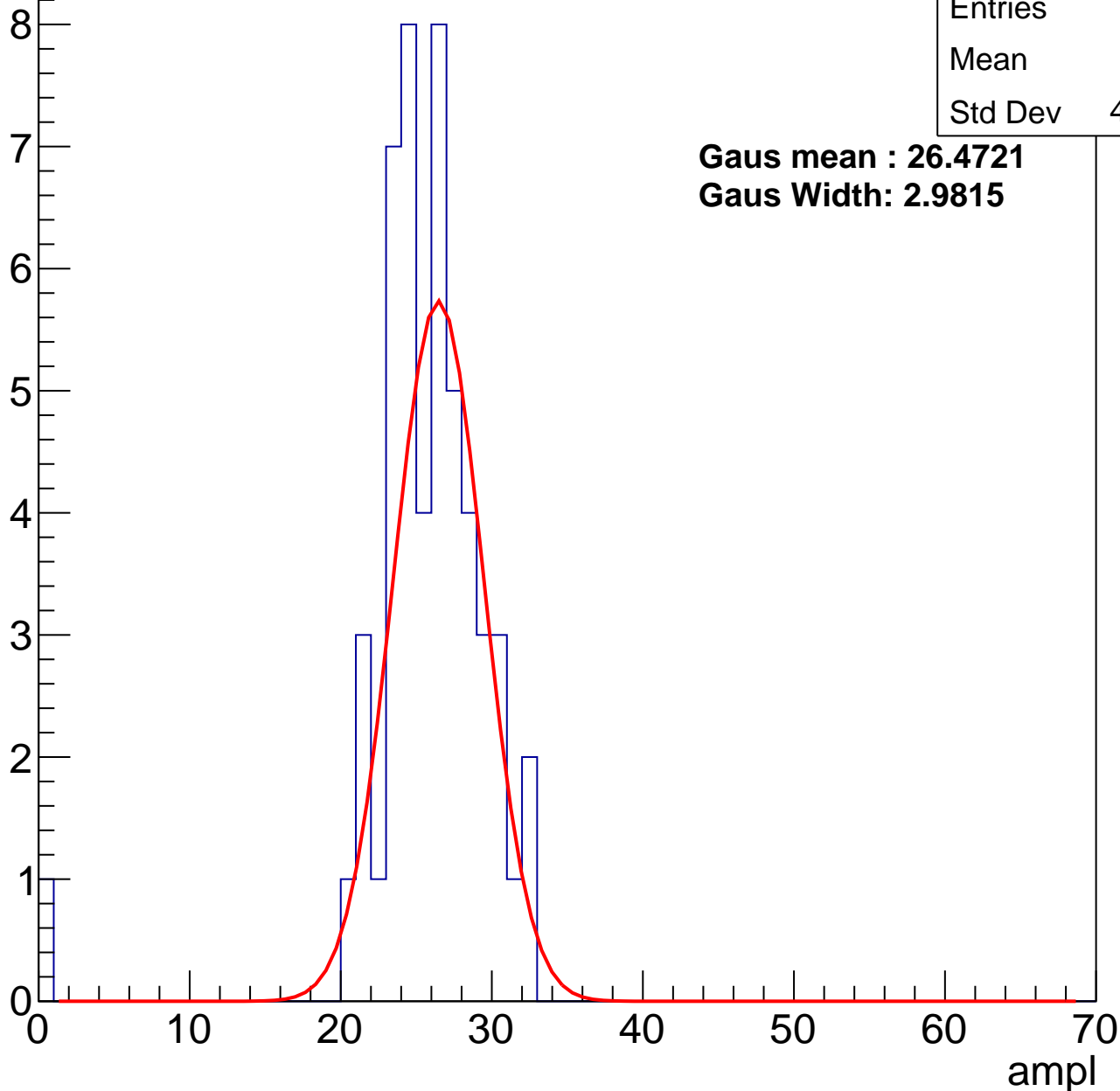
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	25.2
Std Dev	4.563

Gaus mean : 26.4721

Gaus Width: 2.9815



B1L002S, U7-ch36, adc1

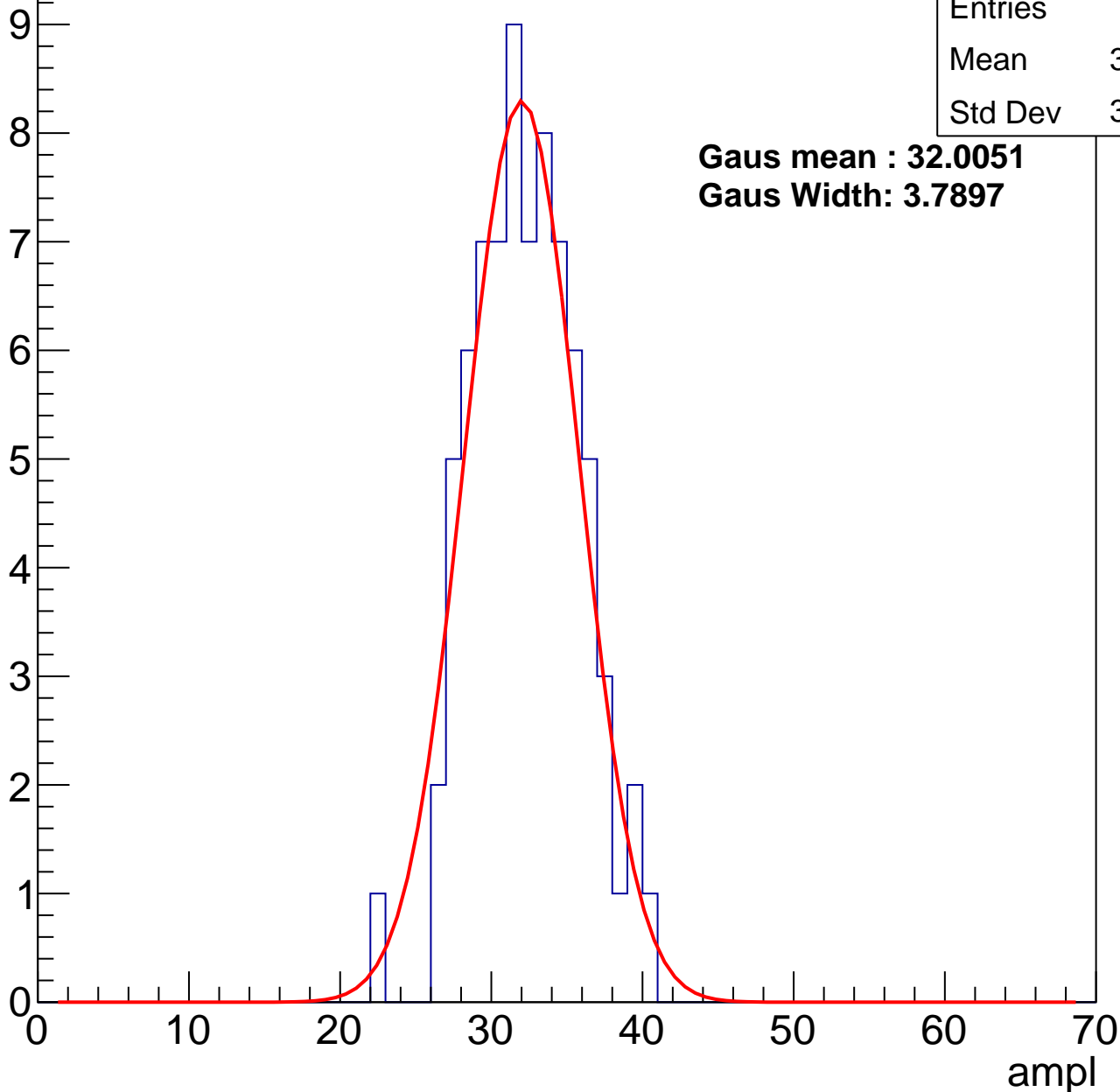
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	31.84
Std Dev	3.483

Gaus mean : 32.0051

Gaus Width: 3.7897



B1L002S, U7-ch36, adc2

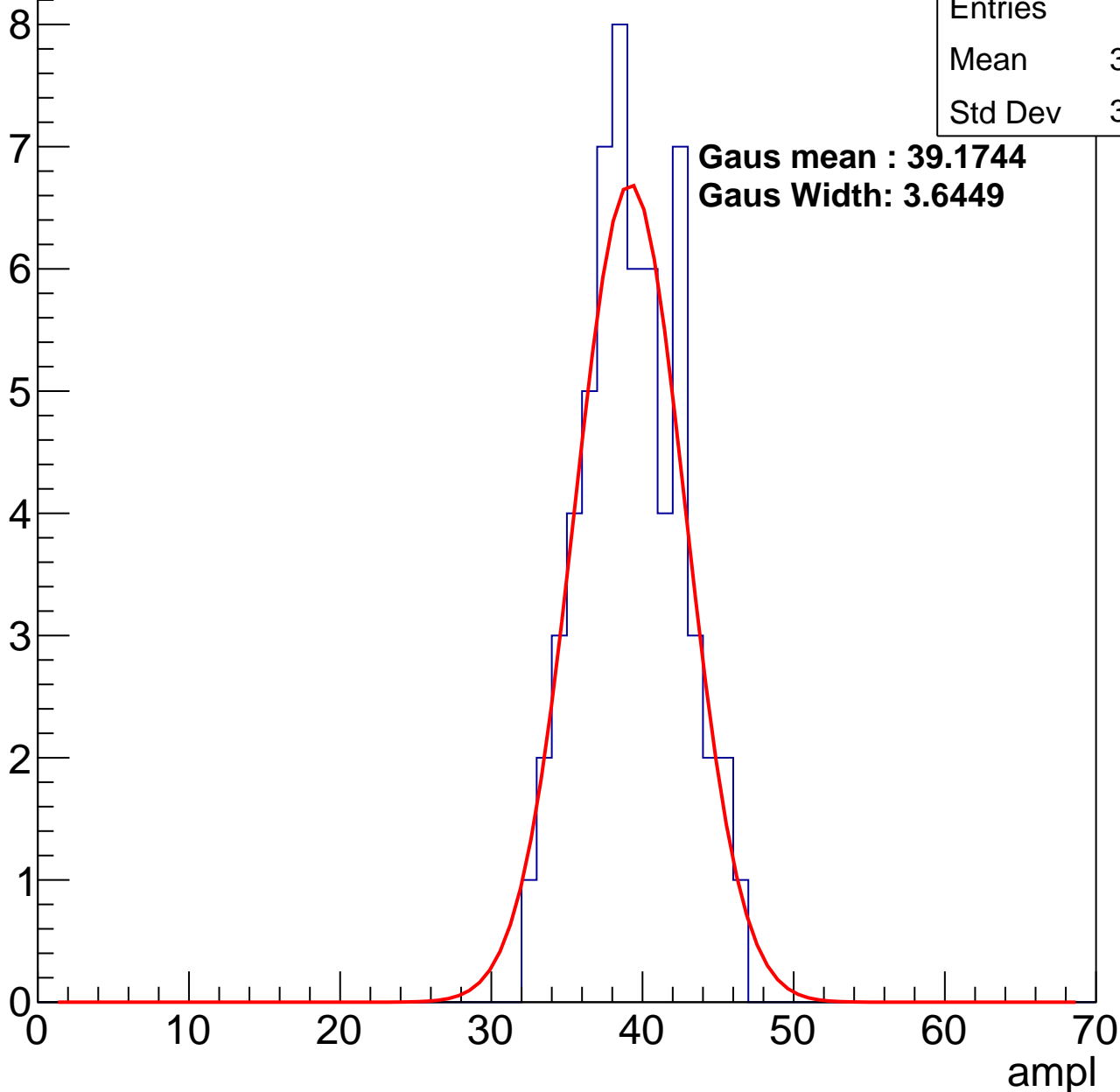
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	38.82
Std Dev	3.247

Gaus mean : 39.1744

Gaus Width: 3.6449

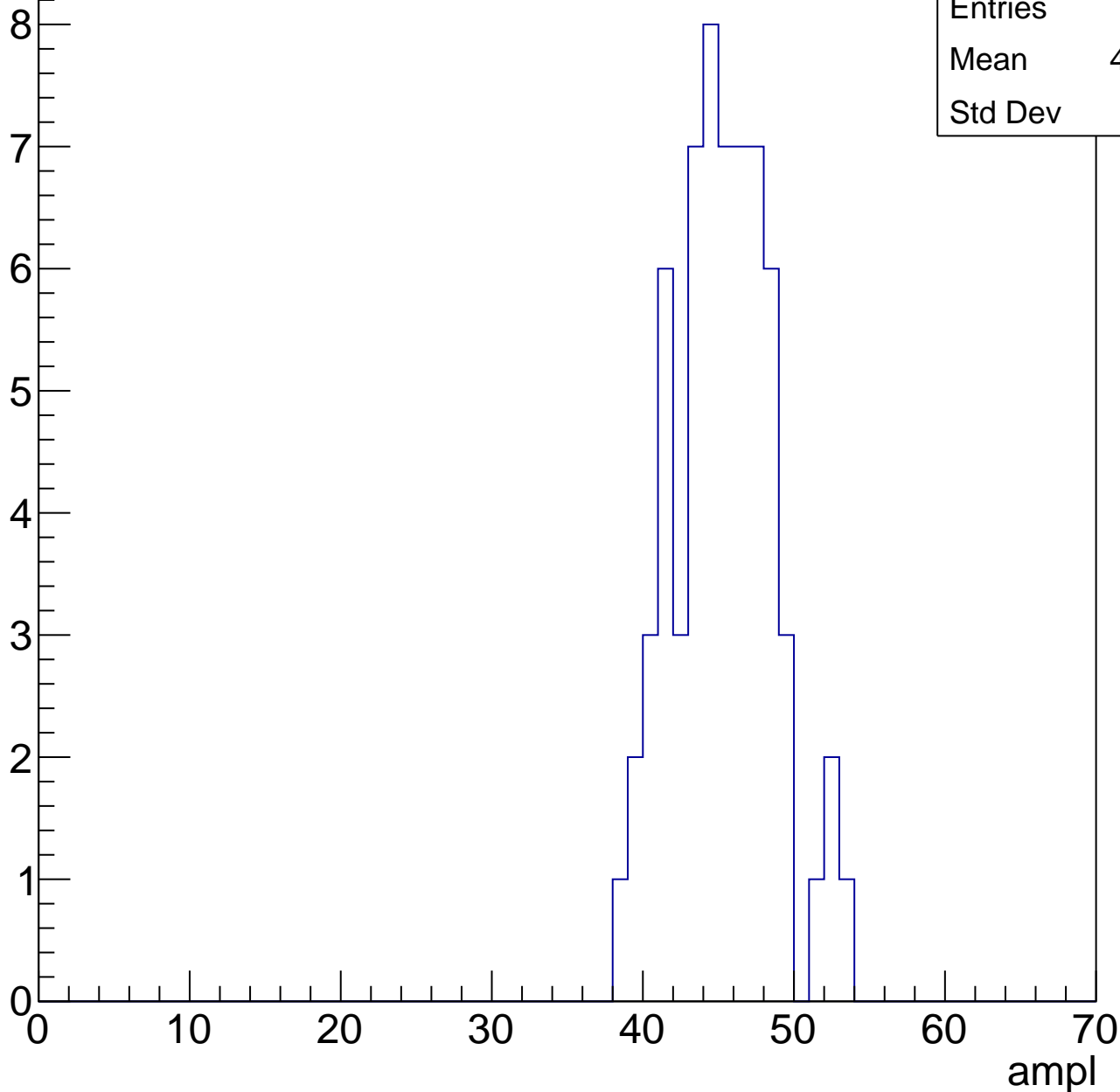


B1L002S, U7-ch36, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	44.84
Std Dev	3.28

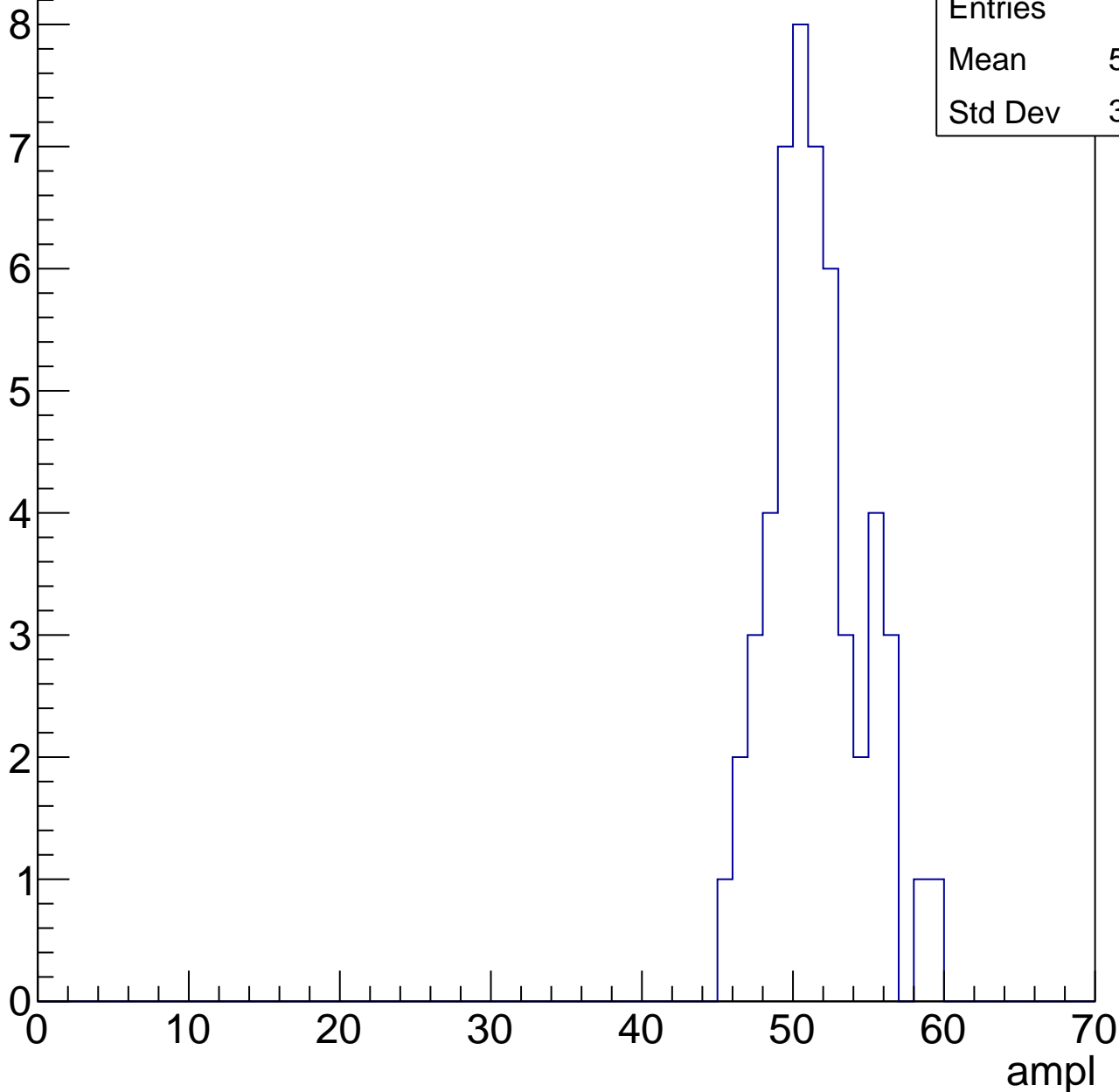


B1L002S, U7-ch36, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.04
Std Dev	3.082



B1L002S, U7-ch36, adc5

calib_packv5_042523_0143.root, FC#10, port B3

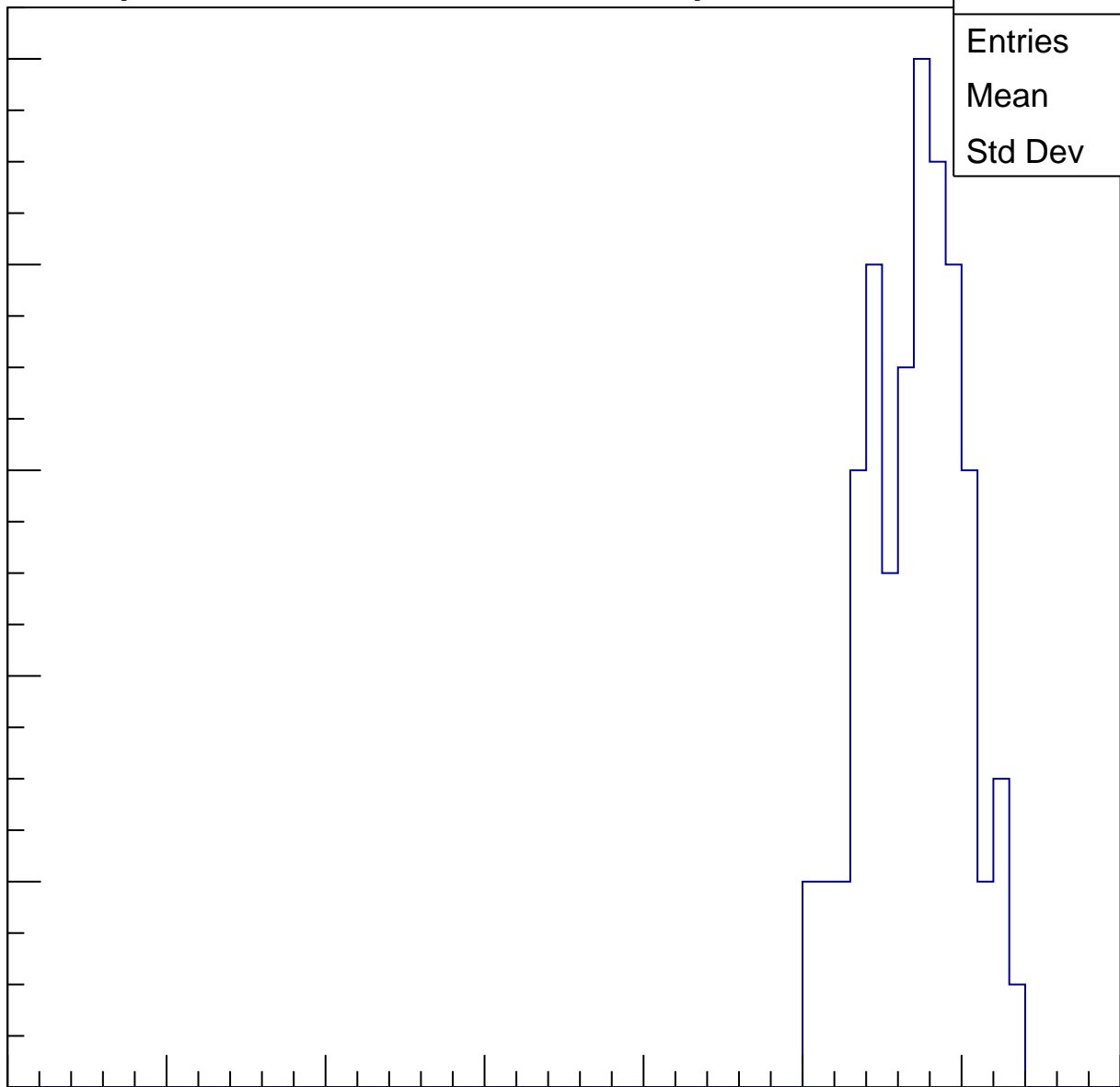
Entries	71
Mean	56.59
Std Dev	3.005

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

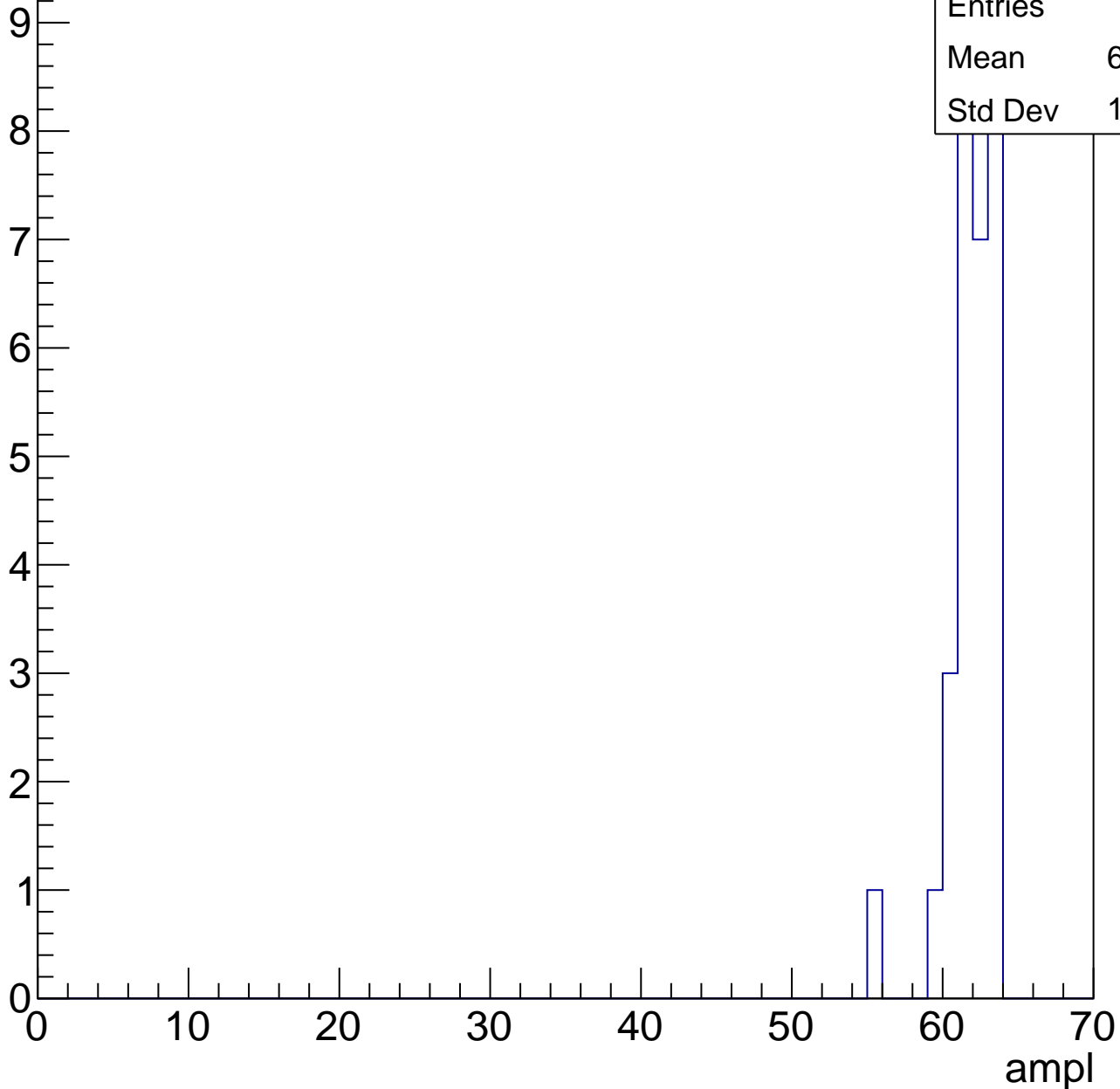
ampl



B1L002S, U7-ch36, adc6

calib_packv5_042523_0143.root, FC#10, port B3

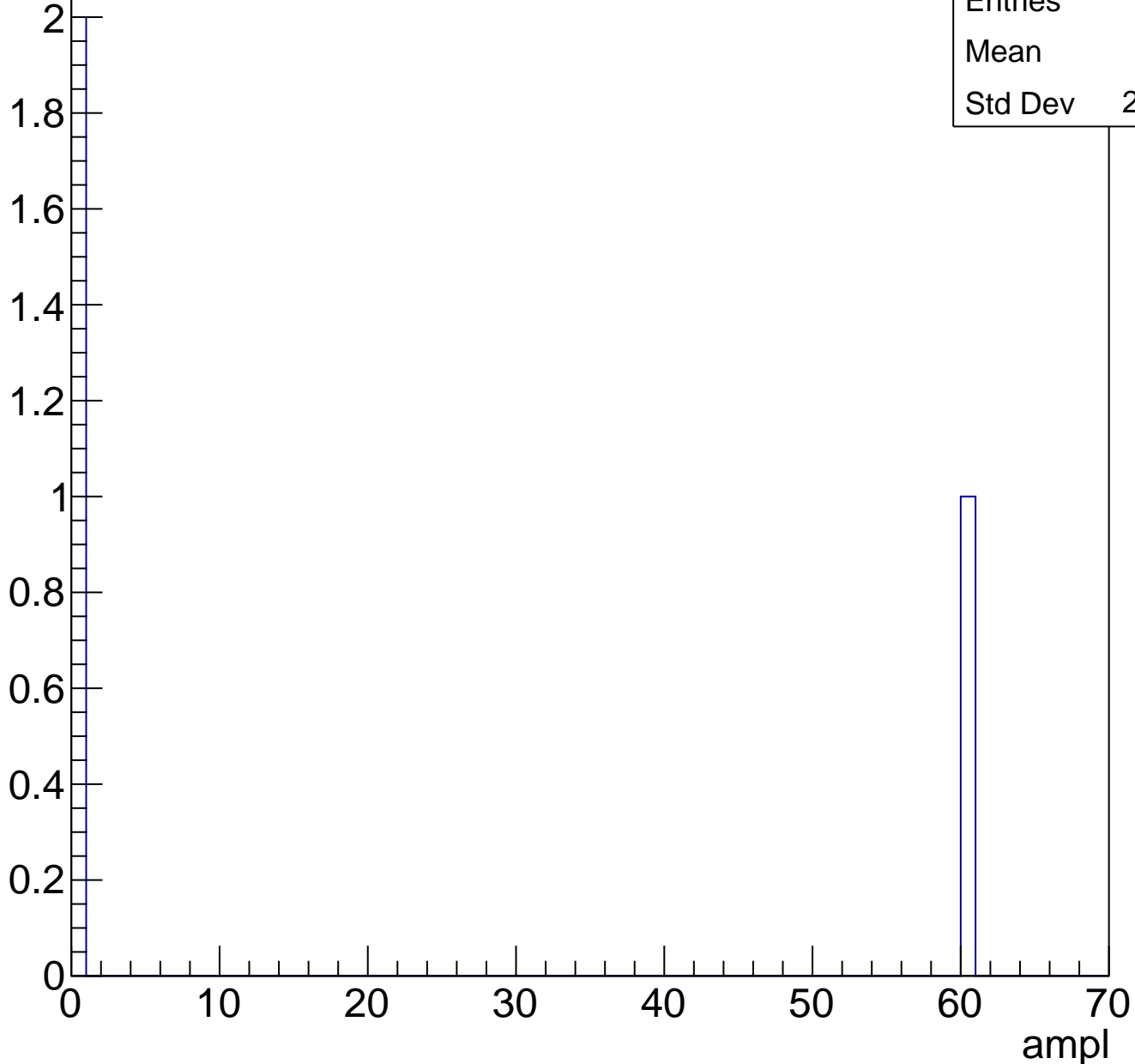
Entry



B1L002S, U7-ch36, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	20
Std Dev	28.28

B1L002S, U7-ch37, adc0

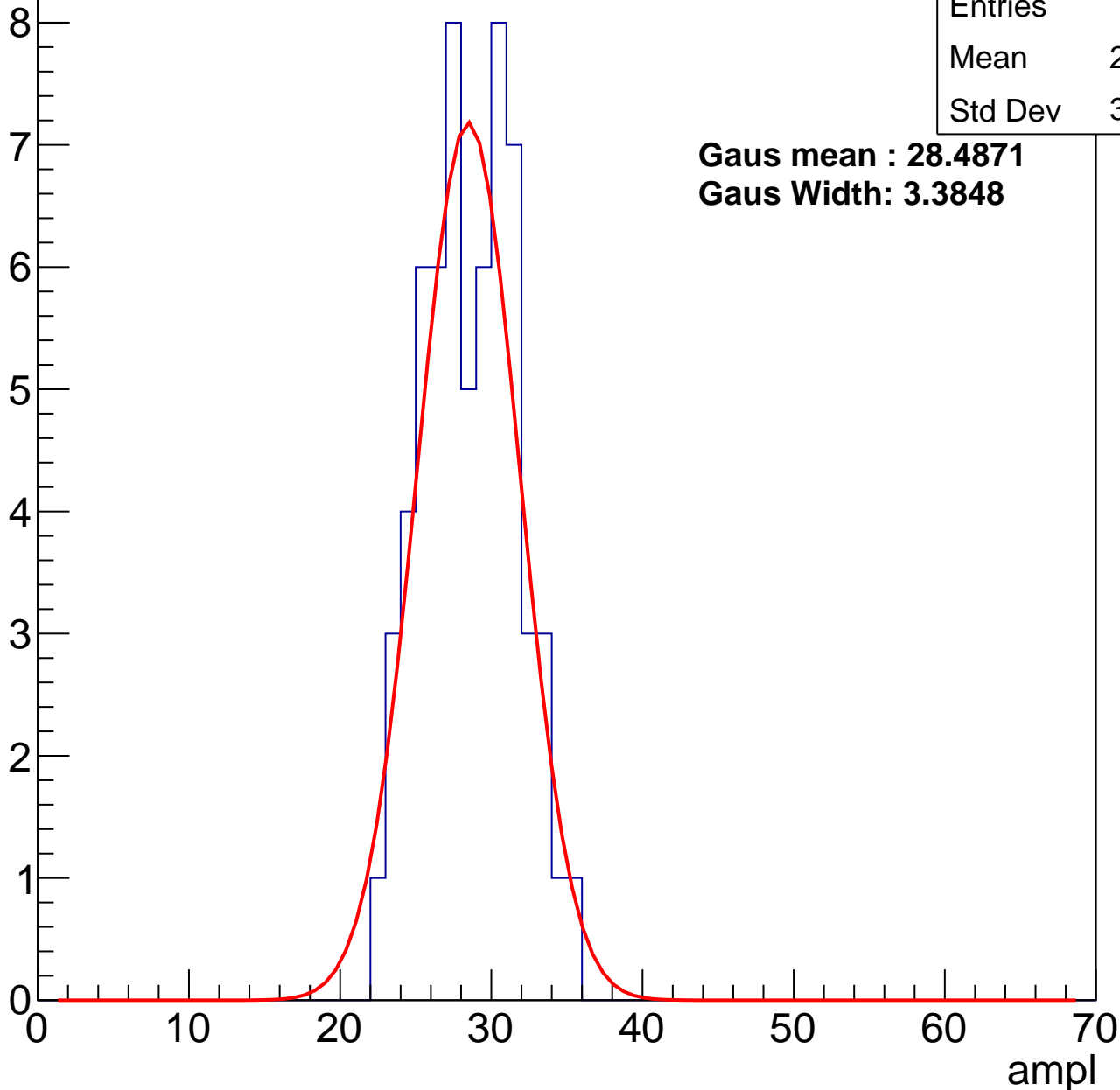
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.13
Std Dev	3.029

Gaus mean : 28.4871

Gaus Width: 3.3848



B1L002S, U7-ch37, adc1

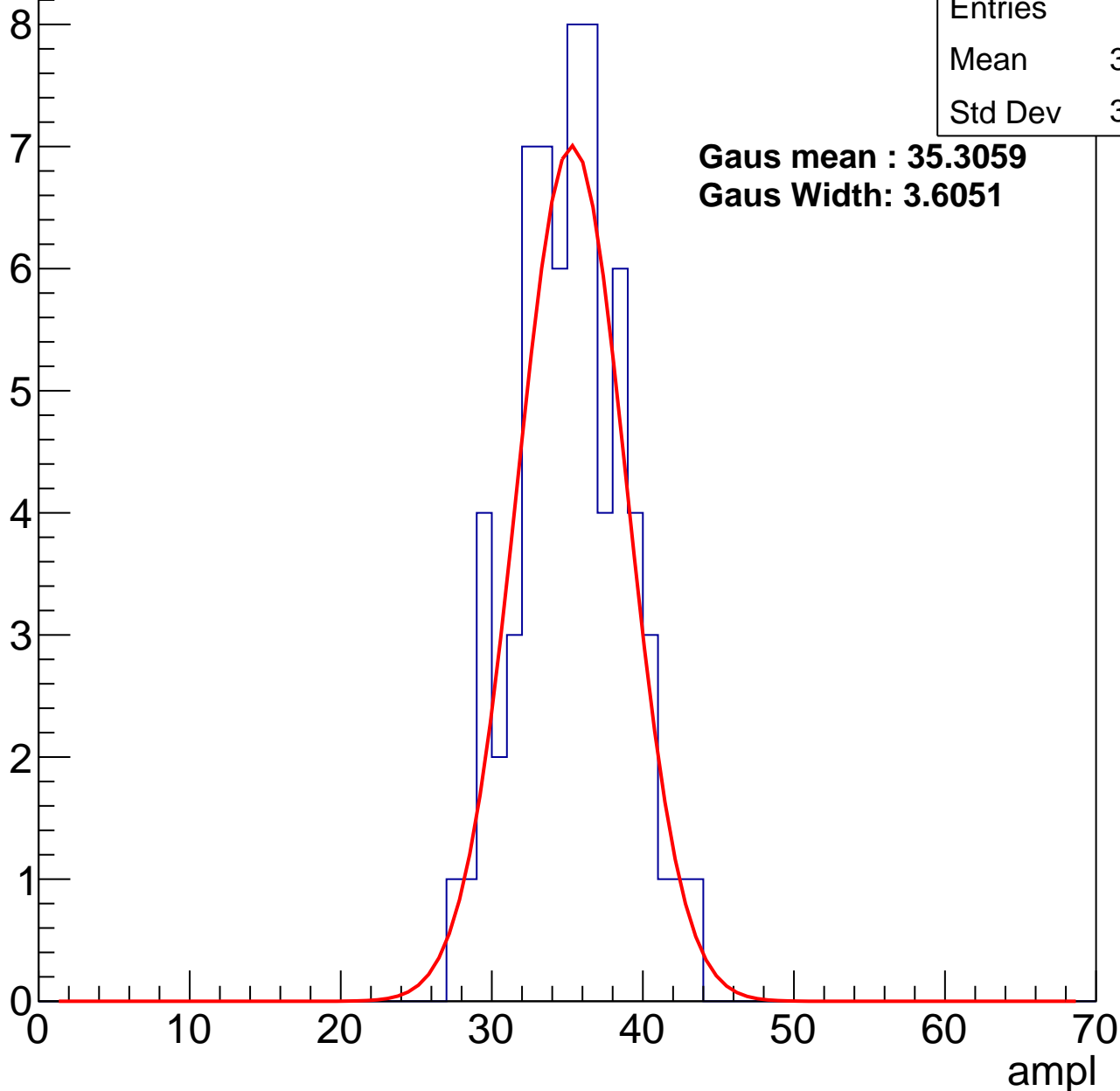
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.76
Std Dev	3.486

Gaus mean : 35.3059

Gaus Width: 3.6051



B1L002S, U7-ch37, adc2

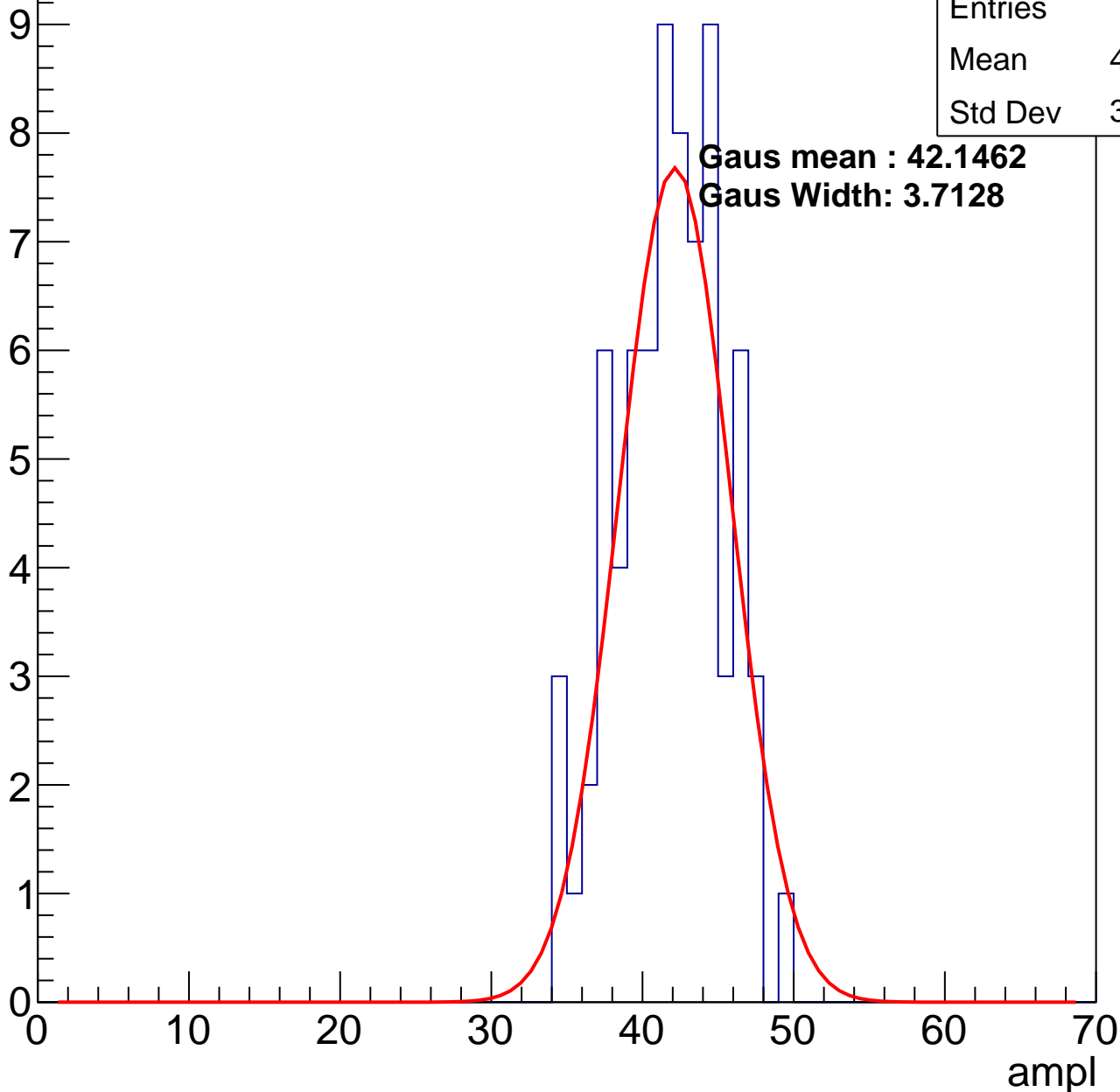
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	41.35
Std Dev	3.434

Gaus mean : 42.1462

Gaus Width: 3.7128

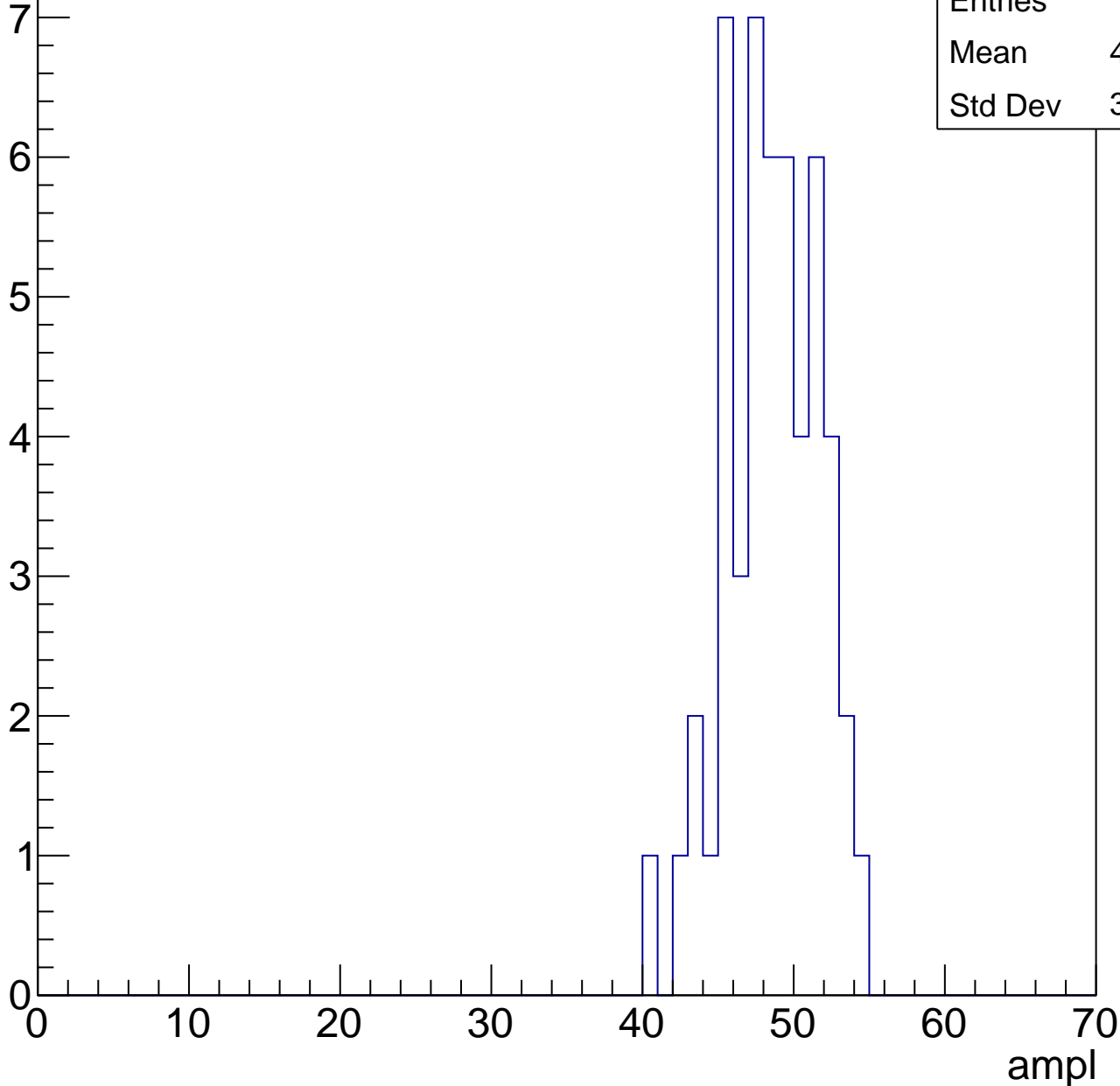


B1L002S, U7-ch37, adc3

calib_packv5_042523_0143.root, FC#10, port B3

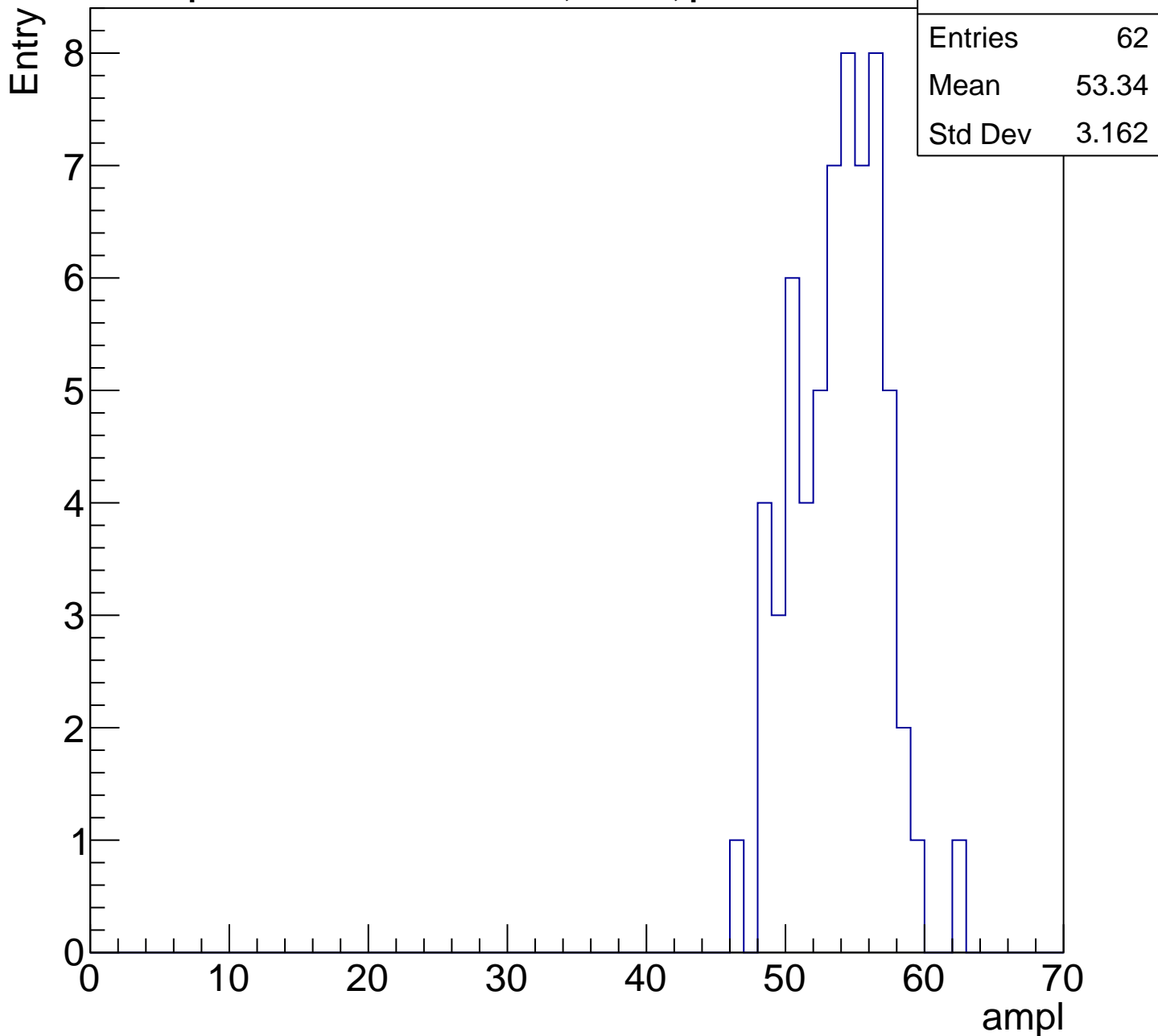
Entry

Entries	51
Mean	48.04
Std Dev	3.048



B1L002S, U7-ch37, adc4

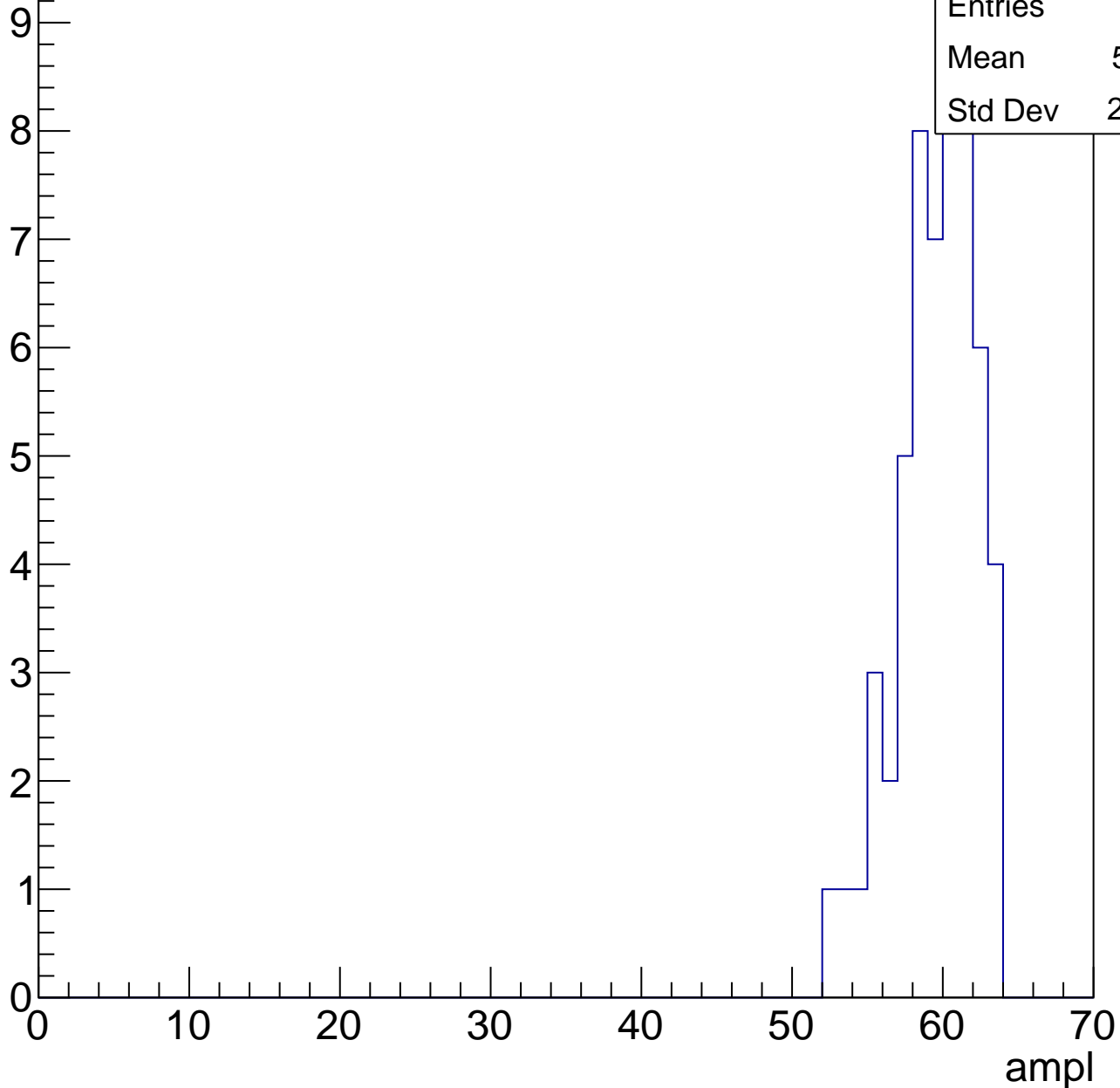
calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch37, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



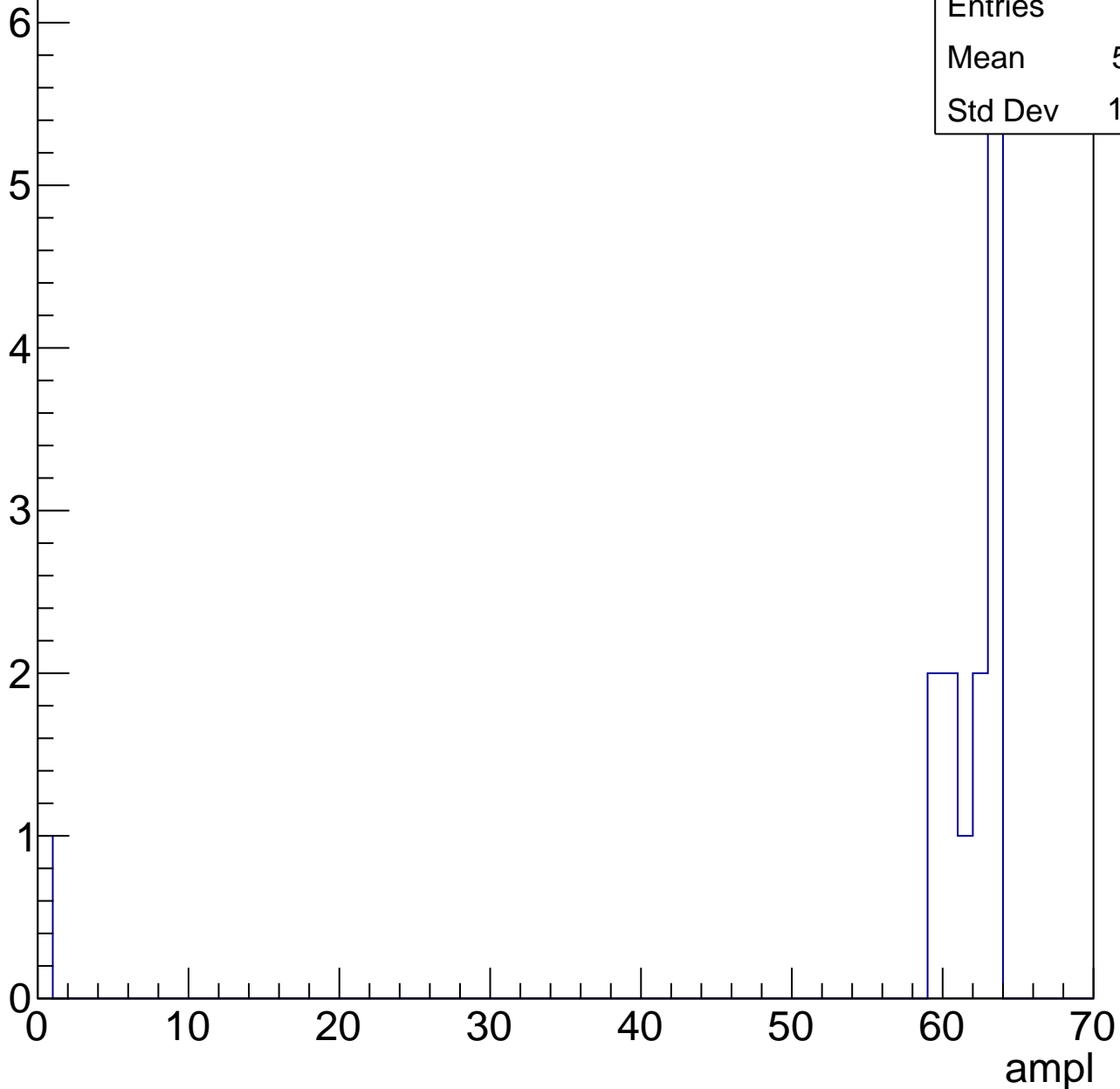
Entries	55
Mean	59.11
Std Dev	2.577

B1L002S, U7-ch37, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

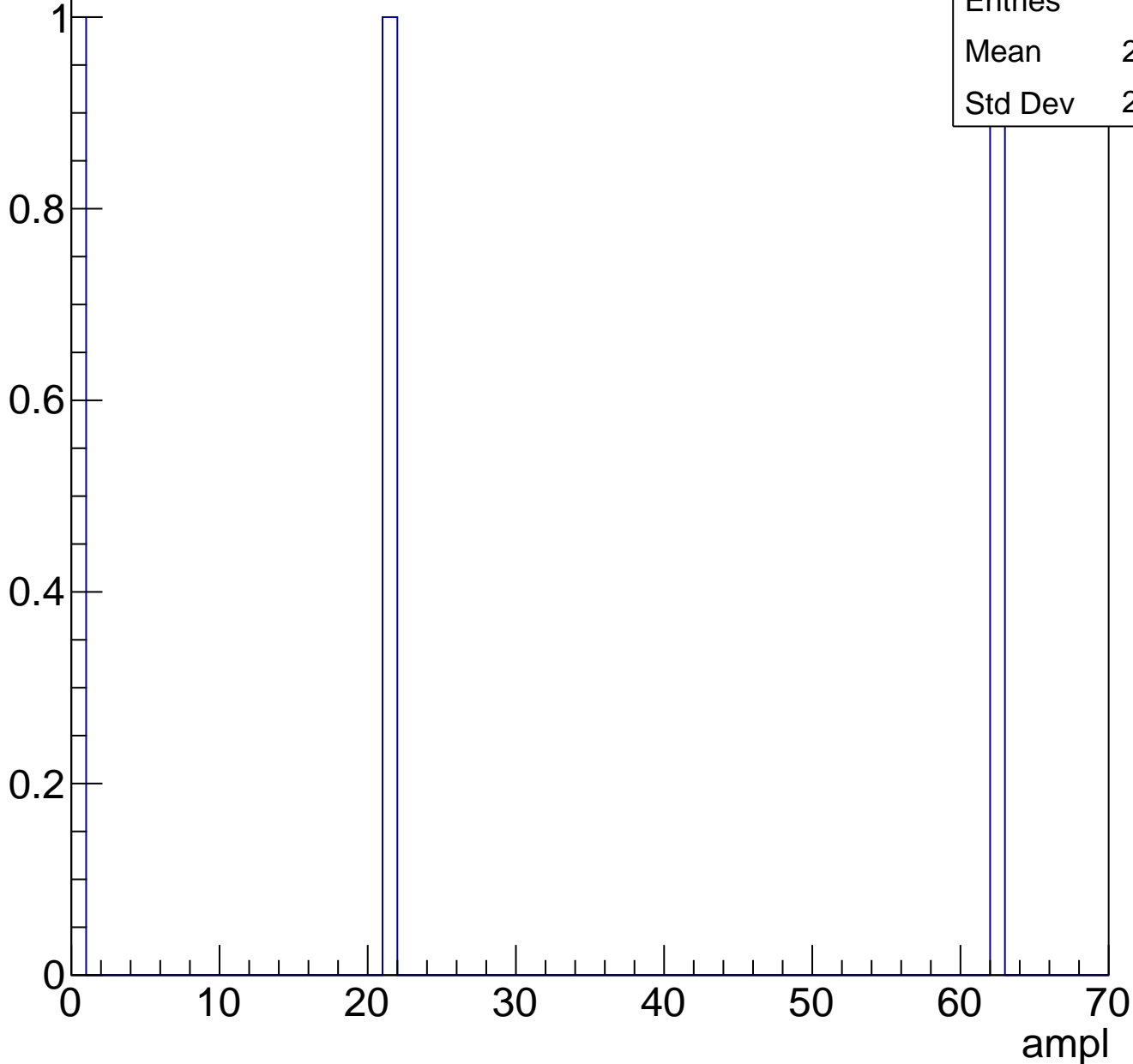
Entries	14
Mean	57.21
Std Dev	15.94



B1L002S, U7-ch37, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B1L002S, U7-ch38, adc0

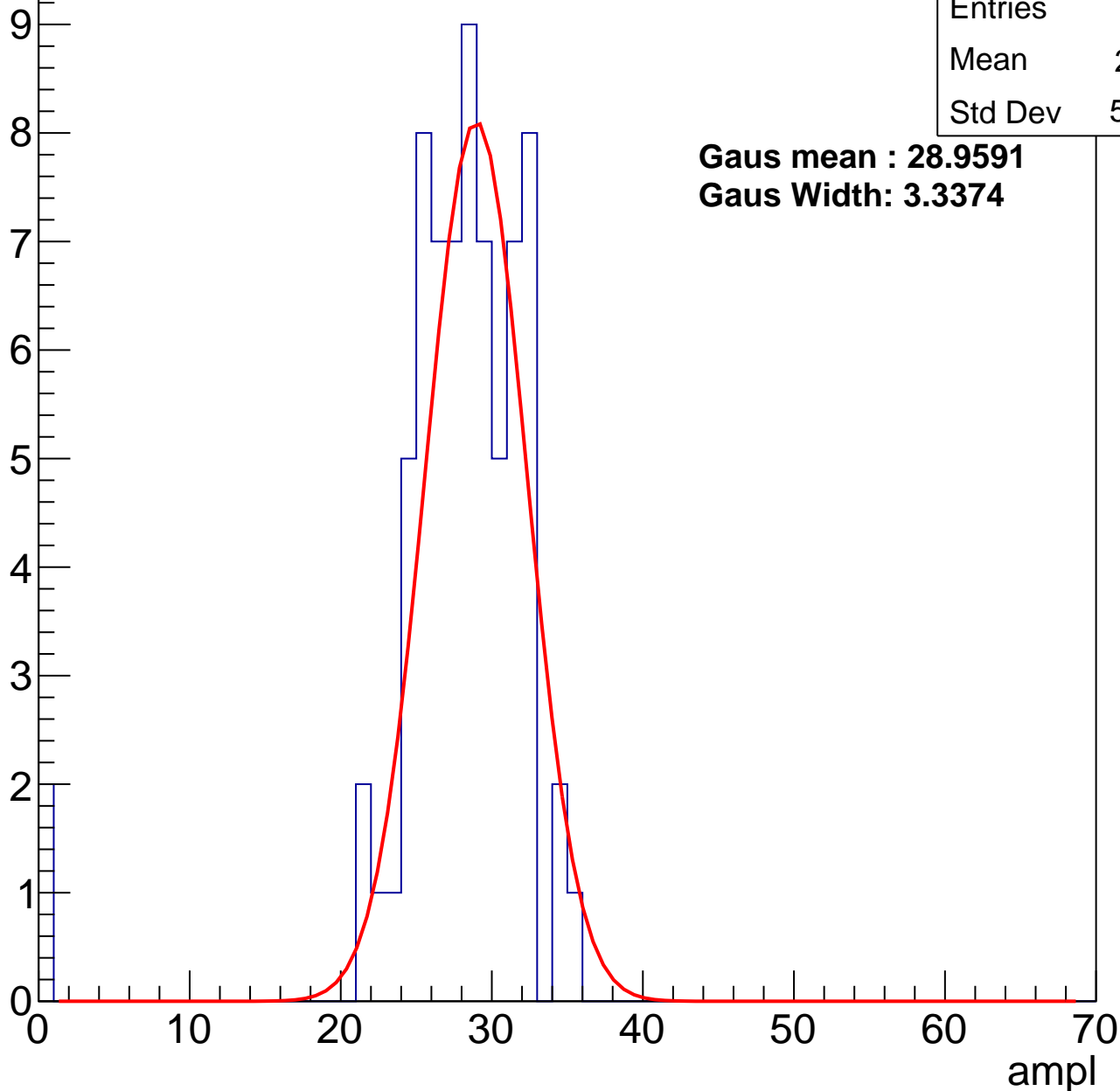
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.21
Std Dev	5.538

Gaus mean : 28.9591

Gaus Width: 3.3374



B1L002S, U7-ch38, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	58
Mean	34.28
Std Dev	2.987

Gaus mean : 35.4102

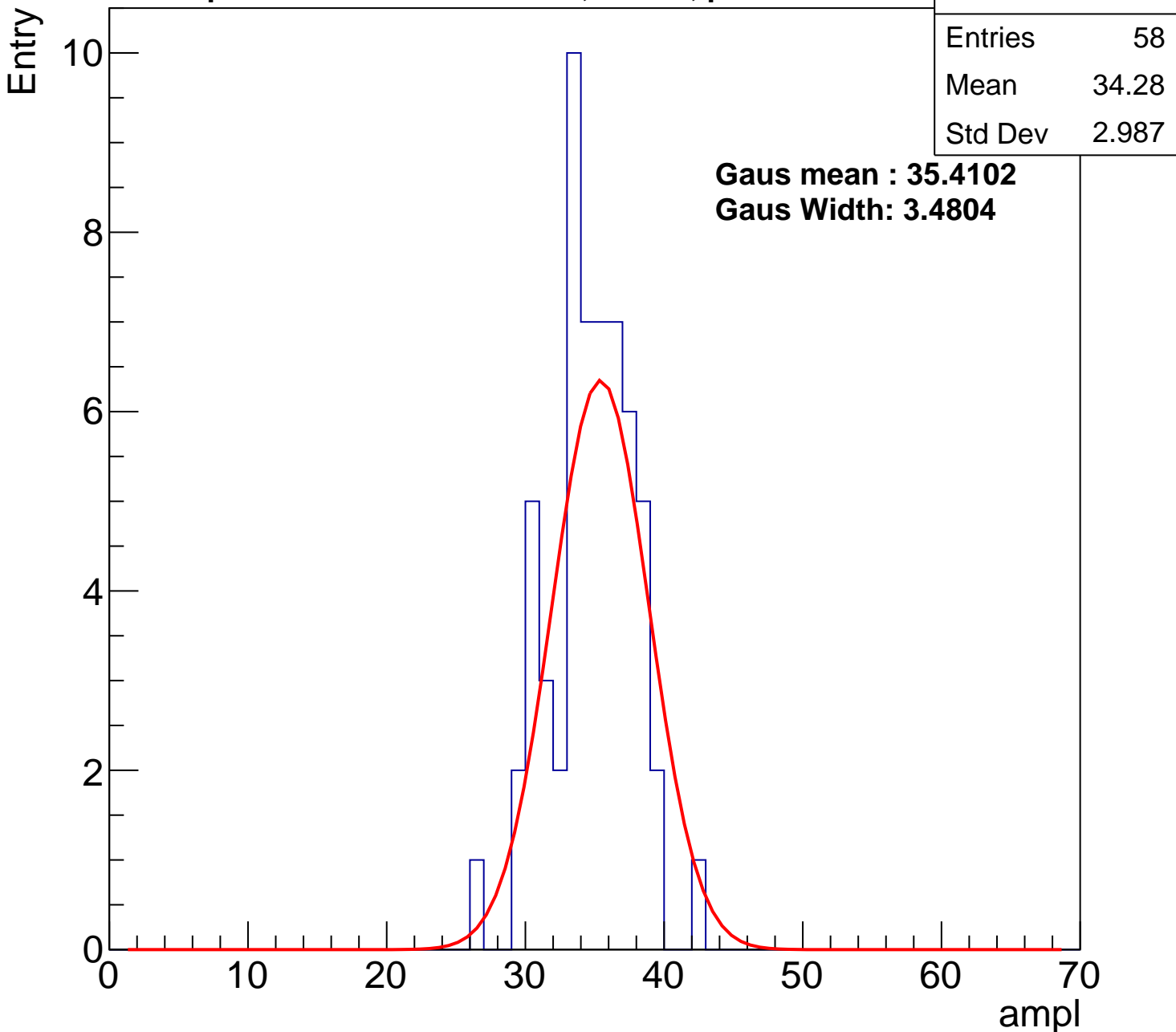
Gaus Width: 3.4804

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch38, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	61
Mean	40.49
Std Dev	3.022

Gaus mean : 40.9143

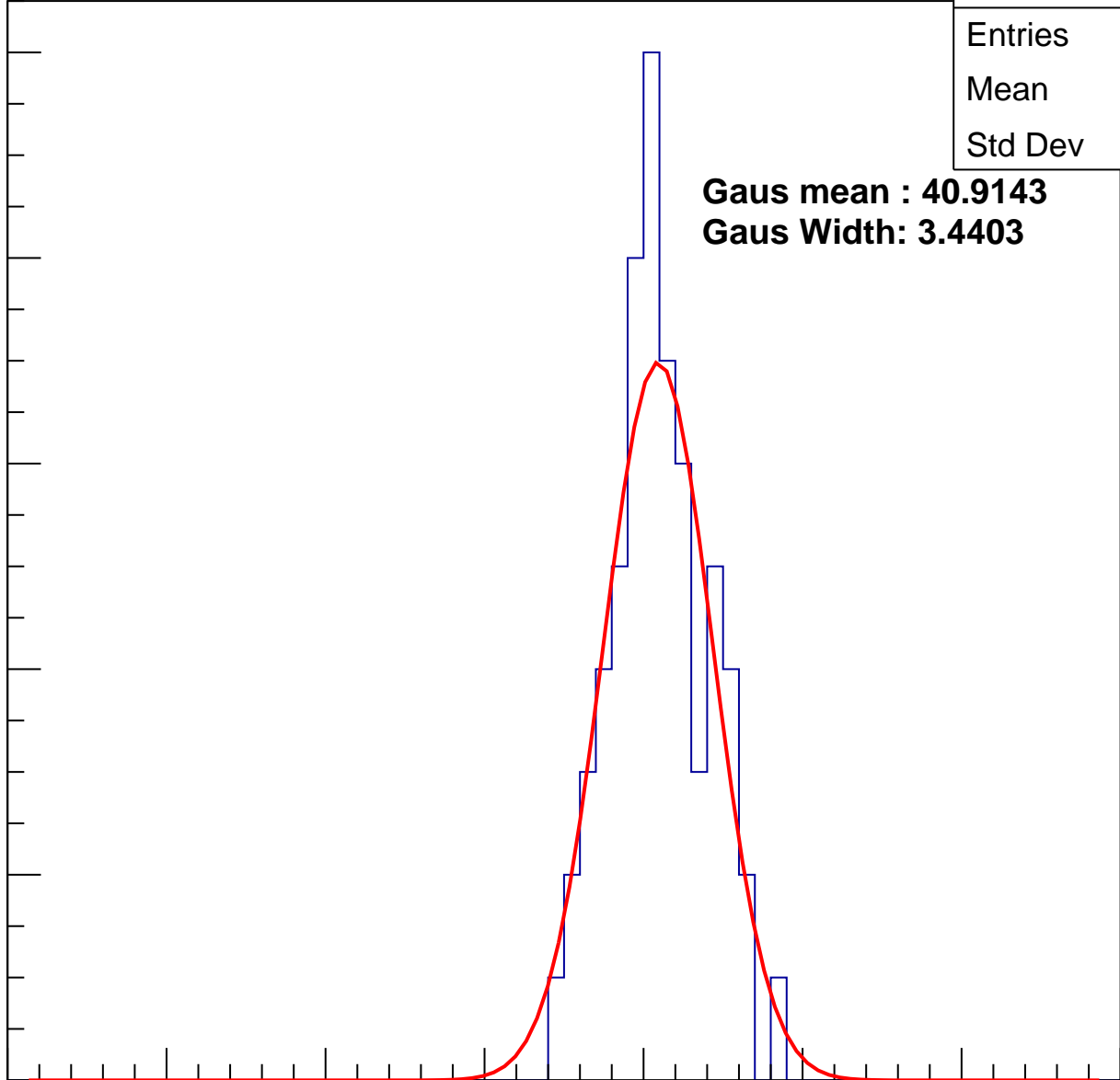
Gaus Width: 3.4403

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

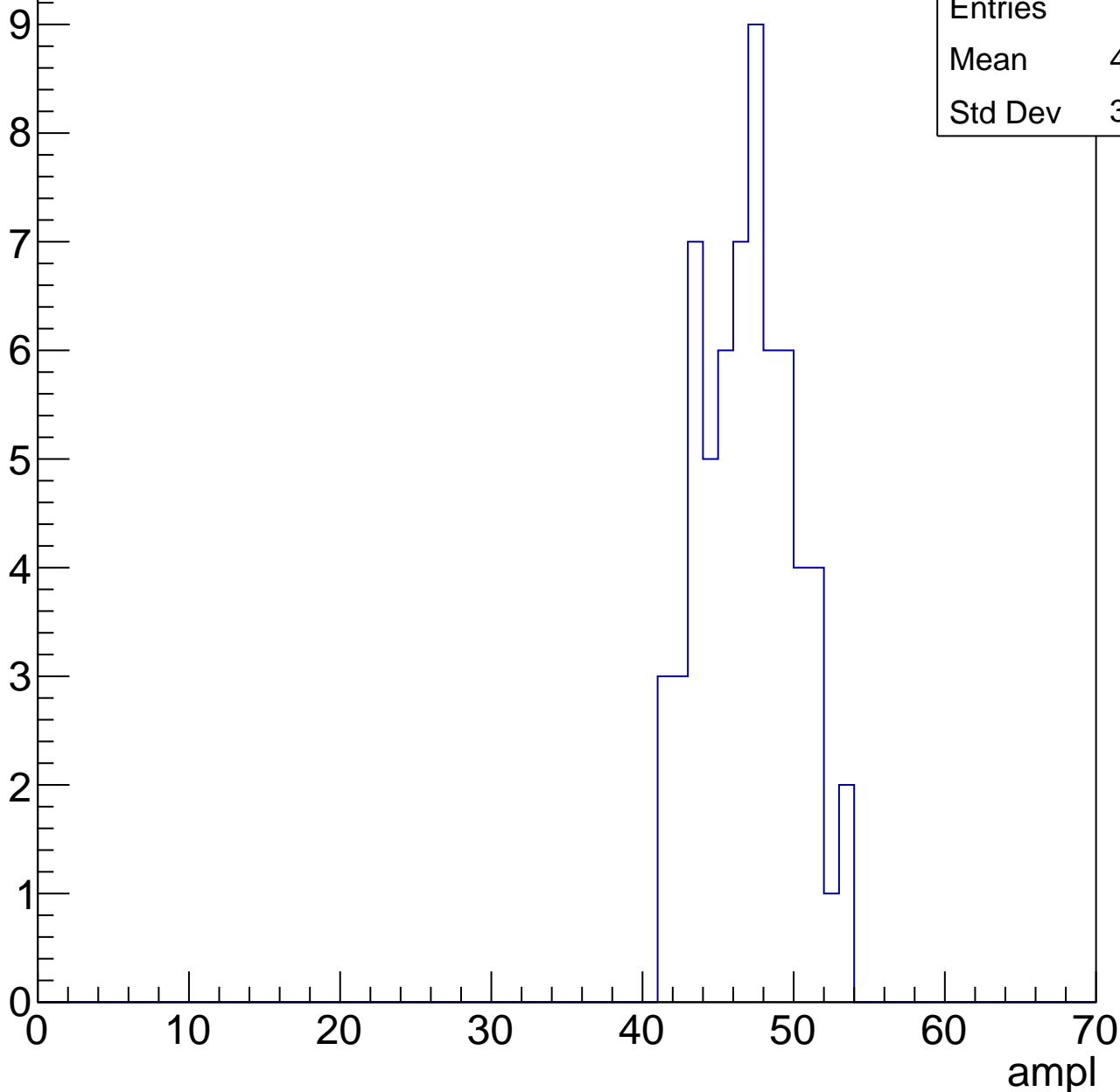


B1L002S, U7-ch38, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

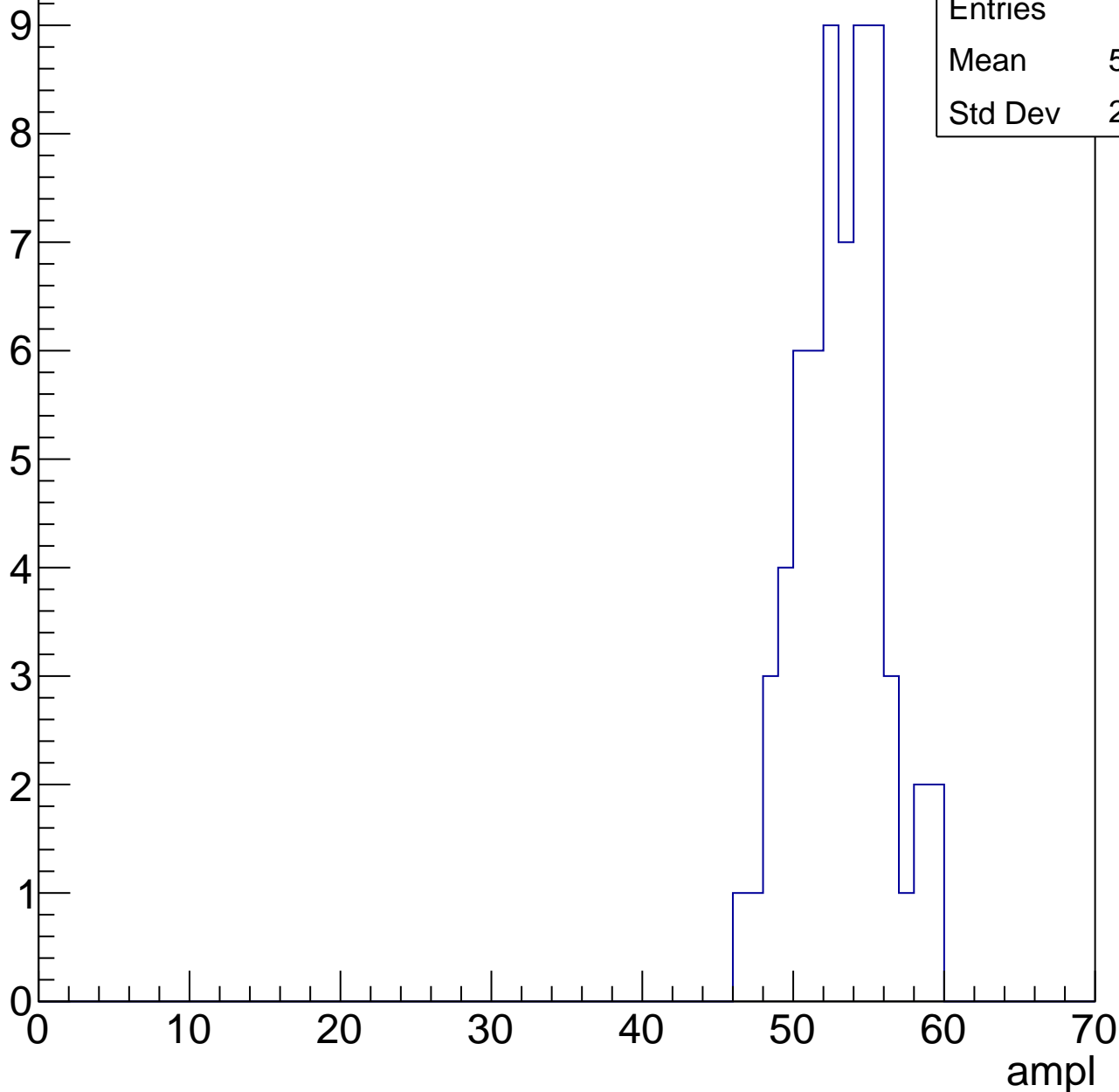
Entries	63
Mean	46.49
Std Dev	3.039



B1L002S, U7-ch38, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	63
Mean	52.67
Std Dev	2.856

B1L002S, U7-ch38, adc5

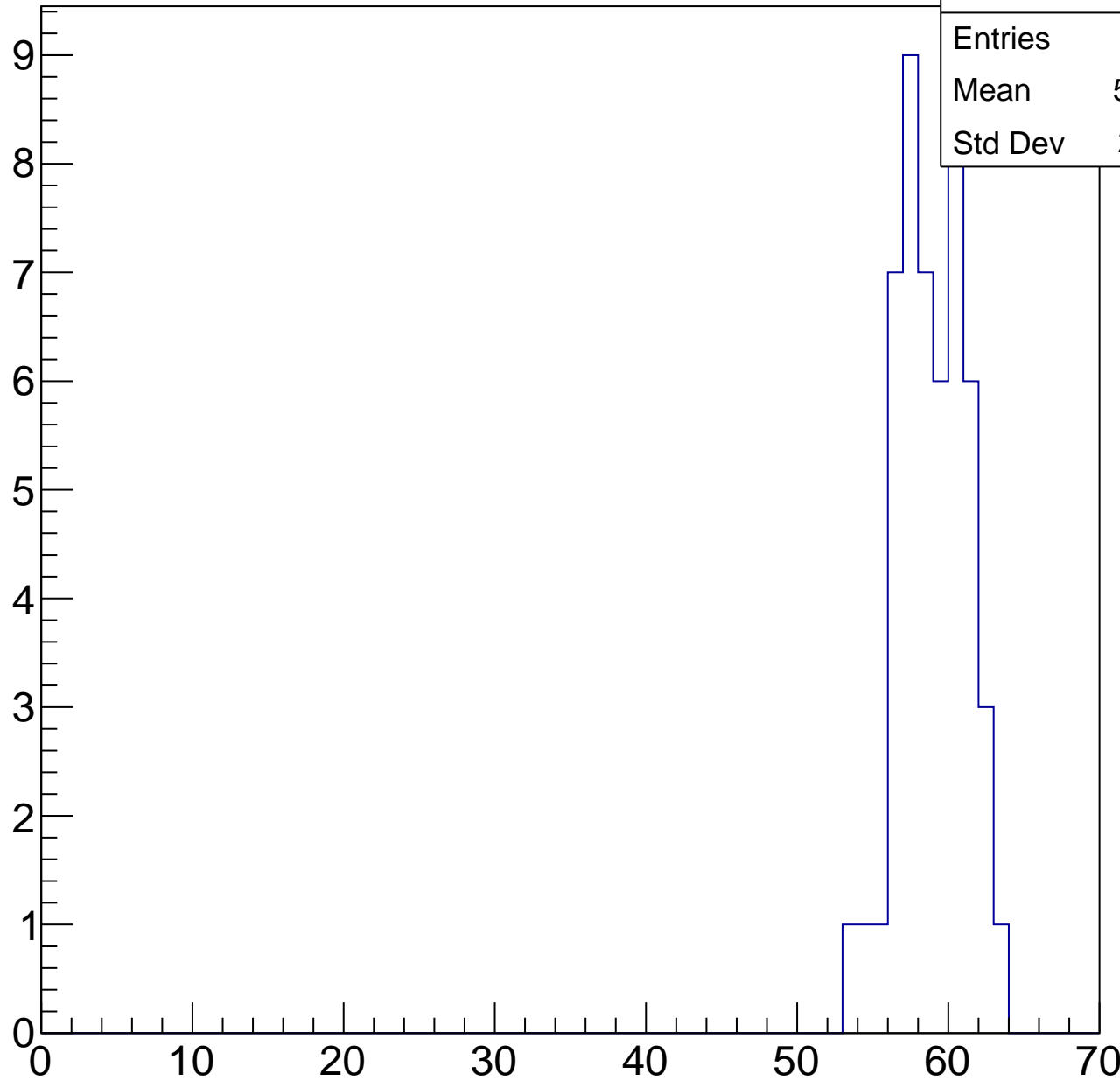
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.44
Std Dev	2.201

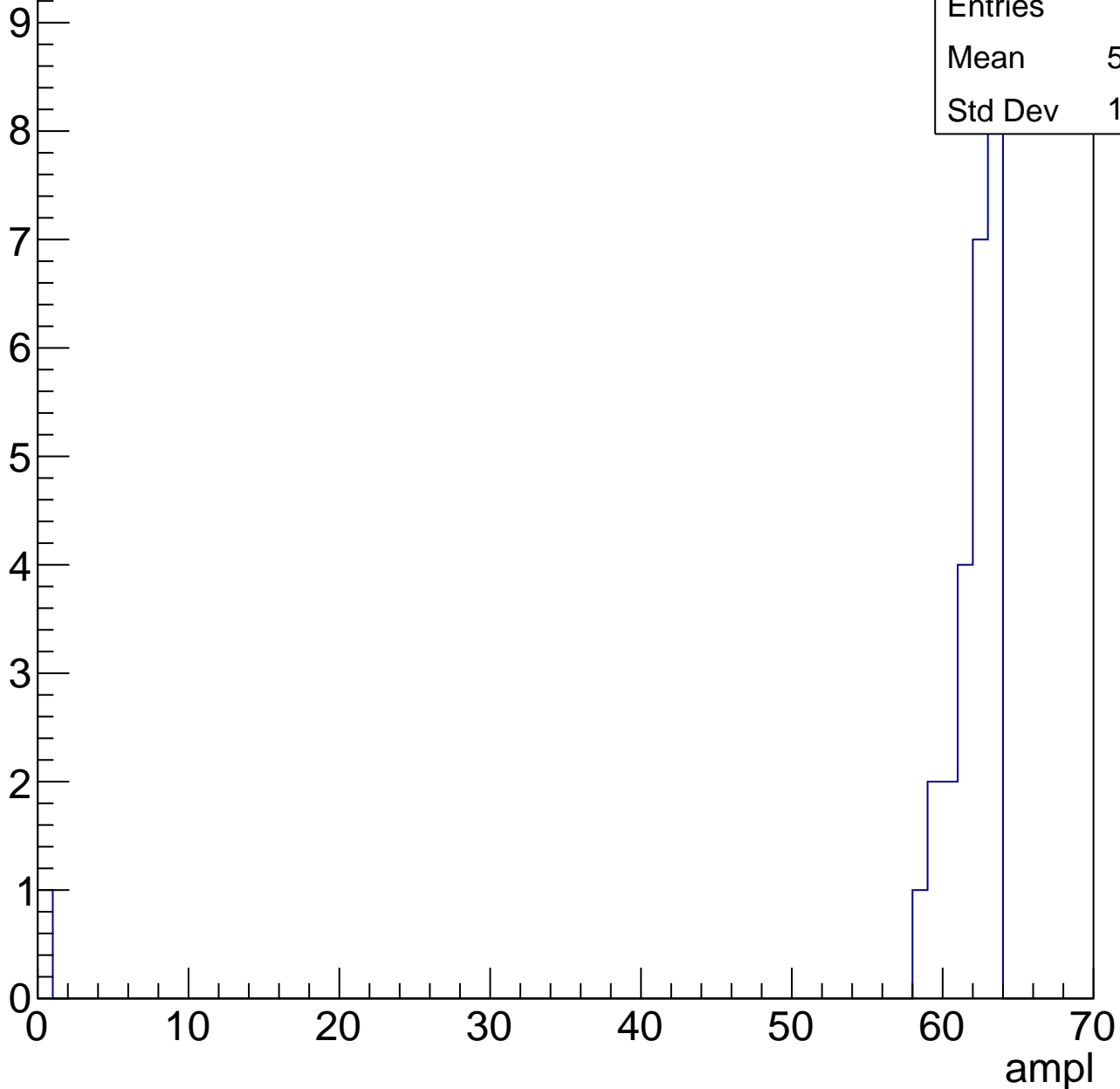
ampl



B1L002S, U7-ch38, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	26
Mean	59.27
Std Dev	11.94

B1L002S, U7-ch38, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L002S, U7-ch39, adc0

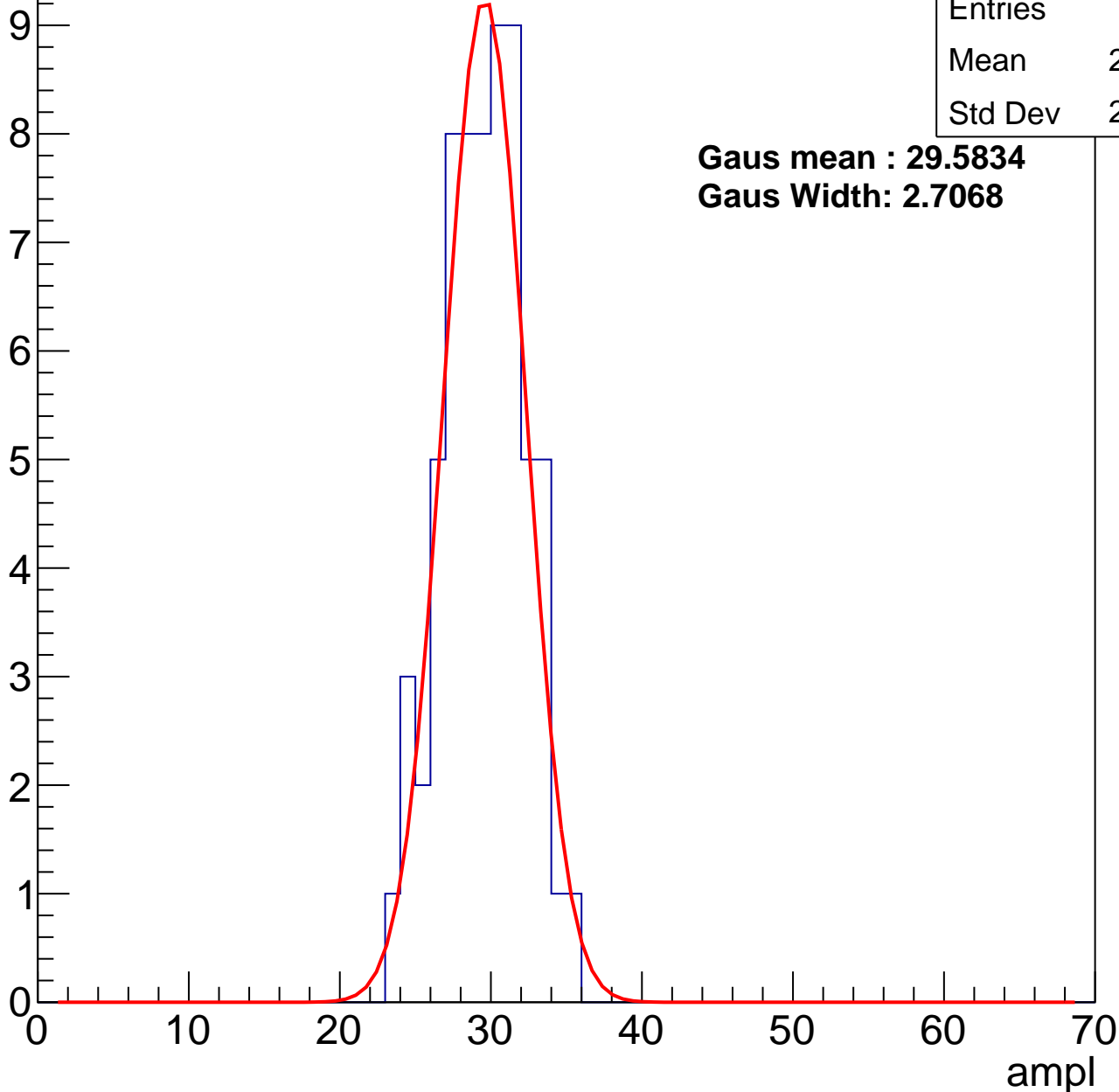
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	29.08
Std Dev	2.656

Gaus mean : 29.5834

Gaus Width: 2.7068



B1L002S, U7-ch39, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	35.69
Std Dev	2.655

Gaus mean : 36.4274

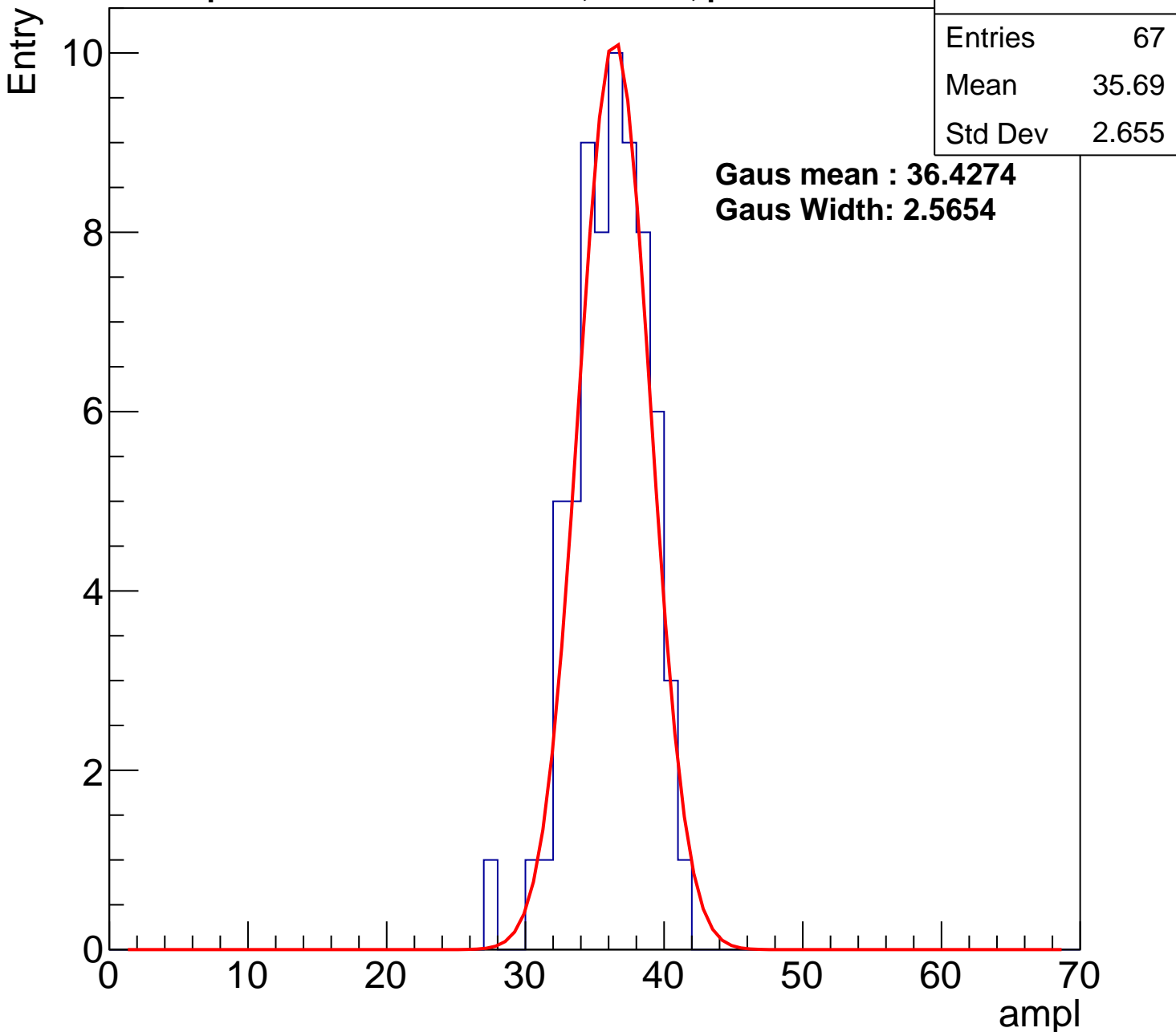
Gaus Width: 2.5654

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch39, adc2

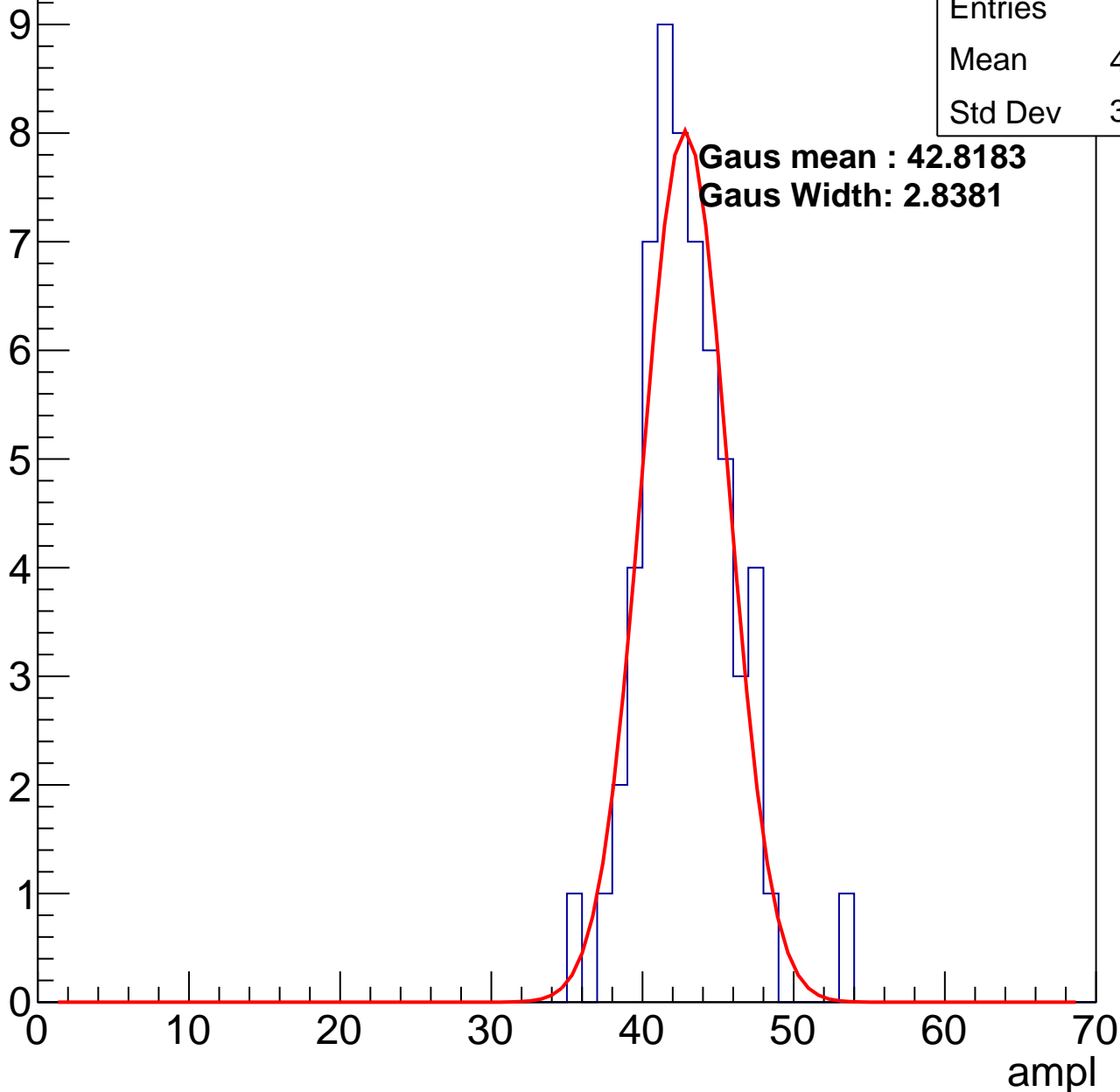
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.47
Std Dev	3.044

Gaus mean : 42.8183

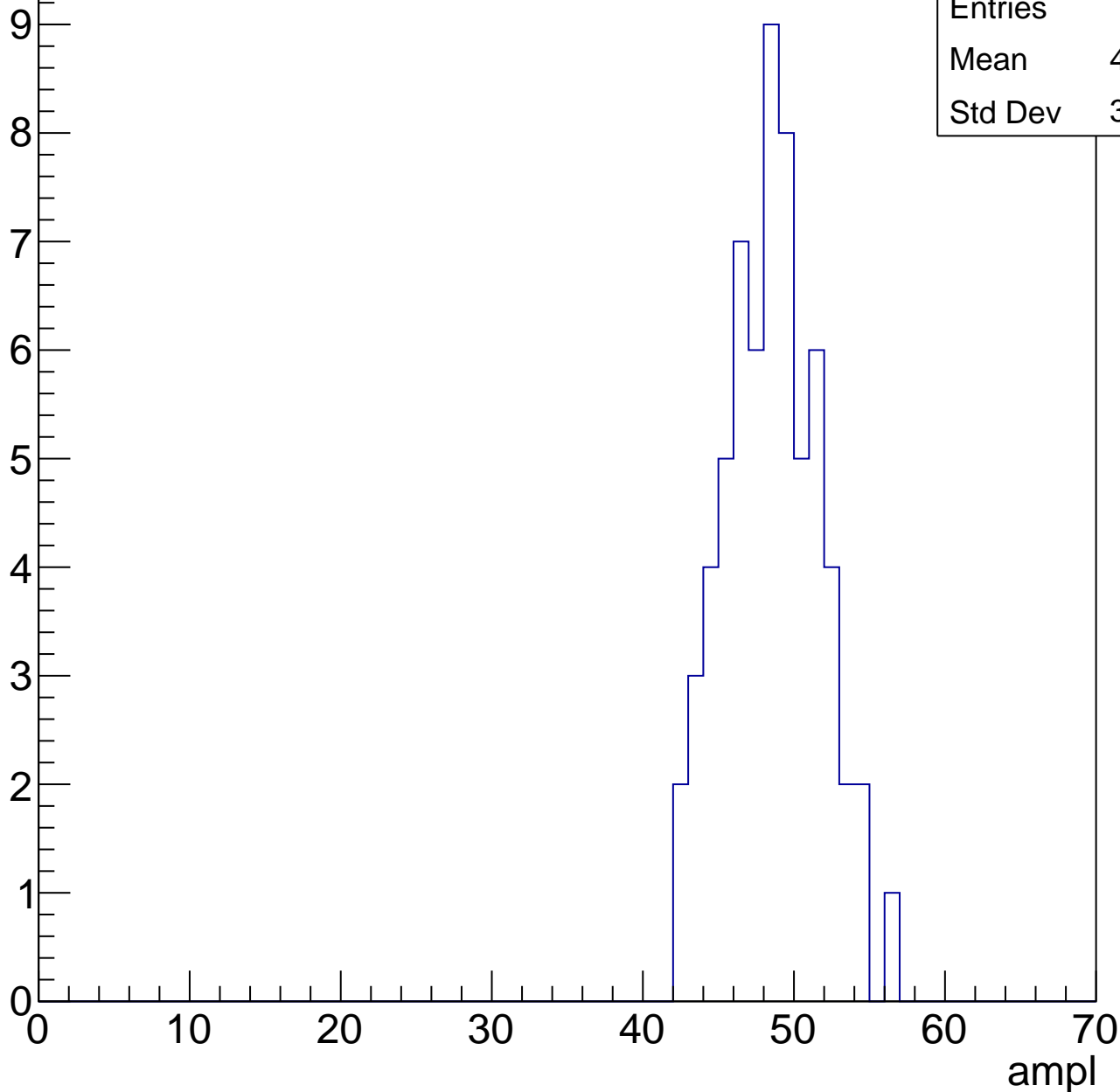
Gaus Width: 2.8381



B1L002S, U7-ch39, adc3

calib_packv5_042523_0143.root, FC#10, port B3

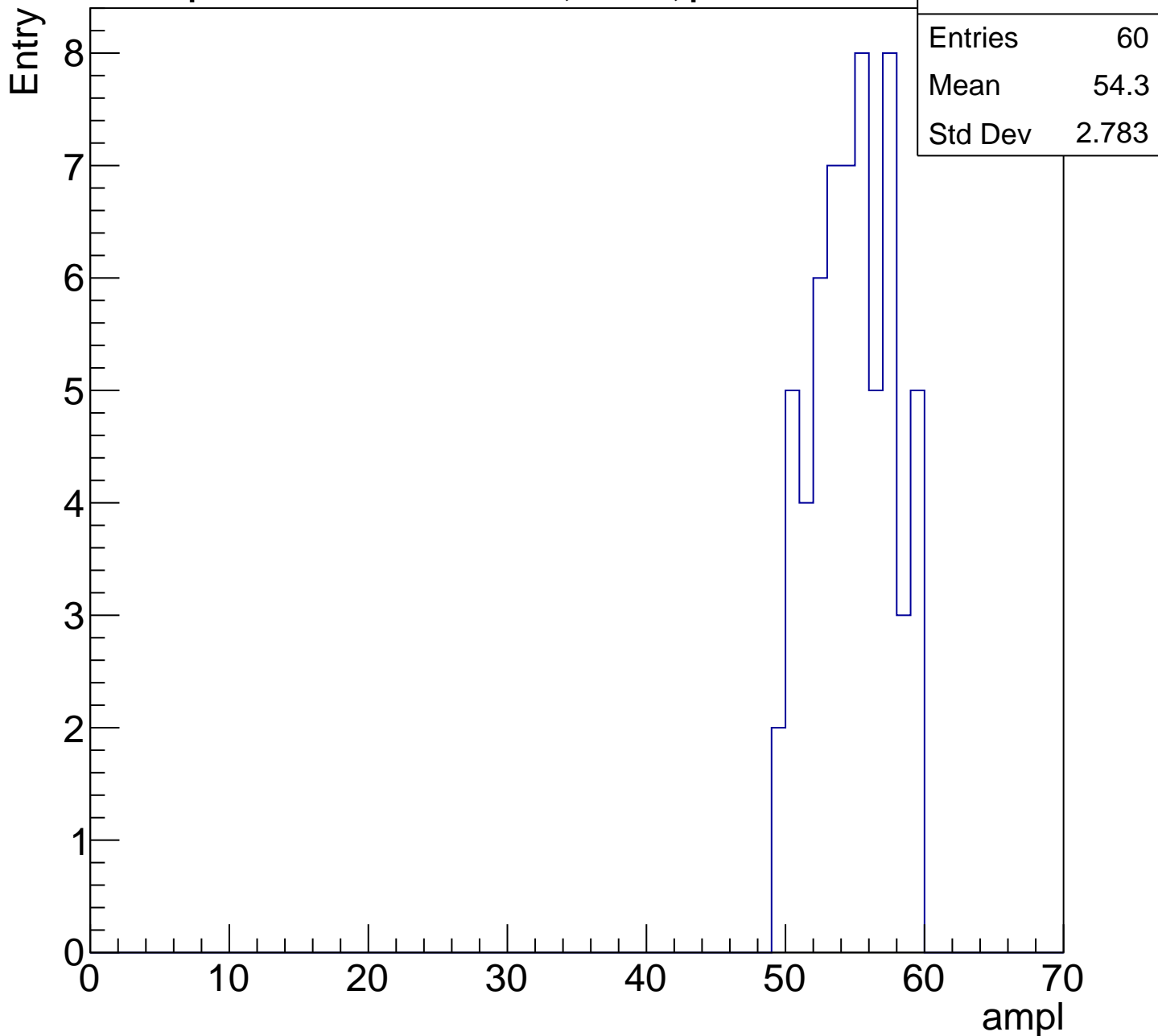
Entry



Entries	64
Mean	48.06
Std Dev	3.117

B1L002S, U7-ch39, adc4

calib_packv5_042523_0143.root, FC#10, port B3

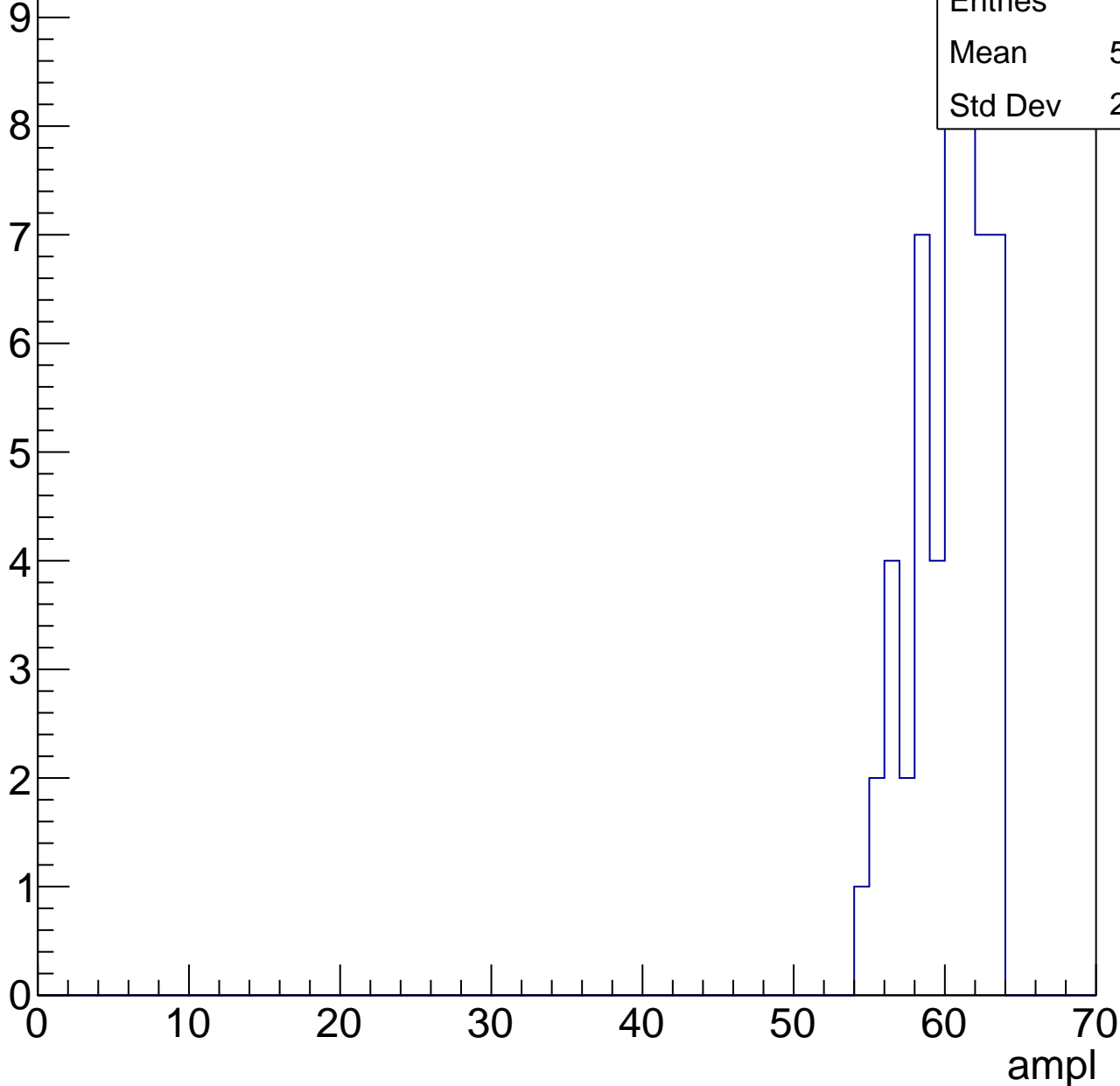


B1L002S, U7-ch39, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

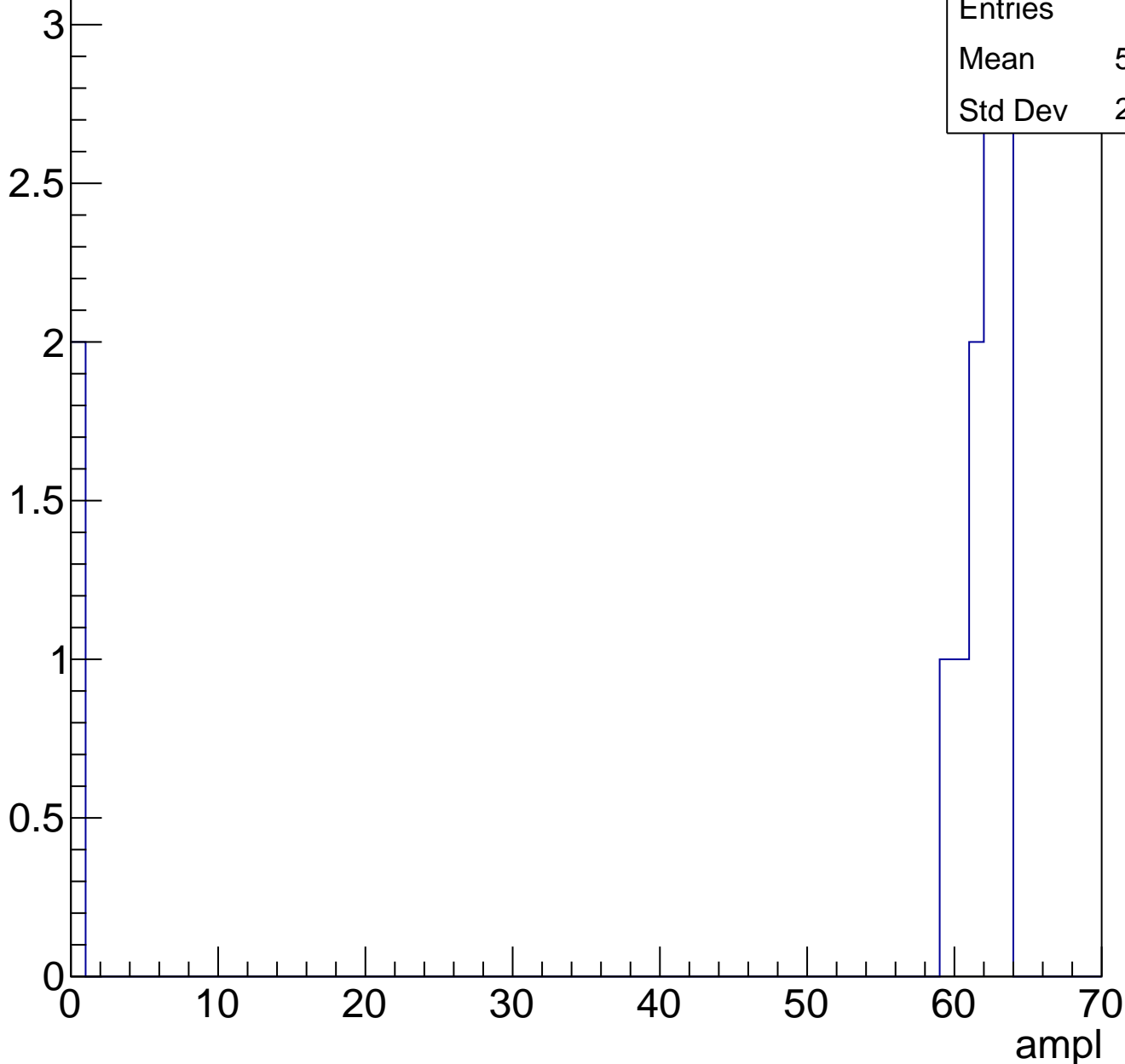
Entries	51
Mean	59.75
Std Dev	2.408



B1L002S, U7-ch39, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch39, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch40, adc0

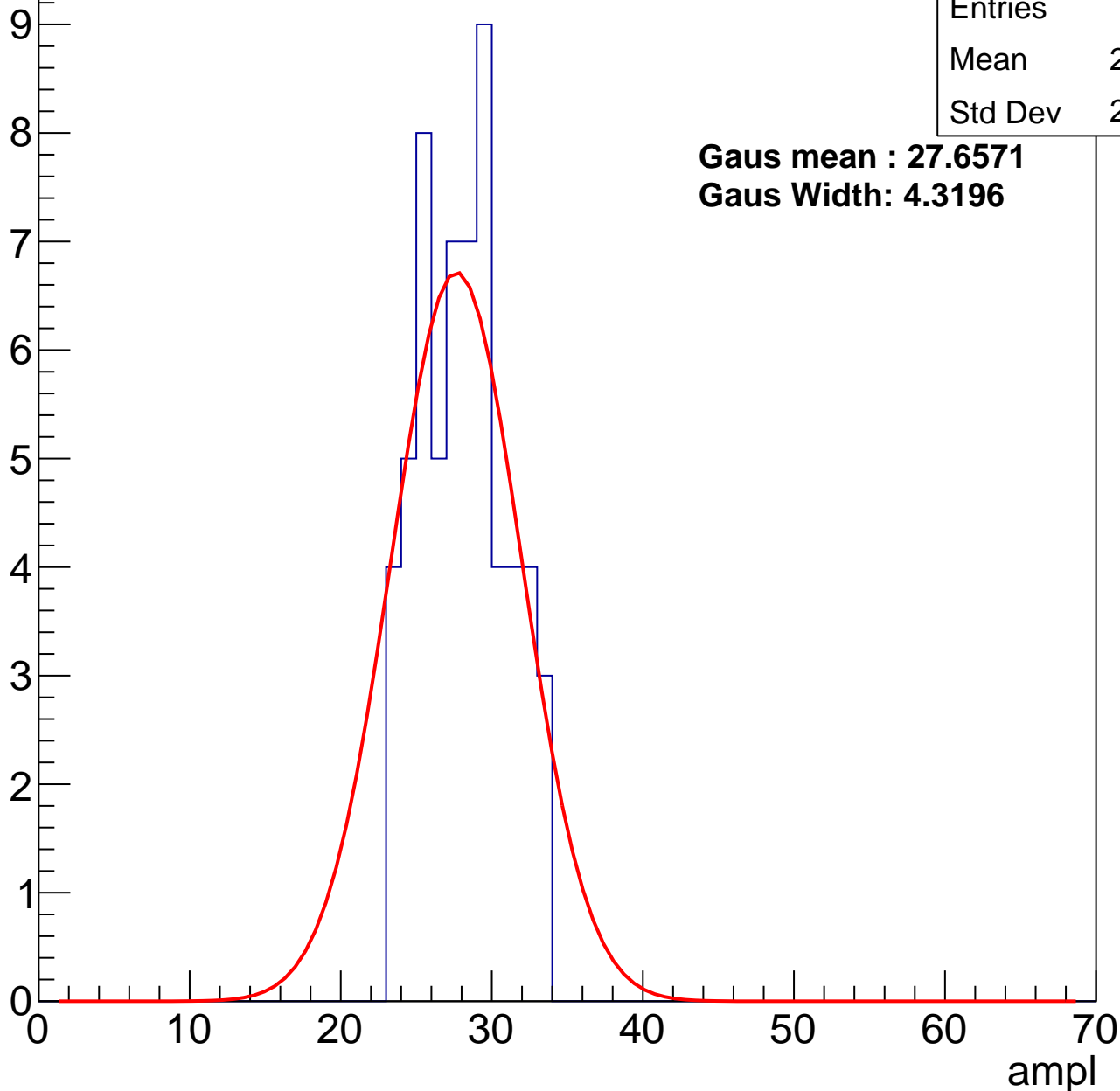
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.65
Std Dev	2.804

Gaus mean : 27.6571

Gaus Width: 4.3196



B1L002S, U7-ch40, adc1

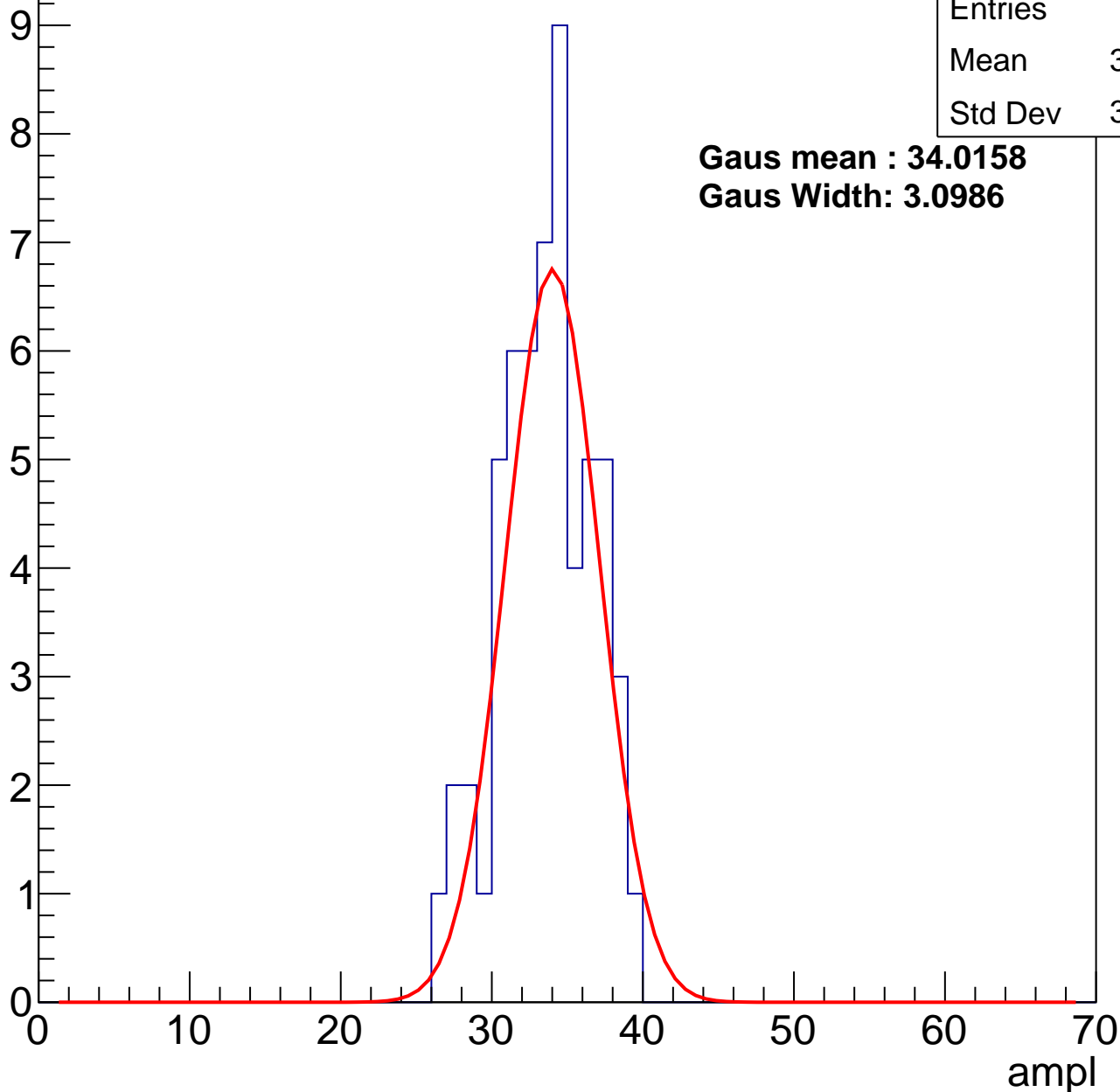
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.12
Std Dev	3.027

Gaus mean : 34.0158

Gaus Width: 3.0986



B1L002S, U7-ch40, adc2

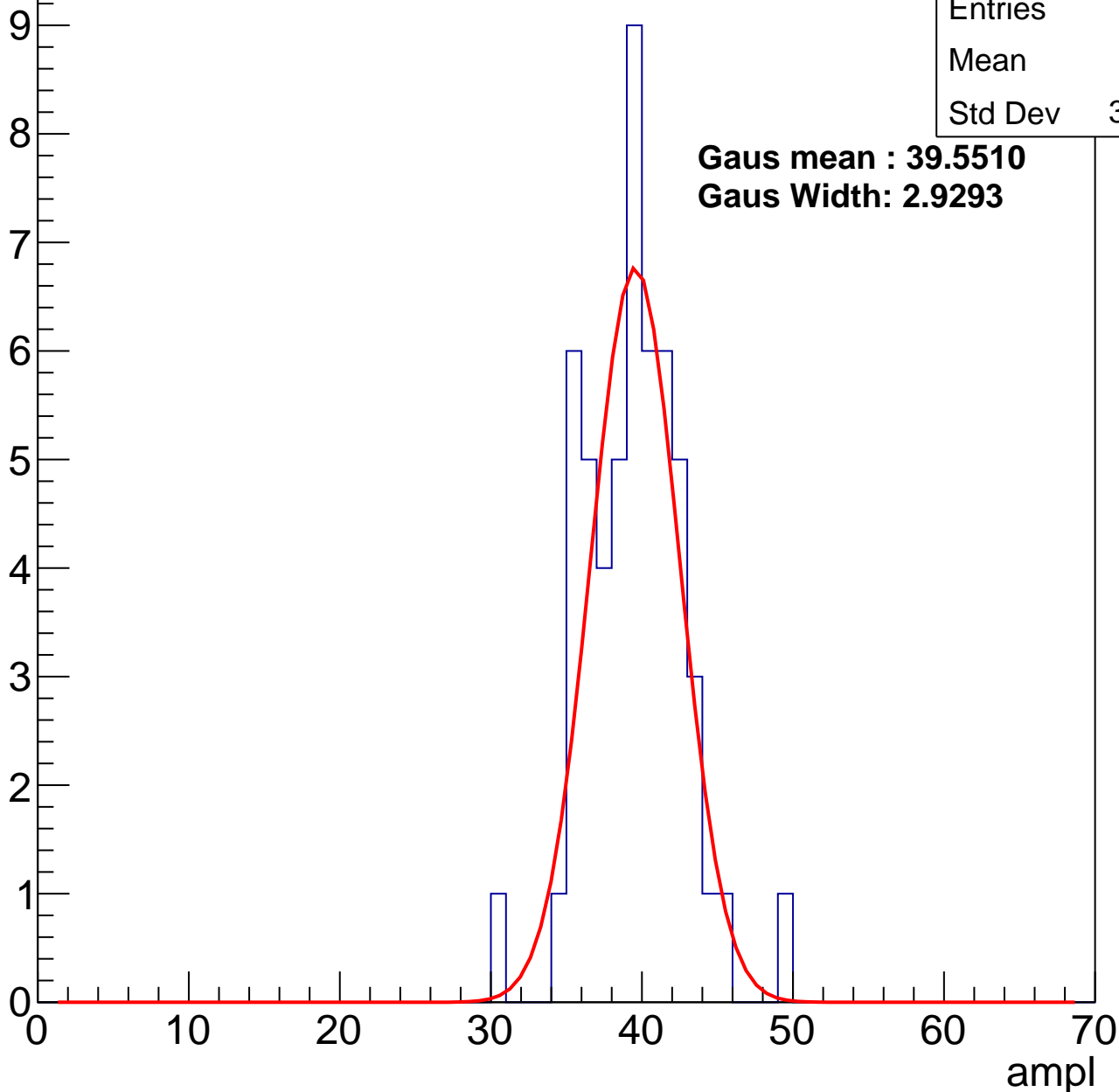
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	39
Std Dev	3.197

Gaus mean : 39.5510

Gaus Width: 2.9293

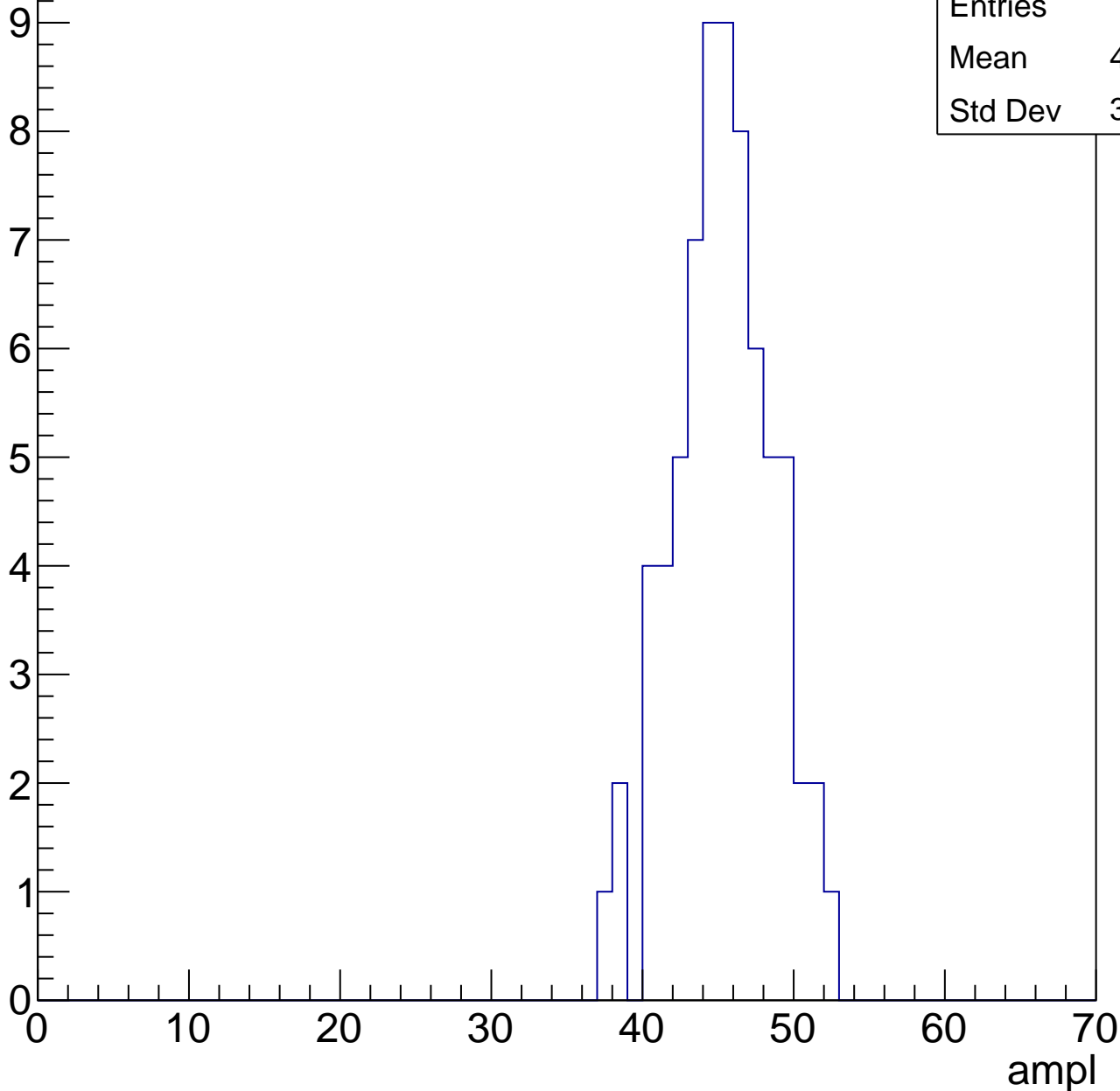


B1L002S, U7-ch40, adc3

calib_packv5_042523_0143.root, FC#10, port B3

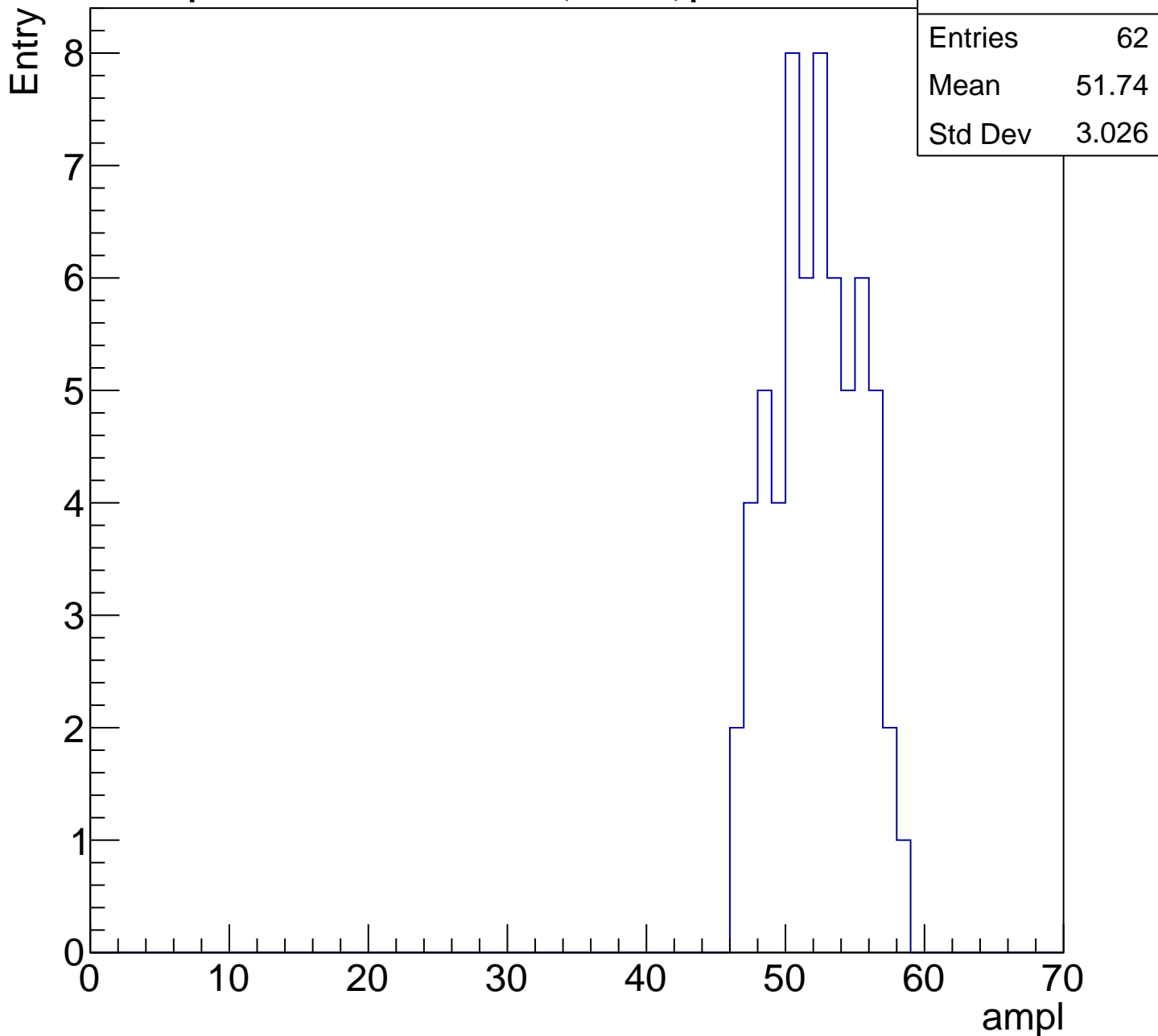
Entry

Entries	70
Mean	44.83
Std Dev	3.238



B1L002S, U7-ch40, adc4

calib_packv5_042523_0143.root, FC#10, port B3

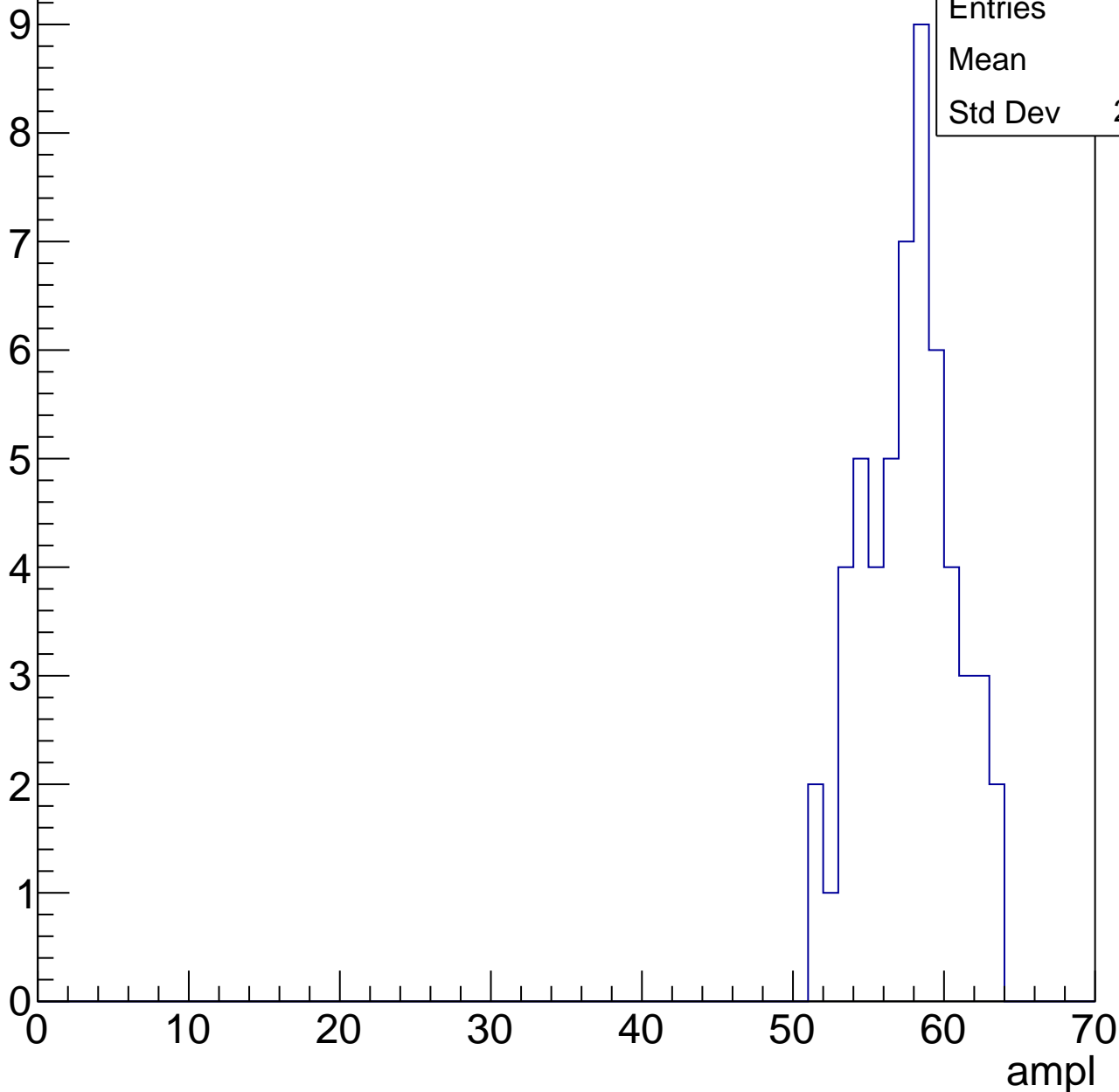


B1L002S, U7-ch40, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.2
Std Dev	2.981

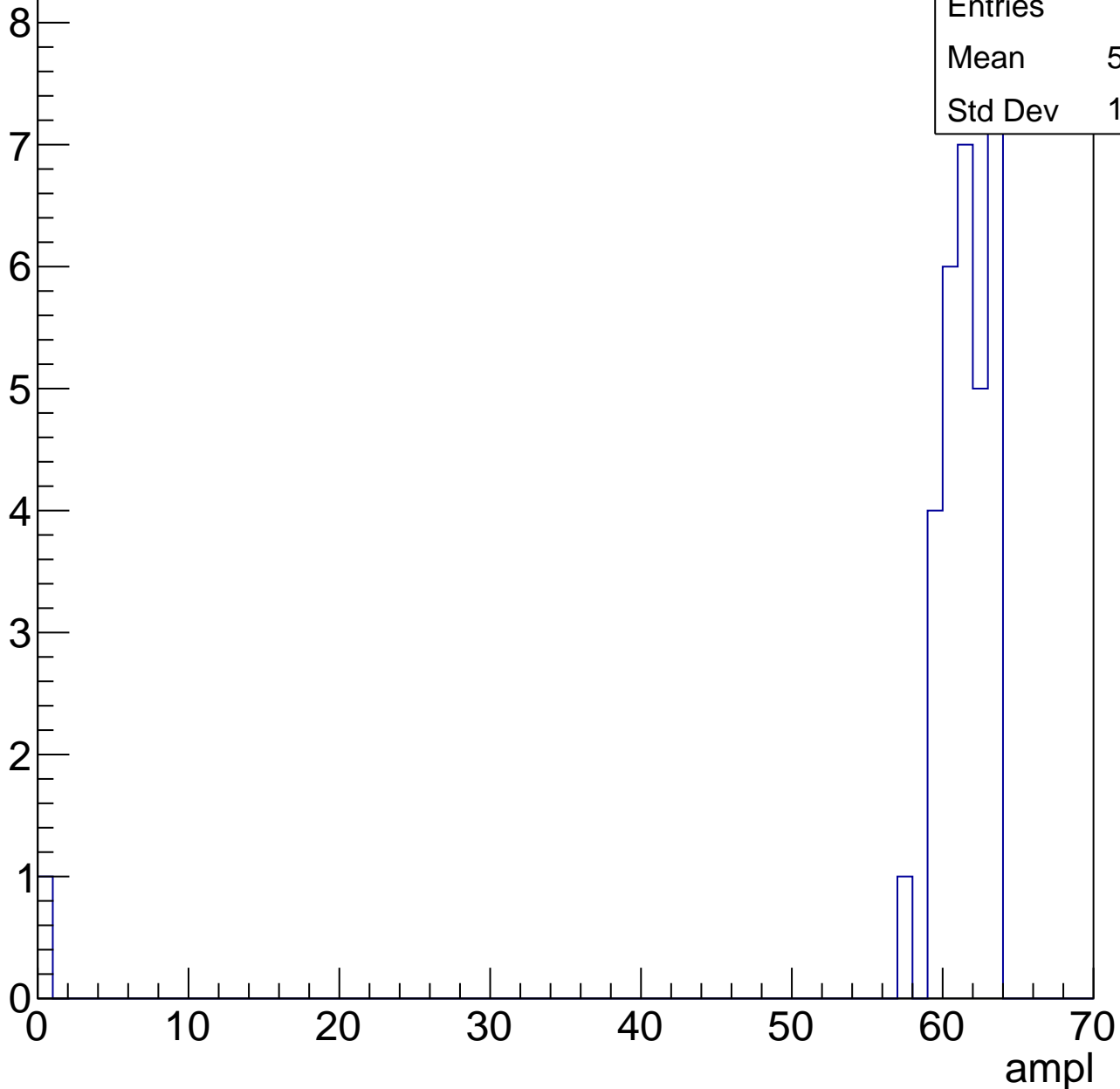


B1L002S, U7-ch40, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

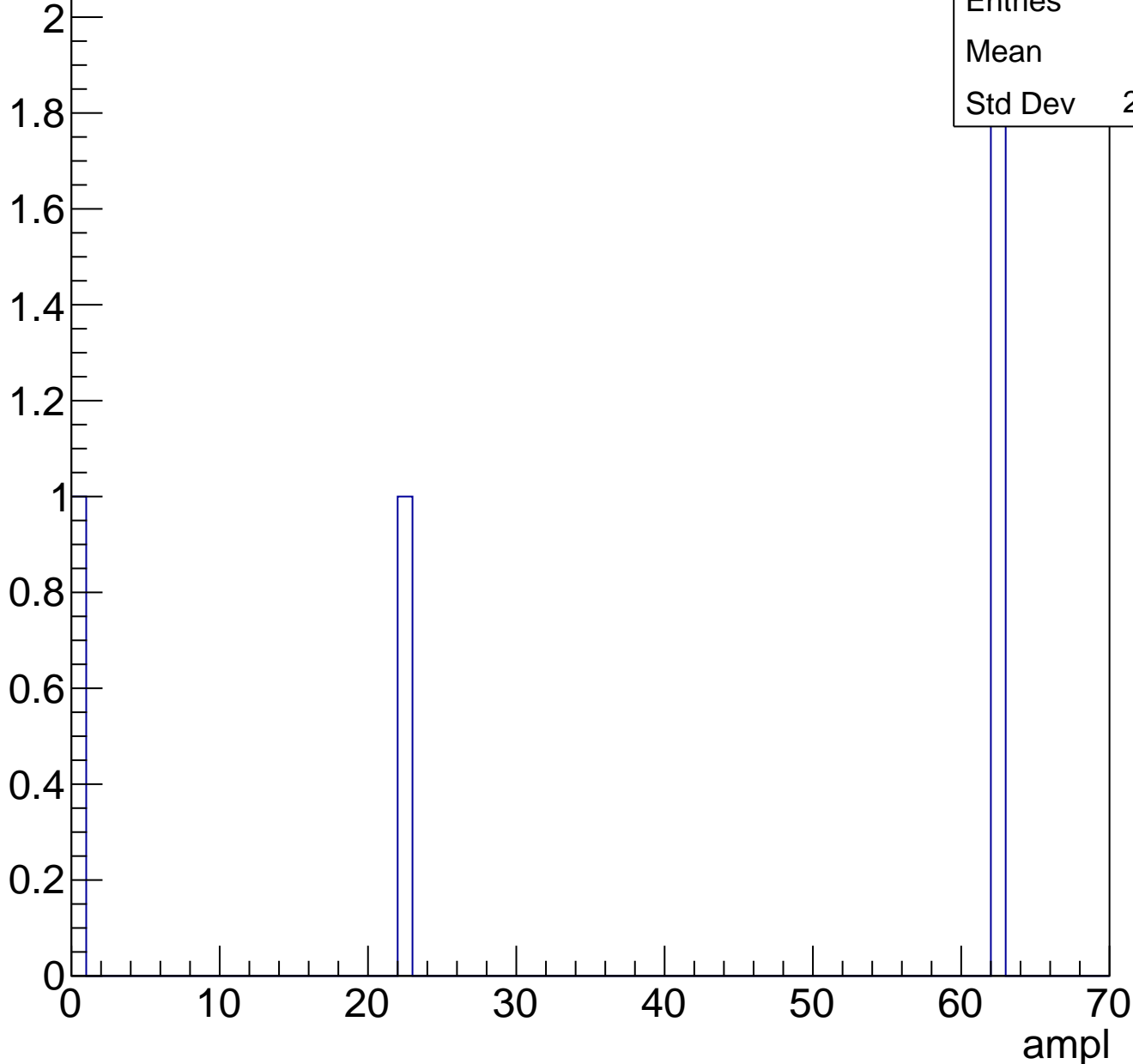
Entries	32
Mean	59.19
Std Dev	10.74



B1L002S, U7-ch40, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	36.5
Std Dev	26.66

B1L002S, U7-ch41, adc0

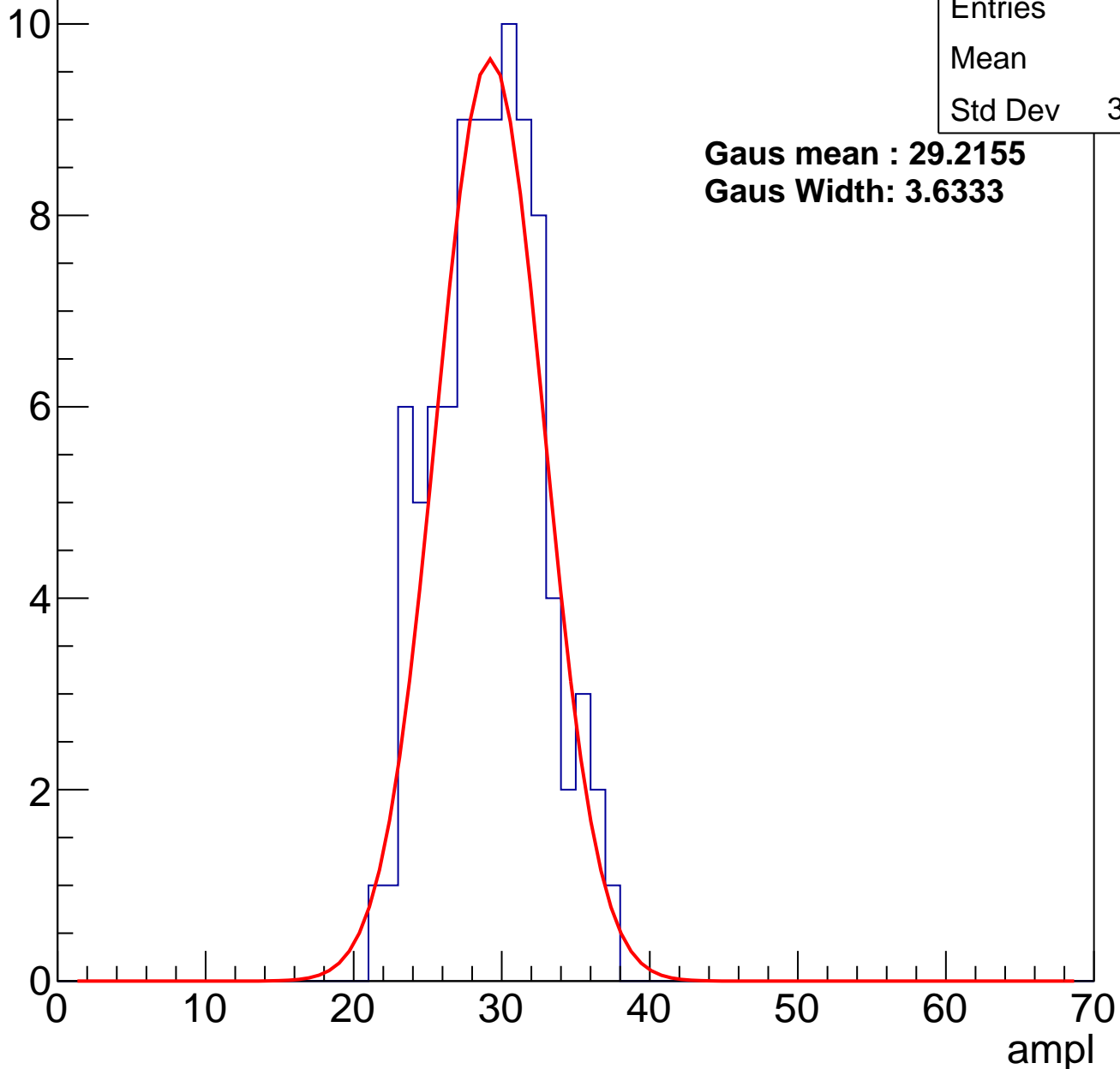
calib_packv5_042523_0143.root, FC#10, port B3

Entries	91
Mean	28.7
Std Dev	3.544

Gaus mean : 29.2155

Gaus Width: 3.6333

Entry



B1L002S, U7-ch41, adc1

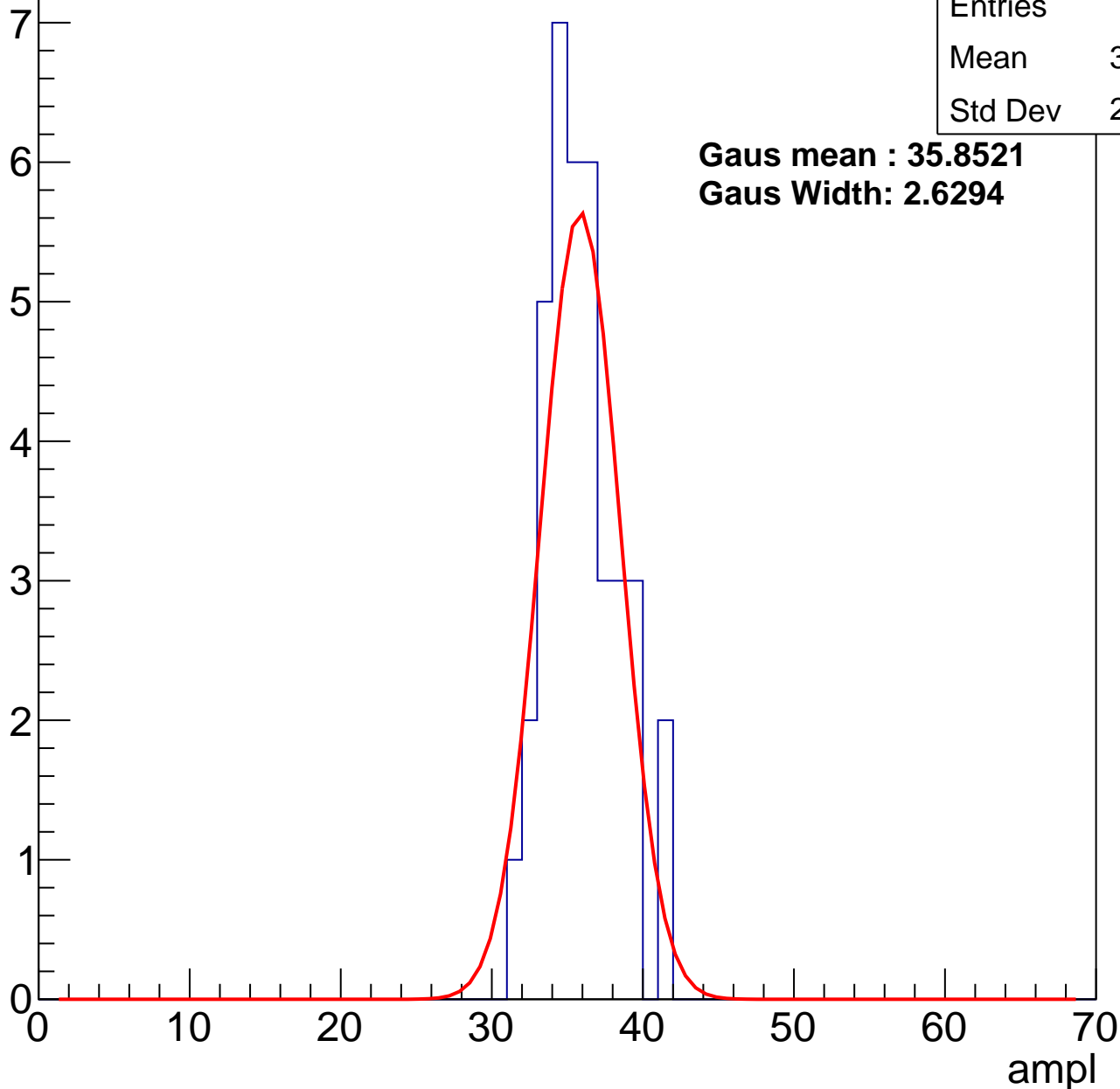
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	38
Mean	35.47
Std Dev	2.392

Gaus mean : 35.8521

Gaus Width: 2.6294



B1L002S, U7-ch41, adc2

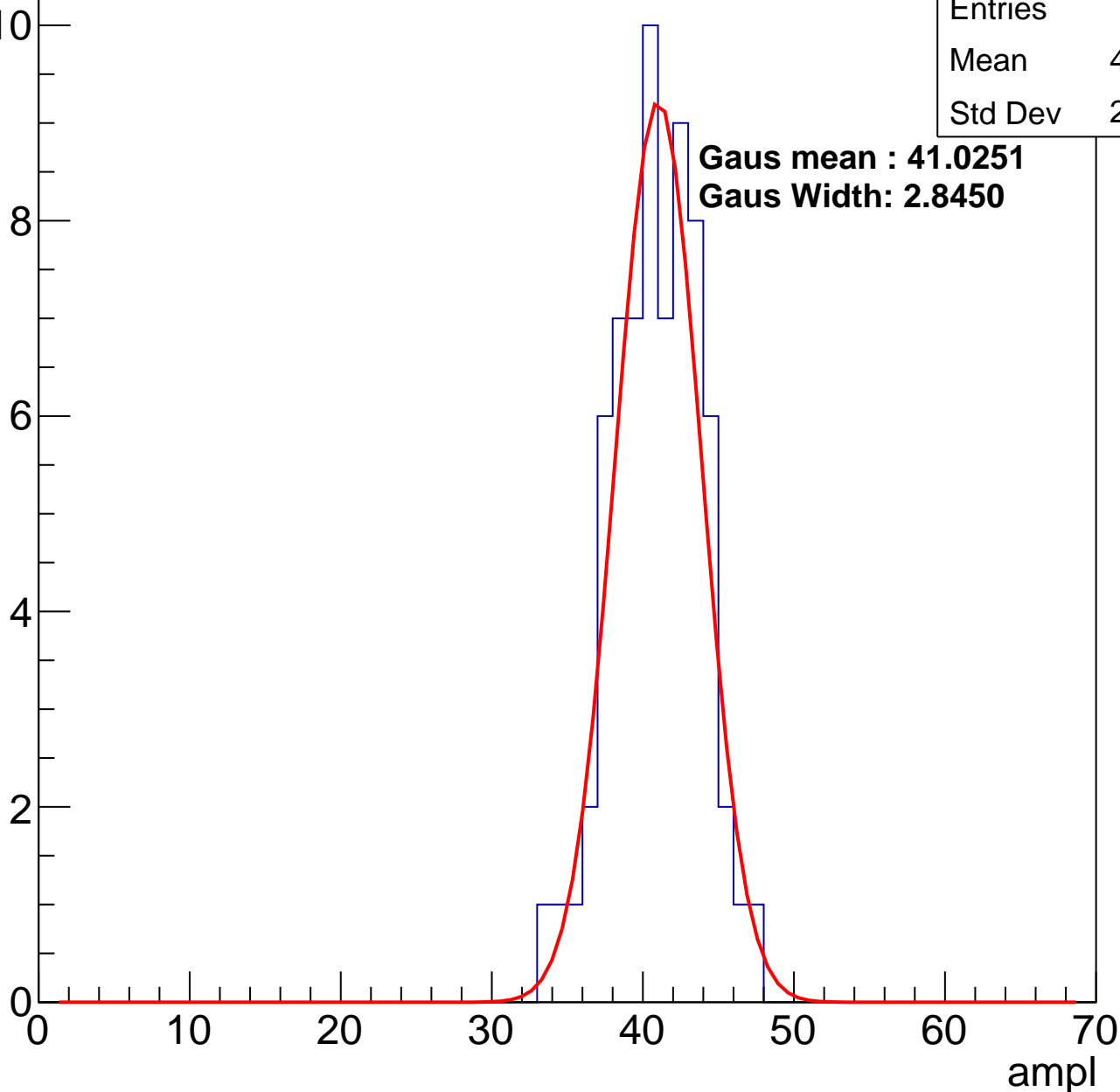
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.45
Std Dev	2.857

Gaus mean : 41.0251

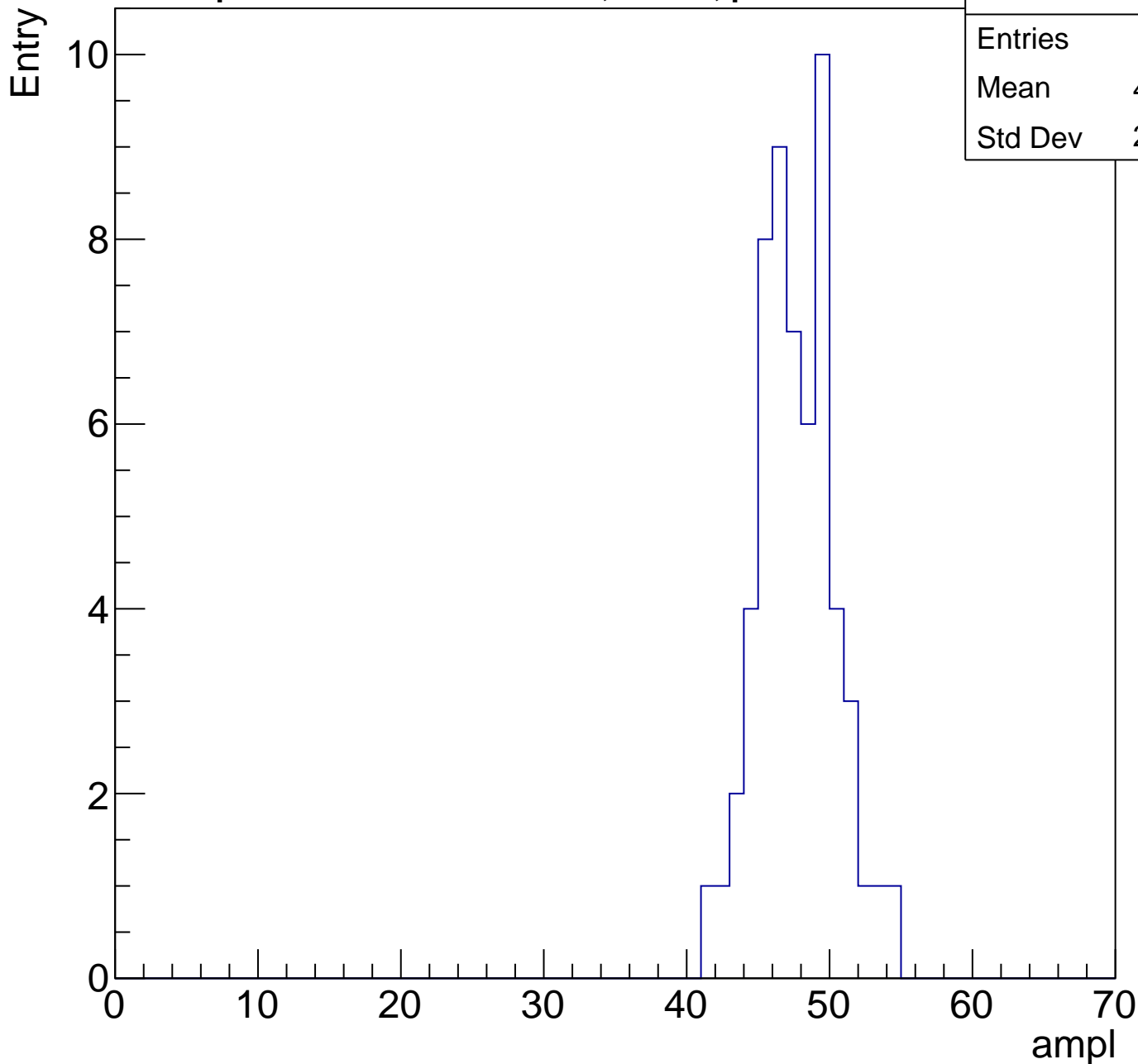
Gaus Width: 2.8450



B1L002S, U7-ch41, adc3

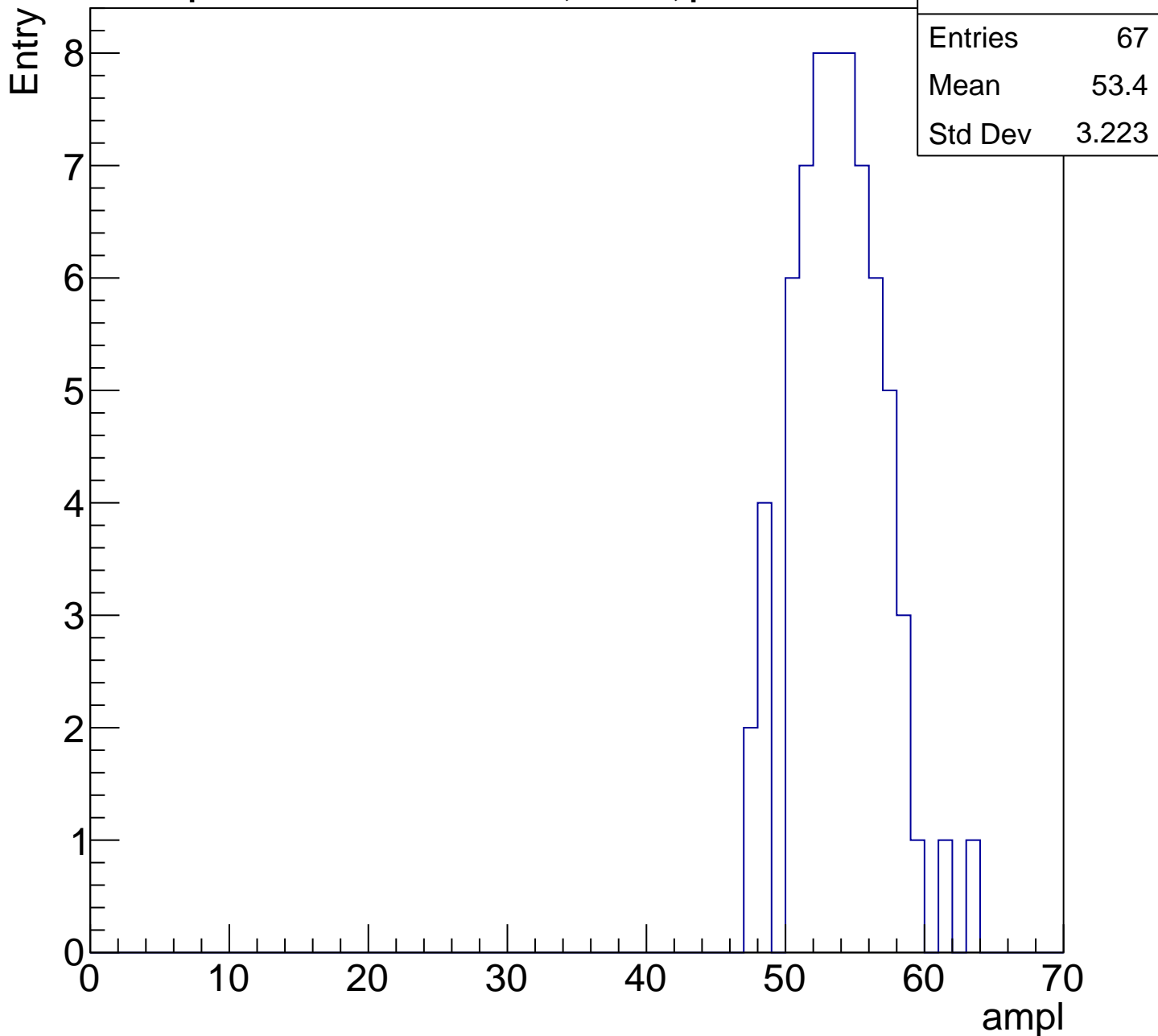
calib_packv5_042523_0143.root, FC#10, port B3

Entries	58
Mean	47.21
Std Dev	2.651



B1L002S, U7-ch41, adc4

calib_packv5_042523_0143.root, FC#10, port B3

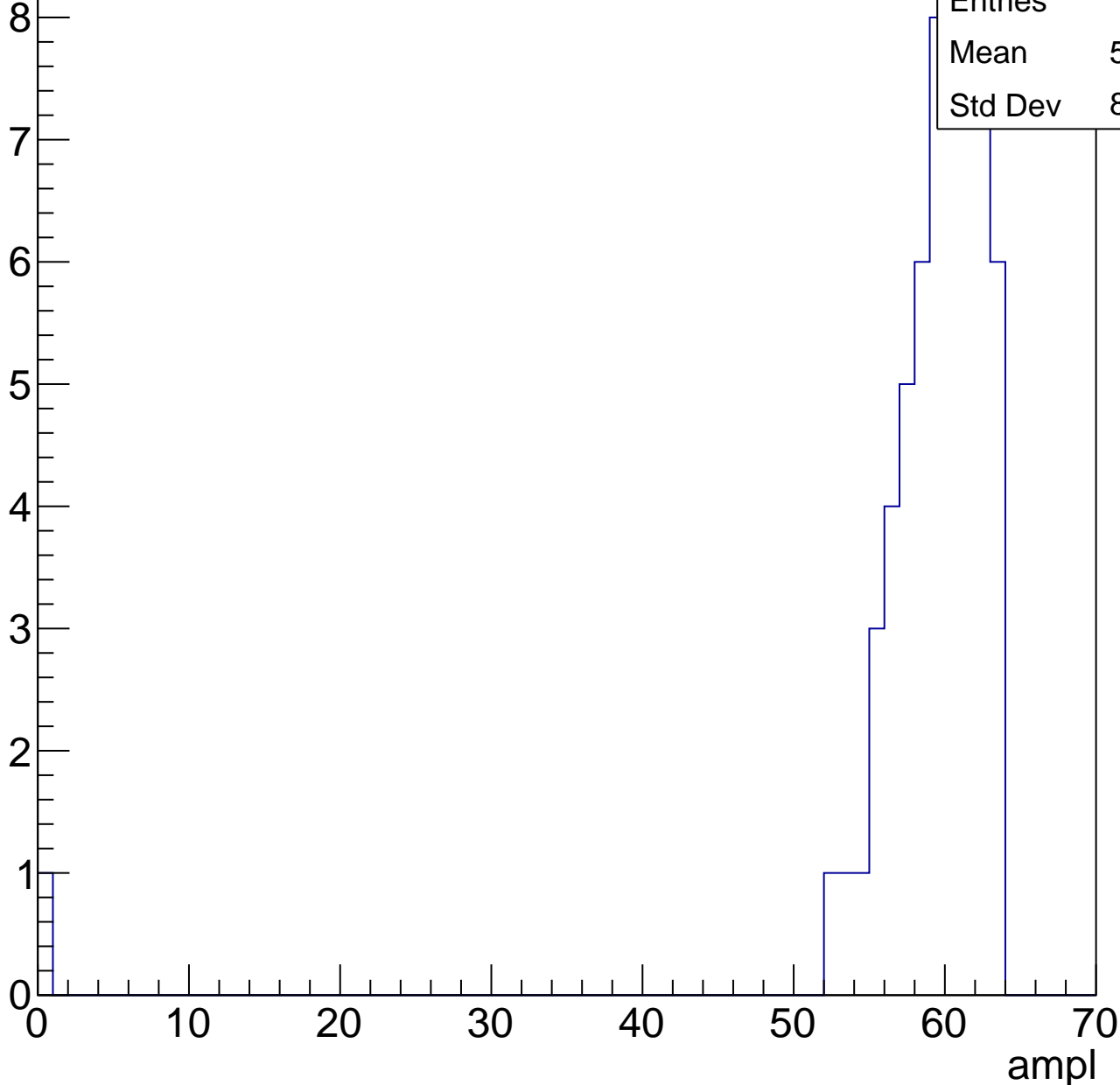


B1L002S, U7-ch41, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.25
Std Dev	8.037



B1L002S, U7-ch41, adc6

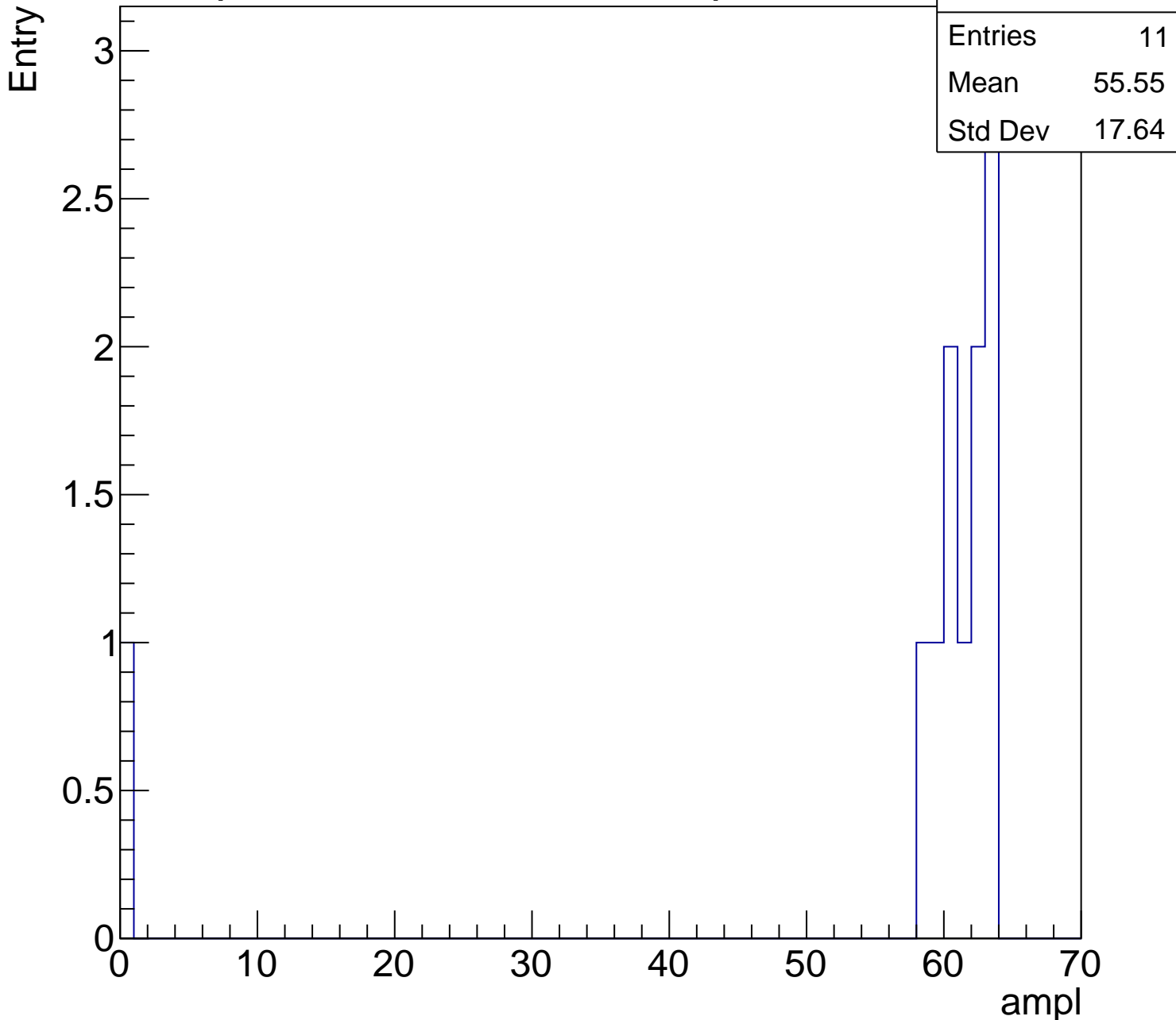
calib_packv5_042523_0143.root, FC#10, port B3

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	55.55
Std Dev	17.64

ampl



B1L002S, U7-ch41, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch42, adc0

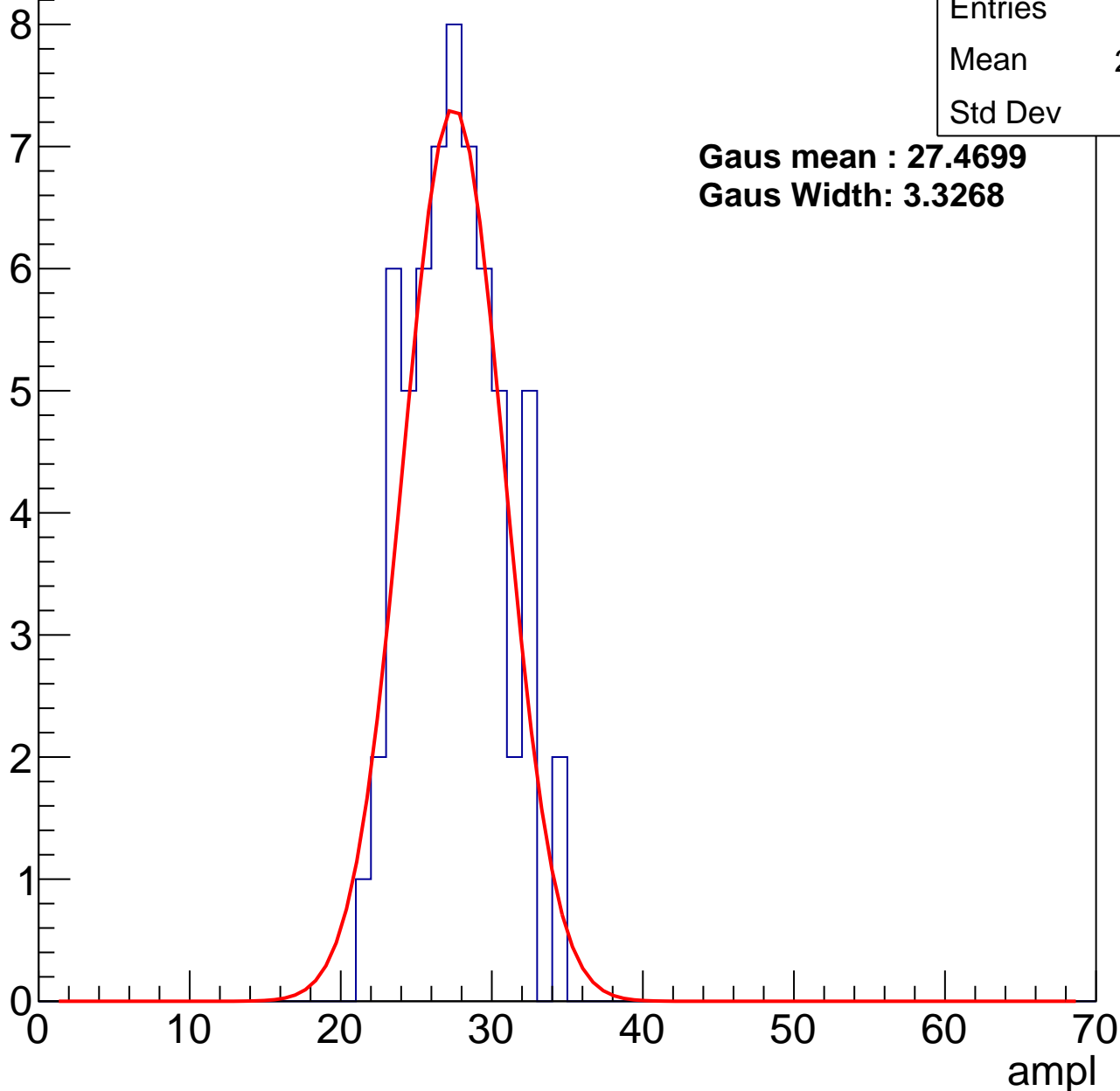
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.11
Std Dev	3.08

Gaus mean : 27.4699

Gaus Width: 3.3268



B1L002S, U7-ch42, adc1

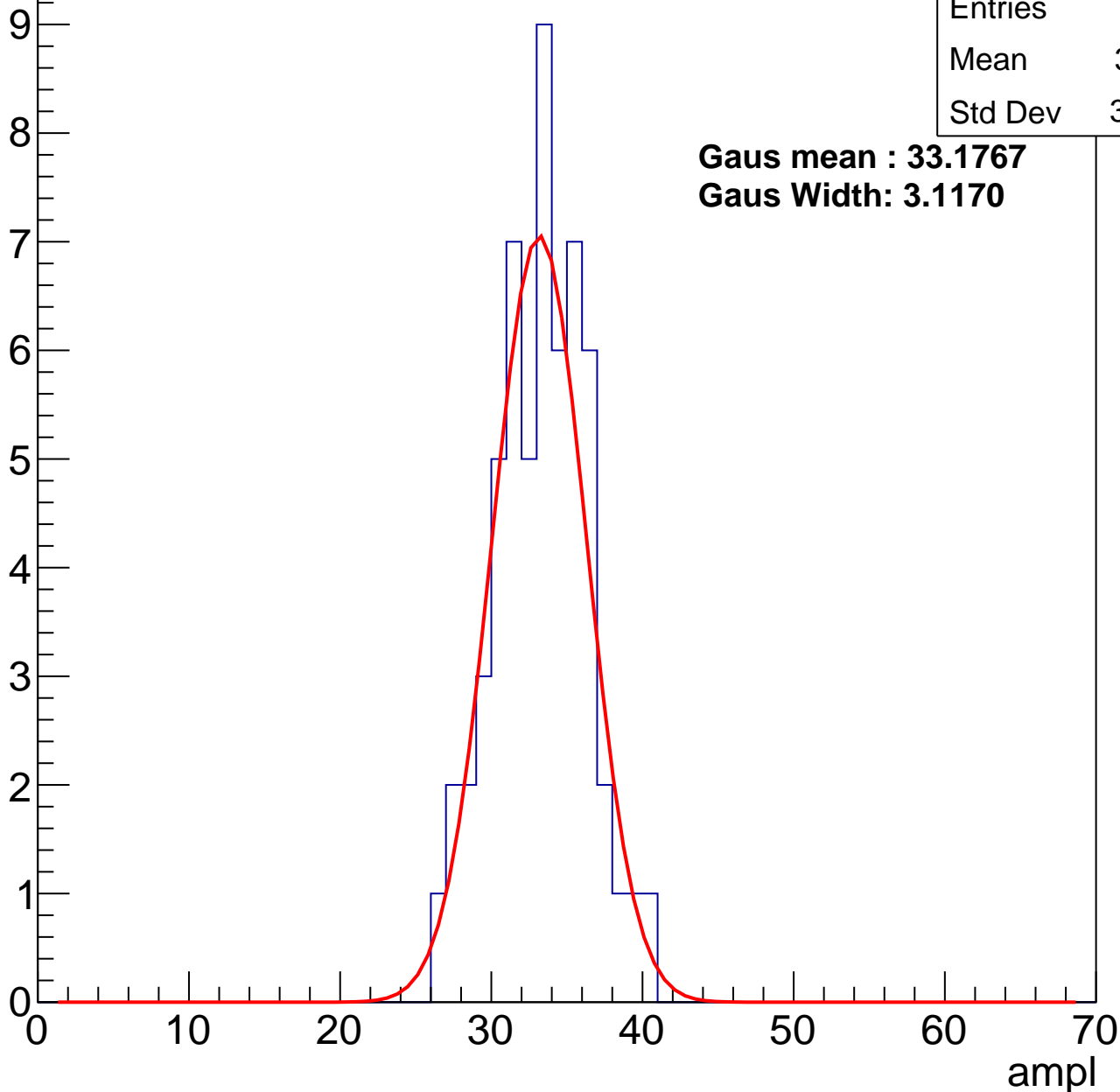
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	32.81
Std Dev	3.008

Gaus mean : 33.1767

Gaus Width: 3.1170



B1L002S, U7-ch42, adc2

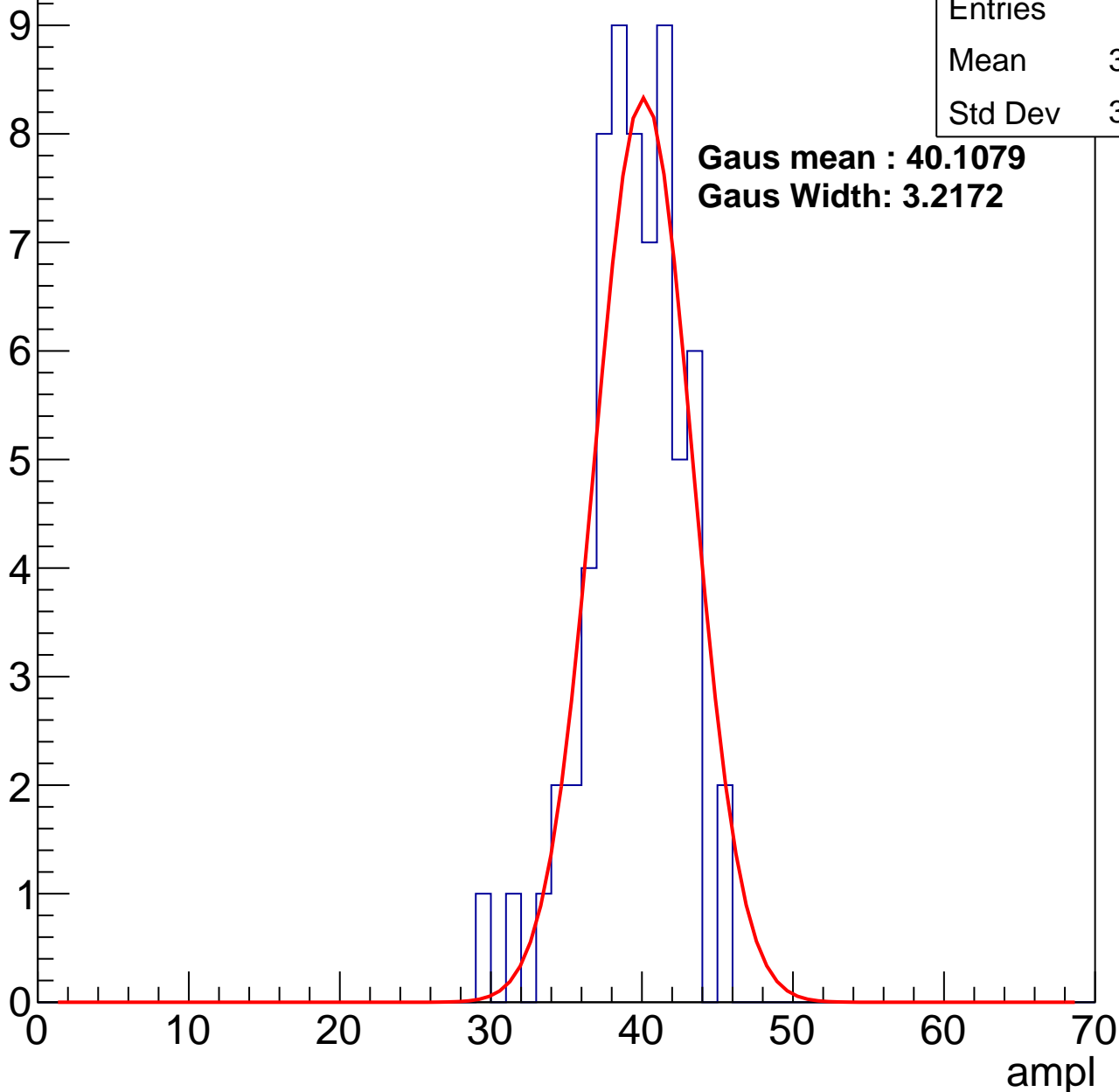
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	38.95
Std Dev	3.076

Gaus mean : 40.1079

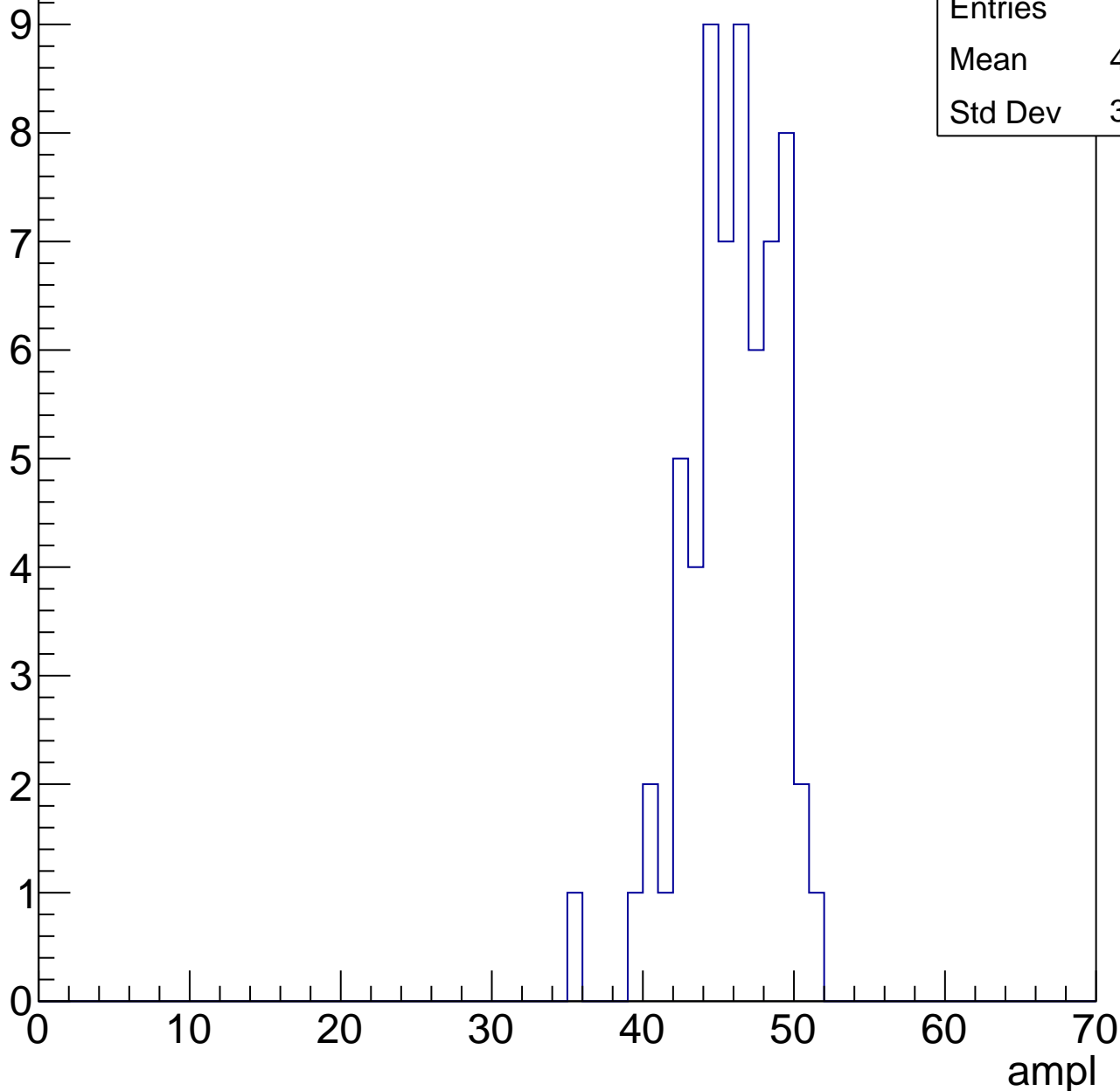
Gaus Width: 3.2172



B1L002S, U7-ch42, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



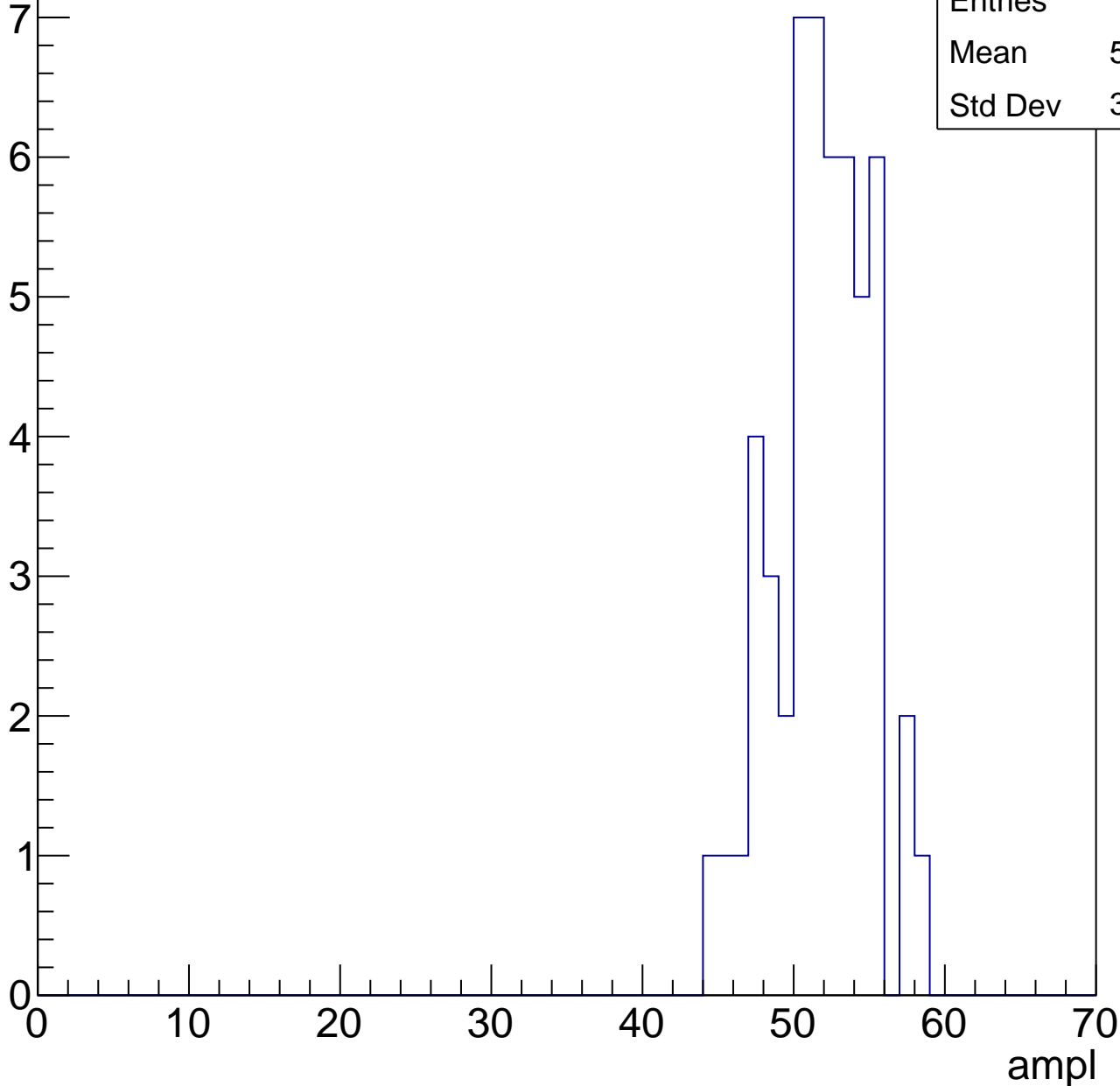
Entries	63
Mean	45.44
Std Dev	3.007

B1L002S, U7-ch42, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.42
Std Dev	3.097



B1L002S, U7-ch42, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	55.86
Std Dev	7.622

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

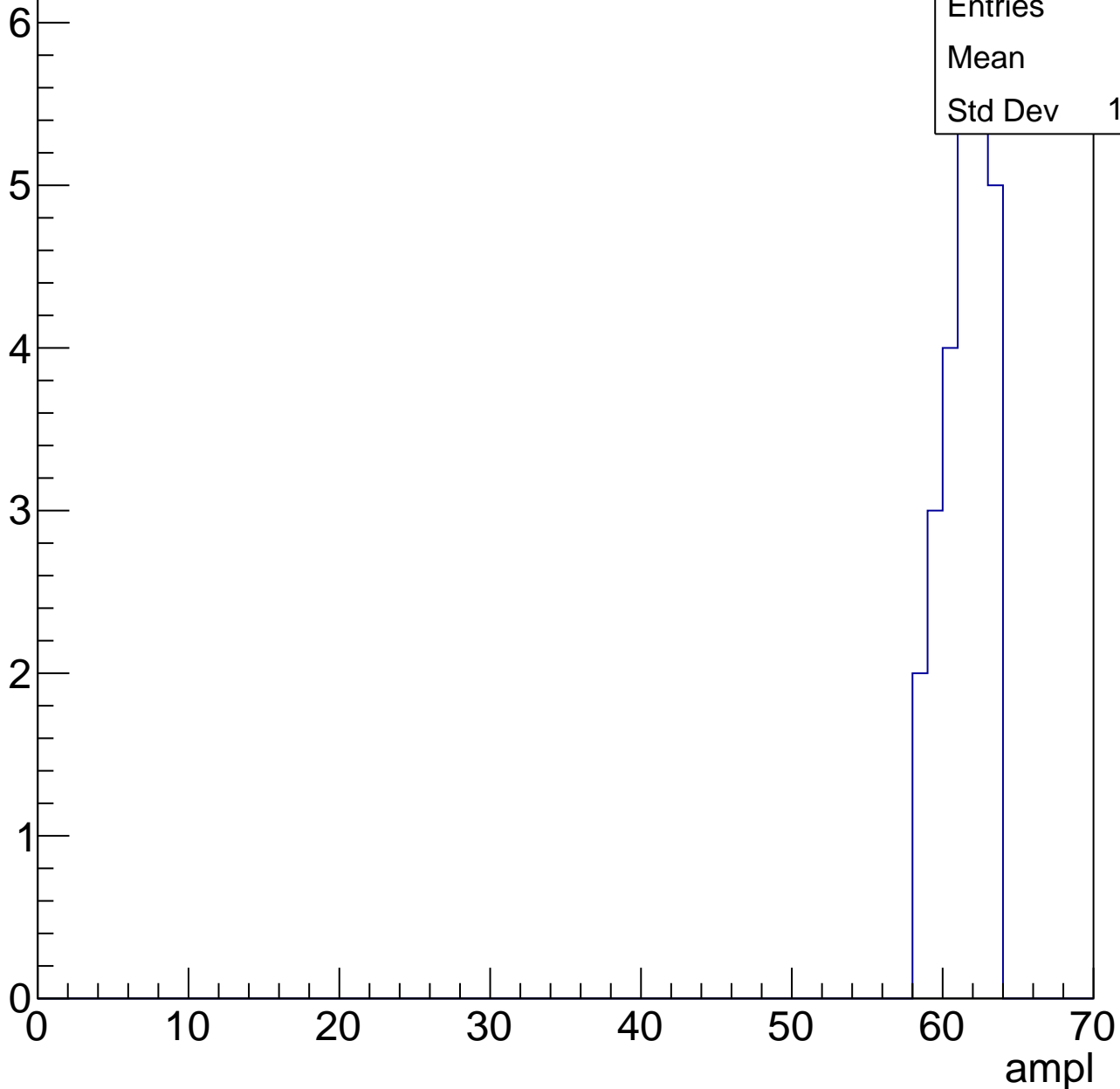
70

B1L002S, U7-ch42, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

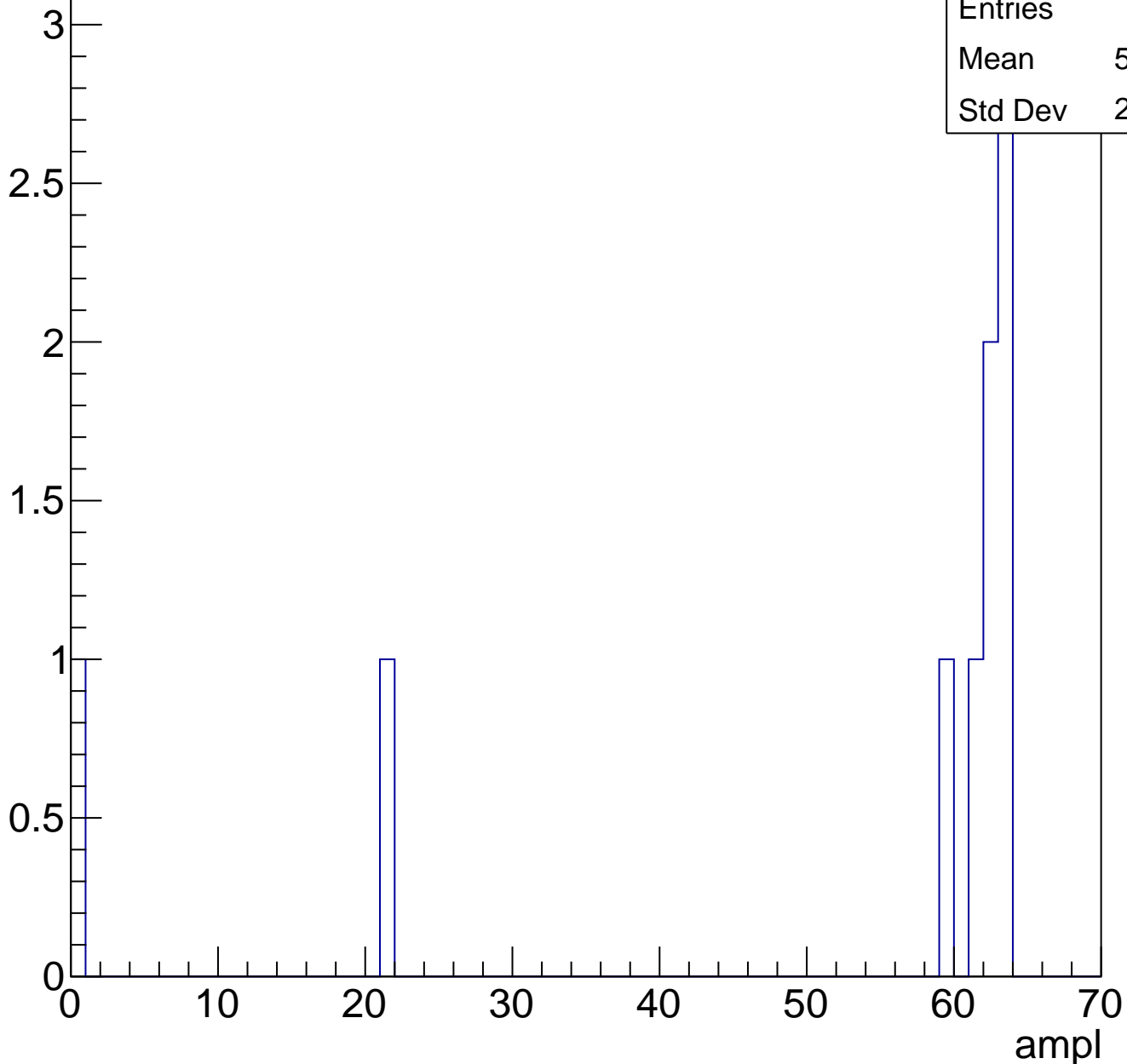
Entries	26
Mean	61
Std Dev	1.519



B1L002S, U7-ch42, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch43, adc0

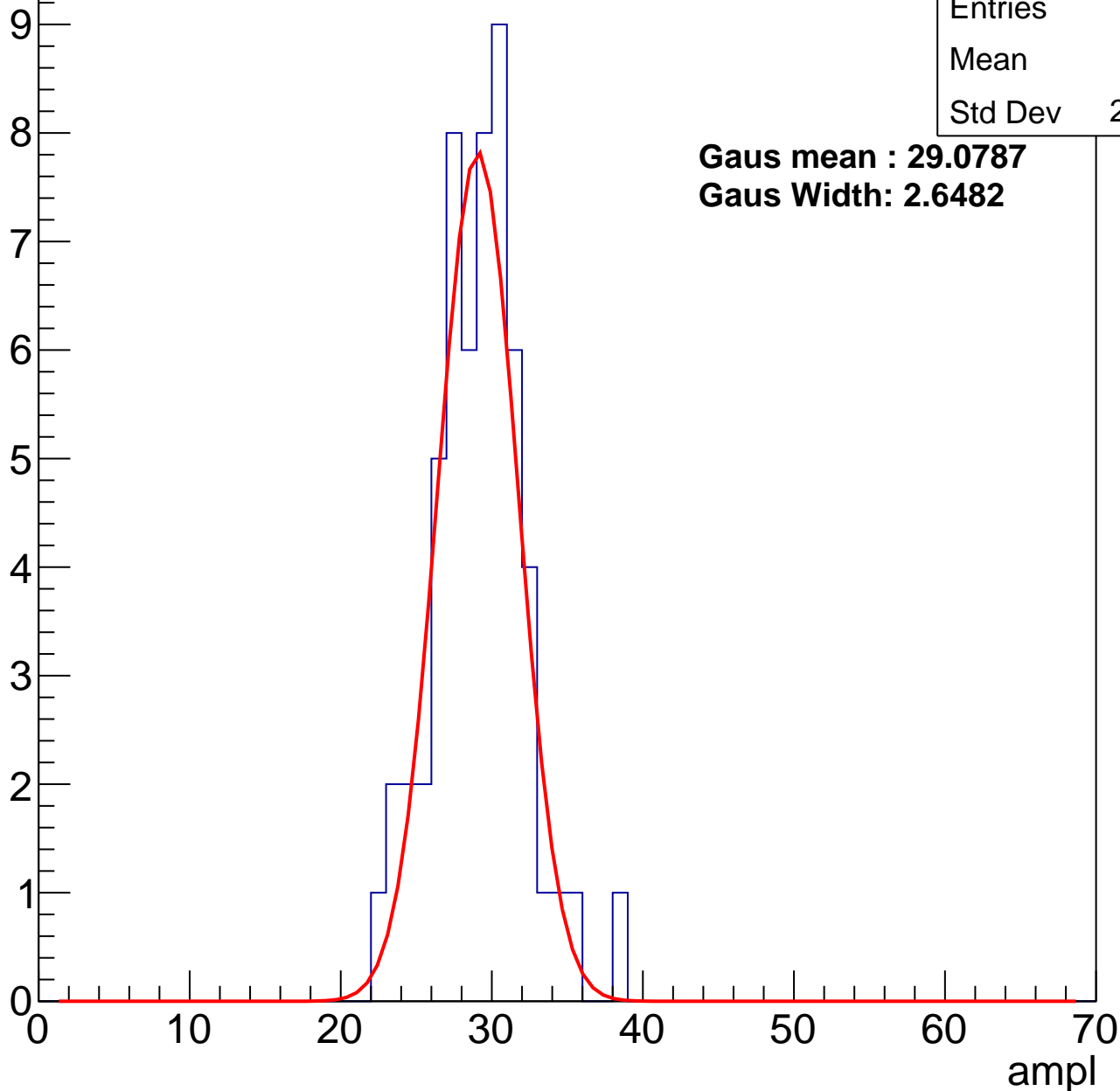
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.7
Std Dev	2.985

Gaus mean : 29.0787

Gaus Width: 2.6482



B1L002S, U7-ch43, adc1

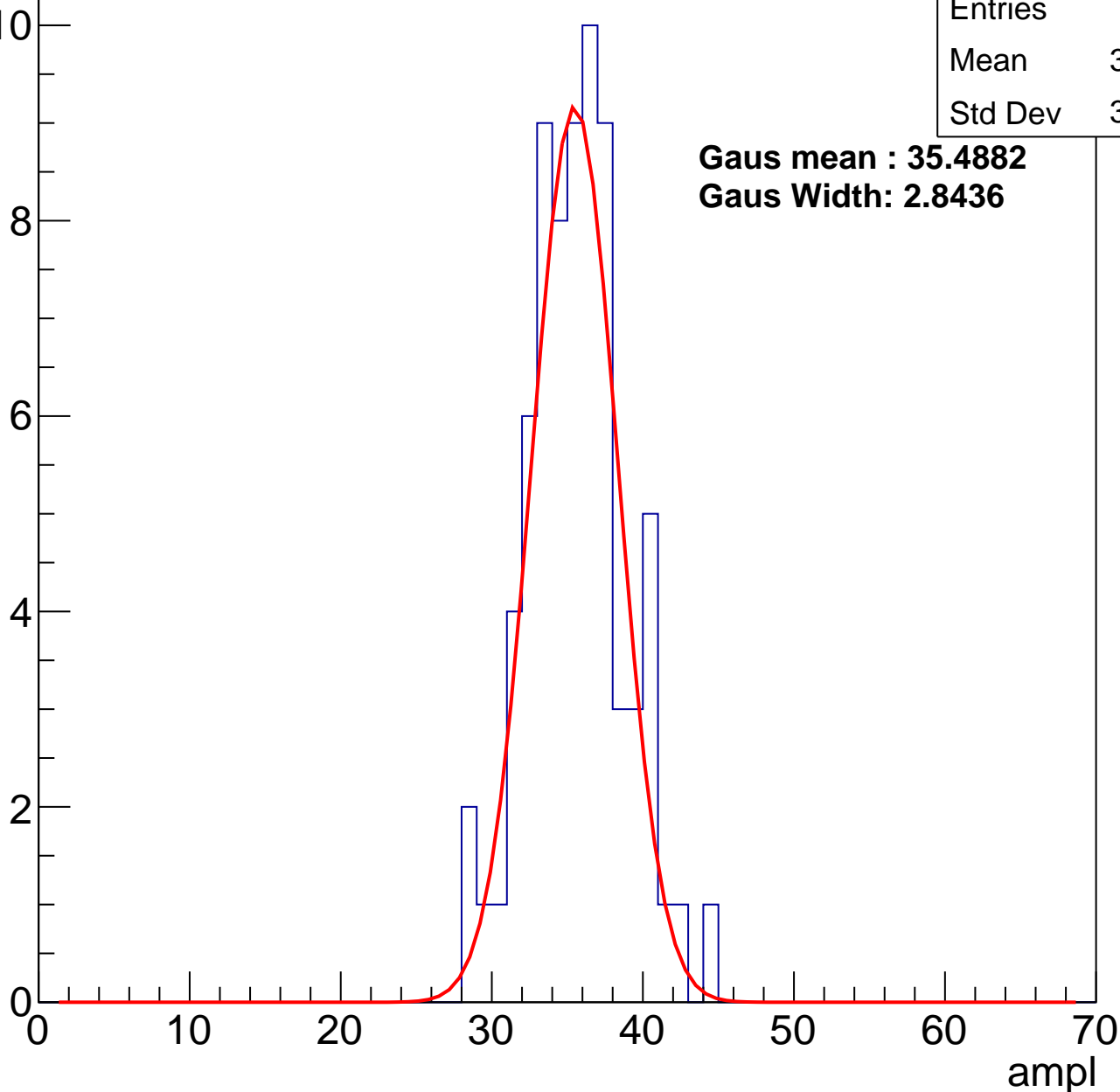
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	35.15
Std Dev	3.165

Gaus mean : 35.4882

Gaus Width: 2.8436



B1L002S, U7-ch43, adc2

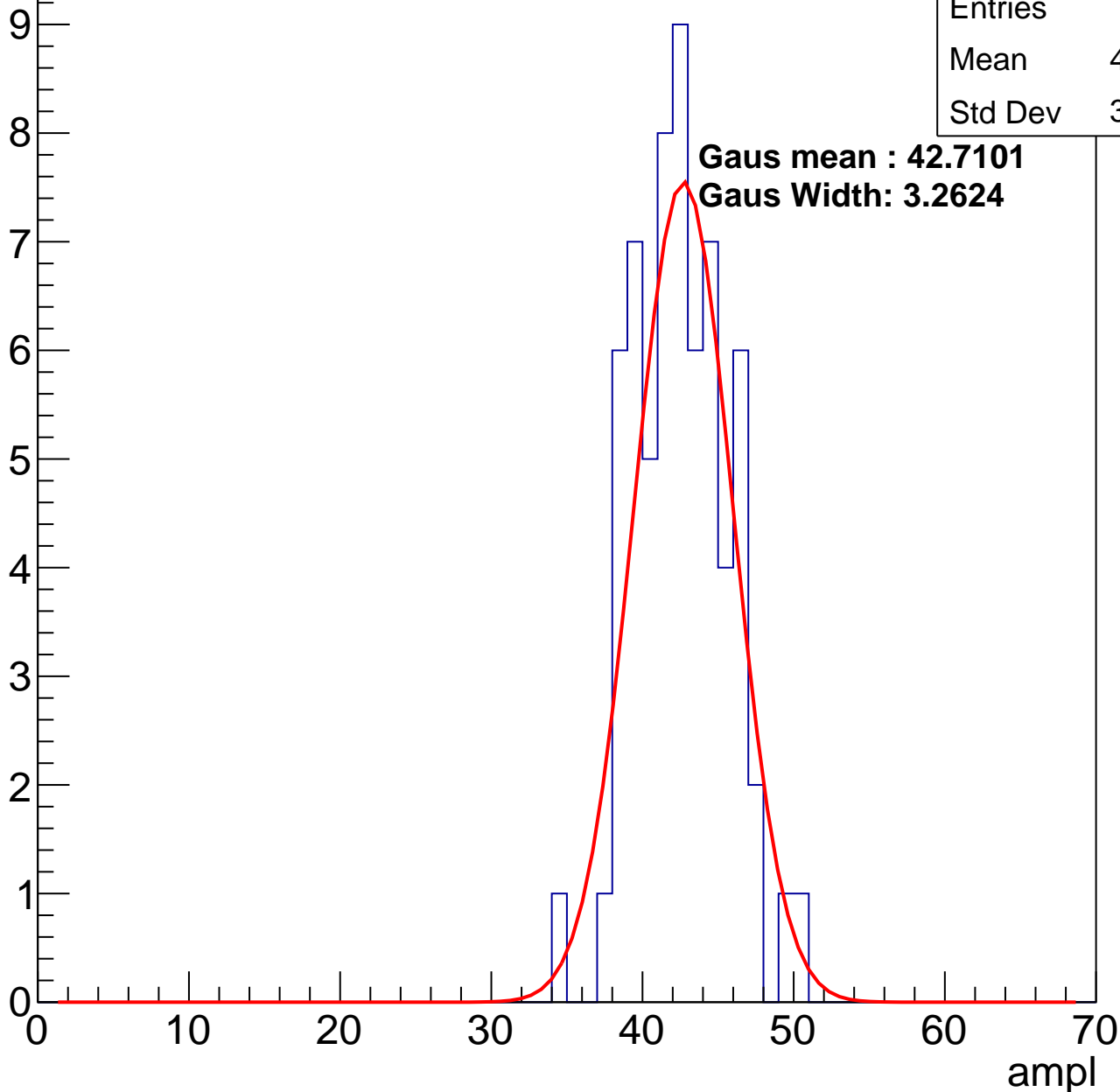
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	42.08
Std Dev	3.074

Gaus mean : 42.7101

Gaus Width: 3.2624

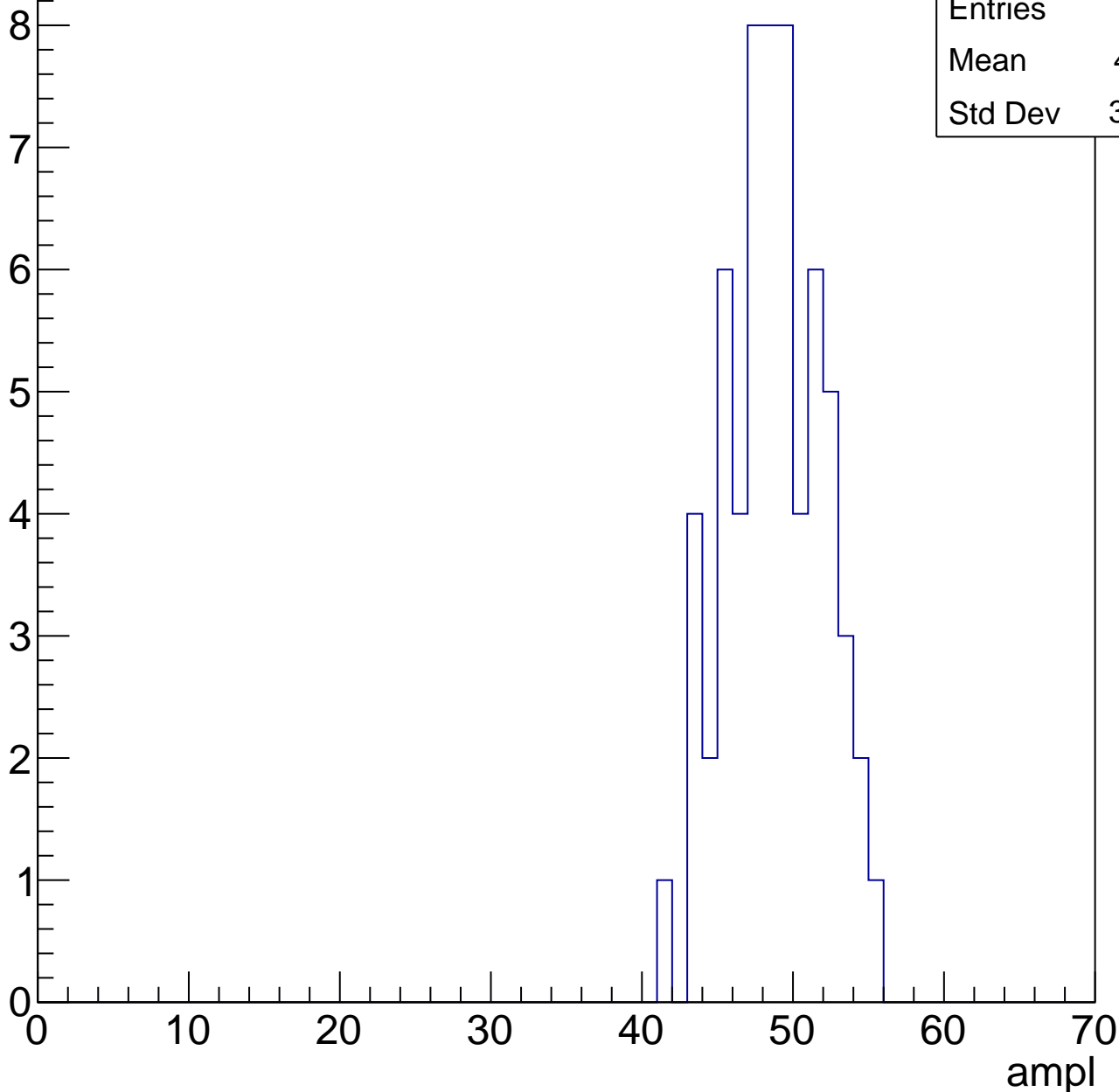


B1L002S, U7-ch43, adc3

calib_packv5_042523_0143.root, FC#10, port B3

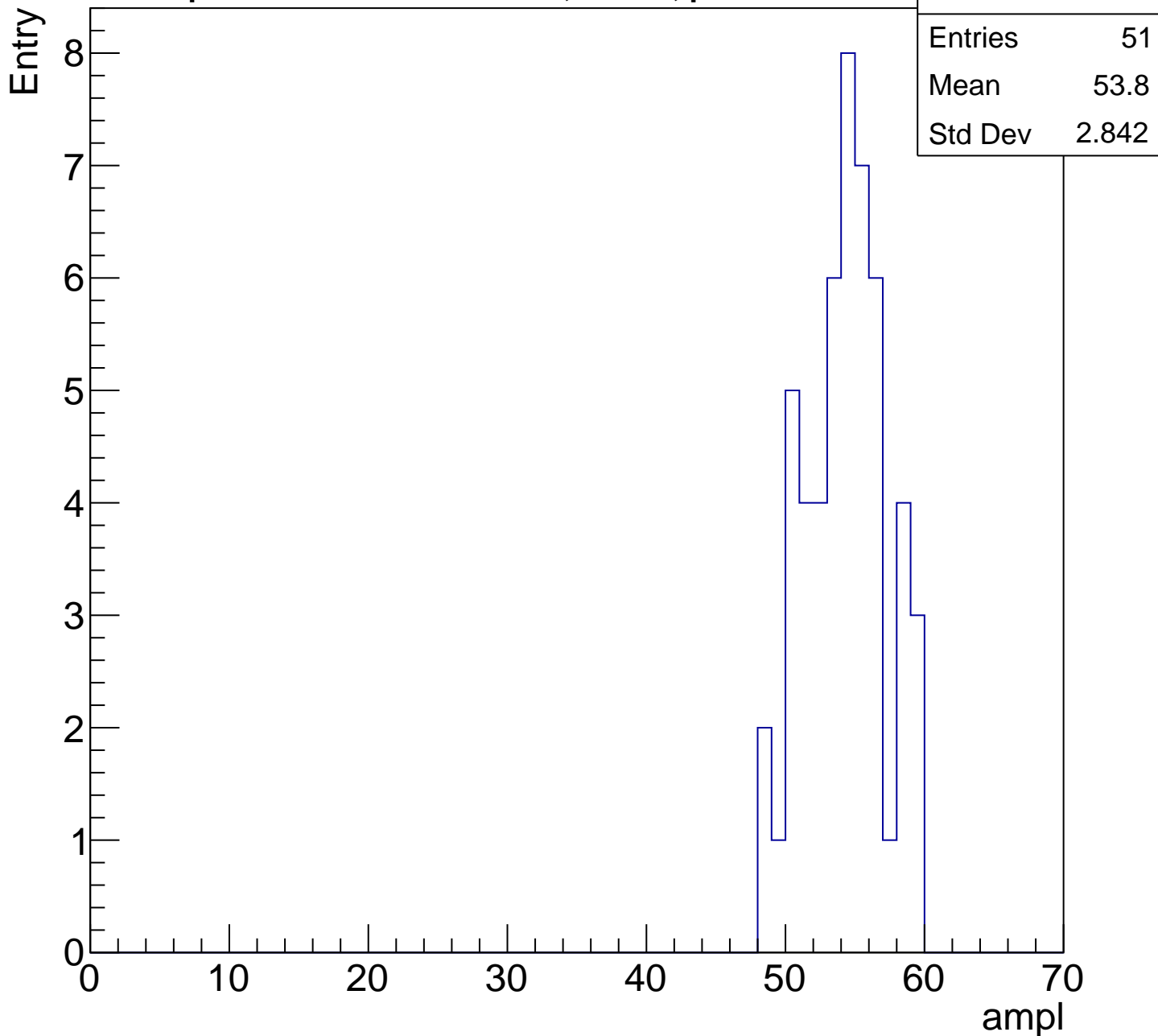
Entry

Entries	62
Mean	48.31
Std Dev	3.129



B1L002S, U7-ch43, adc4

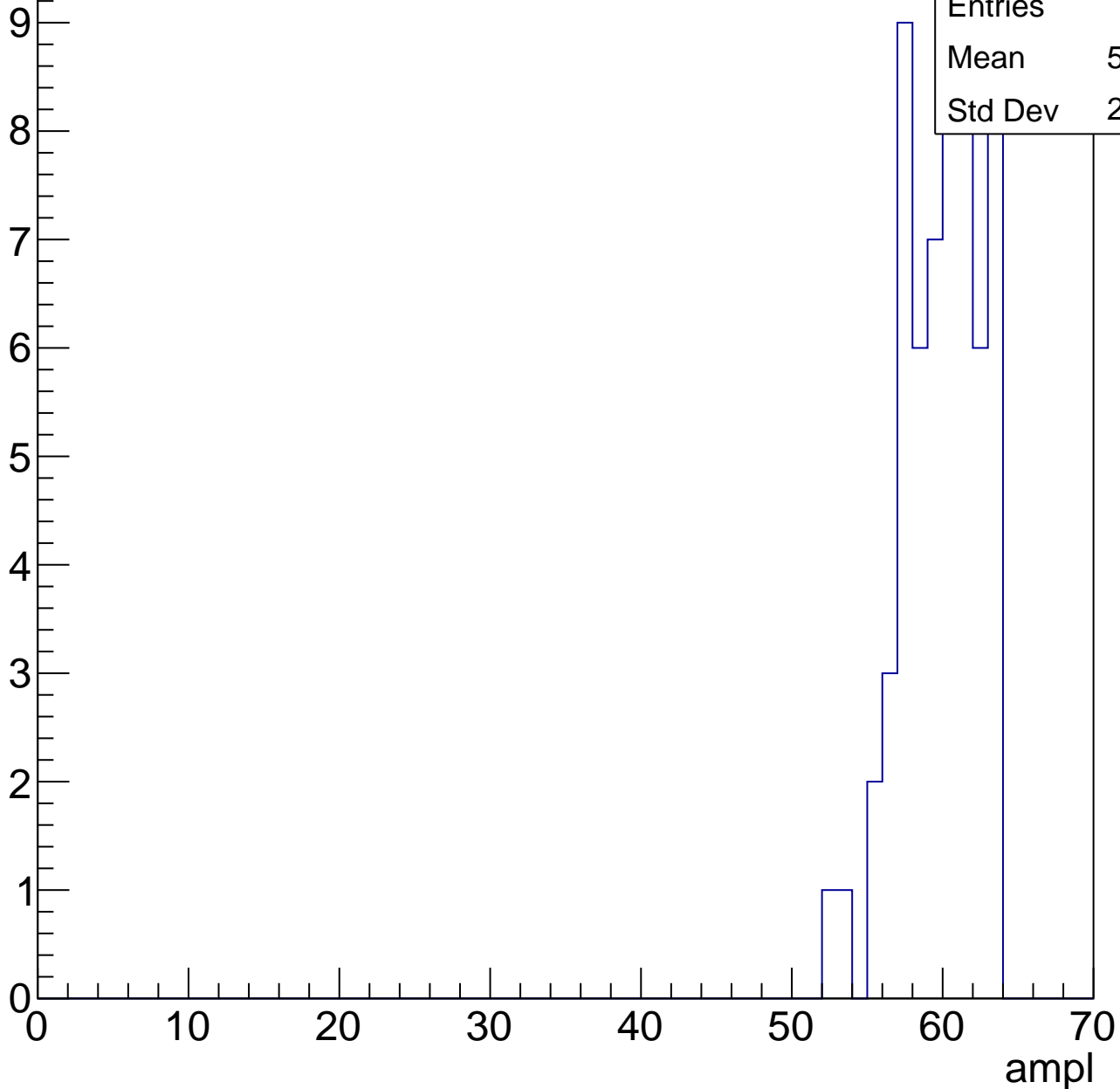
calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch43, adc5

calib_packv5_042523_0143.root, FC#10, port B3

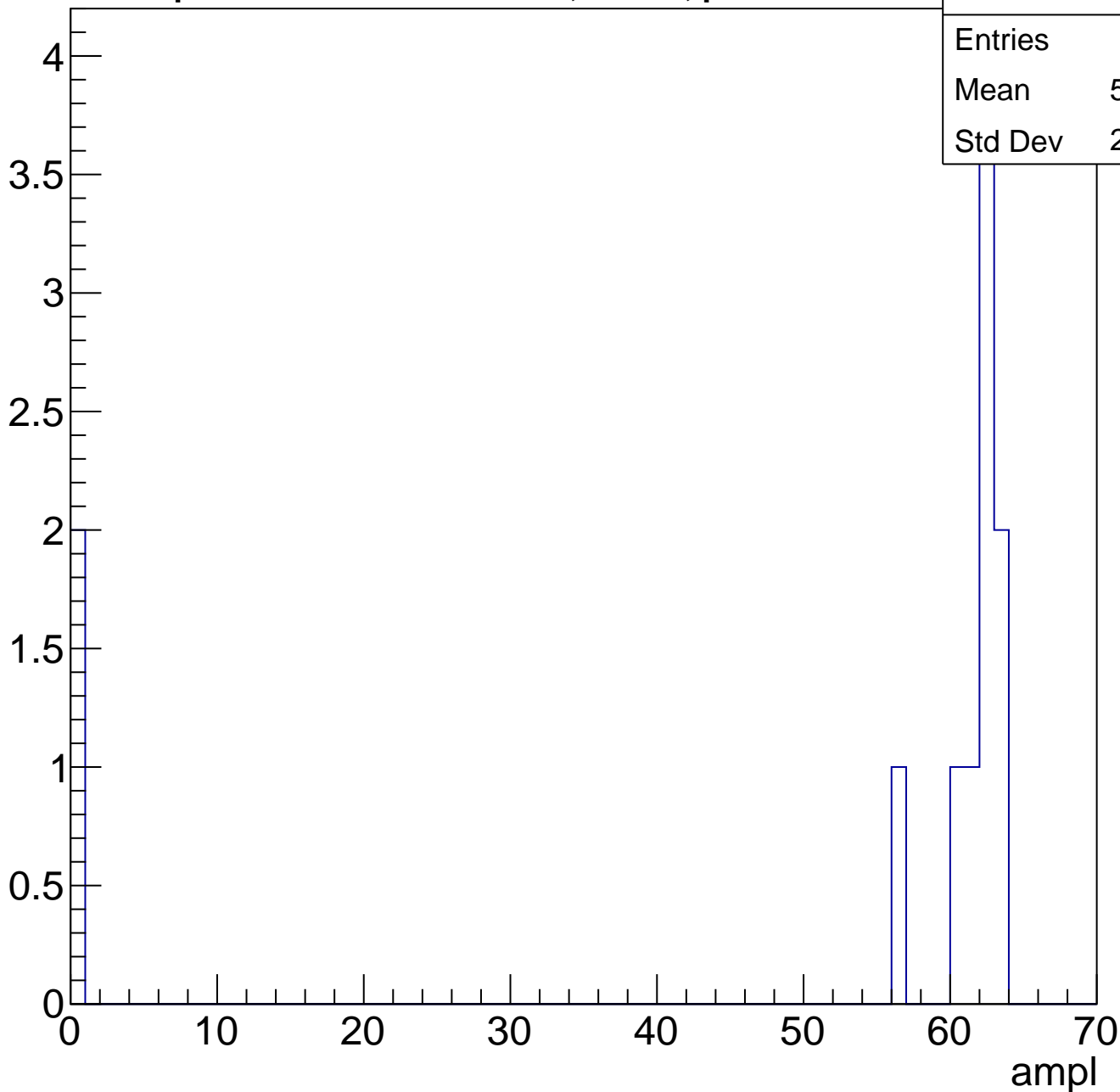
Entry



B1L002S, U7-ch43, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch43, adc7

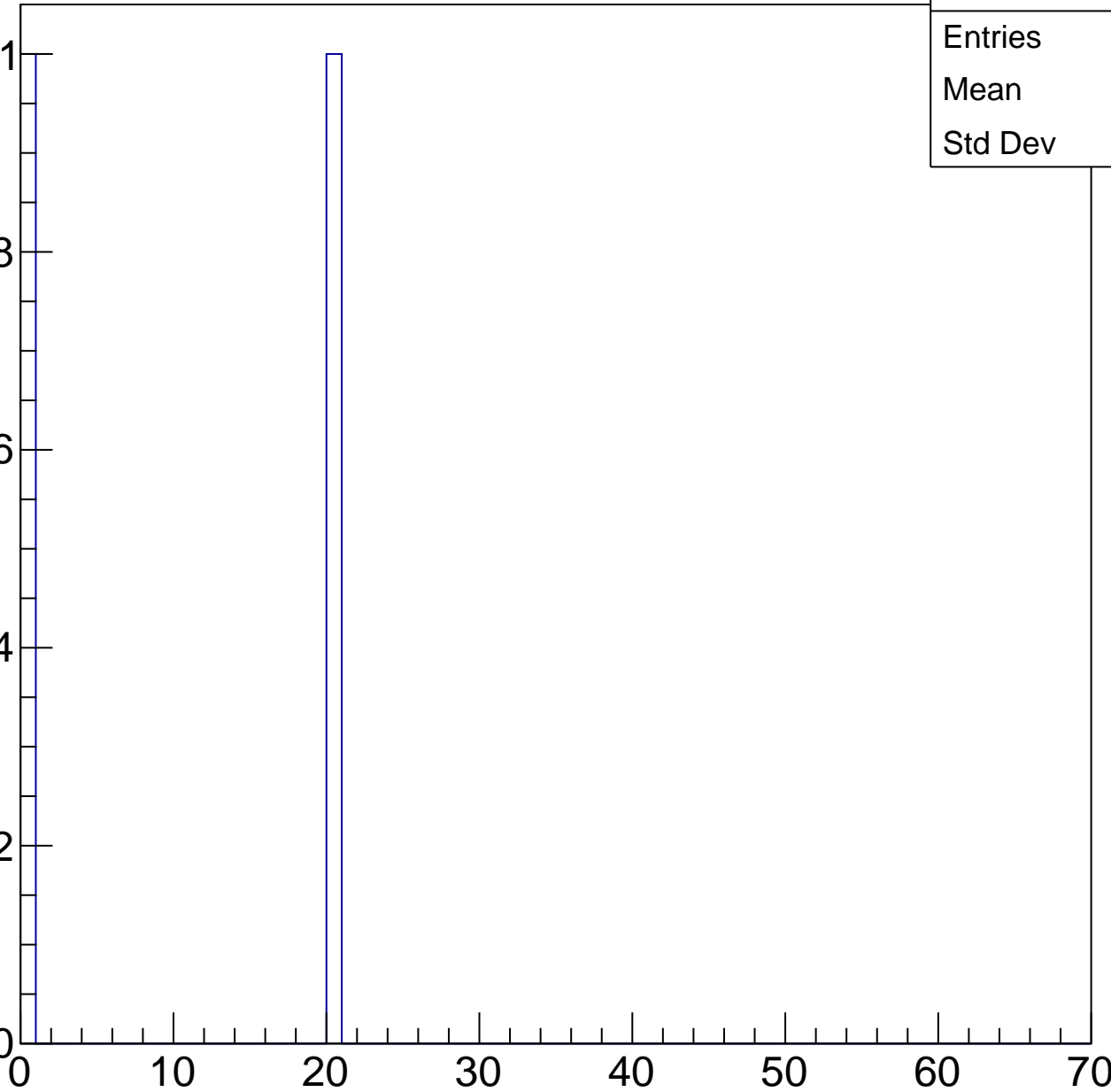
calib_packv5_042523_0143.root, FC#10, port B3

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B1L002S, U7-ch44, adc0

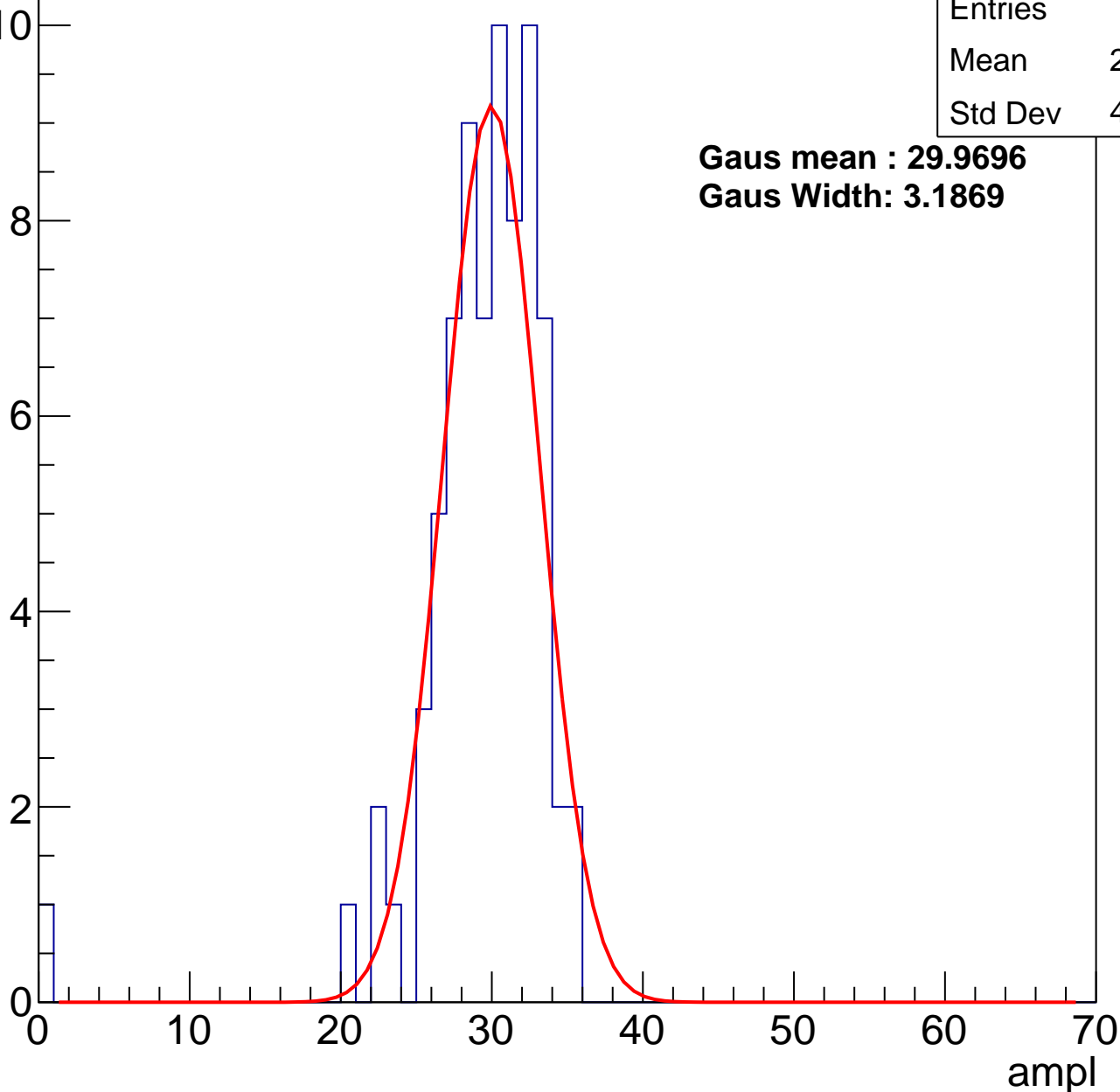
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	28.97
Std Dev	4.549

Gaus mean : 29.9696

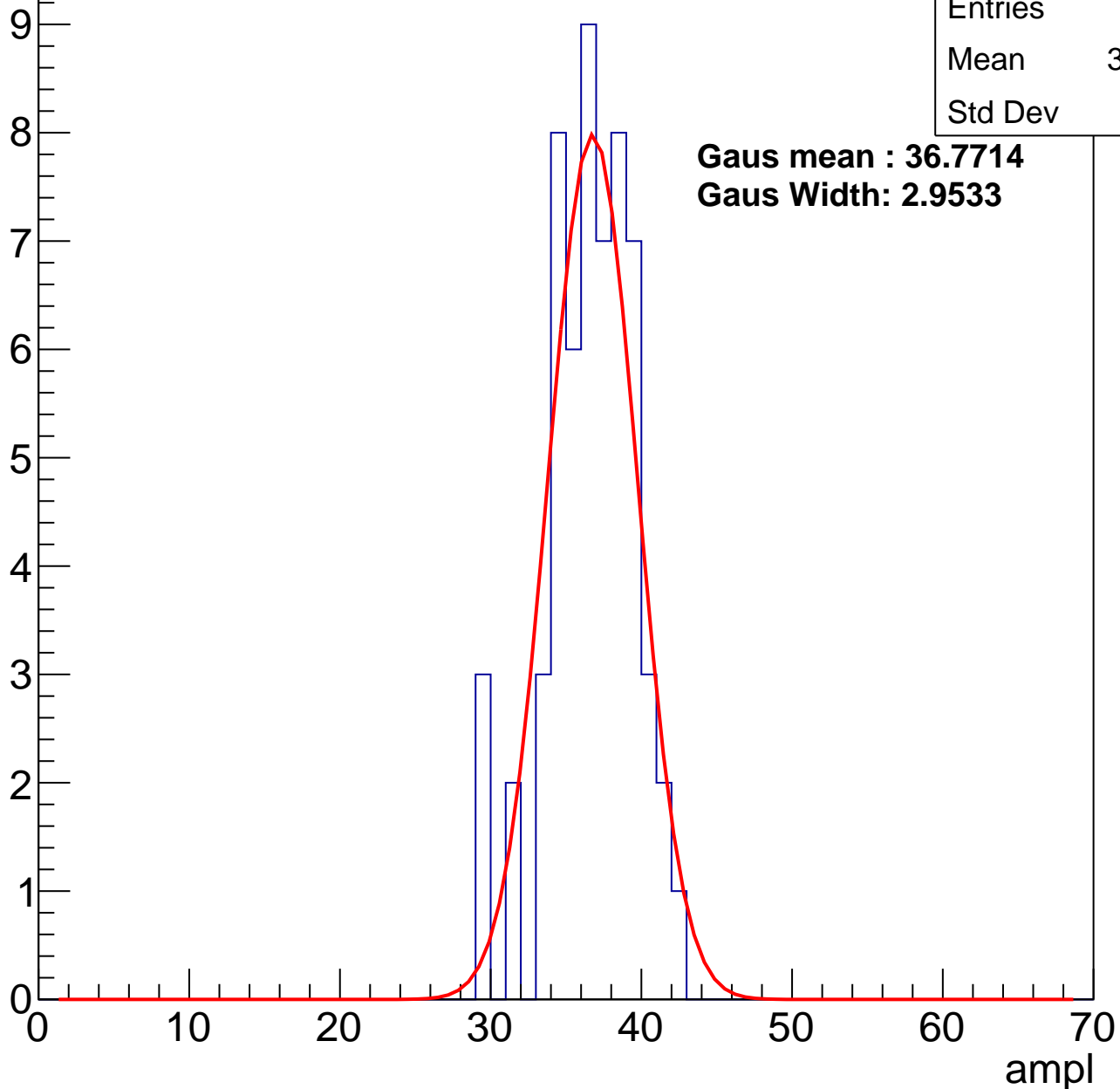
Gaus Width: 3.1869



B1L002S, U7-ch44, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	59
Mean	36.17
Std Dev	2.9

B1L002S, U7-ch44, adc2

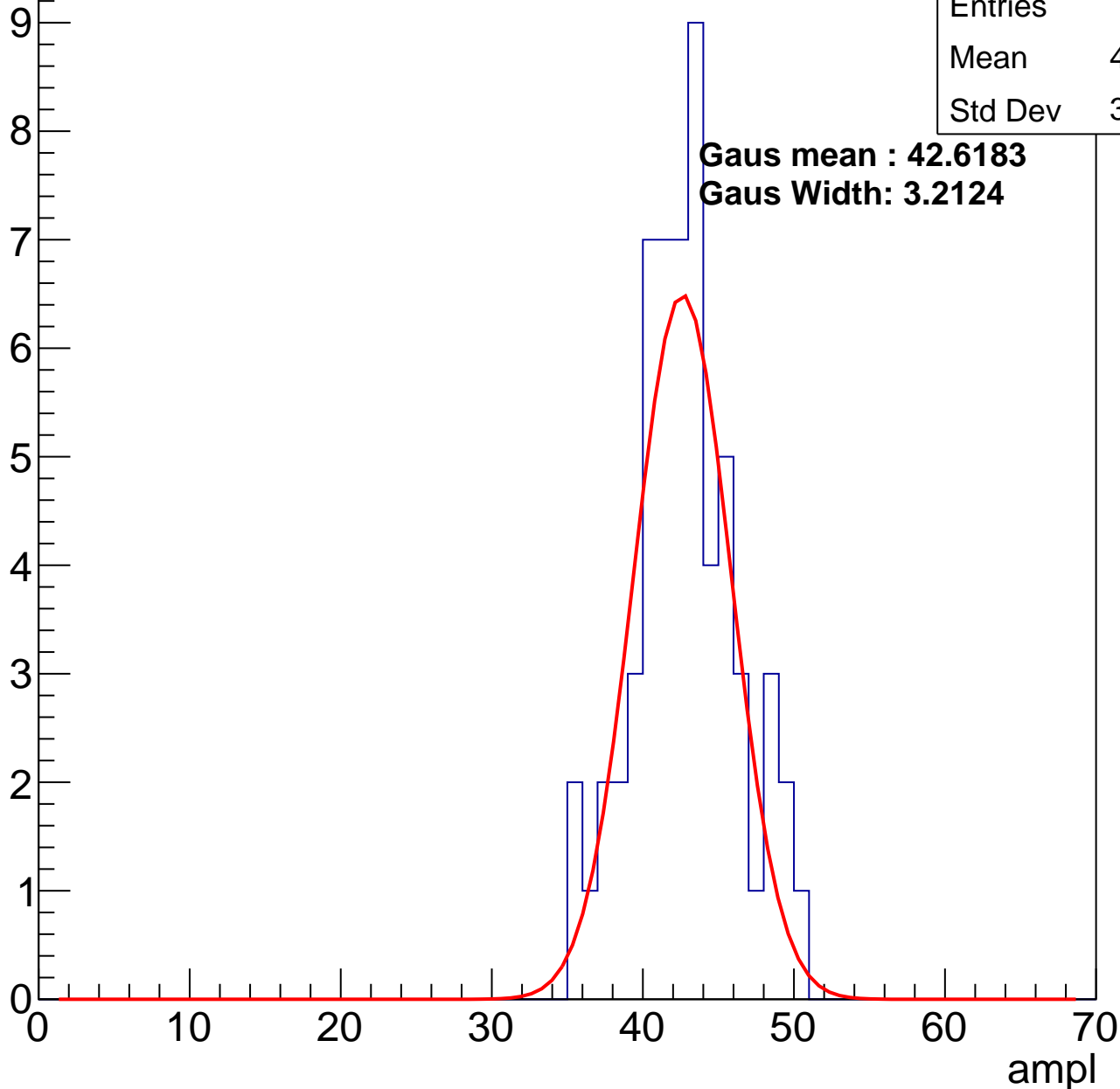
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.36
Std Dev	3.404

Gaus mean : 42.6183

Gaus Width: 3.2124

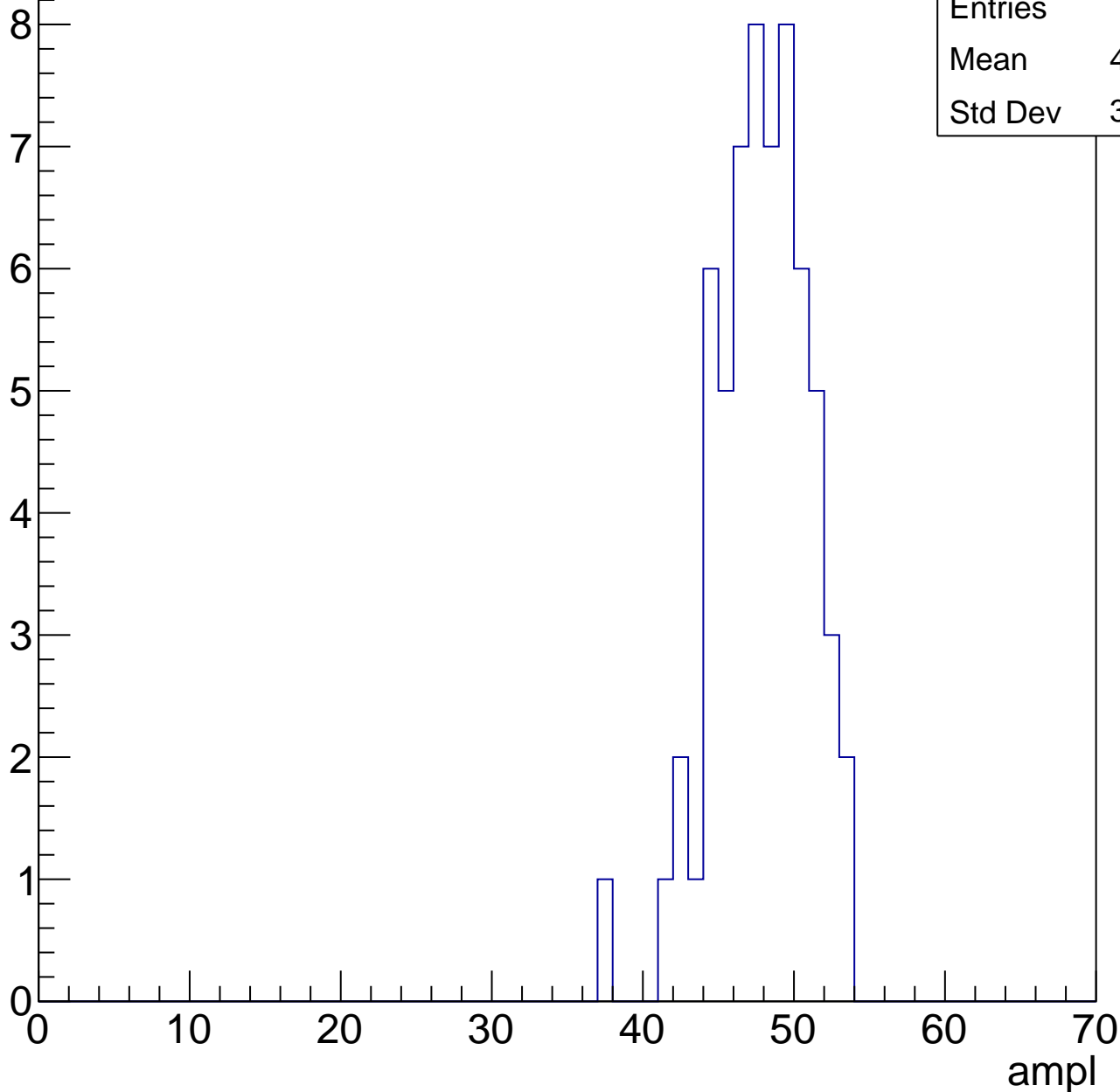


B1L002S, U7-ch44, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	47.37
Std Dev	3.096

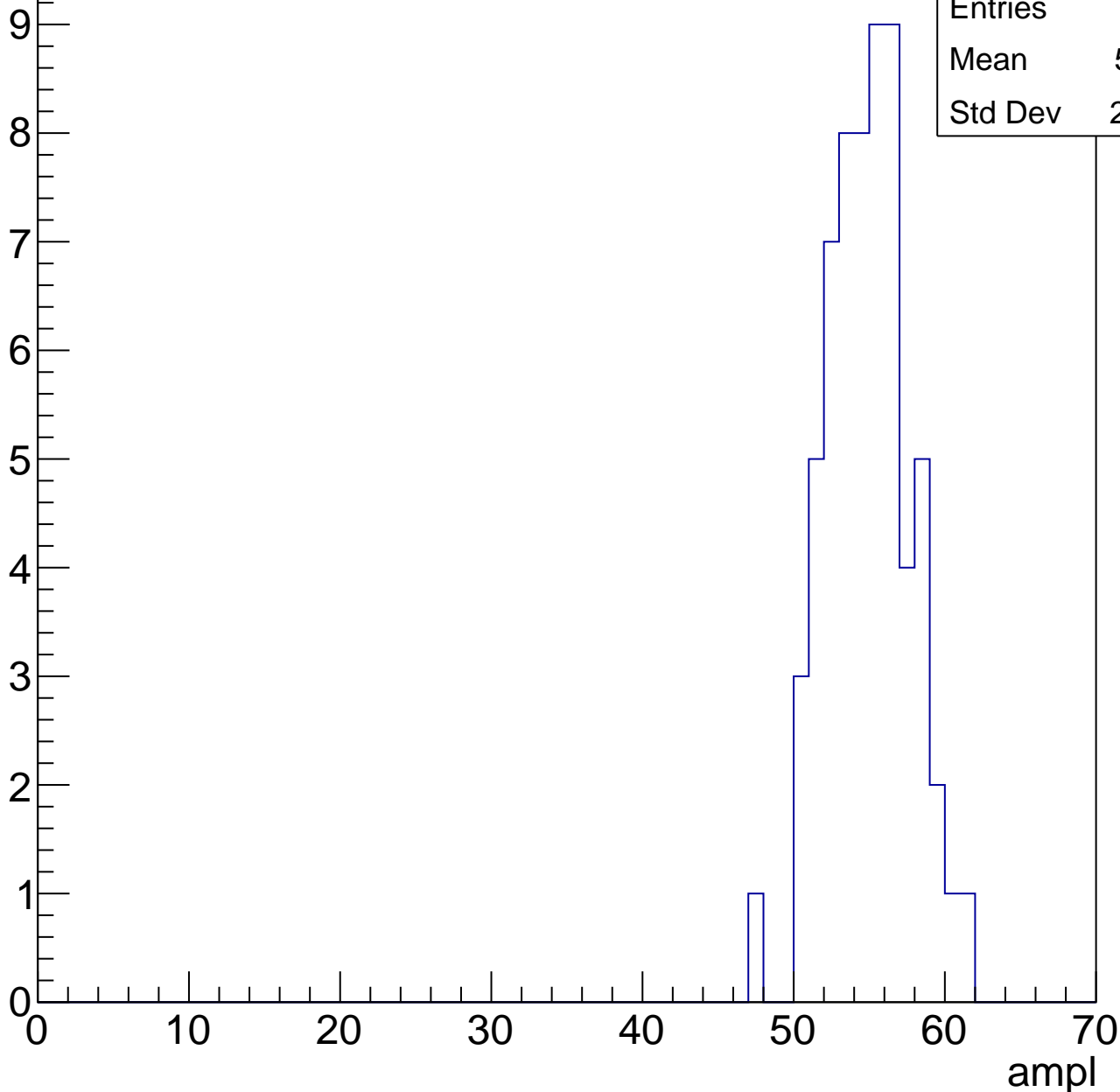


B1L002S, U7-ch44, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	54.41
Std Dev	2.712



B1L002S, U7-ch44, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 59.42

Std Dev 2.433

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

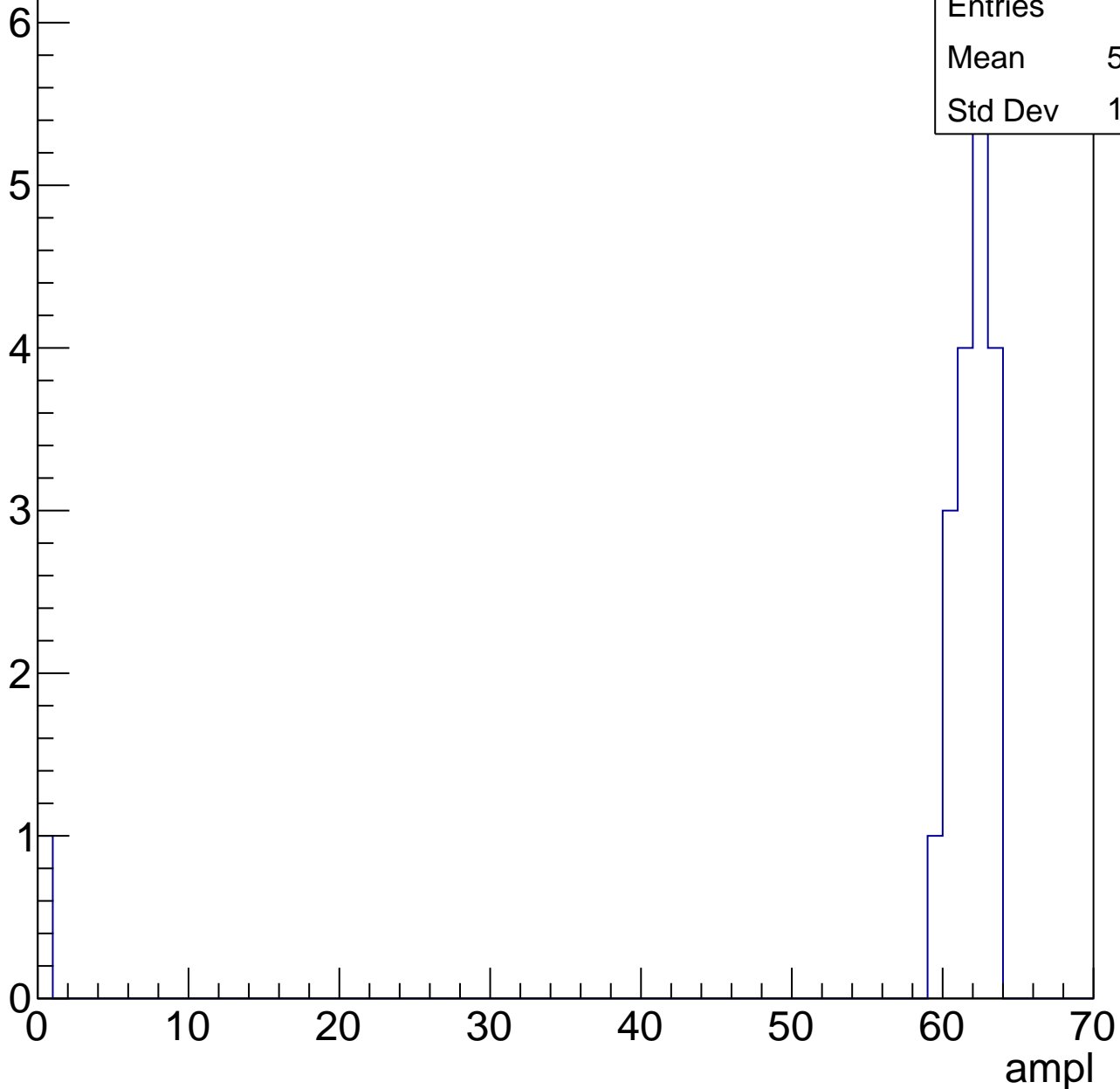
7

B1L002S, U7-ch44, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.26
Std Dev	13.78



B1L002S, U7-ch44, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch45, adc0

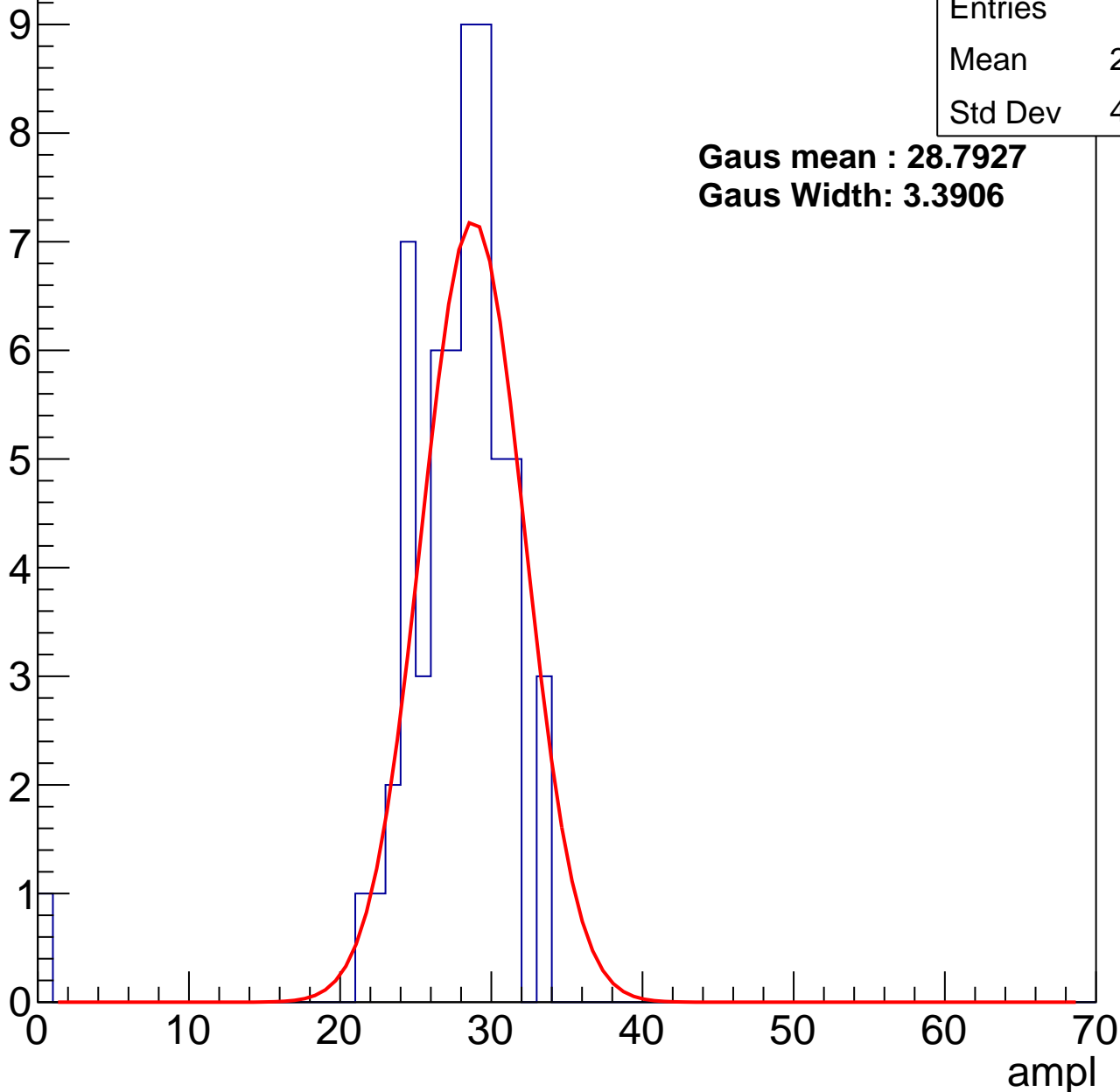
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.02
Std Dev	4.512

Gaus mean : 28.7927

Gaus Width: 3.3906



B1L002S, U7-ch45, adc1

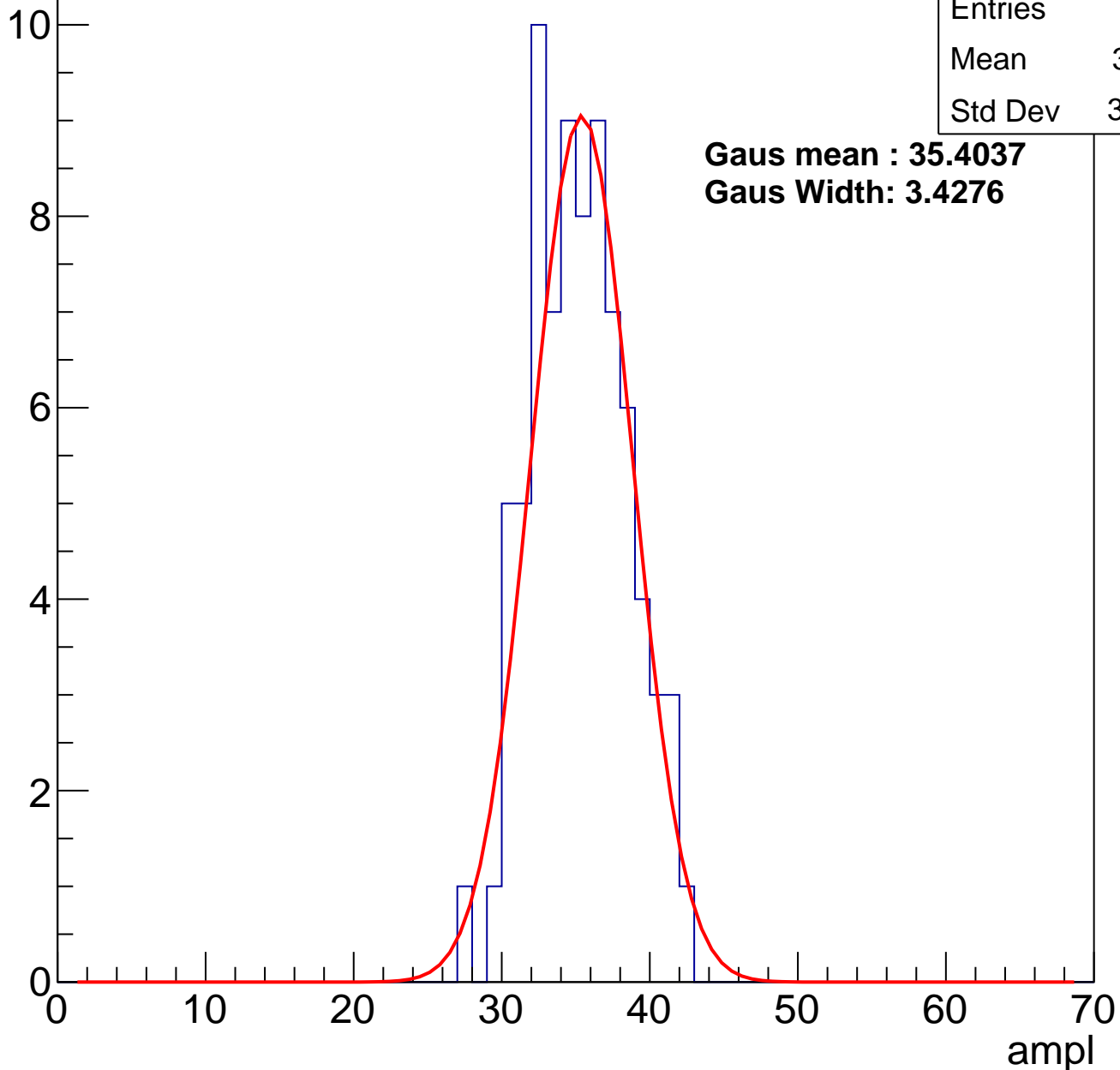
calib_packv5_042523_0143.root, FC#10, port B3

Entries	79
Mean	34.81
Std Dev	3.214

Gaus mean : 35.4037

Gaus Width: 3.4276

Entry



B1L002S, U7-ch45, adc2

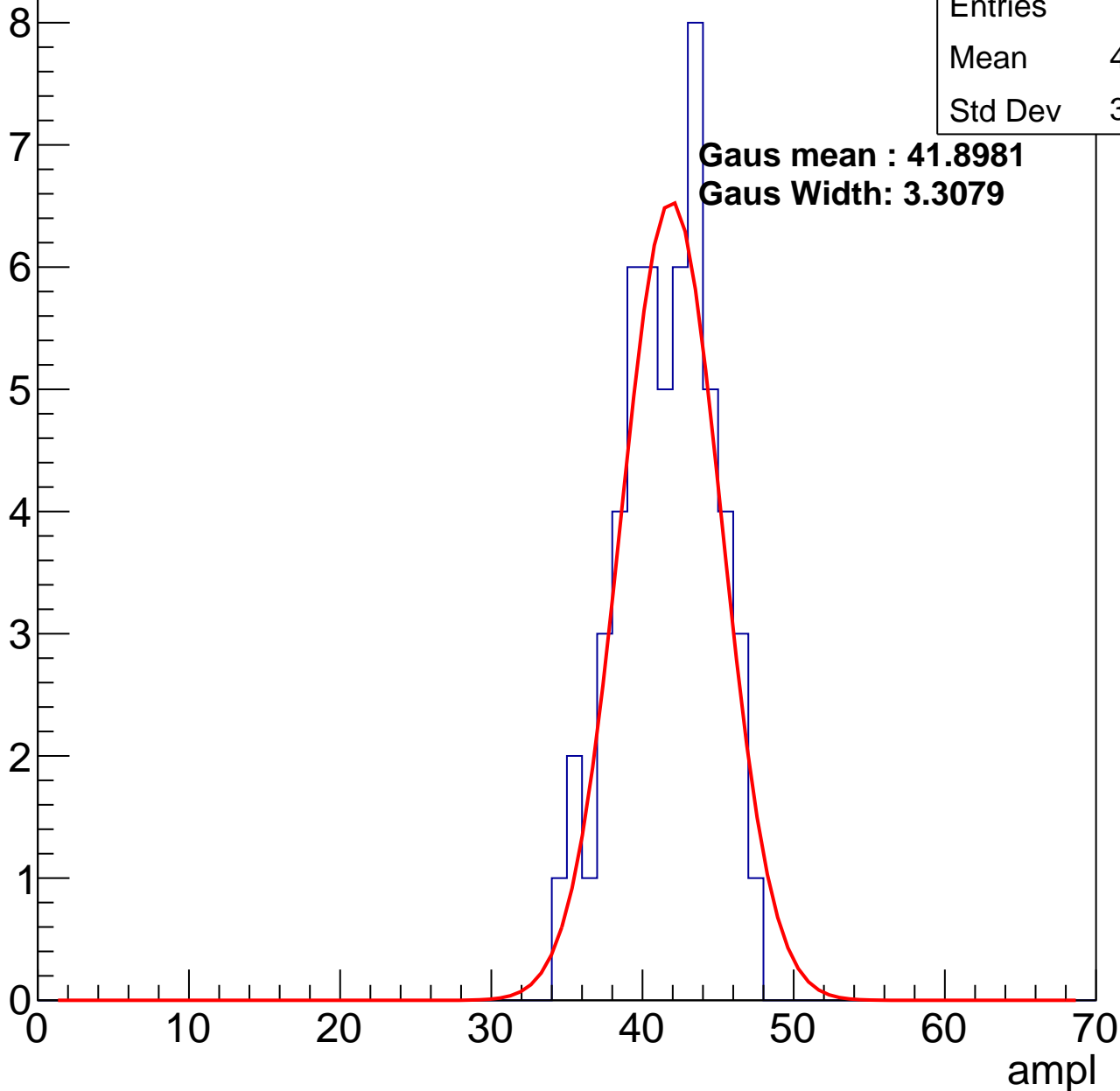
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.15
Std Dev	3.065

Gaus mean : 41.8981

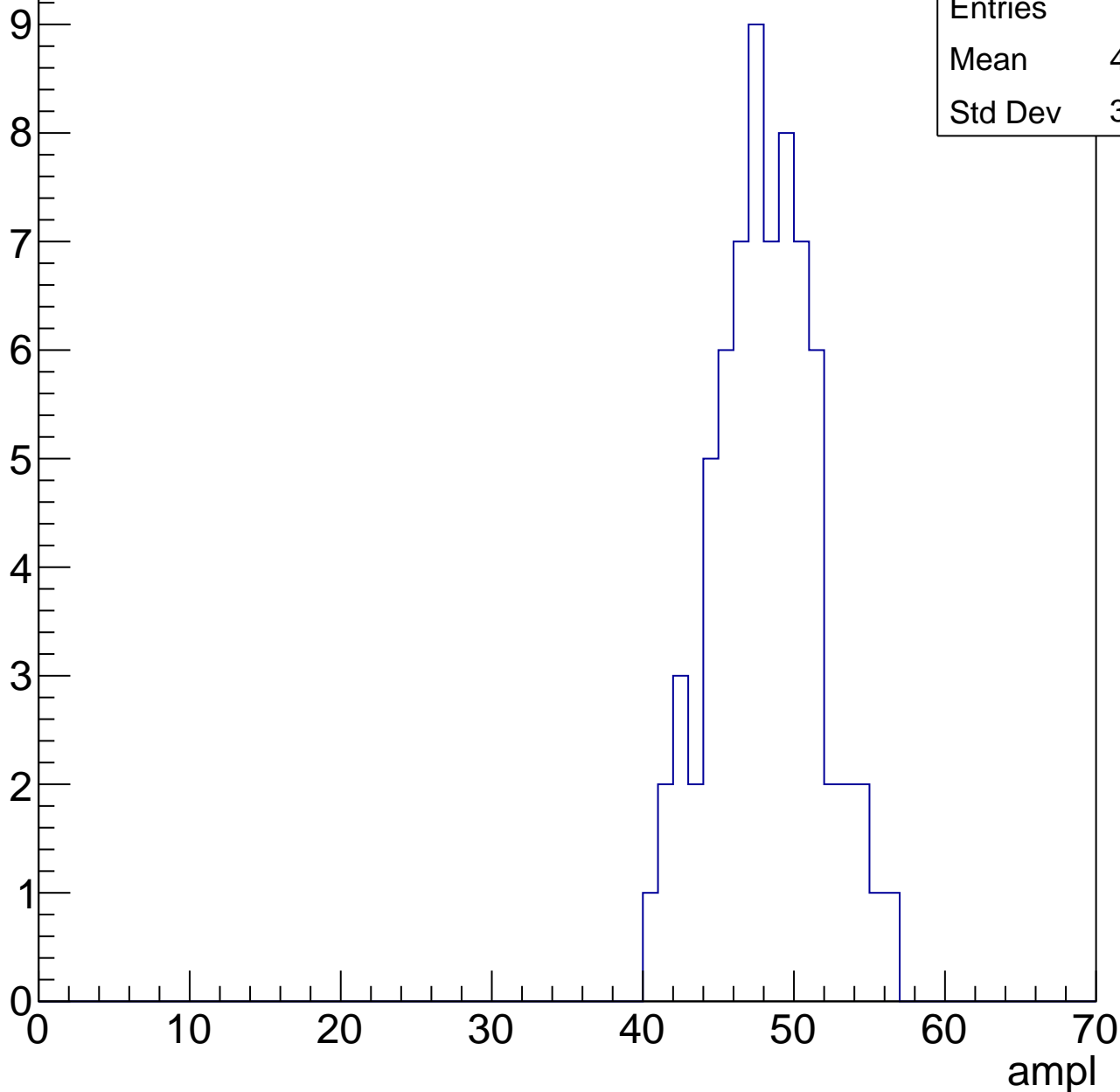
Gaus Width: 3.3079



B1L002S, U7-ch45, adc3

calib_packv5_042523_0143.root, FC#10, port B3

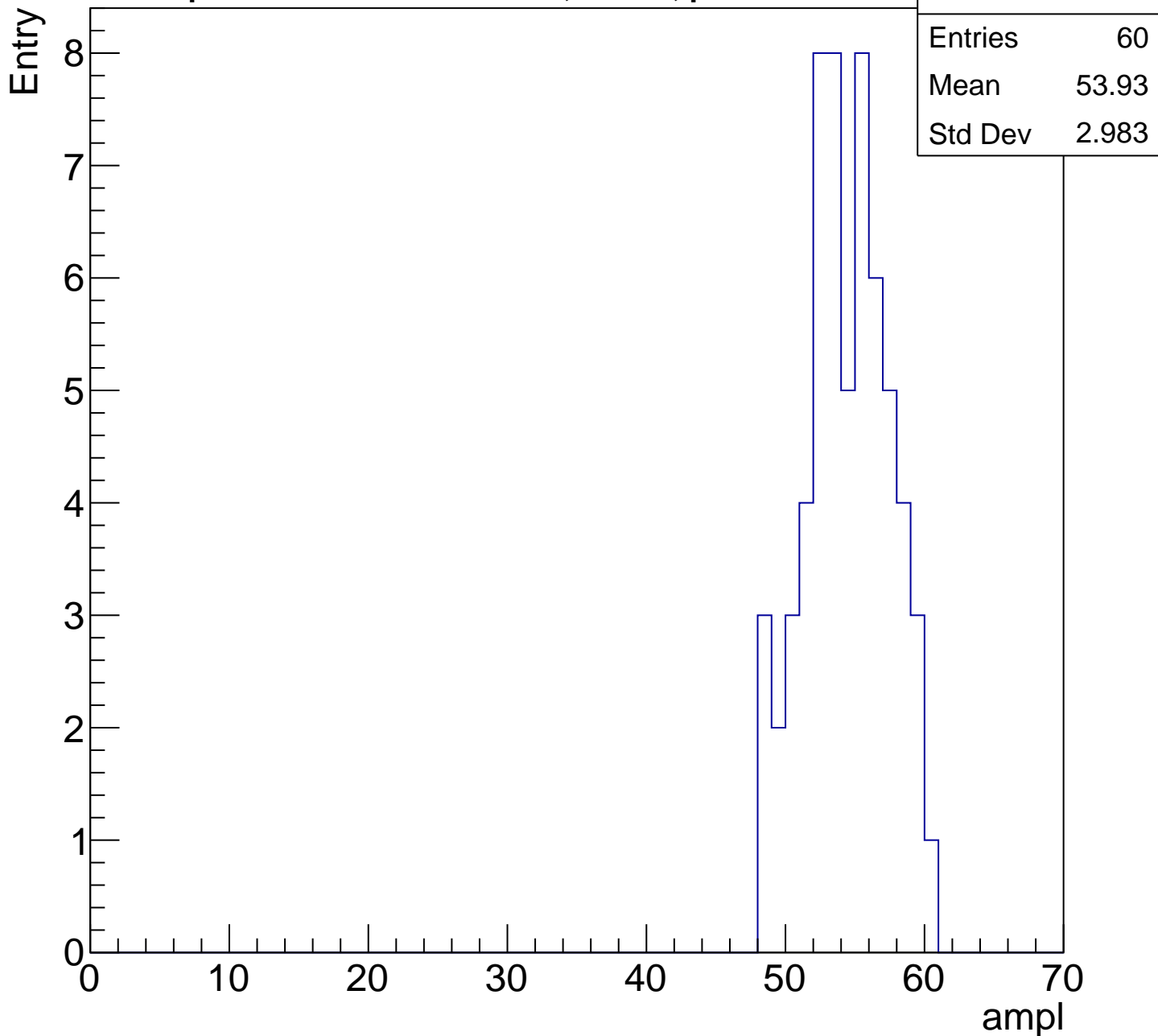
Entry



Entries	71
Mean	47.63
Std Dev	3.437

B1L002S, U7-ch45, adc4

calib_packv5_042523_0143.root, FC#10, port B3

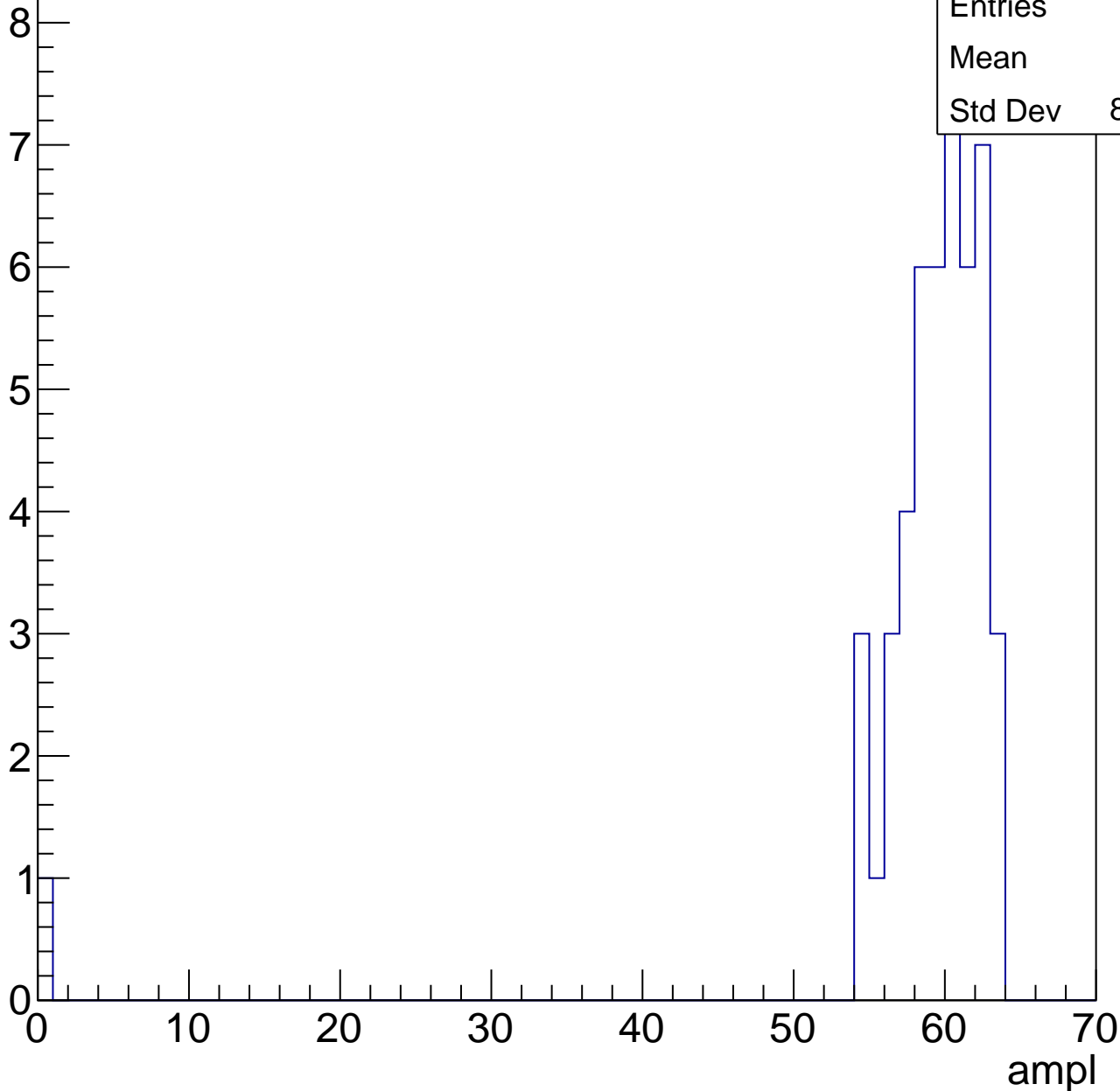


B1L002S, U7-ch45, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58
Std Dev	8.799

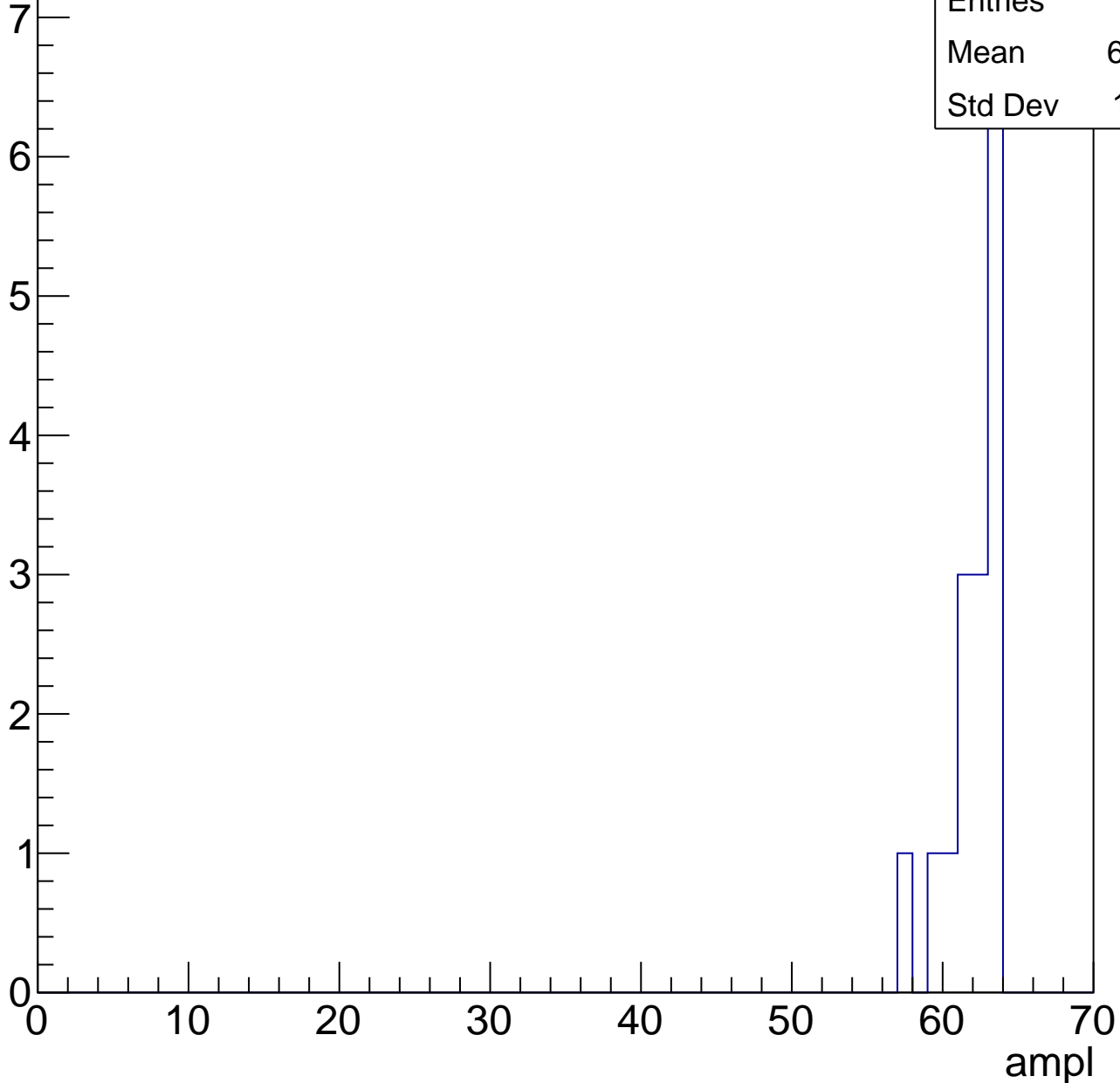


B1L002S, U7-ch45, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

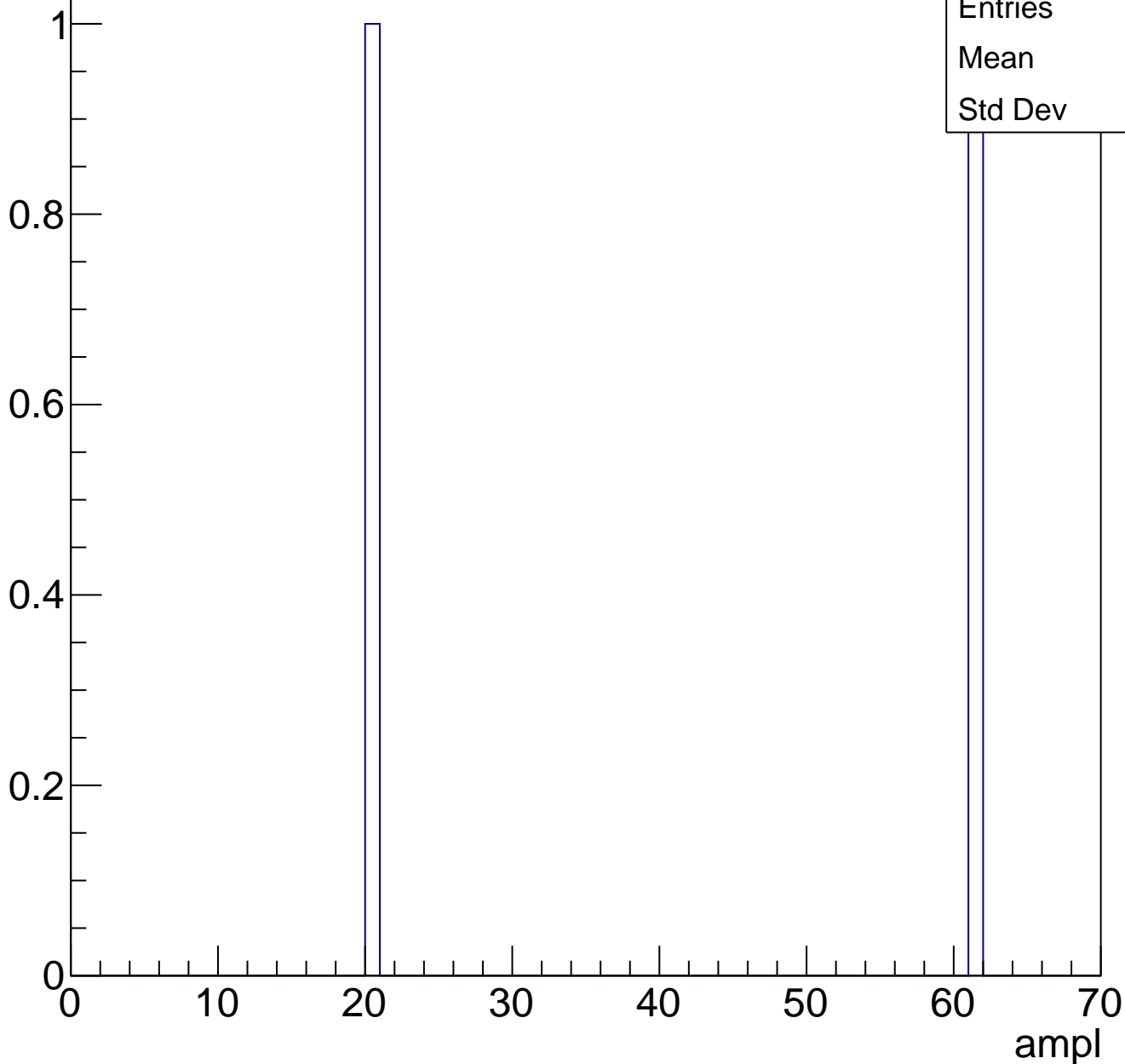
Entries	16
Mean	61.62
Std Dev	1.691



B1L002S, U7-ch45, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch46, adc0

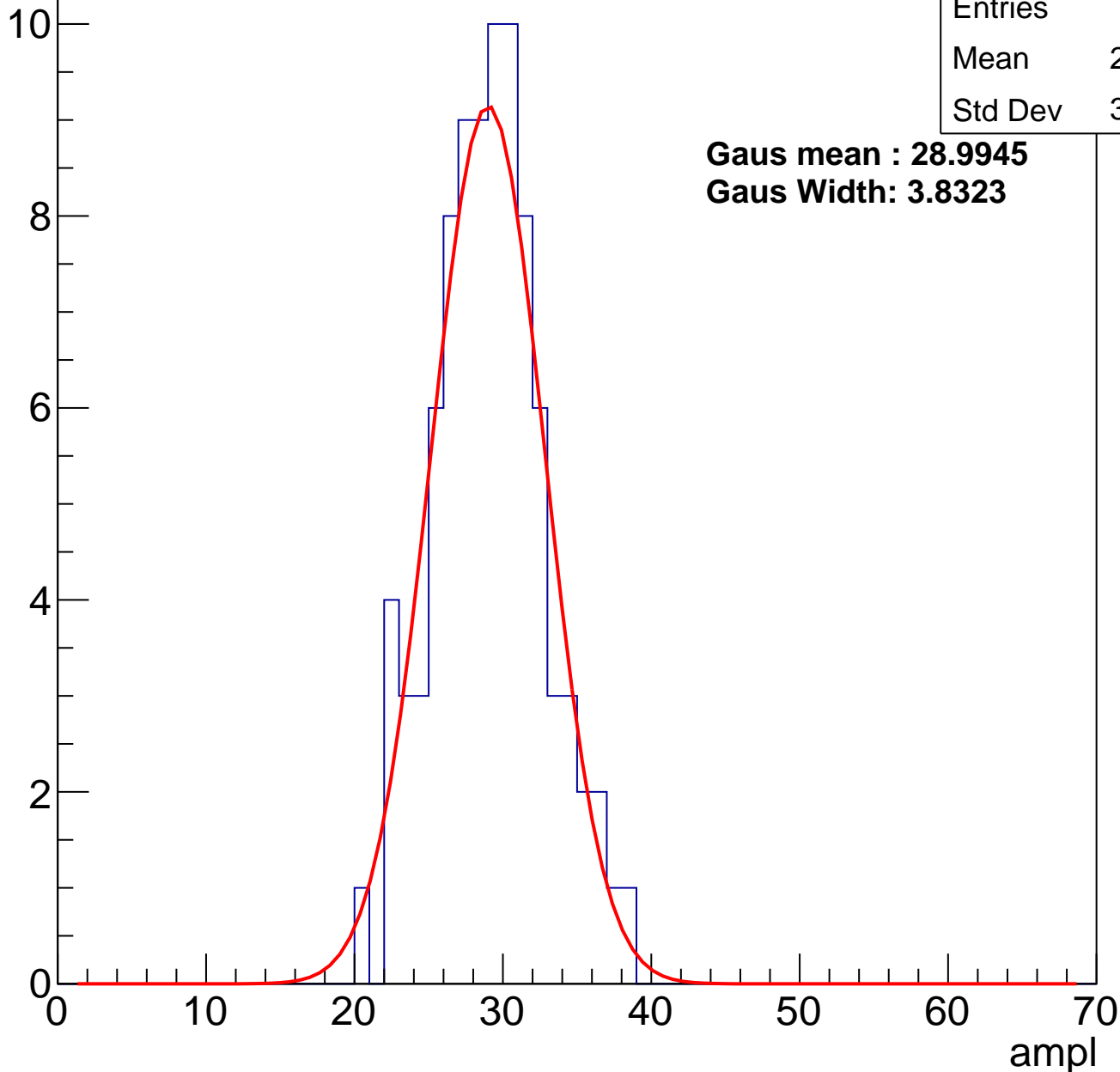
calib_packv5_042523_0143.root, FC#10, port B3

Entries	89
Mean	28.65
Std Dev	3.666

Gaus mean : 28.9945

Gaus Width: 3.8323

Entry



B1L002S, U7-ch46, adc1

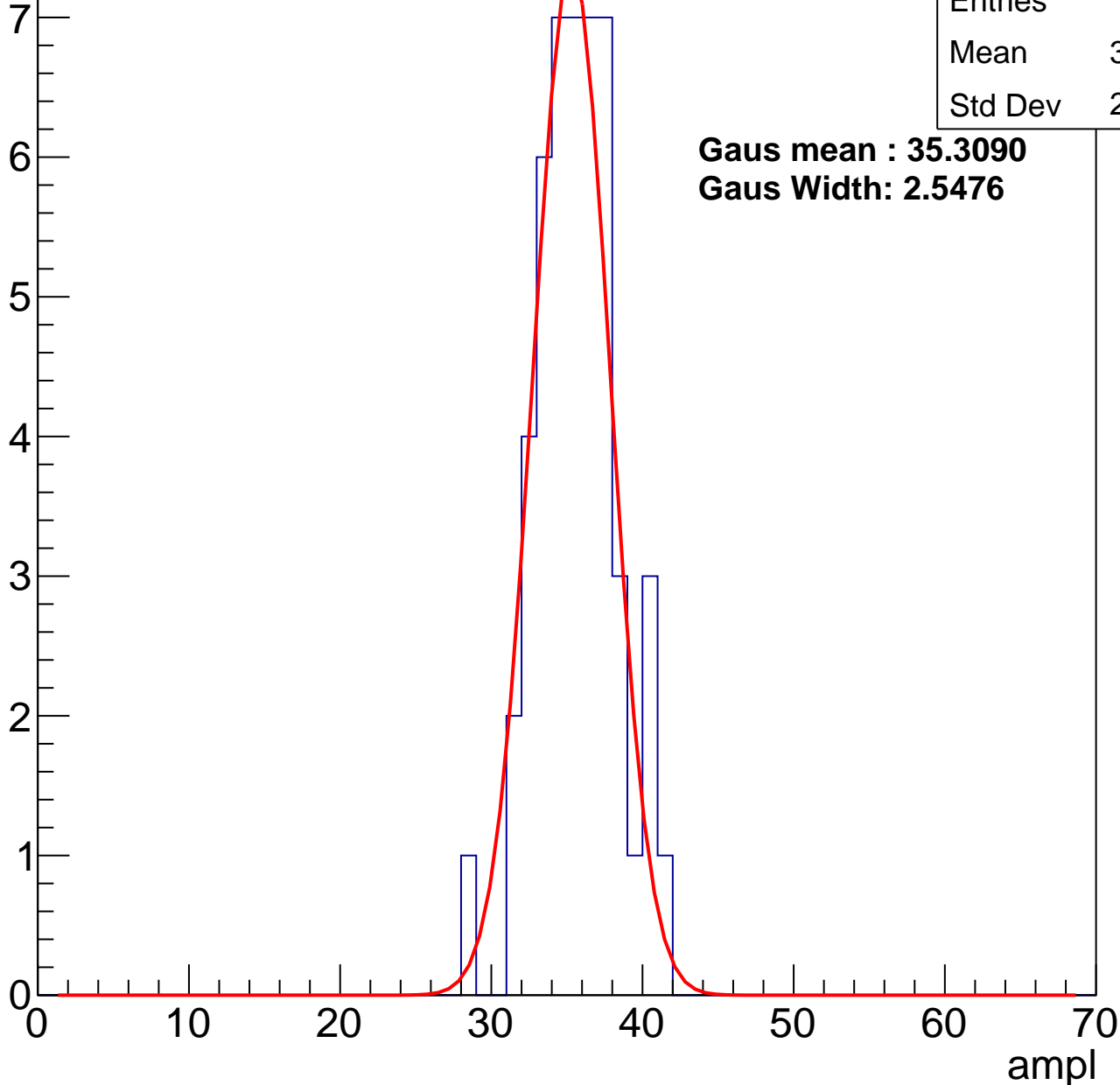
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	35.18
Std Dev	2.616

Gaus mean : 35.3090

Gaus Width: 2.5476



B1L002S, U7-ch46, adc2

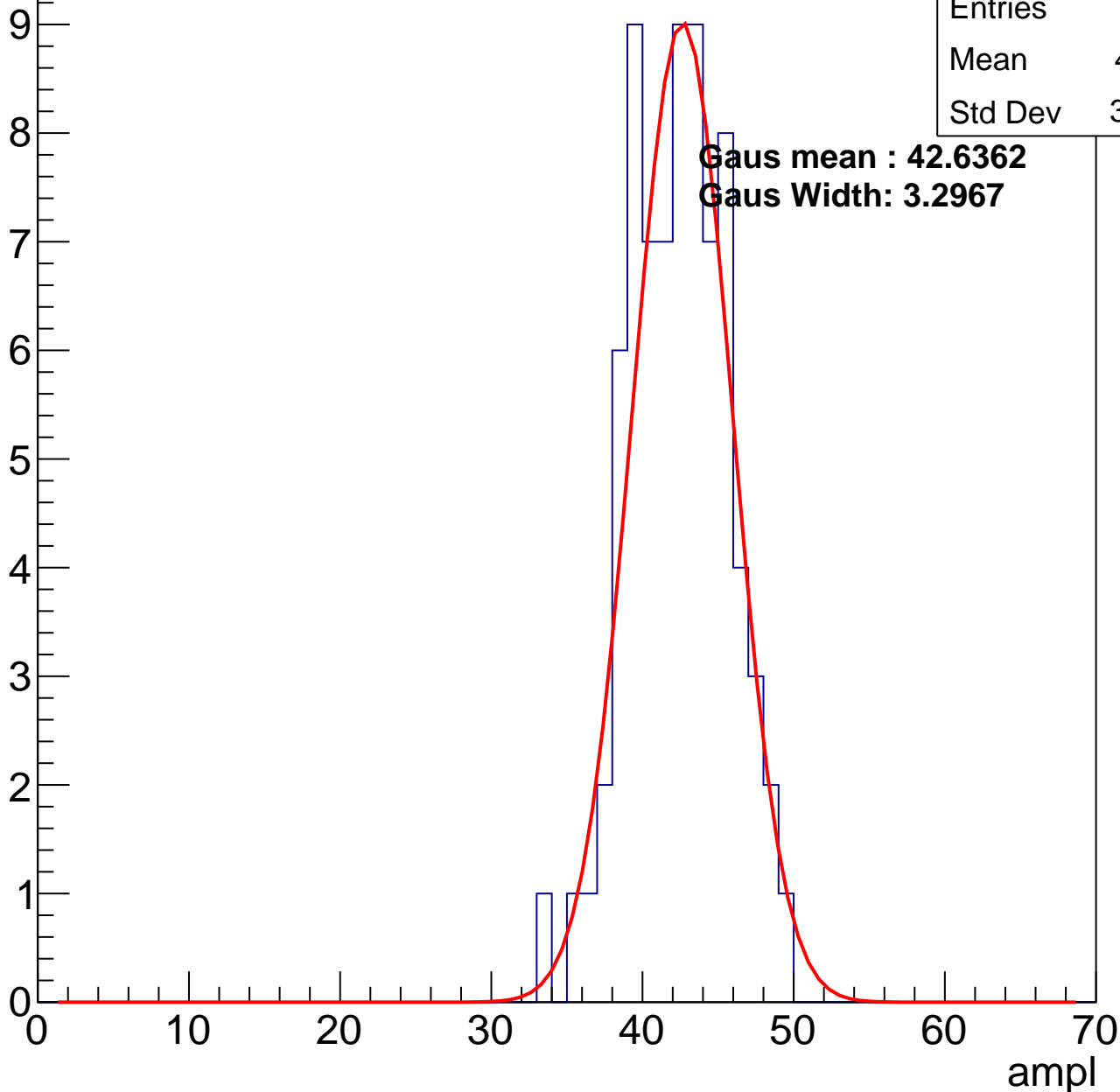
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	41.91
Std Dev	3.216

Gaus mean : 42.6362

Gaus Width: 3.2967

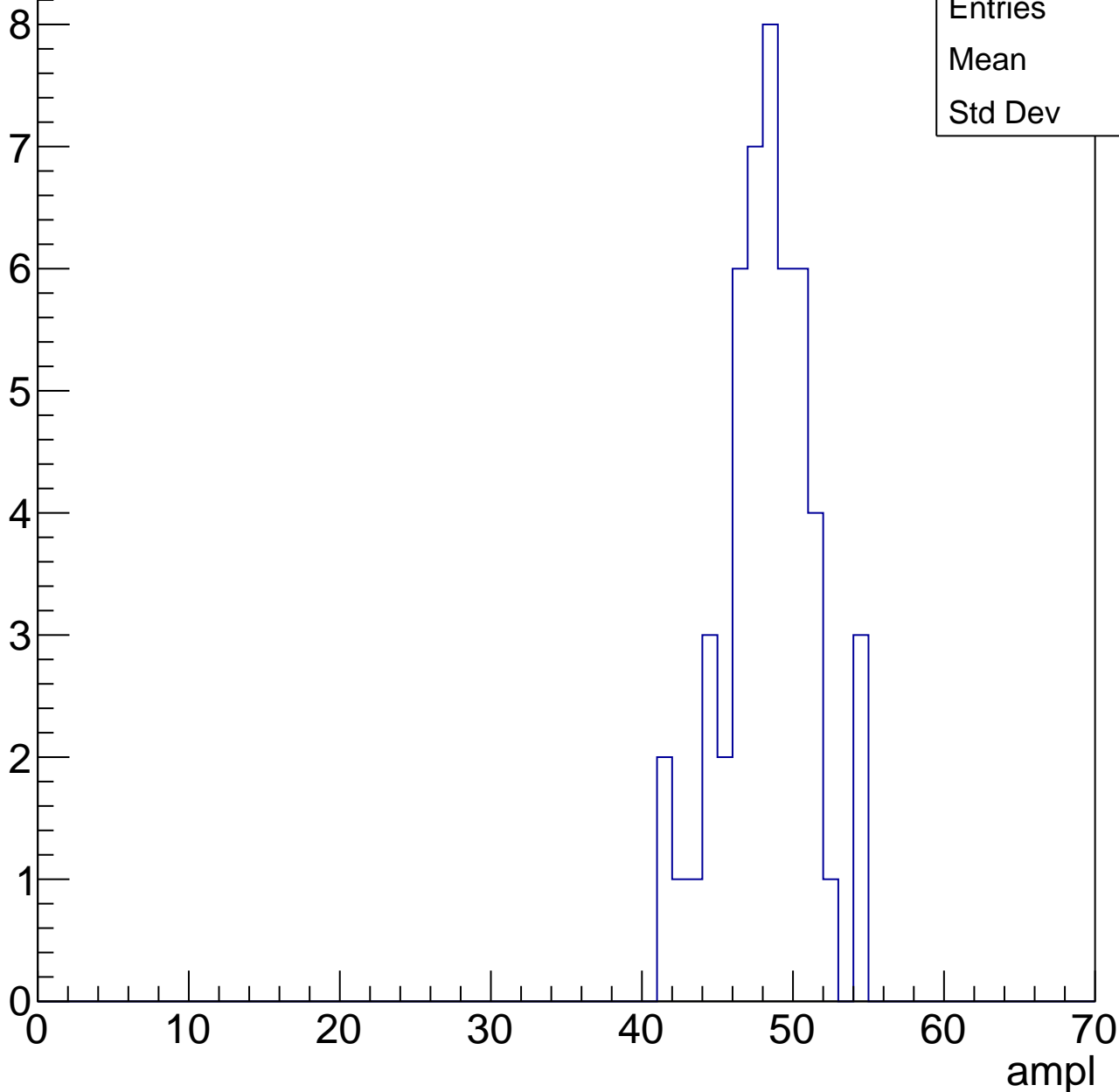


B1L002S, U7-ch46, adc3

calib_packv5_042523_0143.root, FC#10, port B3

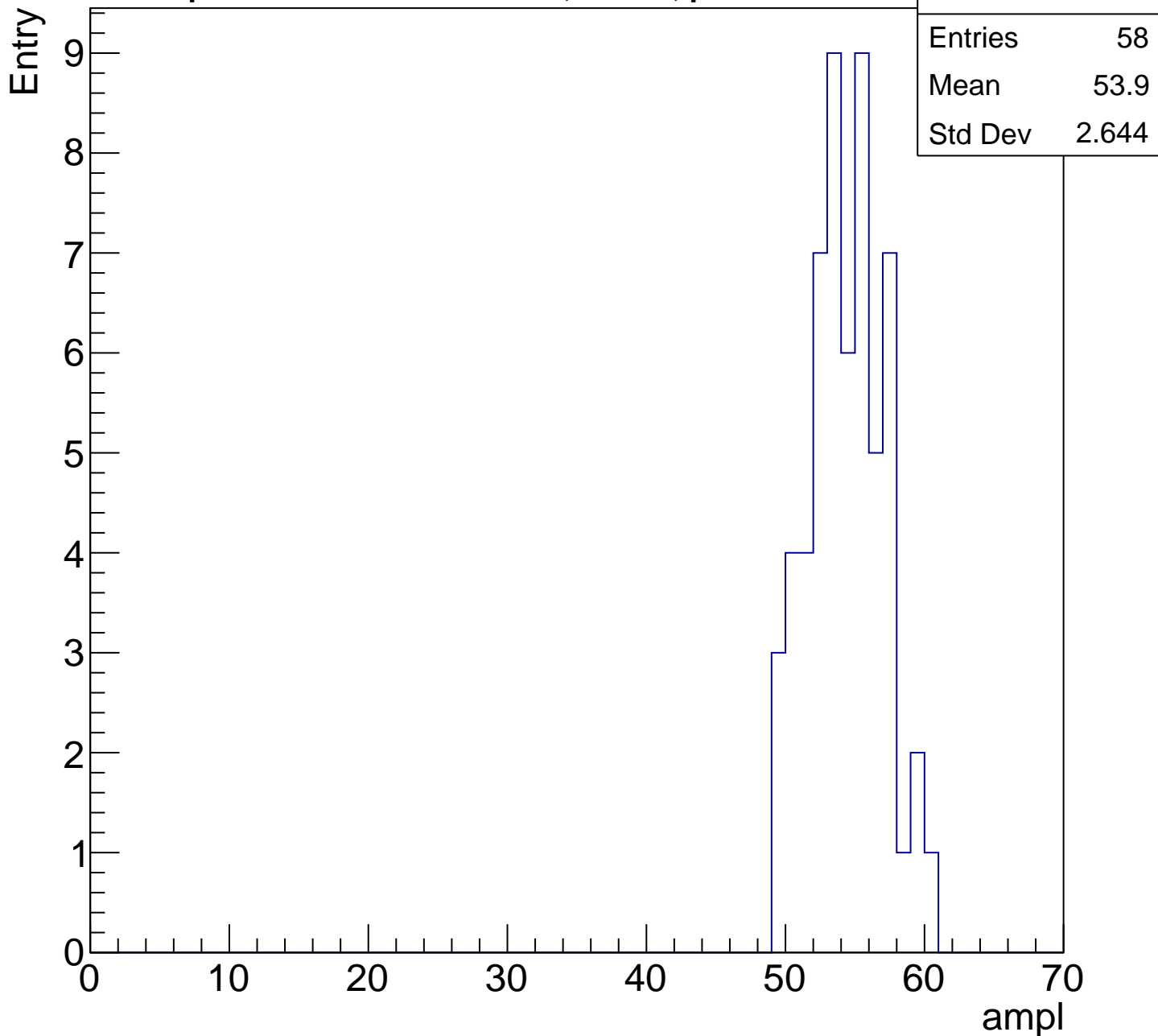
Entry

Entries	50
Mean	47.8
Std Dev	2.98



B1L002S, U7-ch46, adc4

calib_packv5_042523_0143.root, FC#10, port B3

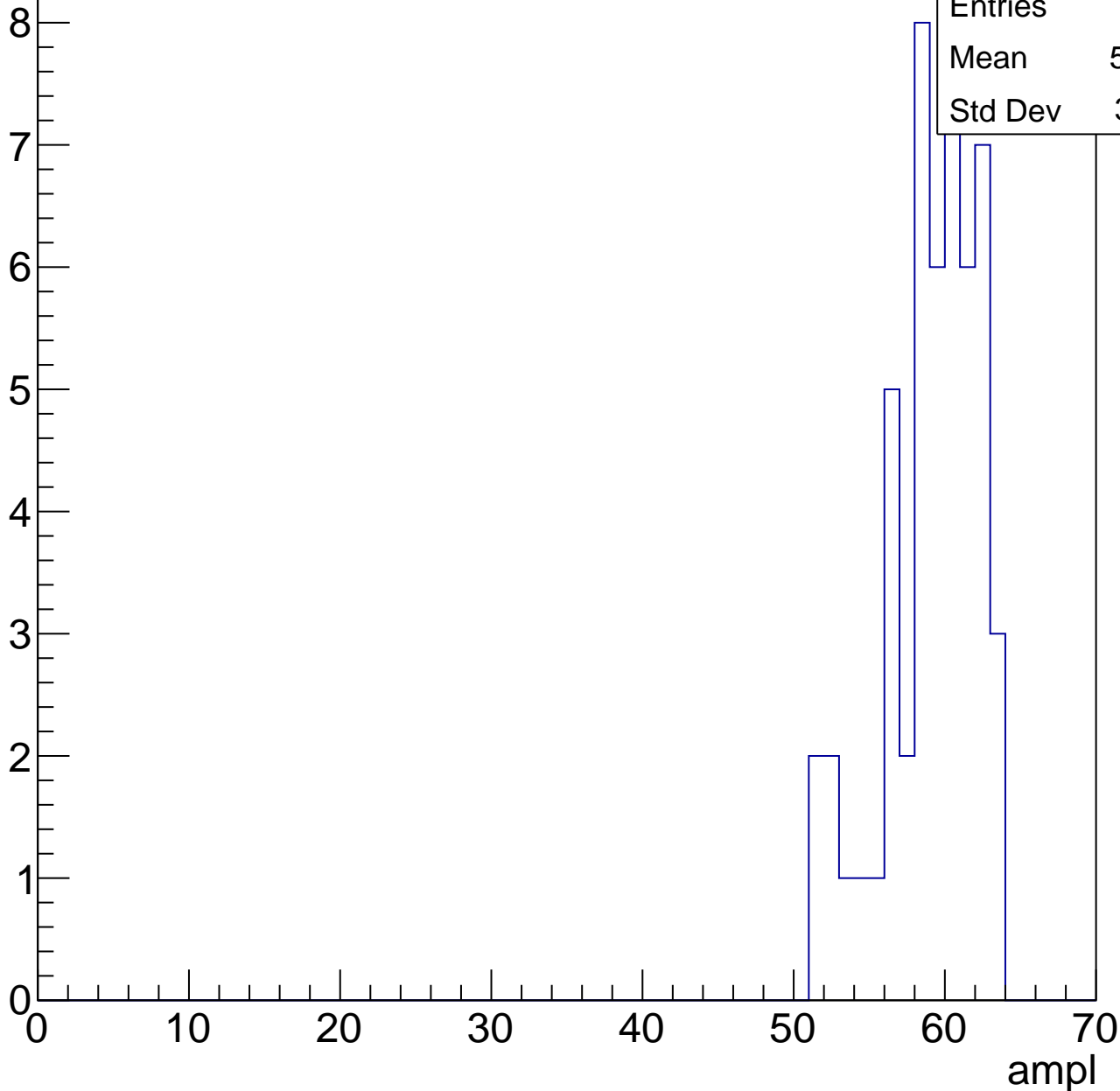


B1L002S, U7-ch46, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.63
Std Dev	3.101

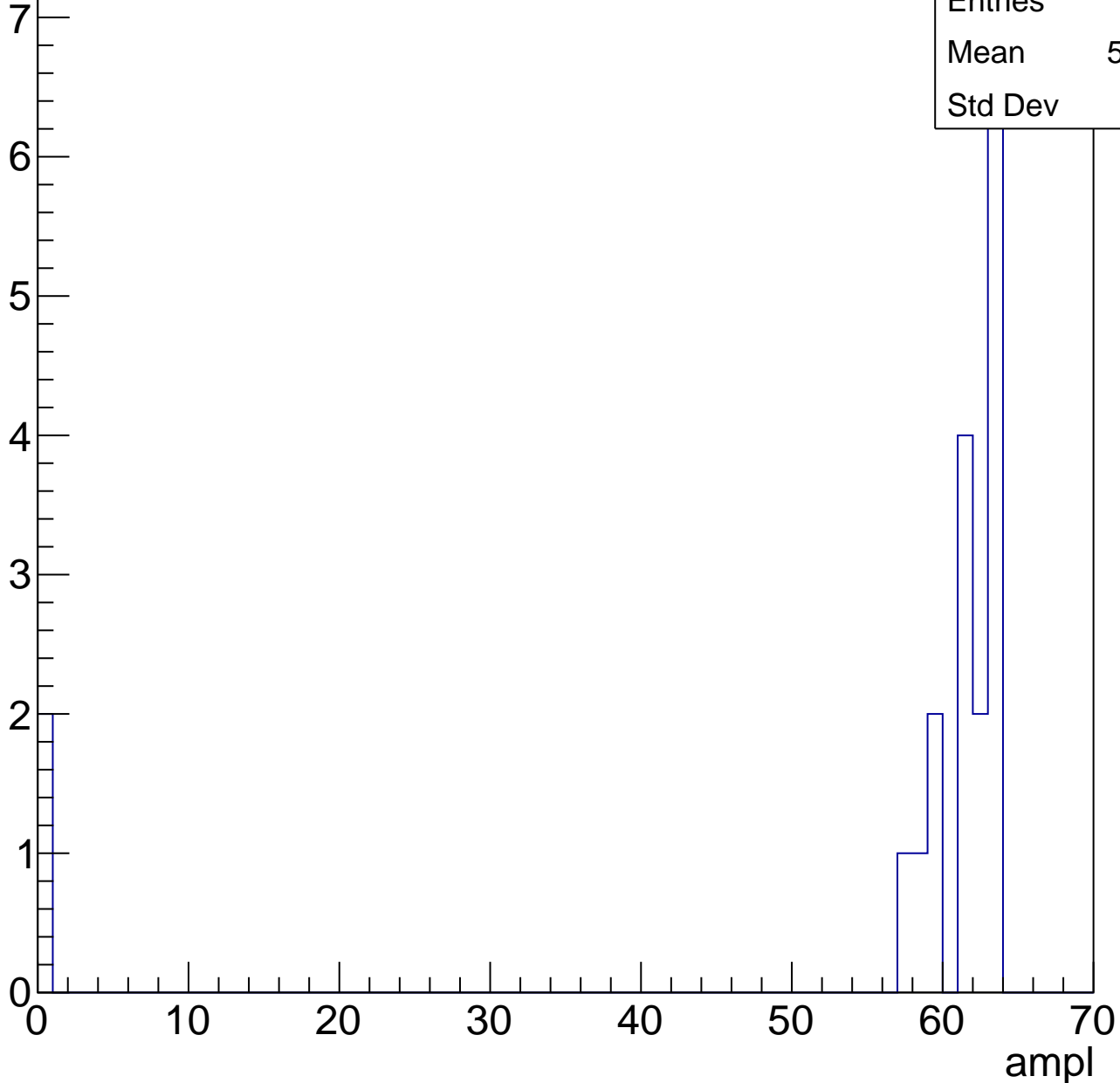


B1L002S, U7-ch46, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

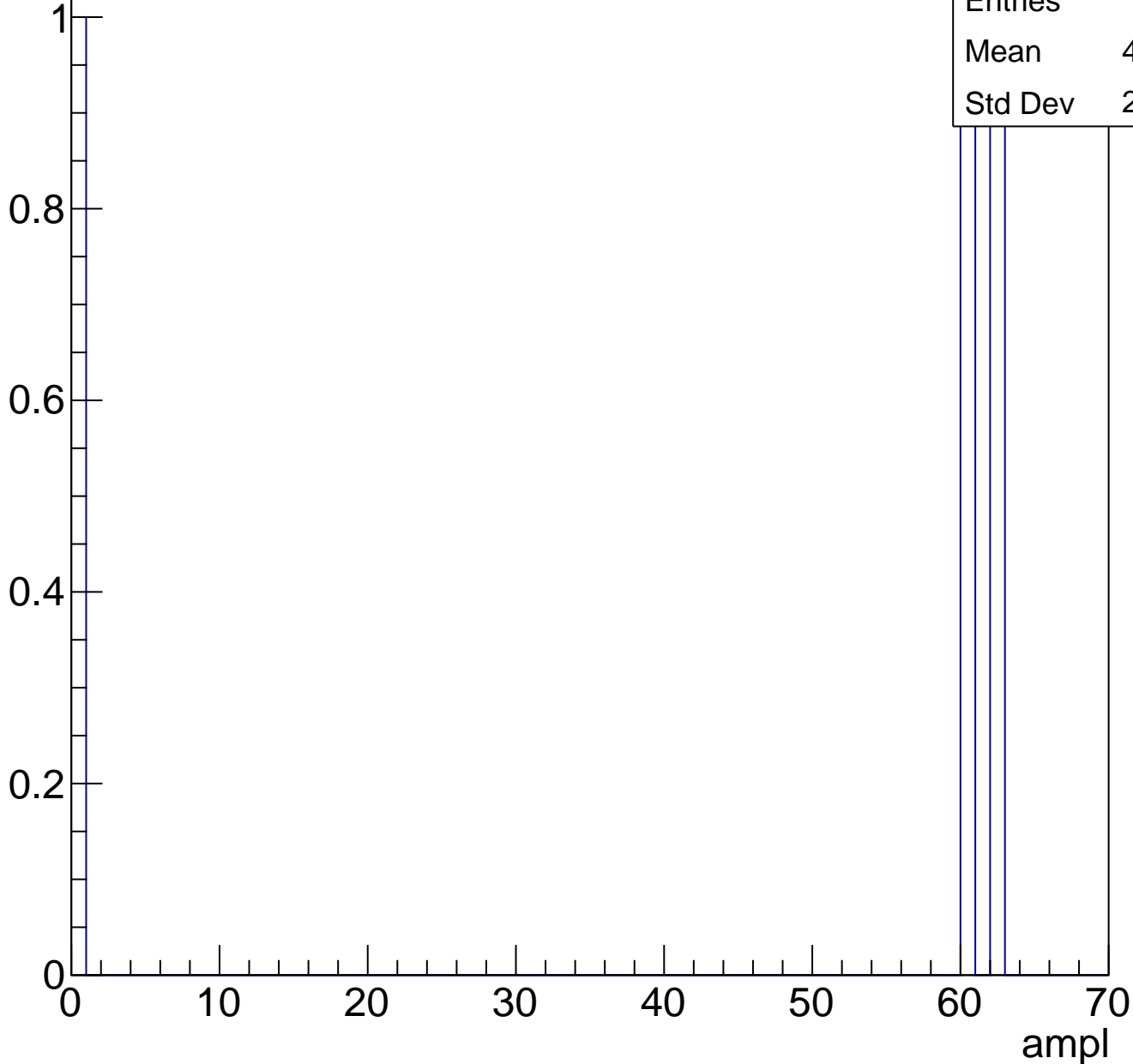
Entries	19
Mean	54.84
Std Dev	18.9



B1L002S, U7-ch46, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch47, adc0

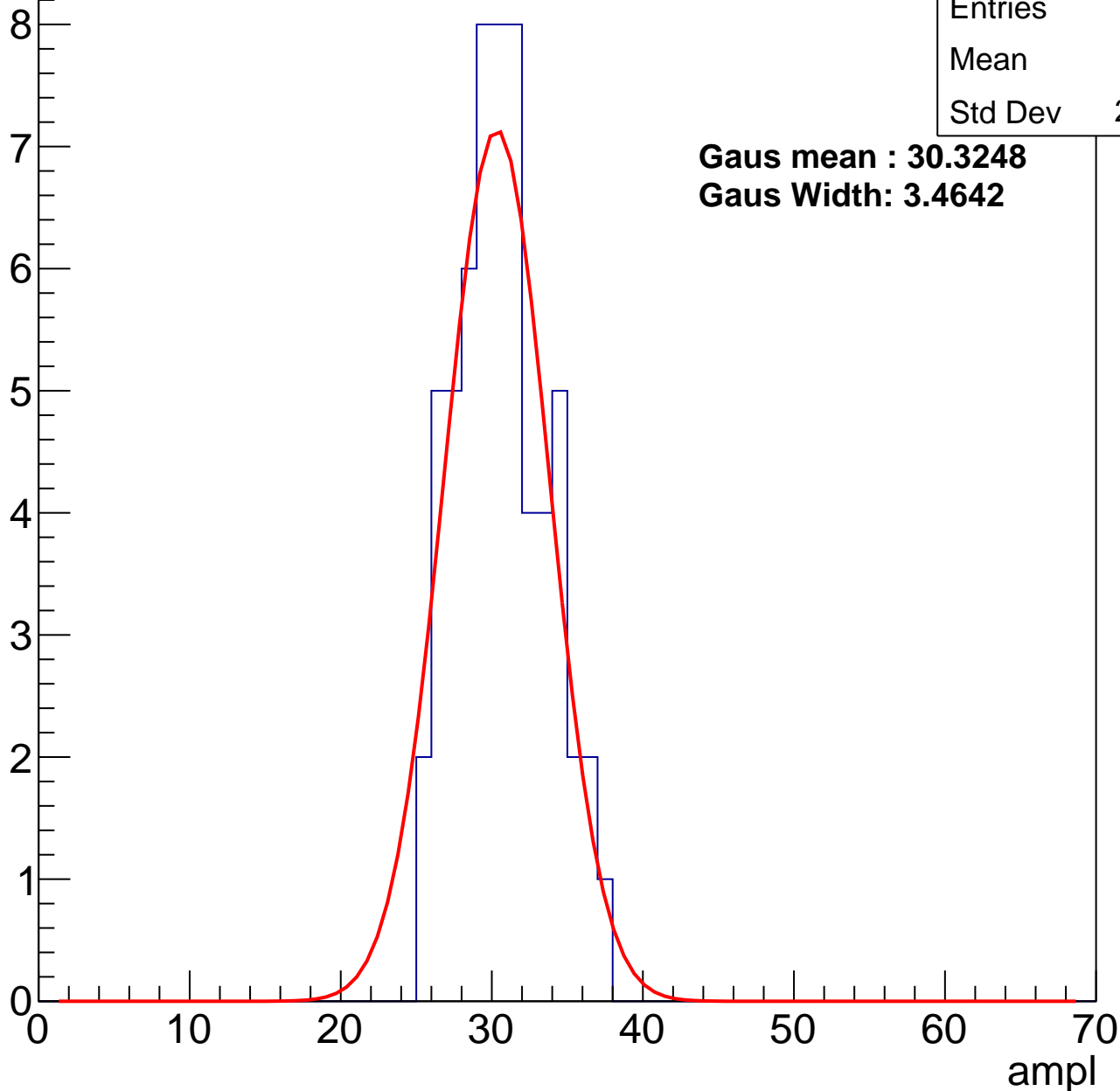
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	30.2
Std Dev	2.931

Gaus mean : 30.3248

Gaus Width: 3.4642



B1L002S, U7-ch47, adc1

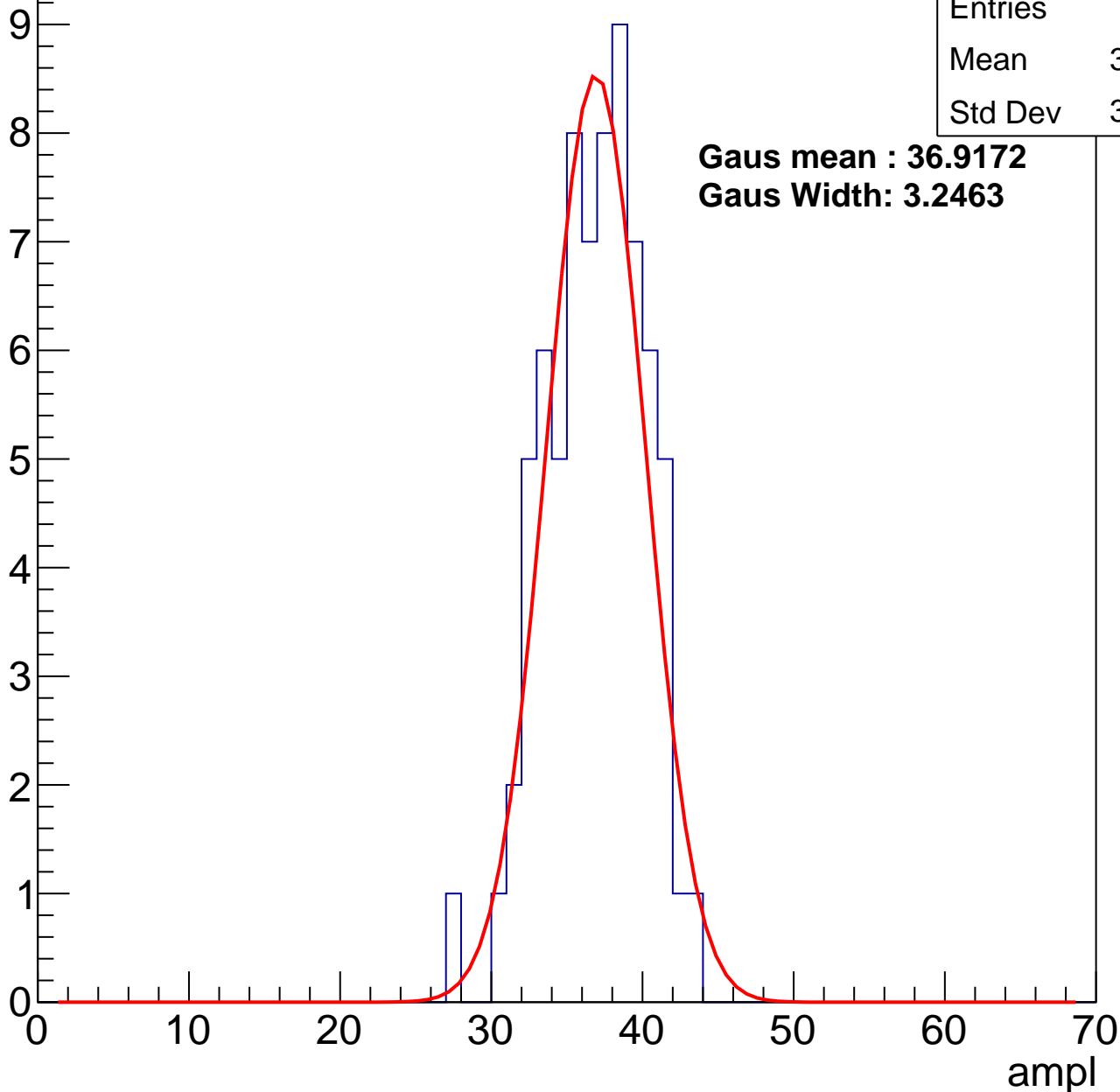
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	36.39
Std Dev	3.187

Gaus mean : 36.9172

Gaus Width: 3.2463



B1L002S, U7-ch47, adc2

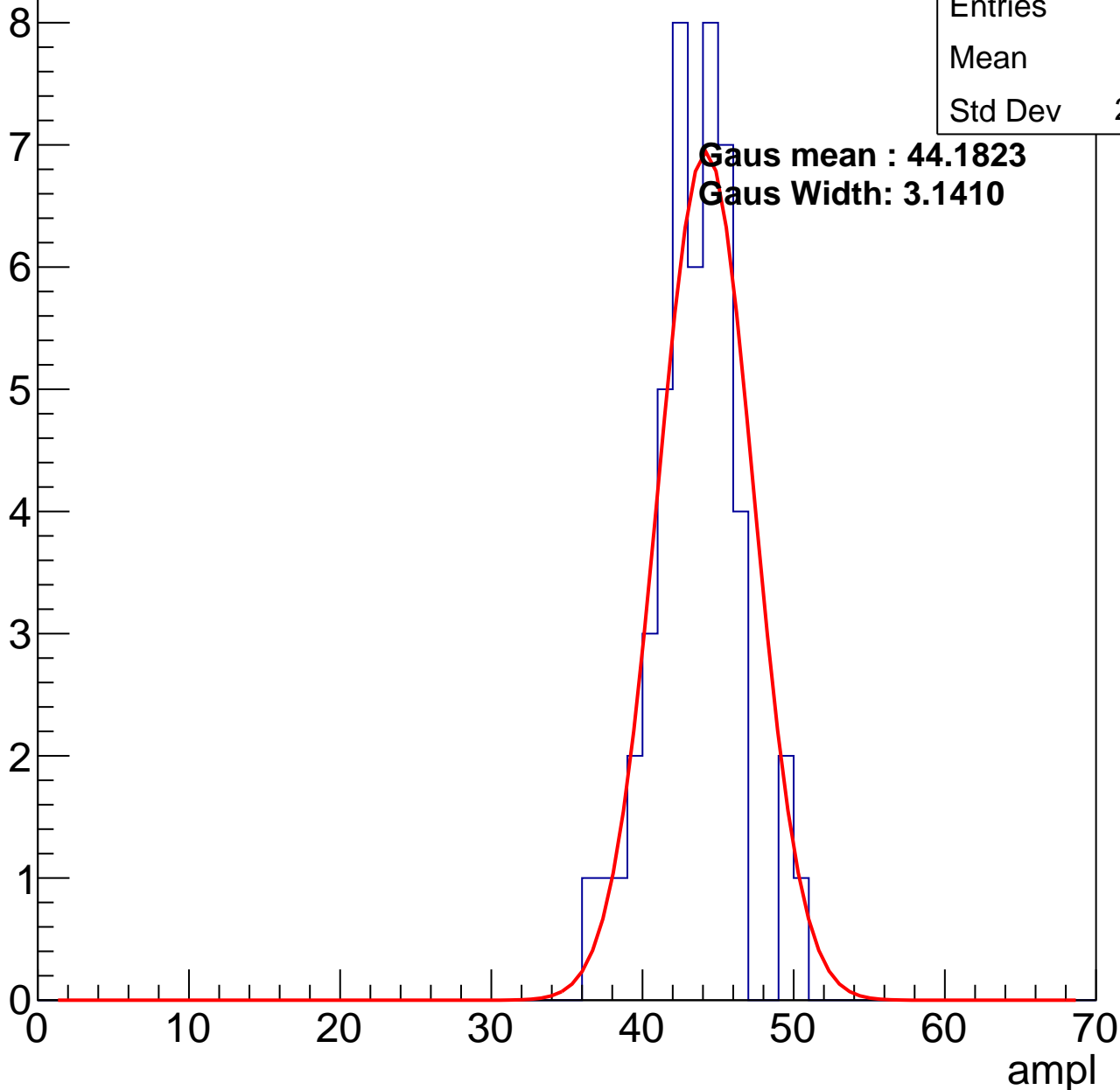
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	43
Std Dev	2.821

Gaus mean : 44.1823

Gaus Width: 3.1410

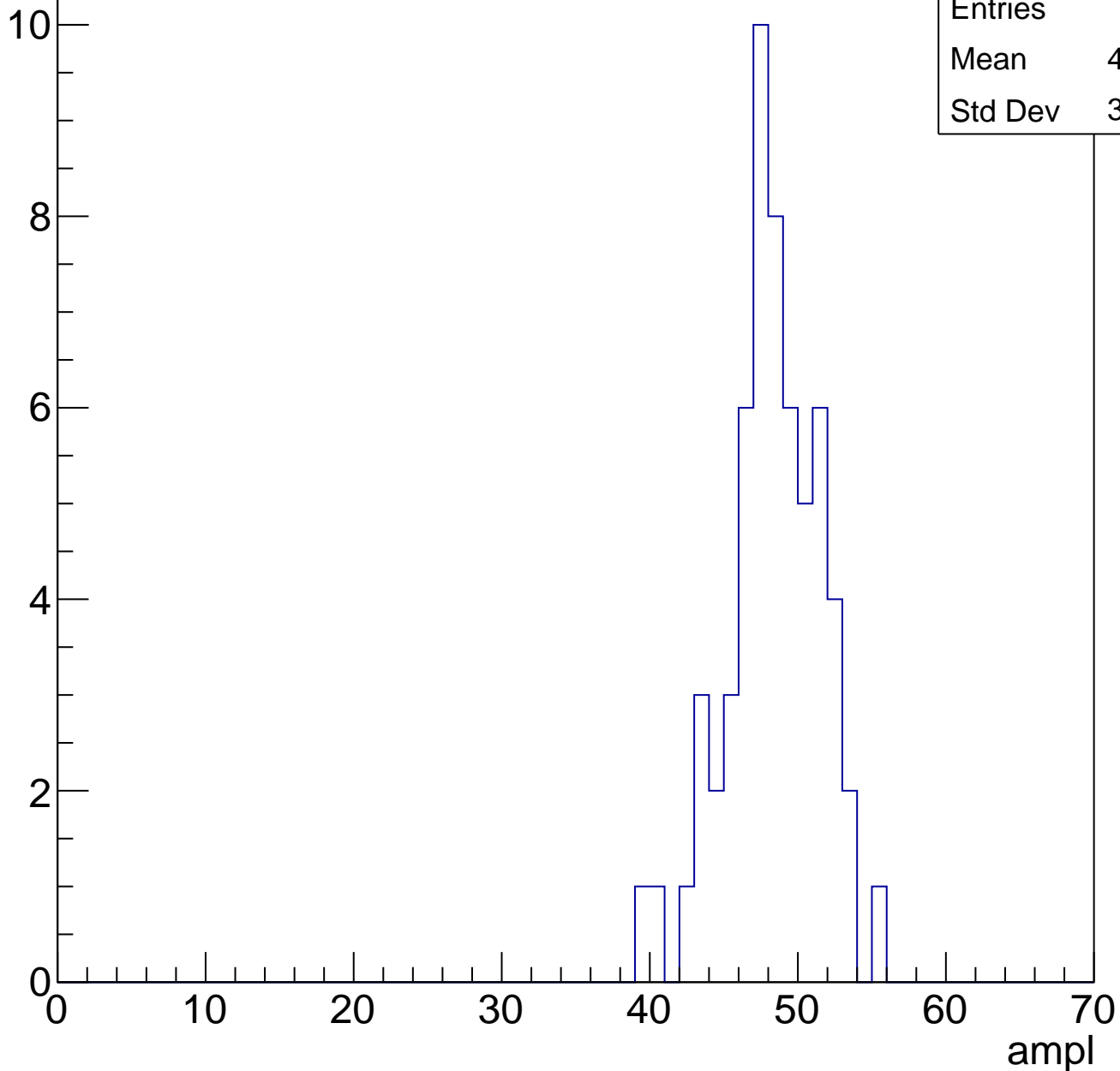


B1L002S, U7-ch47, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	59
Mean	47.83
Std Dev	3.163

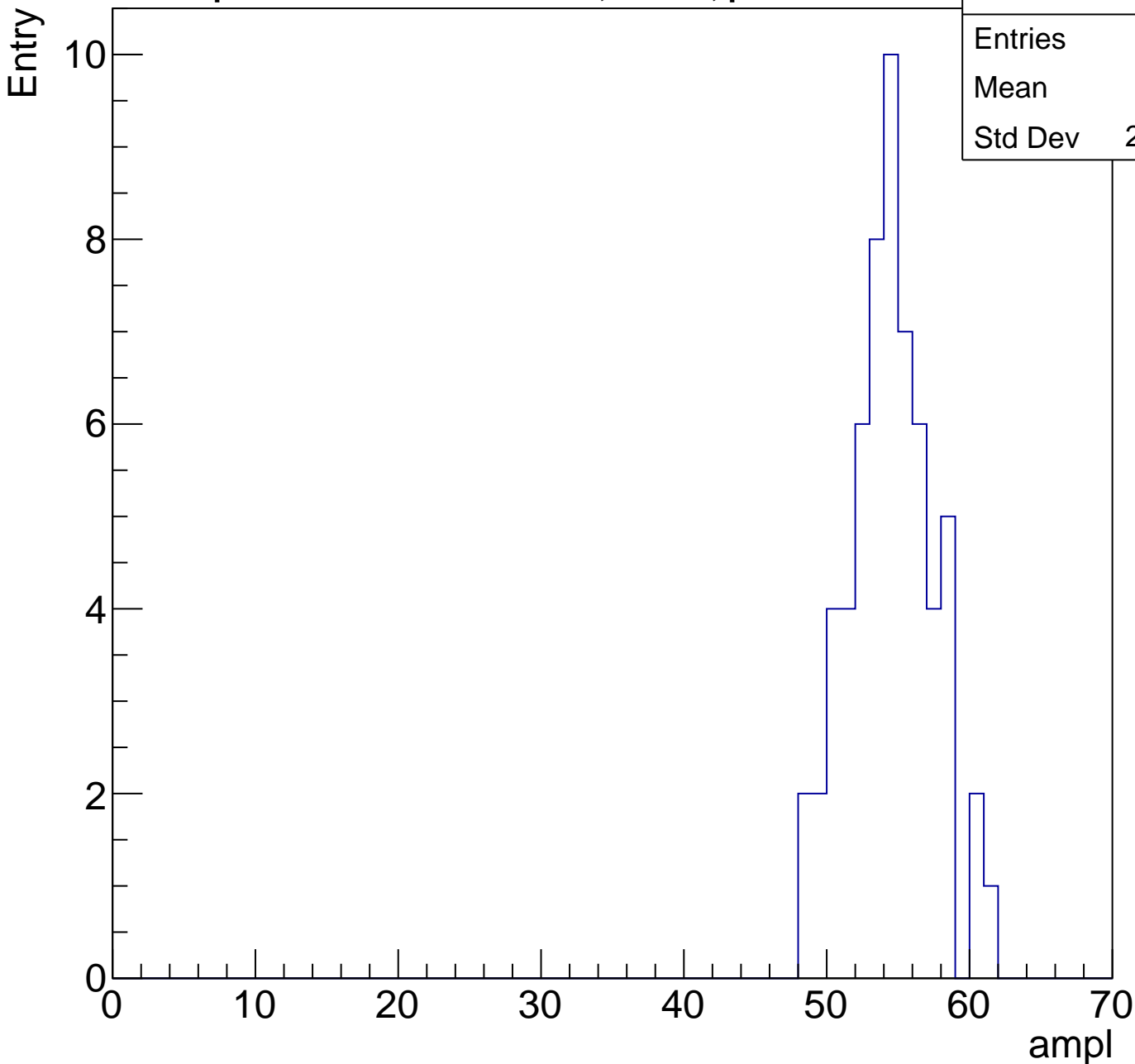
Entry



B1L002S, U7-ch47, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	61
Mean	54
Std Dev	2.925

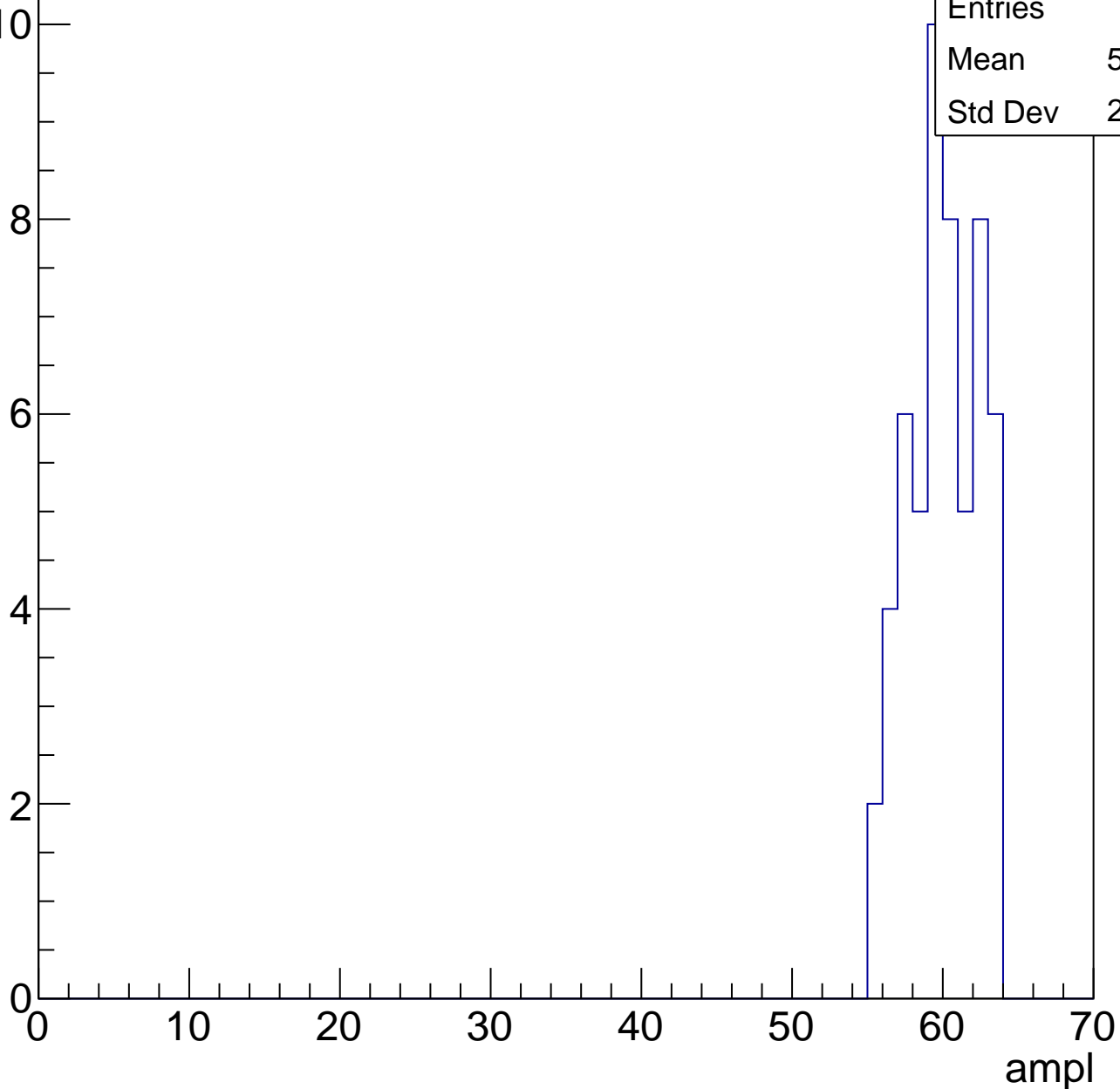


B1L002S, U7-ch47, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

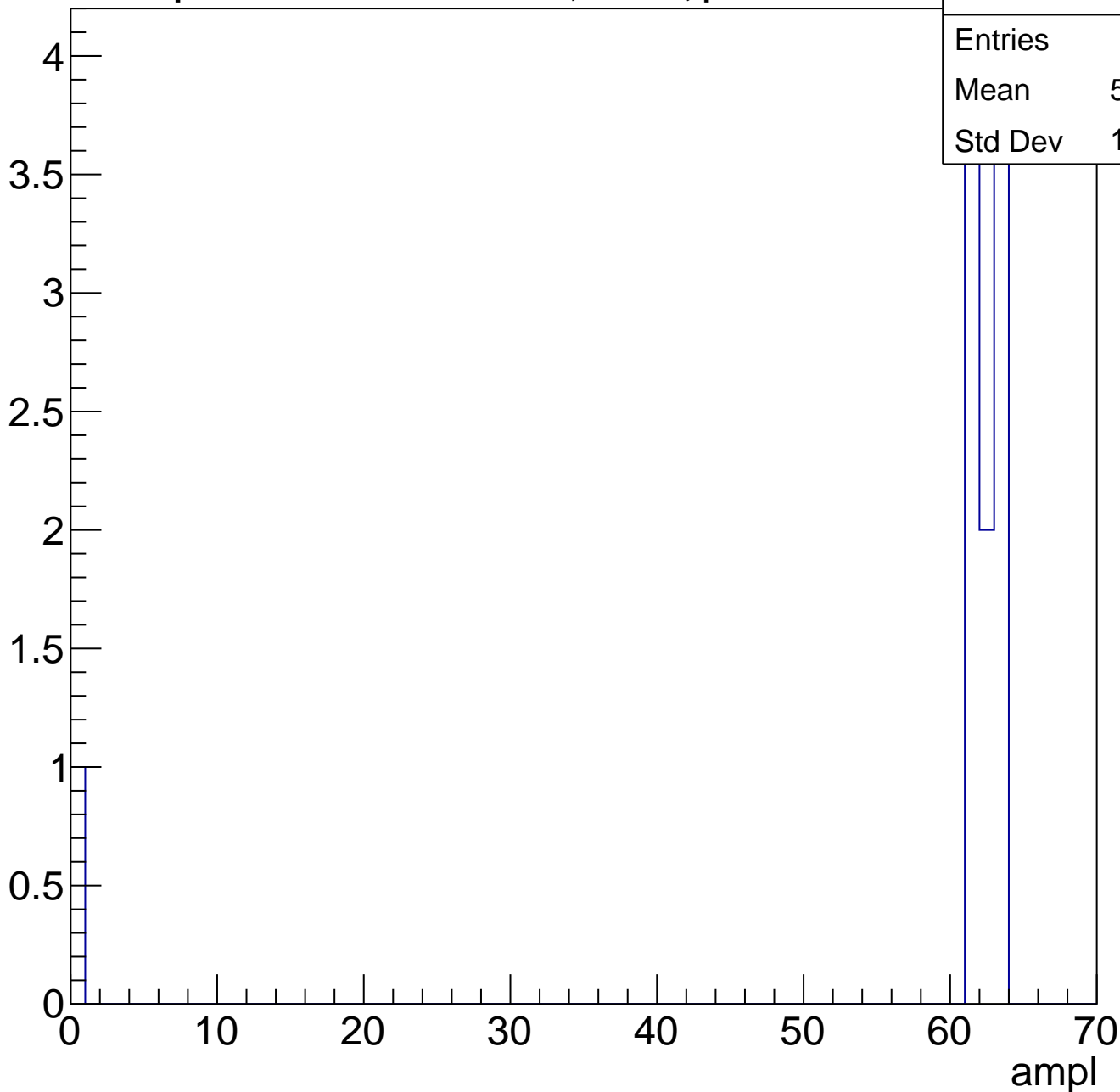
Entries	54
Mean	59.54
Std Dev	2.267



B1L002S, U7-ch47, adc6

calib_packv5_042523_0143.root, FC#10, port B3

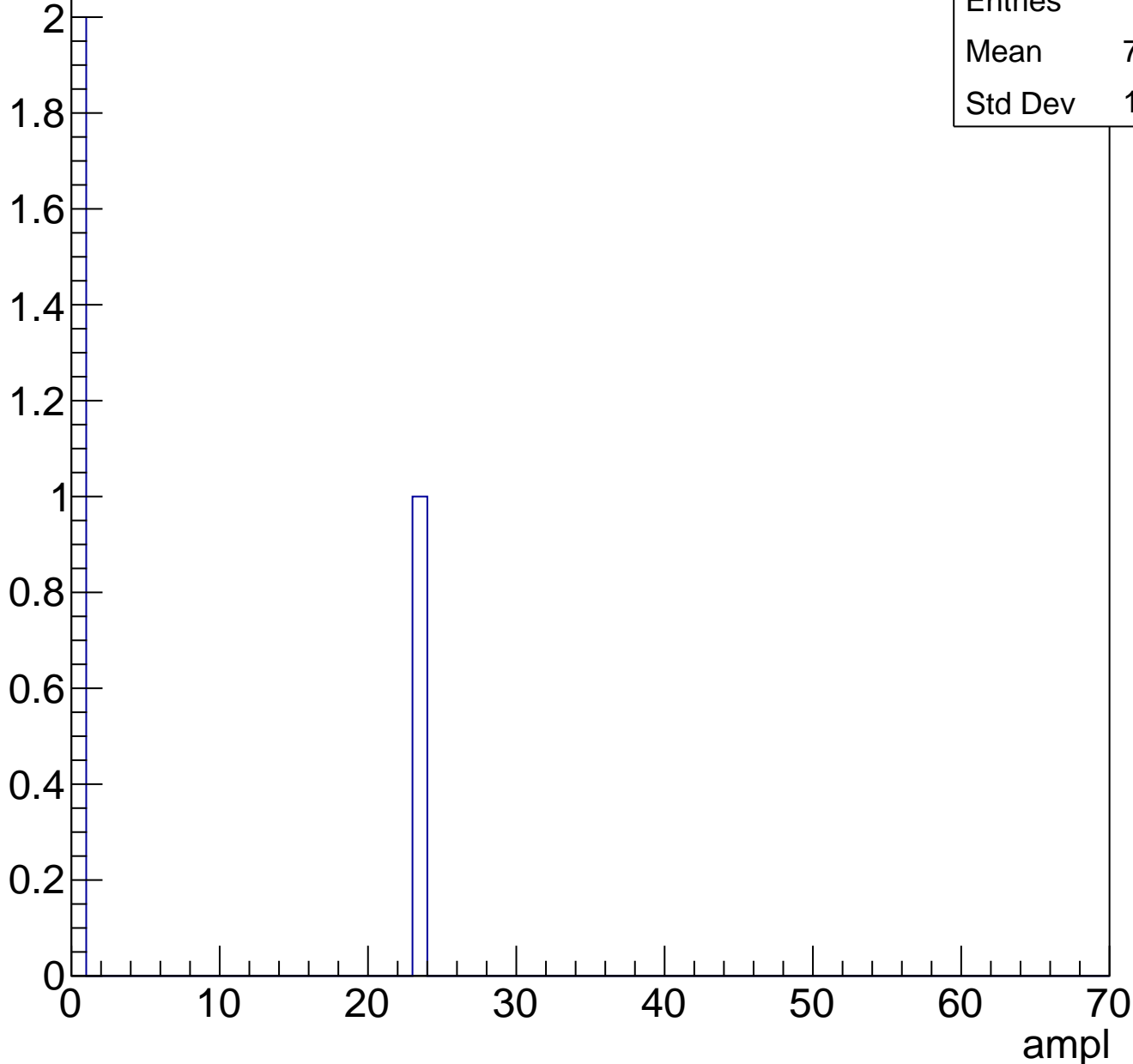
Entry



B1L002S, U7-ch47, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

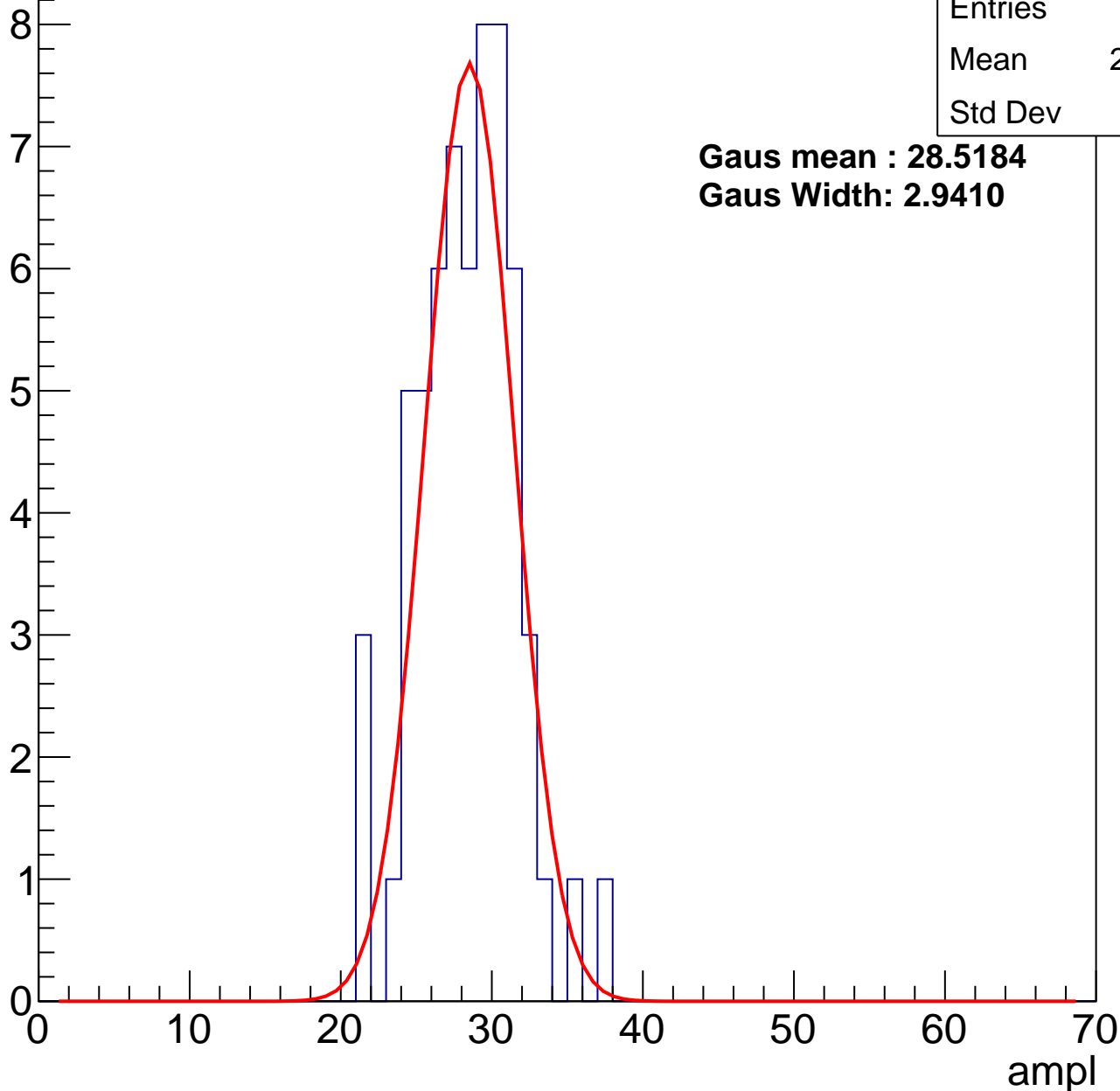


Entries	3
Mean	7.667
Std Dev	10.84

B1L002S, U7-ch48, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch48, adc1

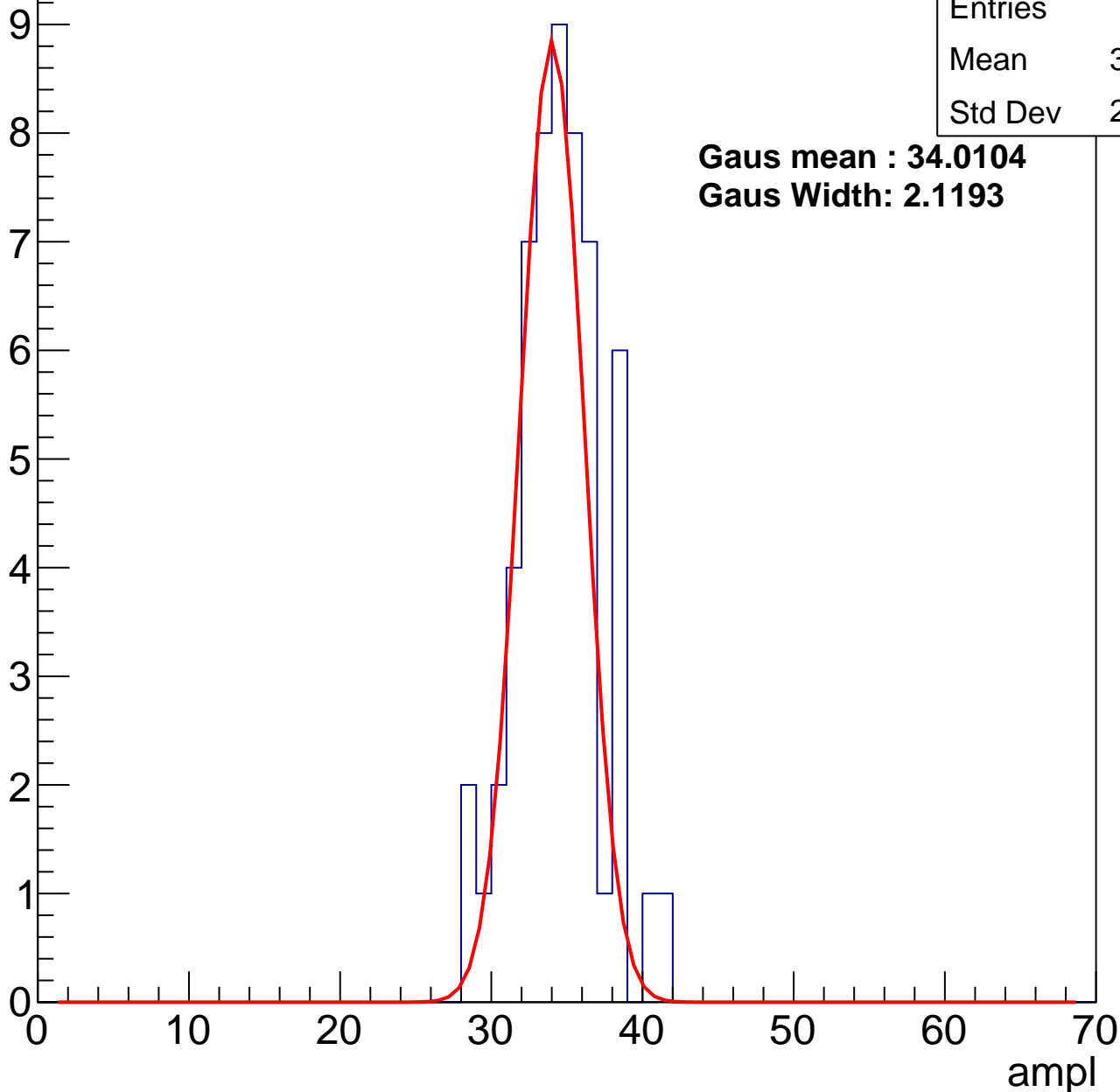
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.05
Std Dev	2.737

Gaus mean : 34.0104

Gaus Width: 2.1193



B1L002S, U7-ch48, adc2

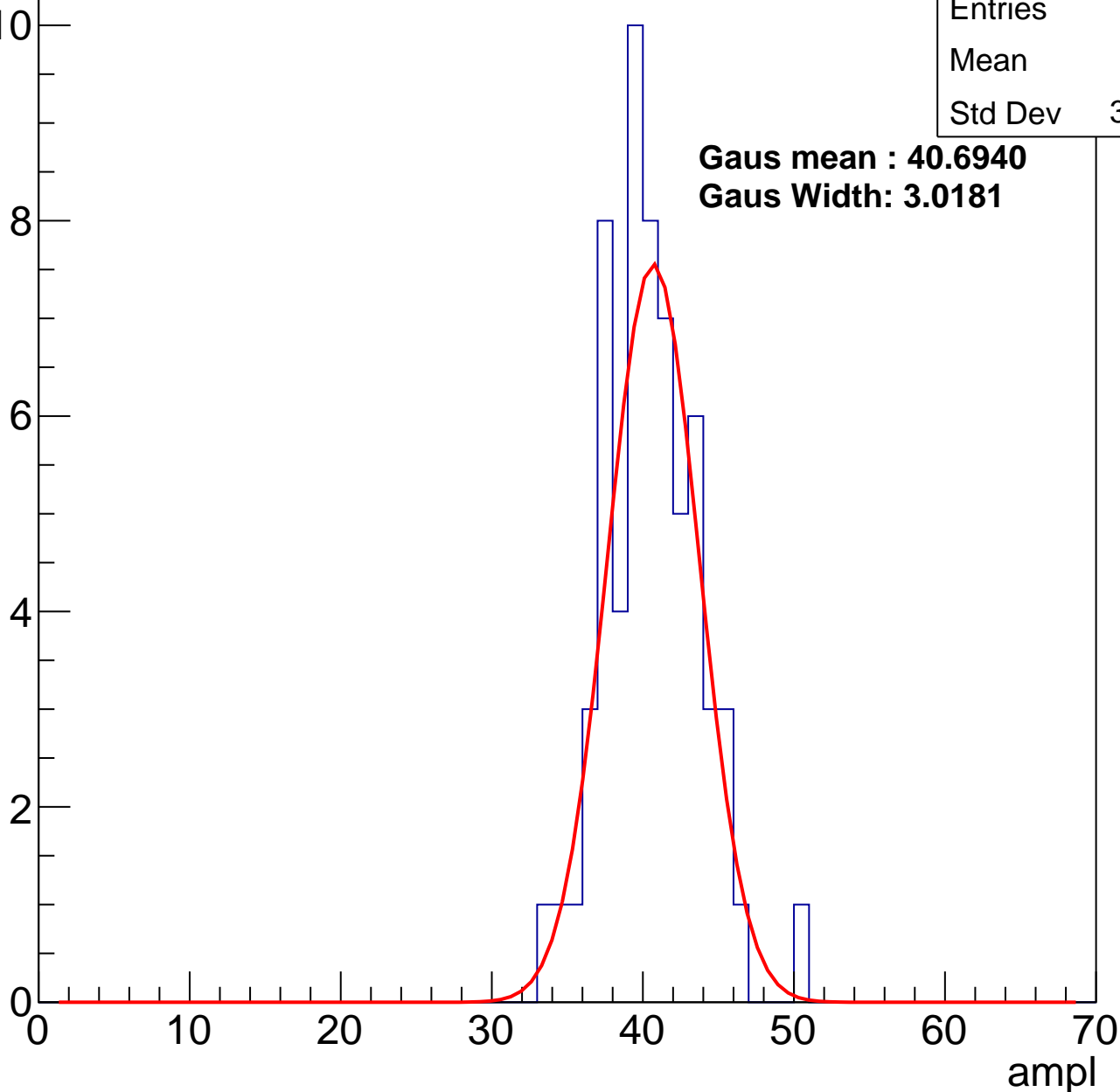
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	40.1
Std Dev	3.099

Gaus mean : 40.6940

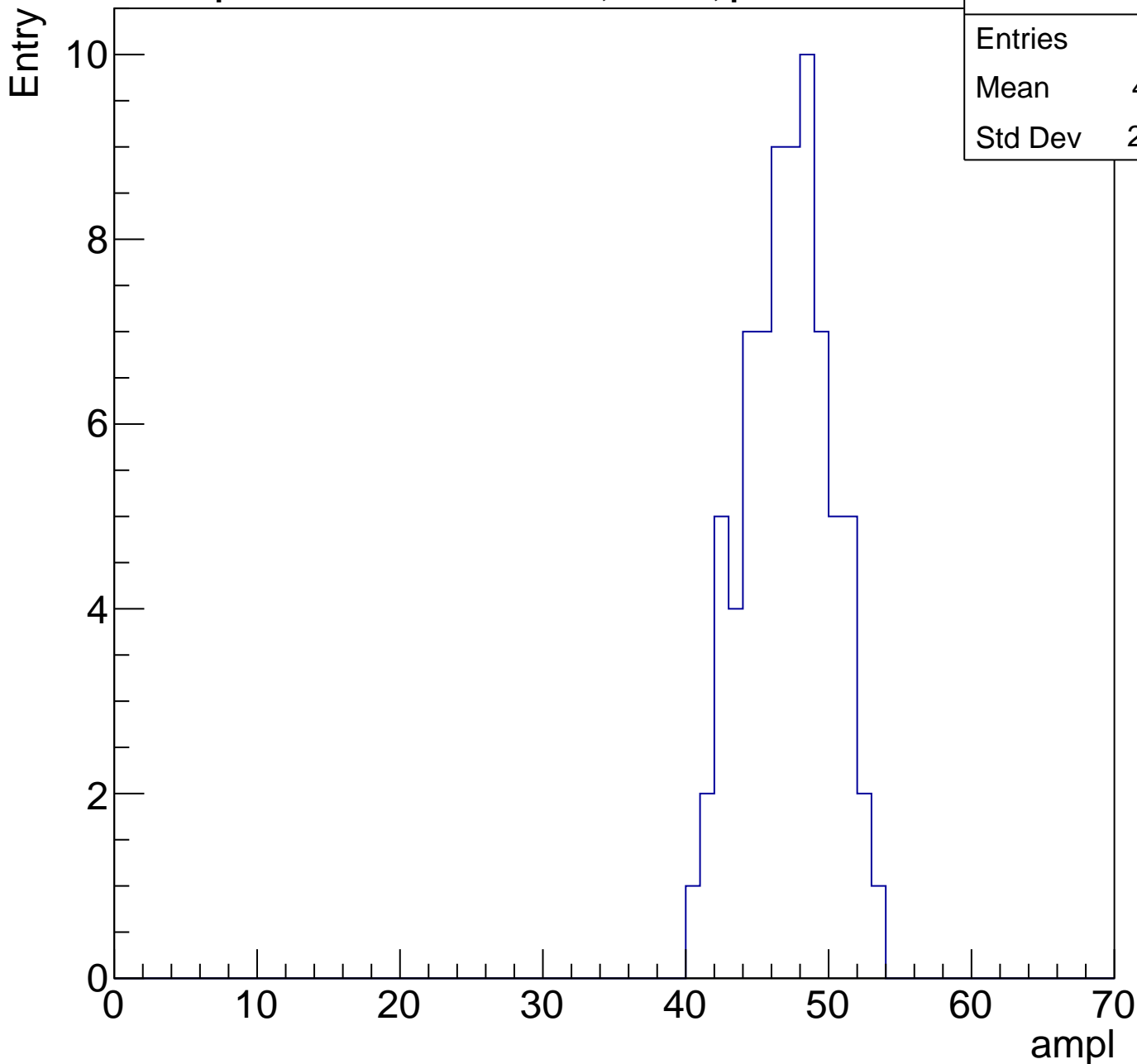
Gaus Width: 3.0181



B1L002S, U7-ch48, adc3

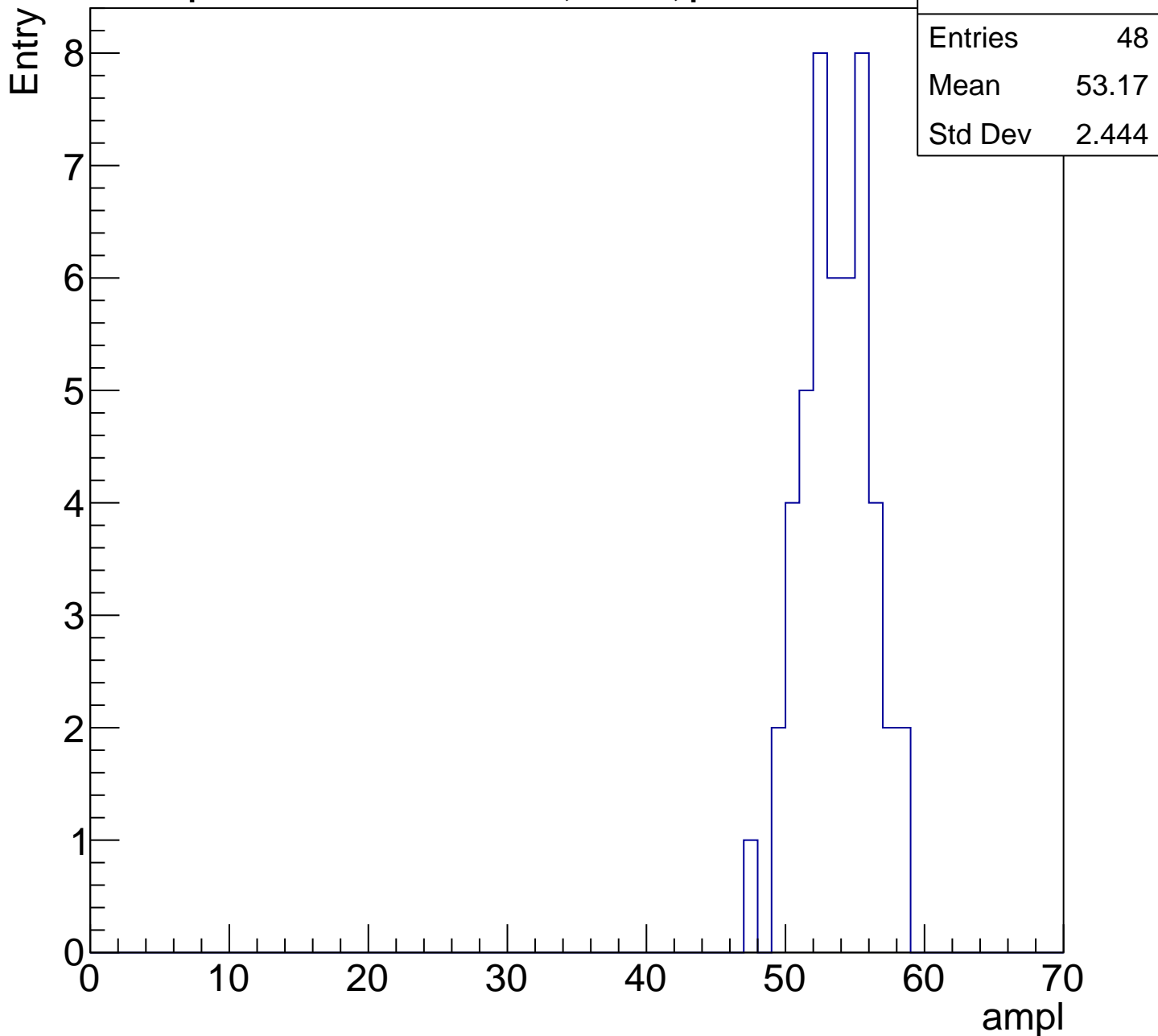
calib_packv5_042523_0143.root, FC#10, port B3

Entries	74
Mean	46.61
Std Dev	2.958



B1L002S, U7-ch48, adc4

calib_packv5_042523_0143.root, FC#10, port B3

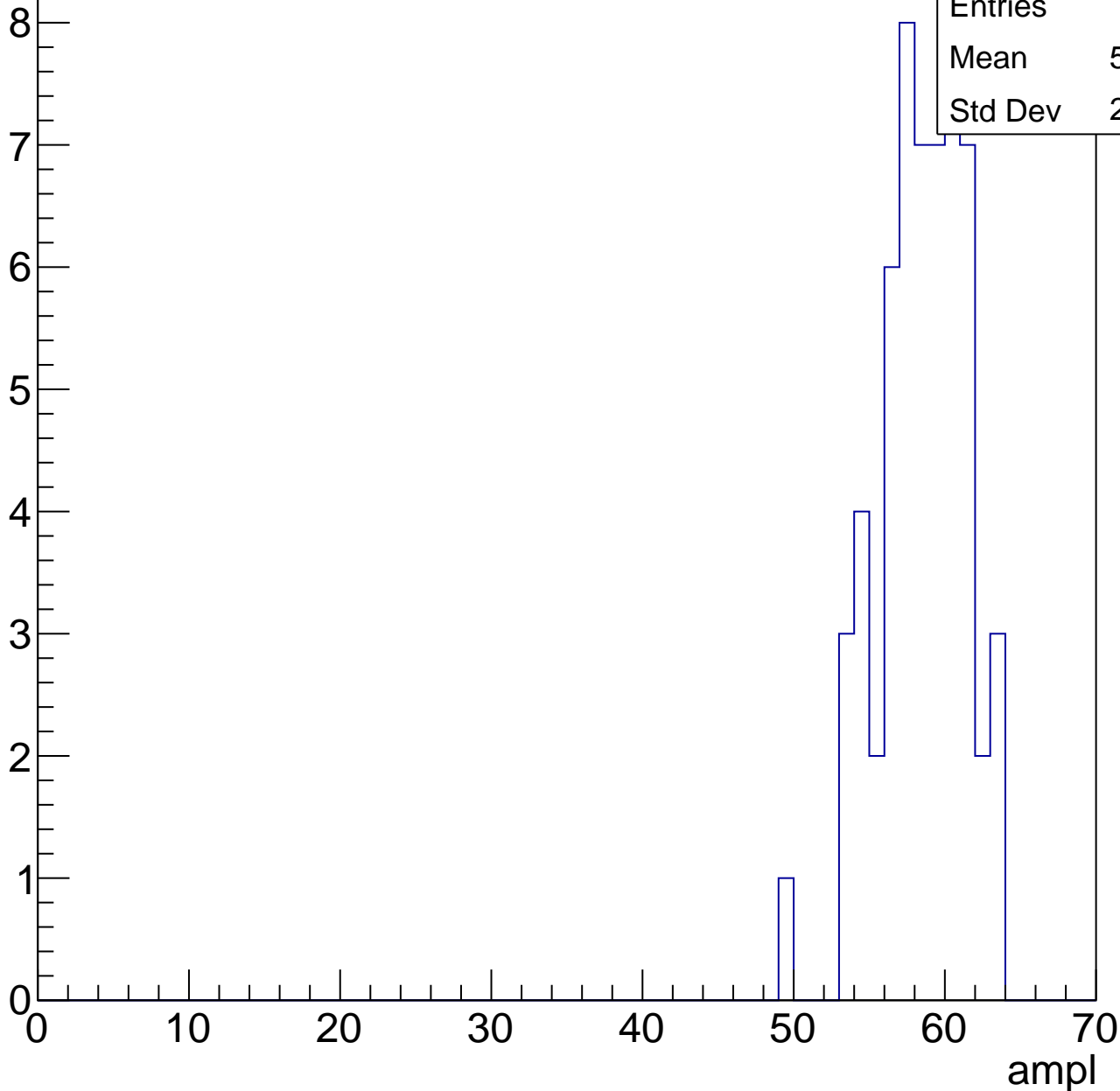


B1L002S, U7-ch48, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.02
Std Dev	2.874

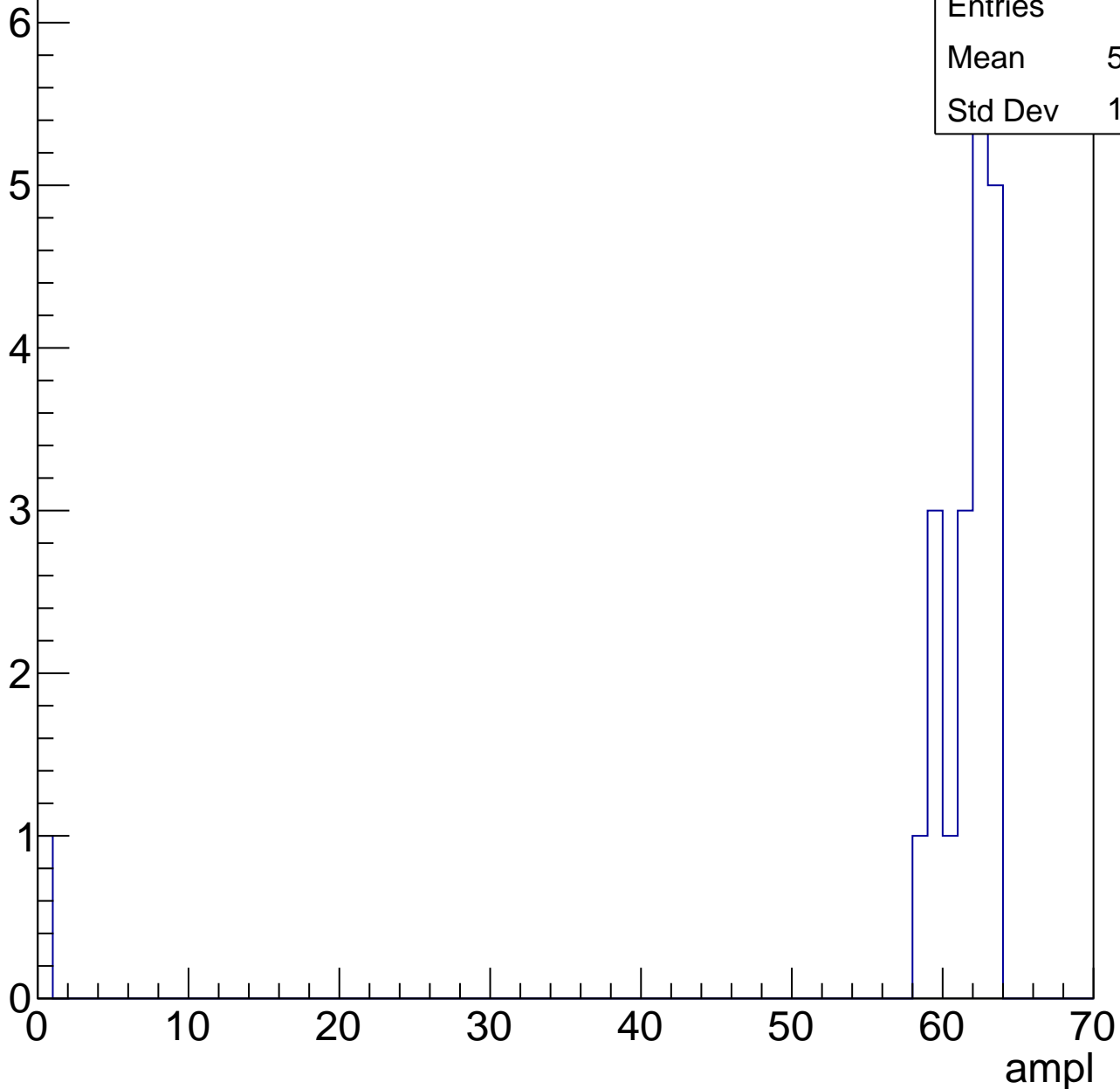


B1L002S, U7-ch48, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

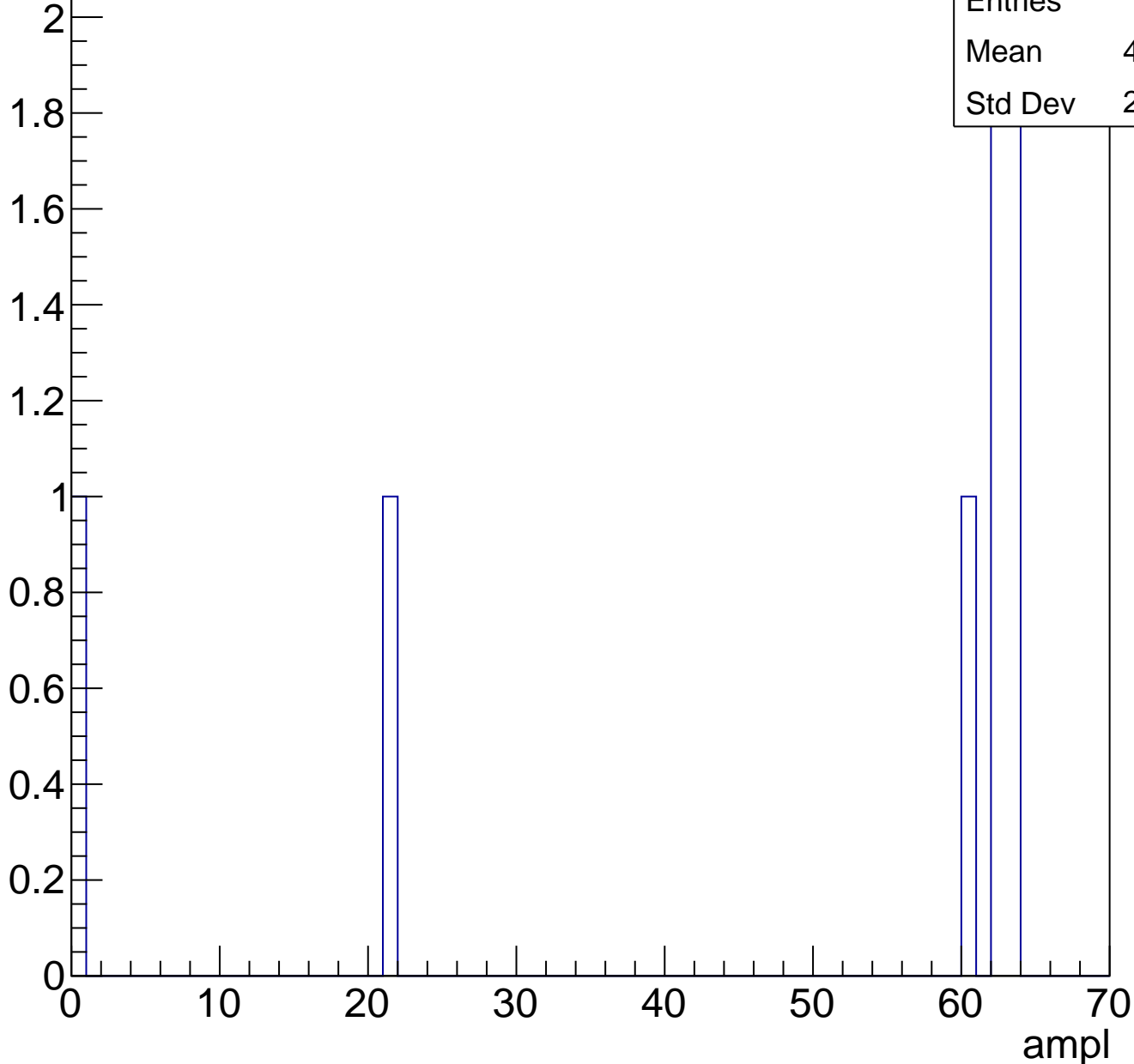
Entries	20
Mean	58.25
Std Dev	13.45



B1L002S, U7-ch48, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch49, adc0

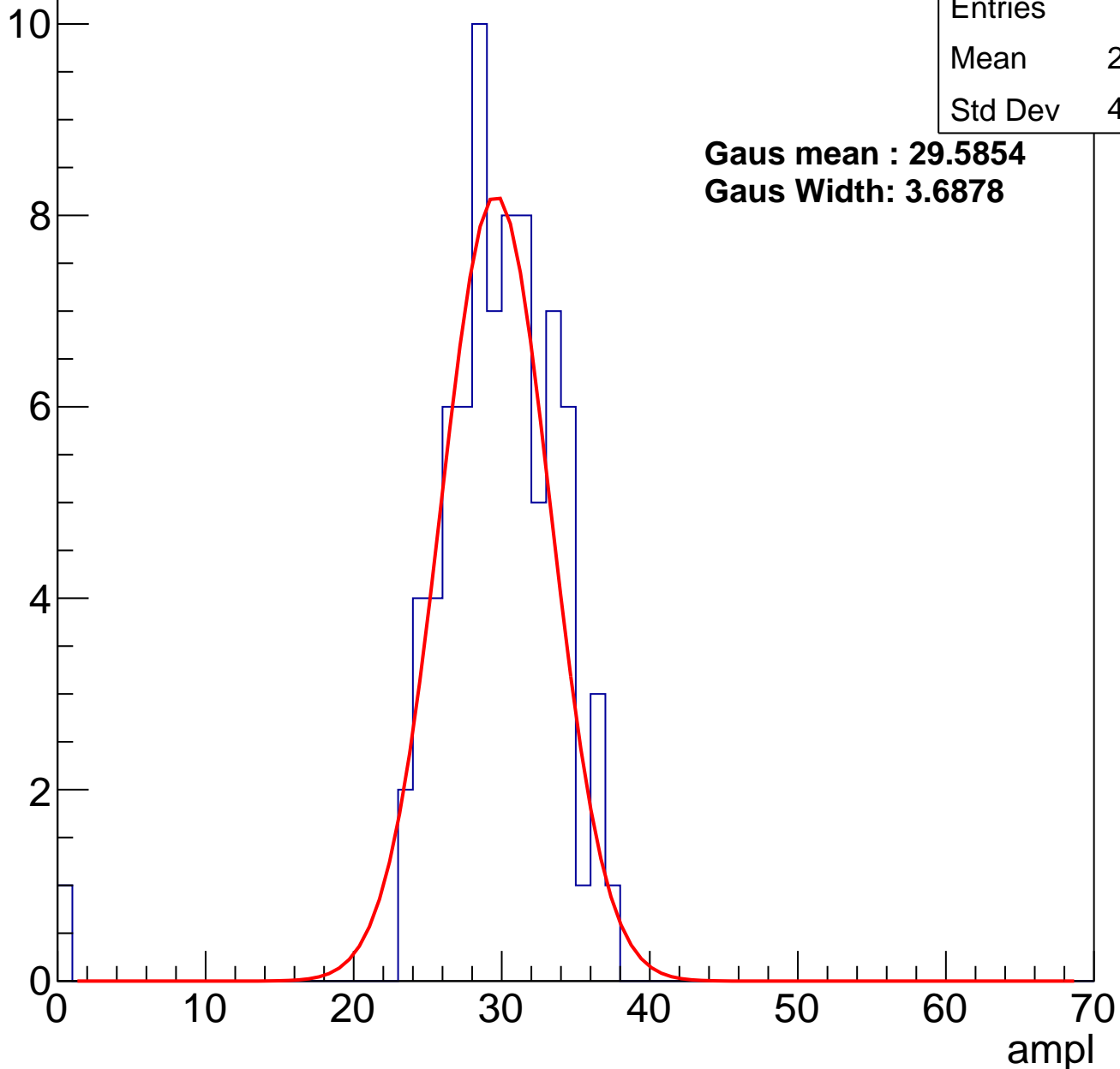
calib_packv5_042523_0143.root, FC#10, port B3

Entries	79
Mean	29.19
Std Dev	4.715

Gaus mean : 29.5854

Gaus Width: 3.6878

Entry



B1L002S, U7-ch49, adc1

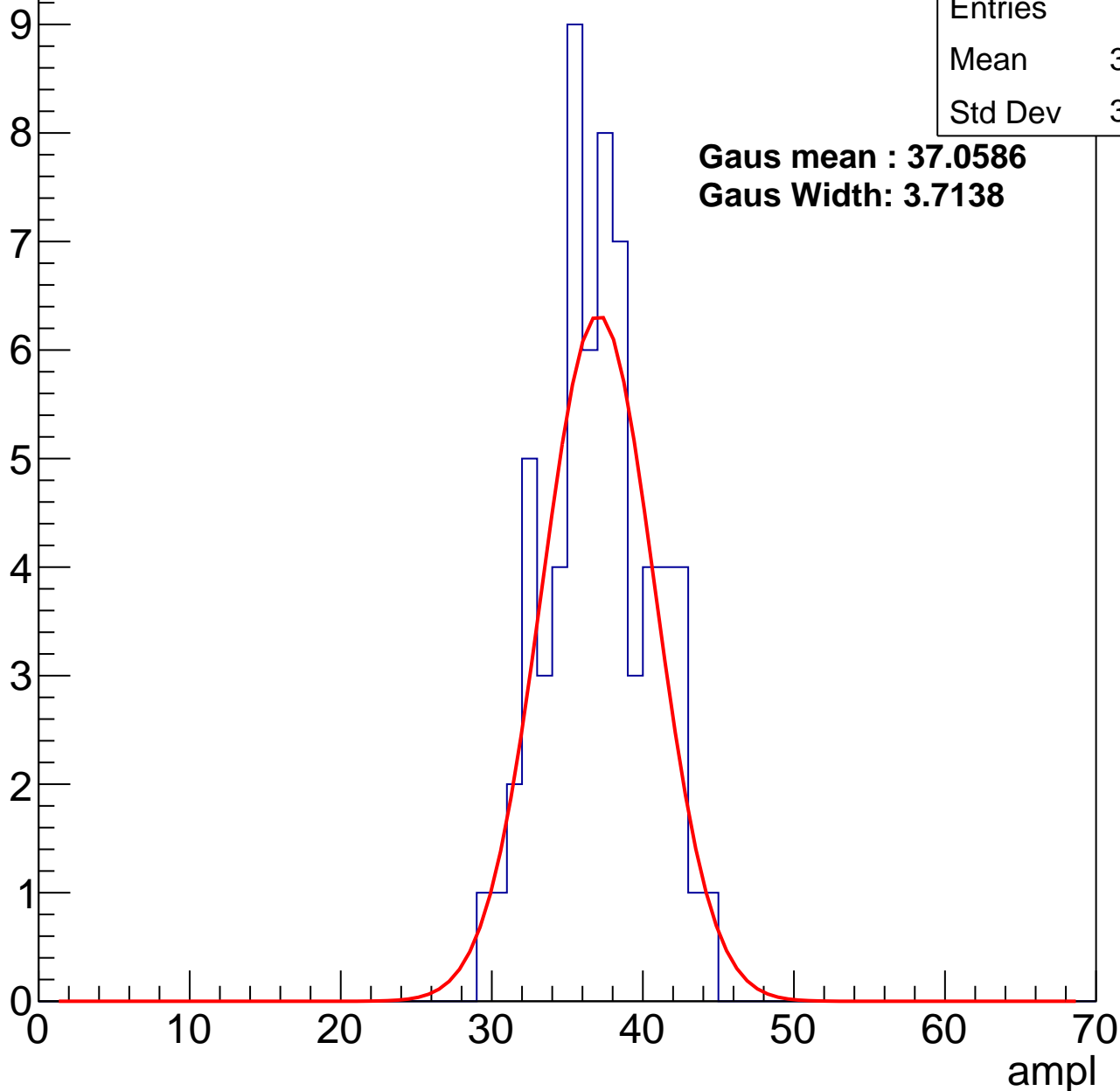
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.59
Std Dev	3.402

Gaus mean : 37.0586

Gaus Width: 3.7138

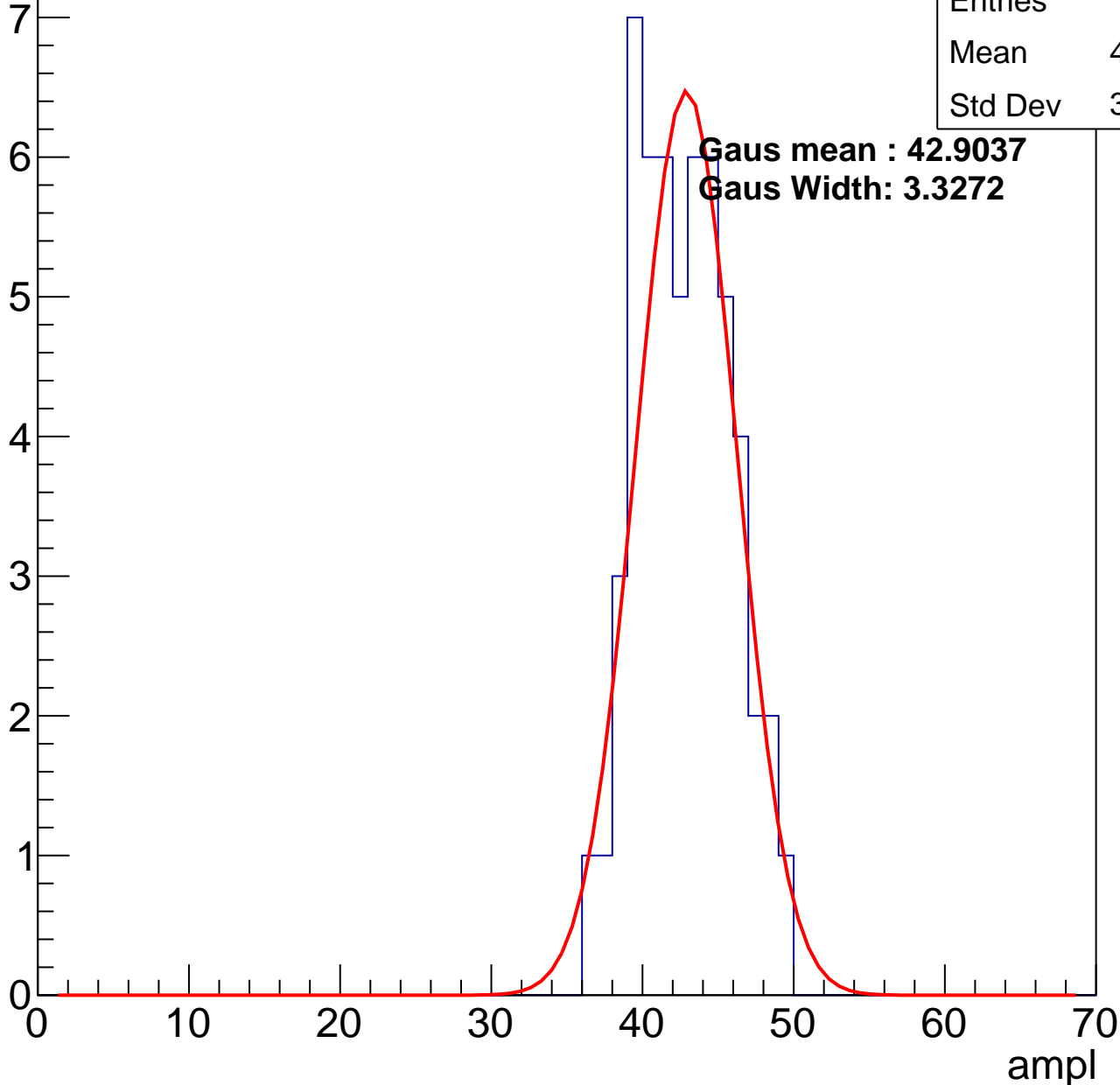


B1L002S, U7-ch49, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	42.29
Std Dev	3.037

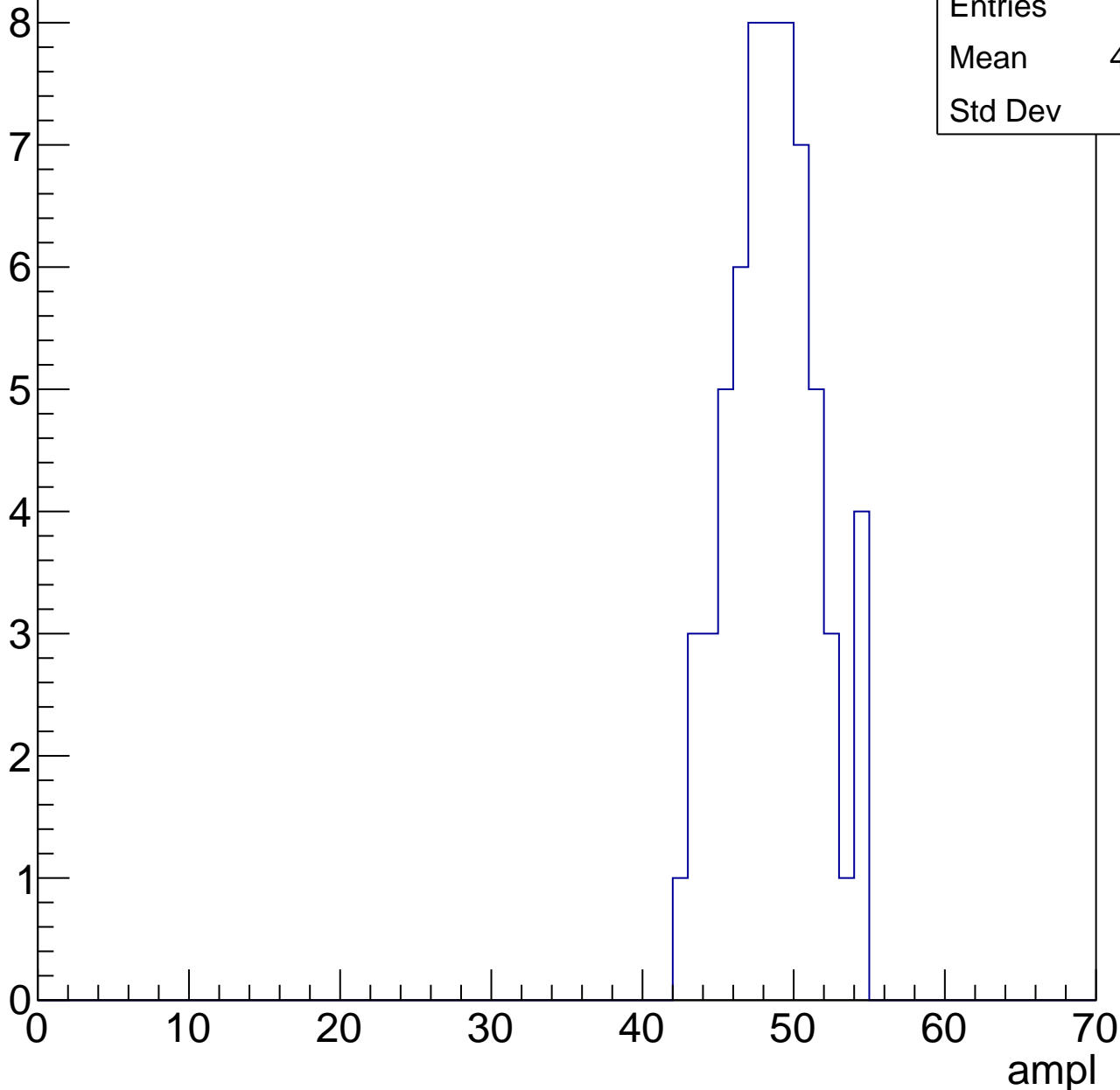


B1L002S, U7-ch49, adc3

calib_packv5_042523_0143.root, FC#10, port B3

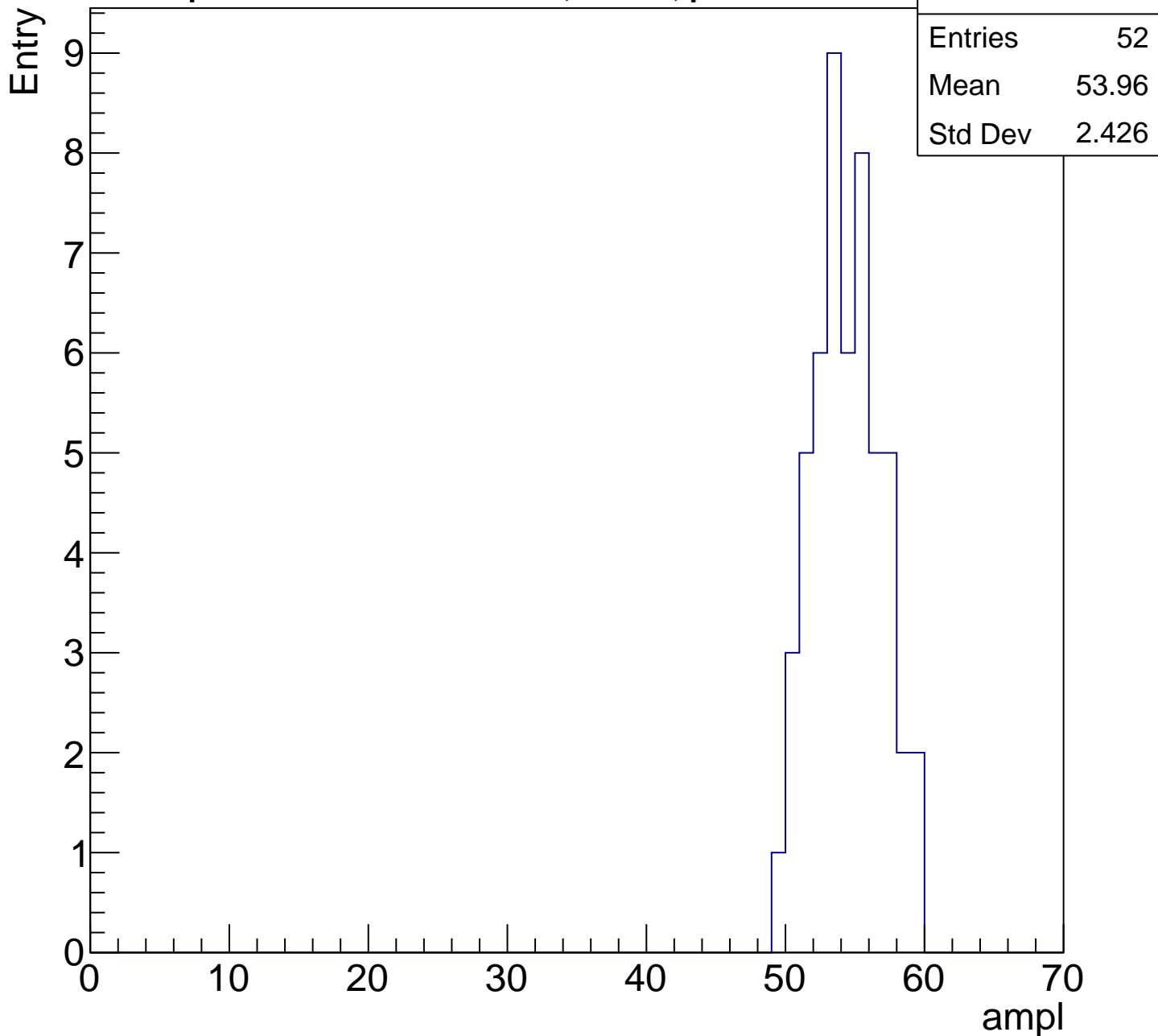
Entry

Entries	62
Mean	48.16
Std Dev	2.93



B1L002S, U7-ch49, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch49, adc5

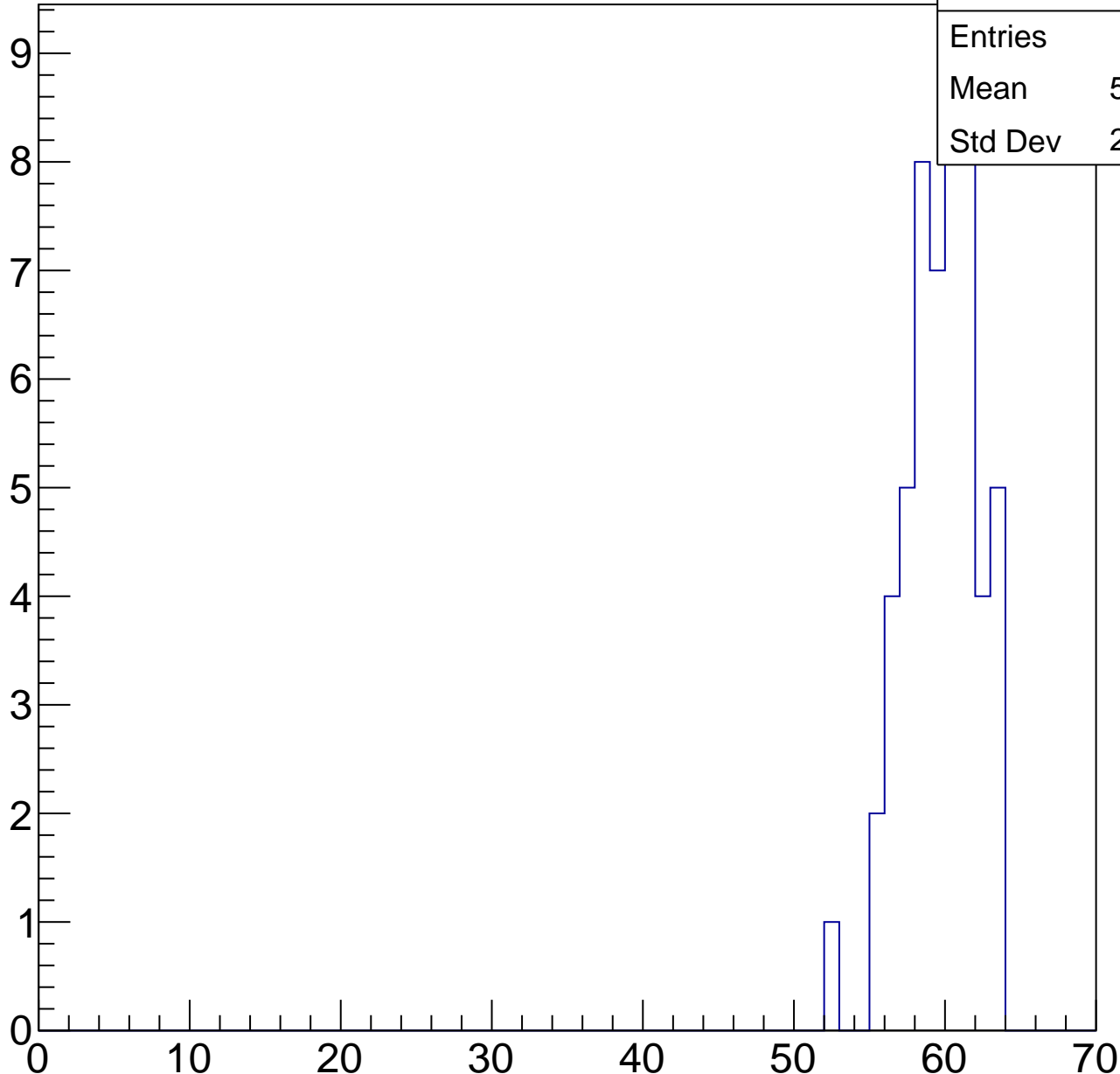
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.23
Std Dev	2.376

ampl

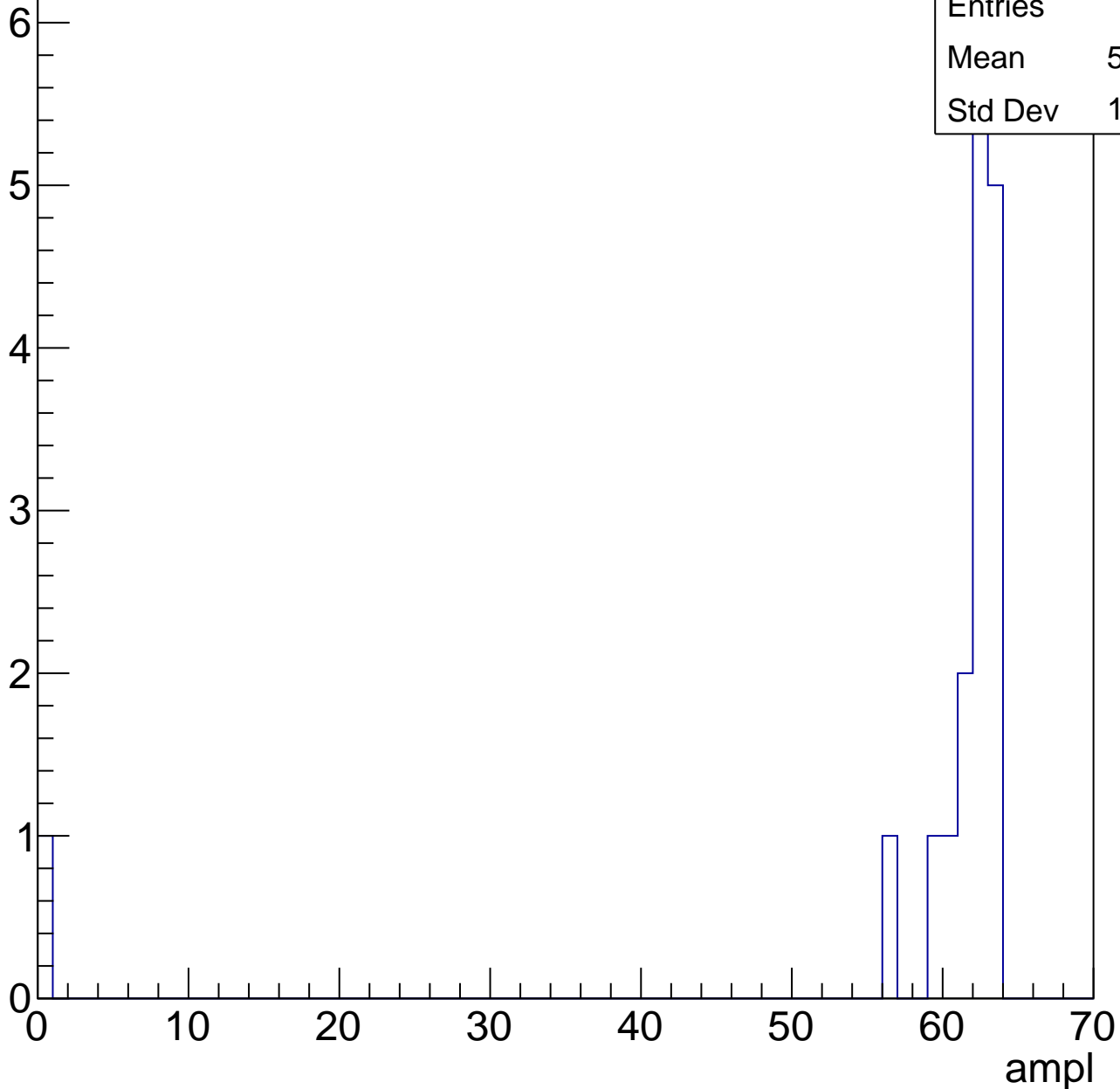


B1L002S, U7-ch49, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	17
Mean	57.88
Std Dev	14.58



B1L002S, U7-ch49, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

B1L002S, U7-ch50, adc0

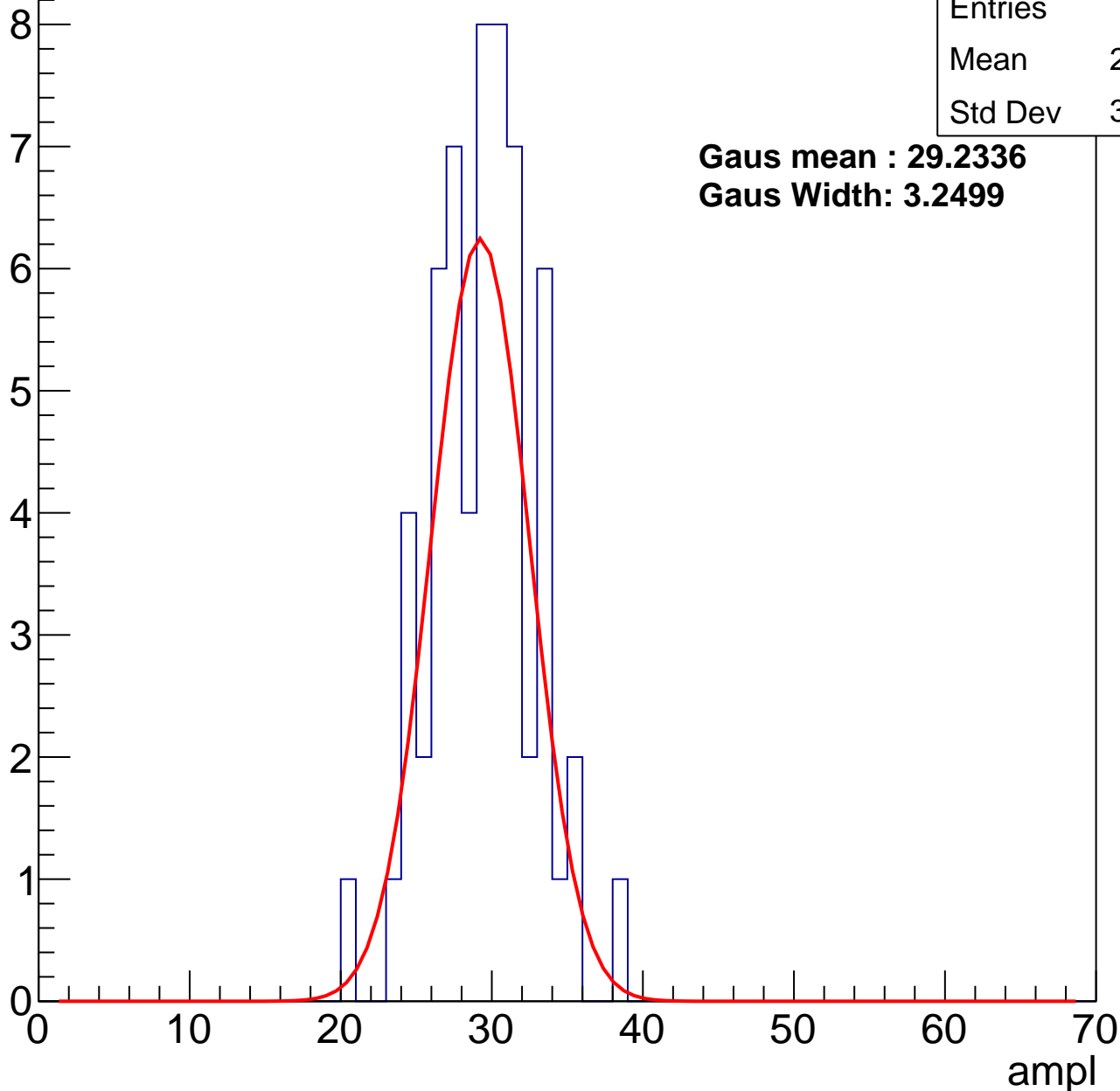
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.98
Std Dev	3.324

Gaus mean : 29.2336

Gaus Width: 3.2499



B1L002S, U7-ch50, adc1

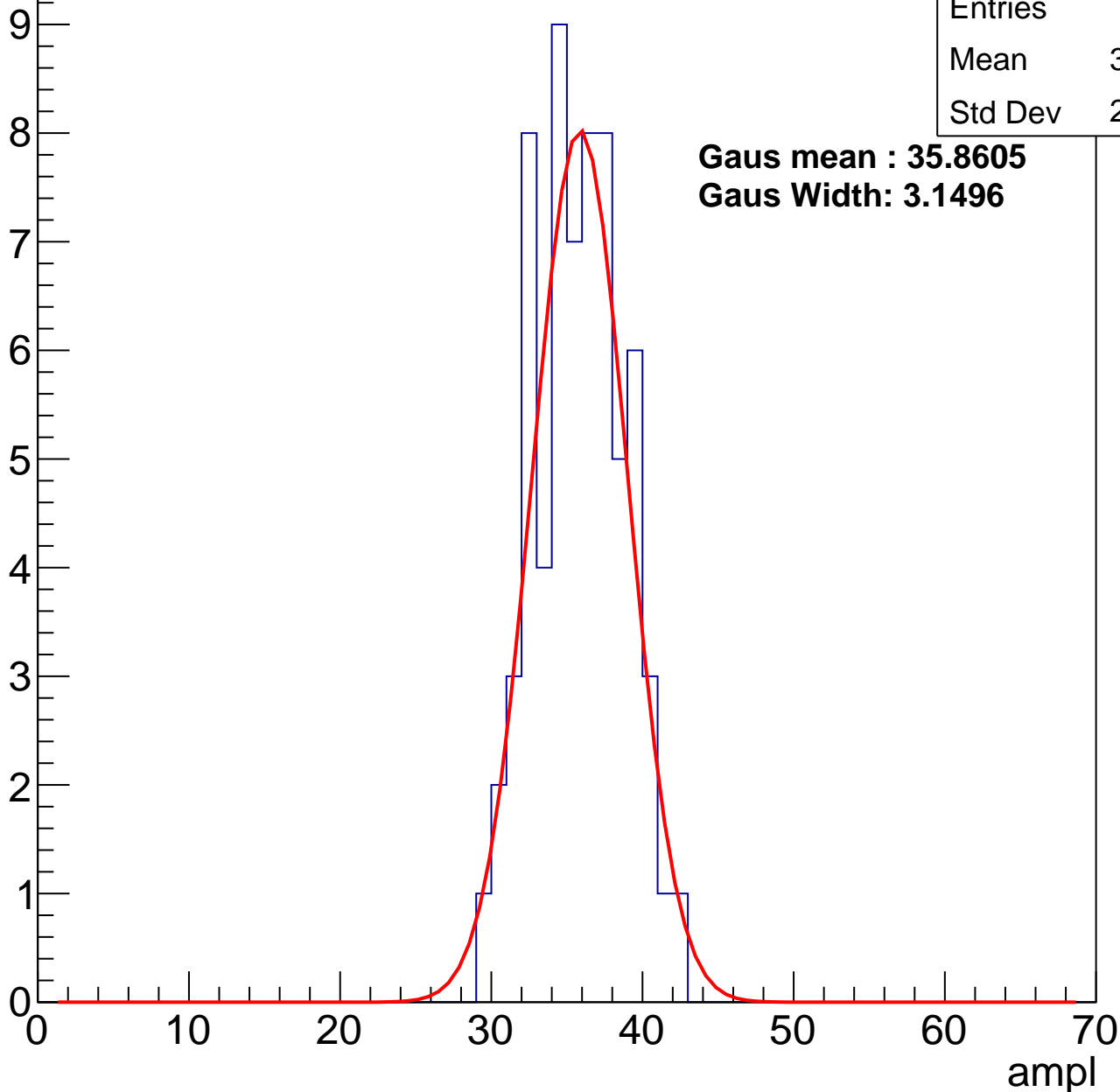
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	35.33
Std Dev	2.925

Gaus mean : 35.8605

Gaus Width: 3.1496



B1L002S, U7-ch50, adc2

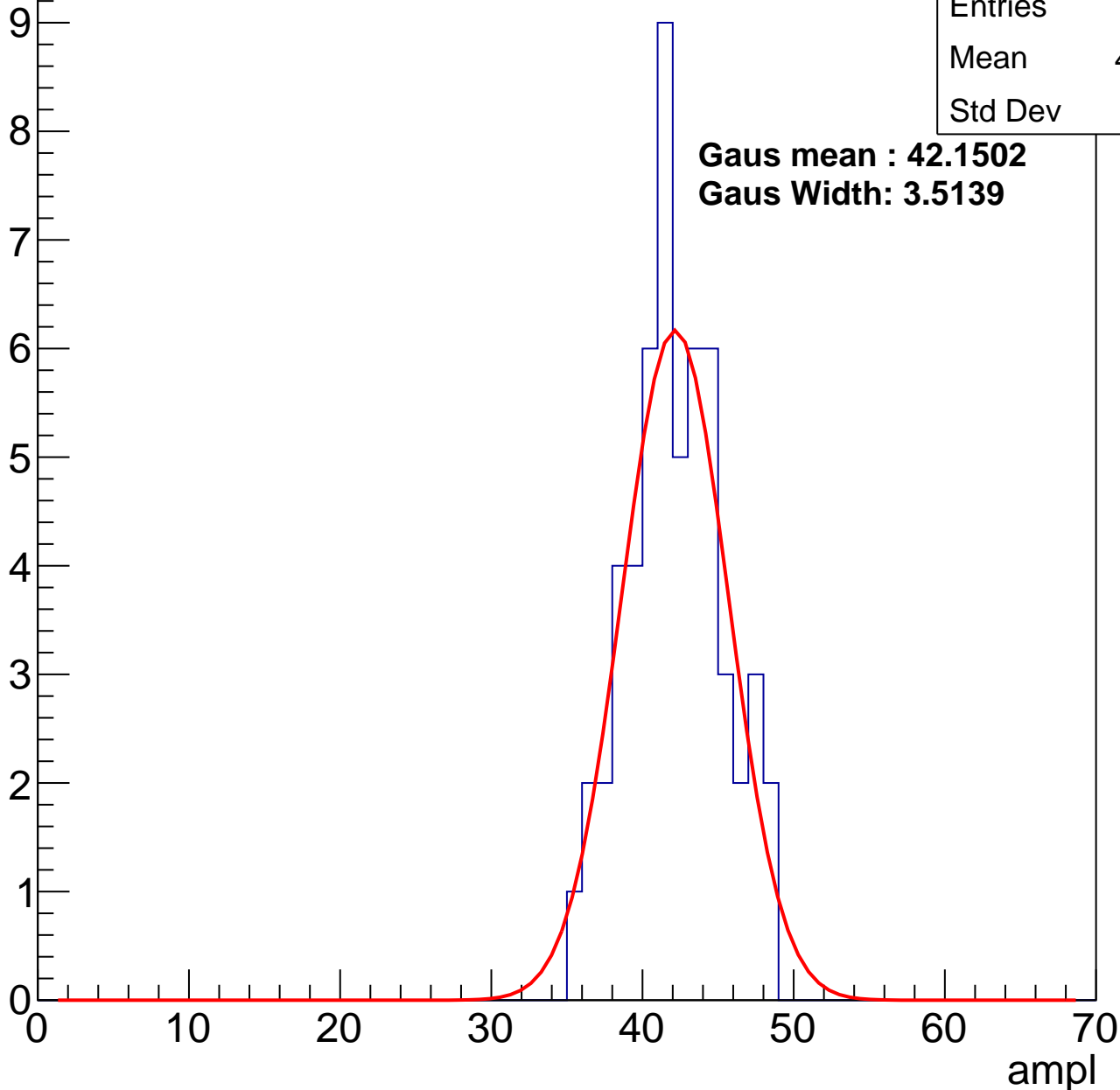
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.71
Std Dev	3.12

Gaus mean : 42.1502

Gaus Width: 3.5139

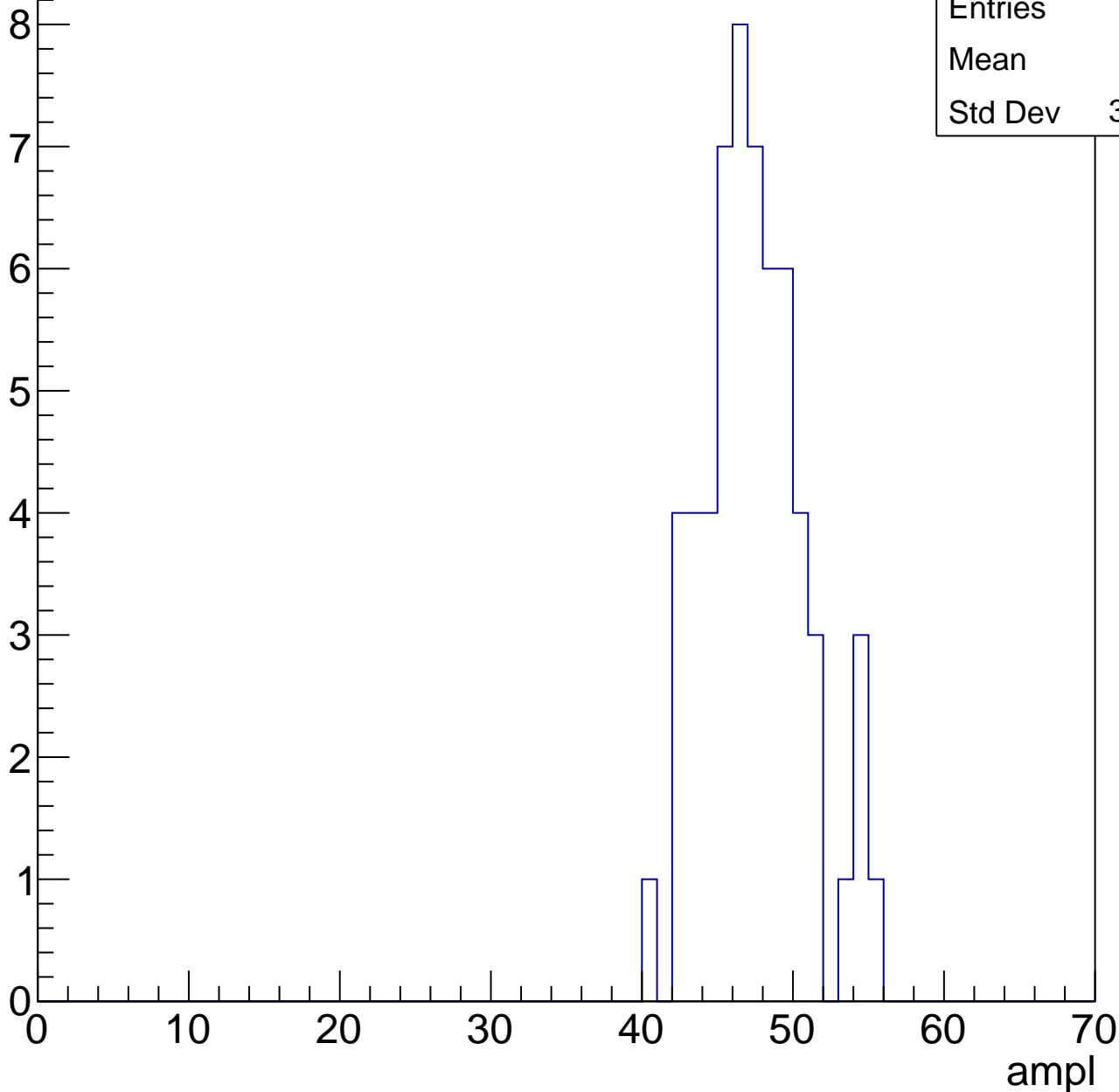


B1L002S, U7-ch50, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47
Std Dev	3.309

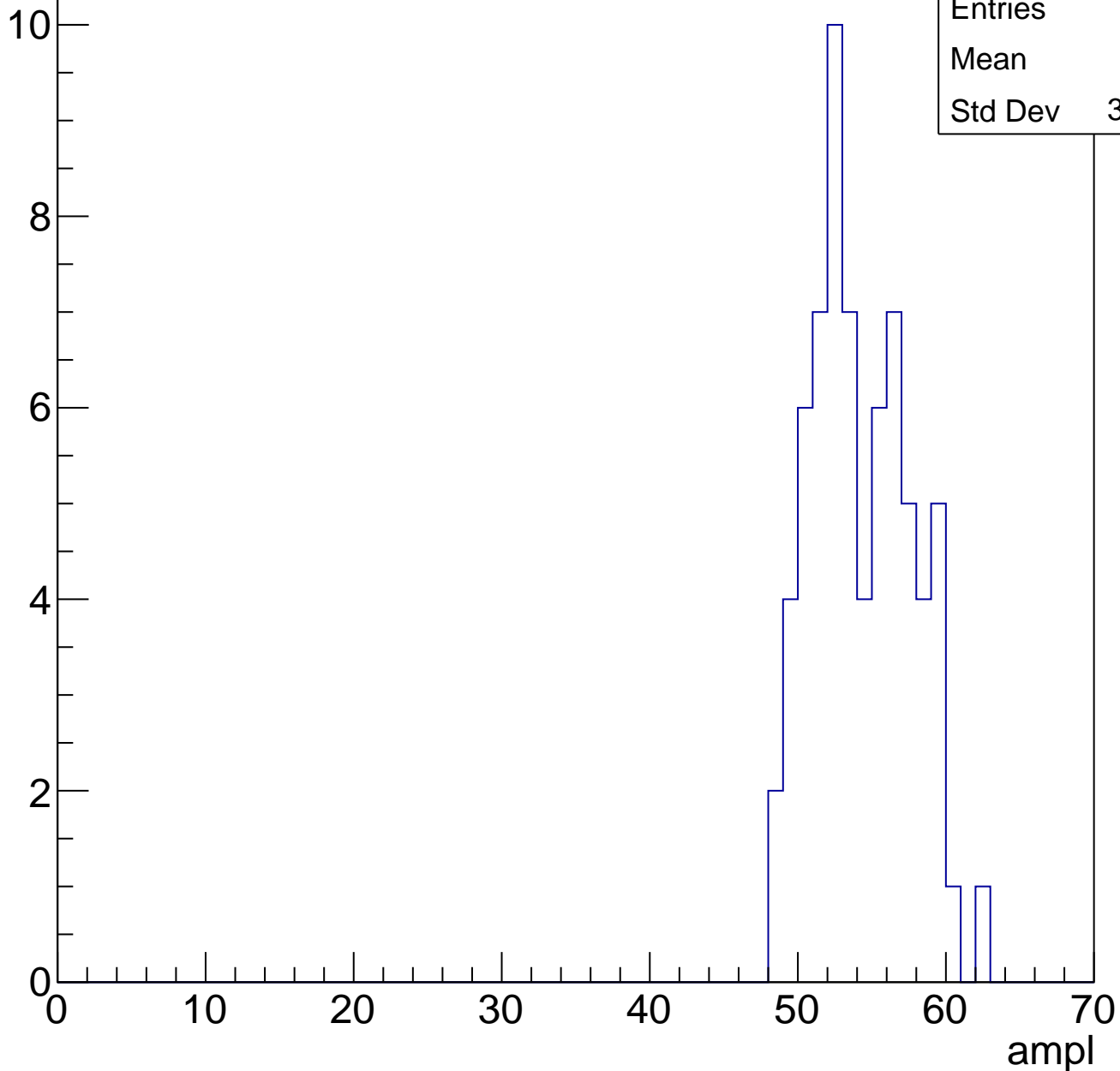


B1L002S, U7-ch50, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	69
Mean	53.8
Std Dev	3.282

Entry



B1L002S, U7-ch50, adc5

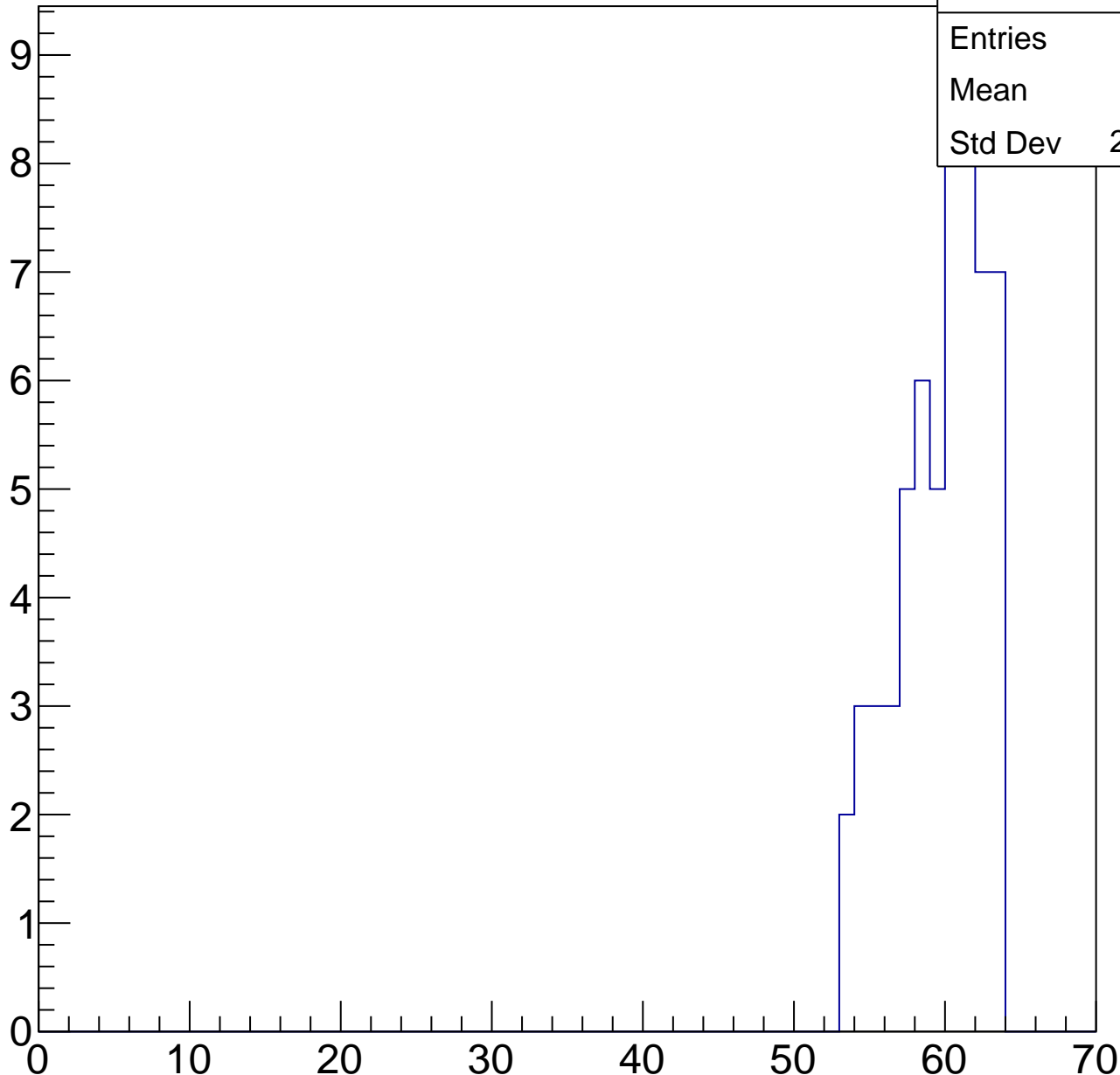
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	59.2
Std Dev	2.809

ampl



B1L002S, U7-ch50, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	7
Mean	53.43
Std Dev	21.82

B1L002S, U7-ch50, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch51, adc0

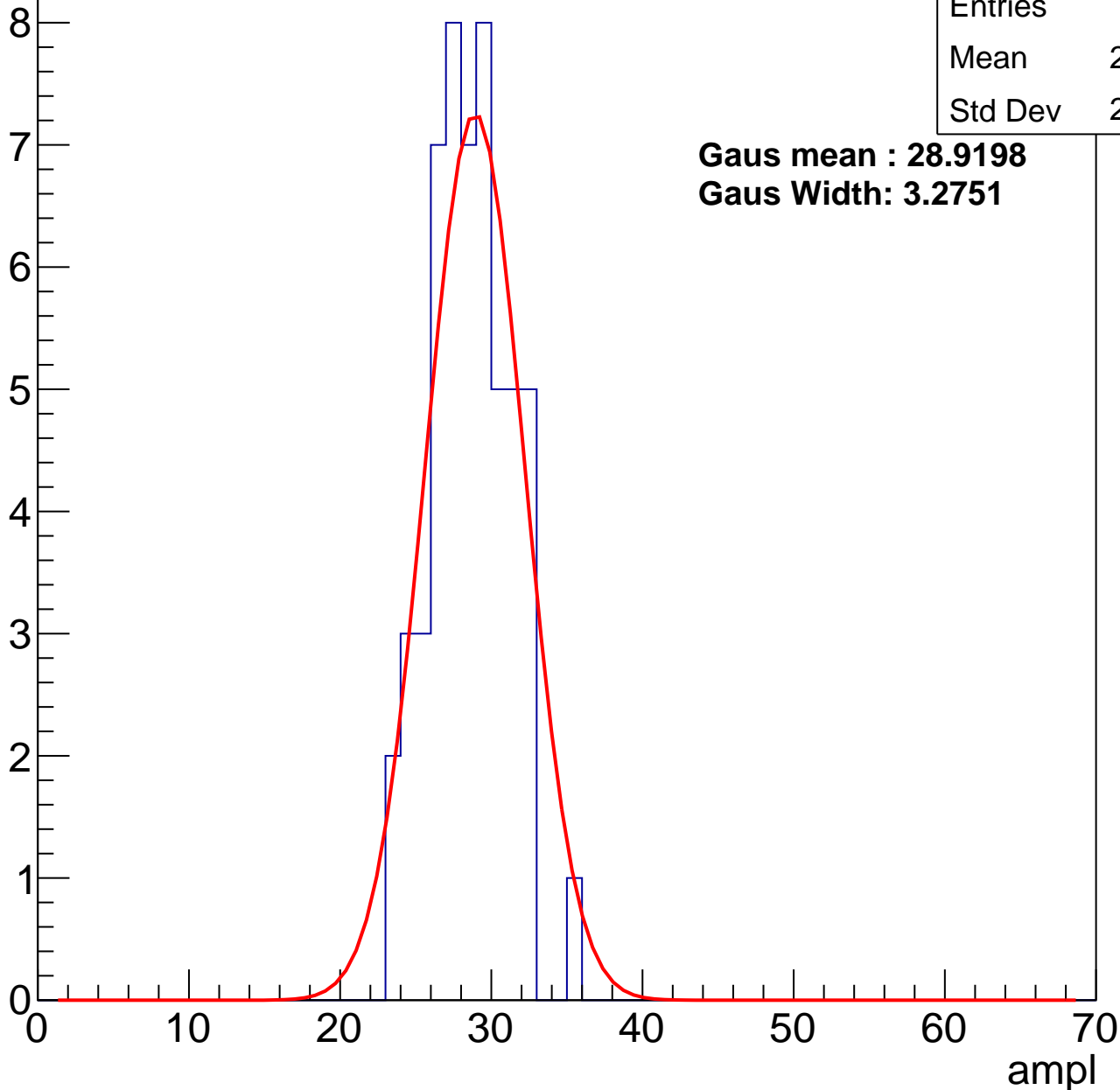
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.13
Std Dev	2.589

Gaus mean : 28.9198

Gaus Width: 3.2751



B1L002S, U7-ch51, adc1

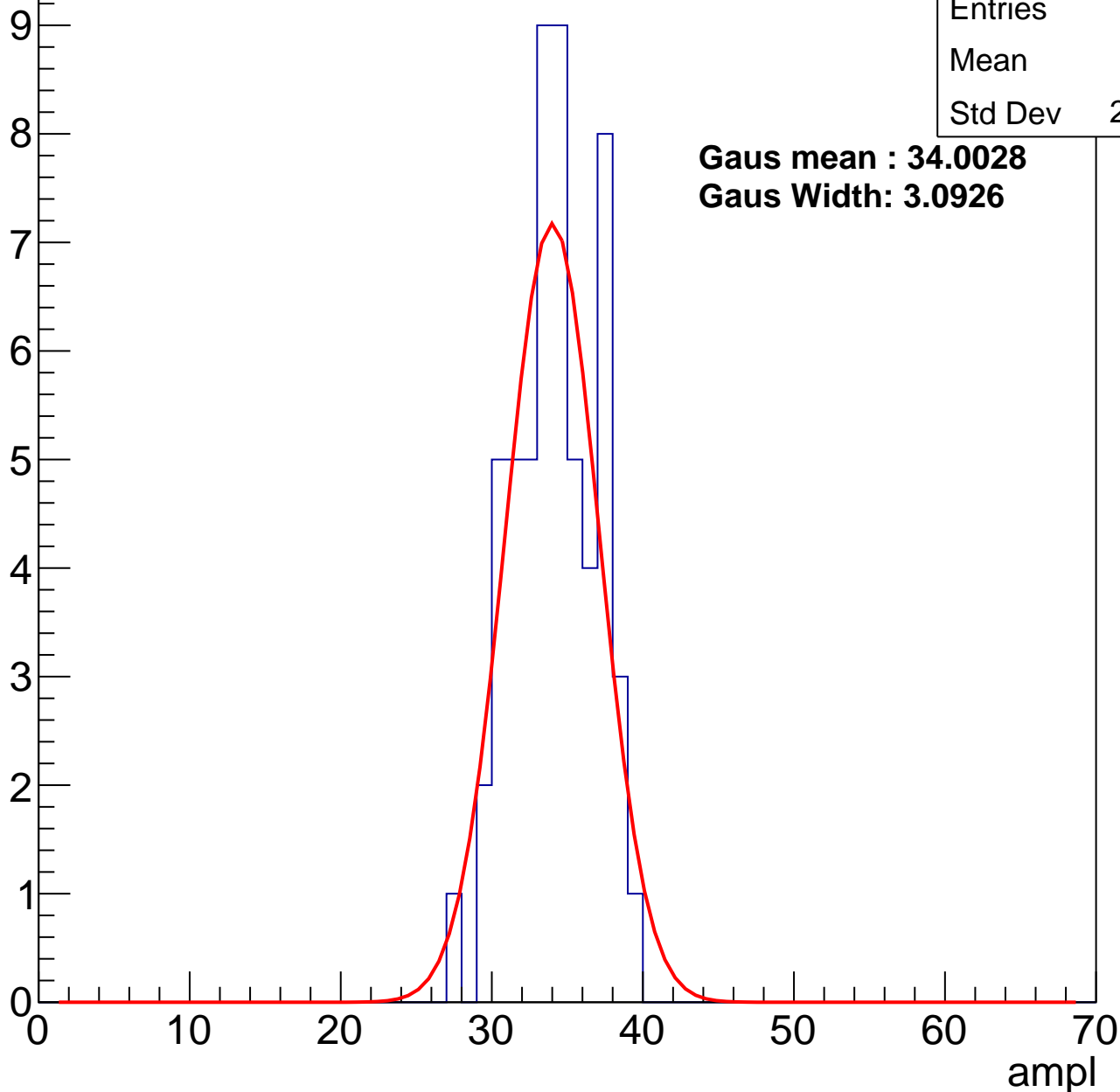
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.7
Std Dev	2.695

Gaus mean : 34.0028

Gaus Width: 3.0926



B1L002S, U7-ch51, adc2

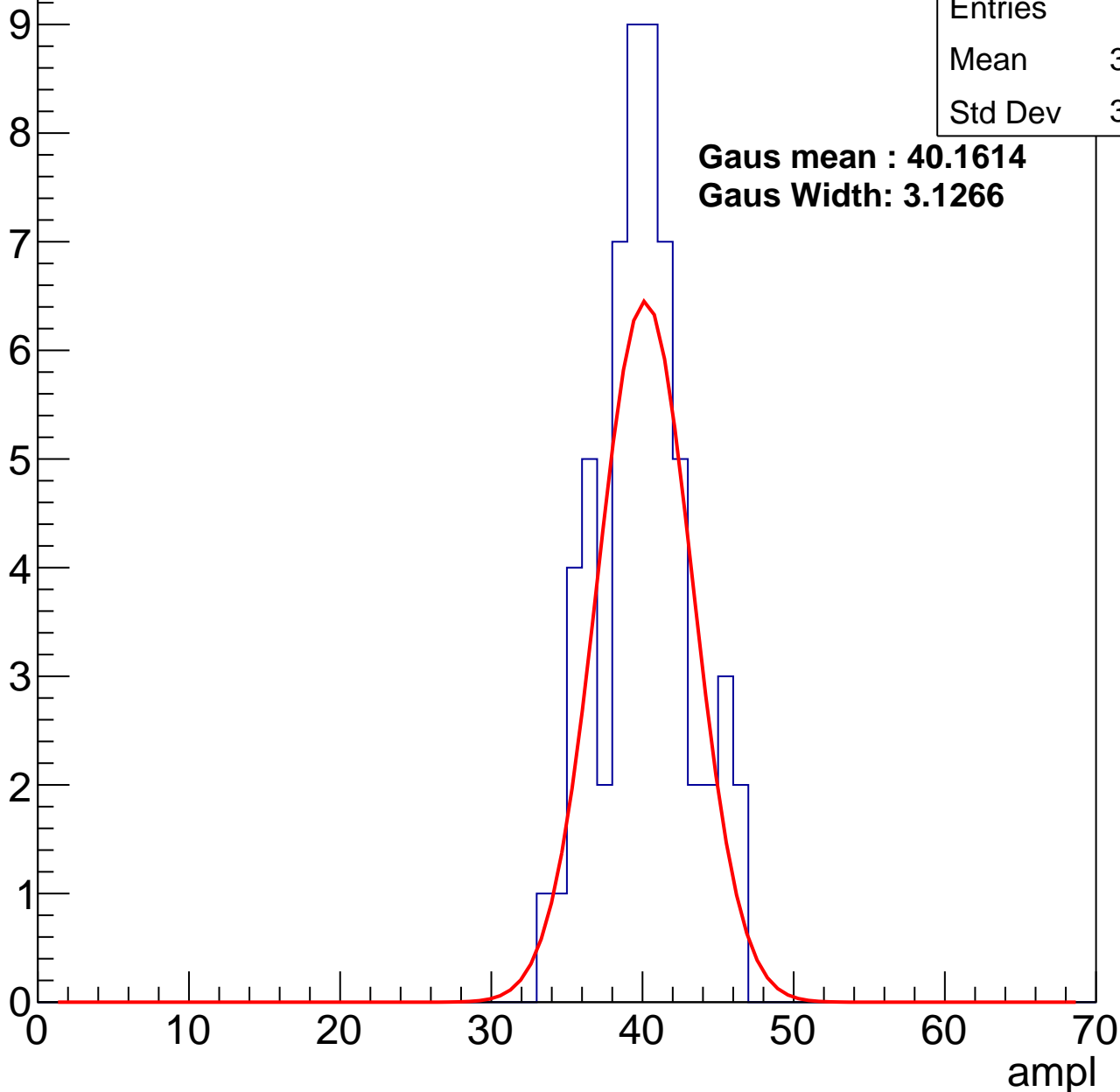
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	39.59
Std Dev	3.009

Gaus mean : 40.1614

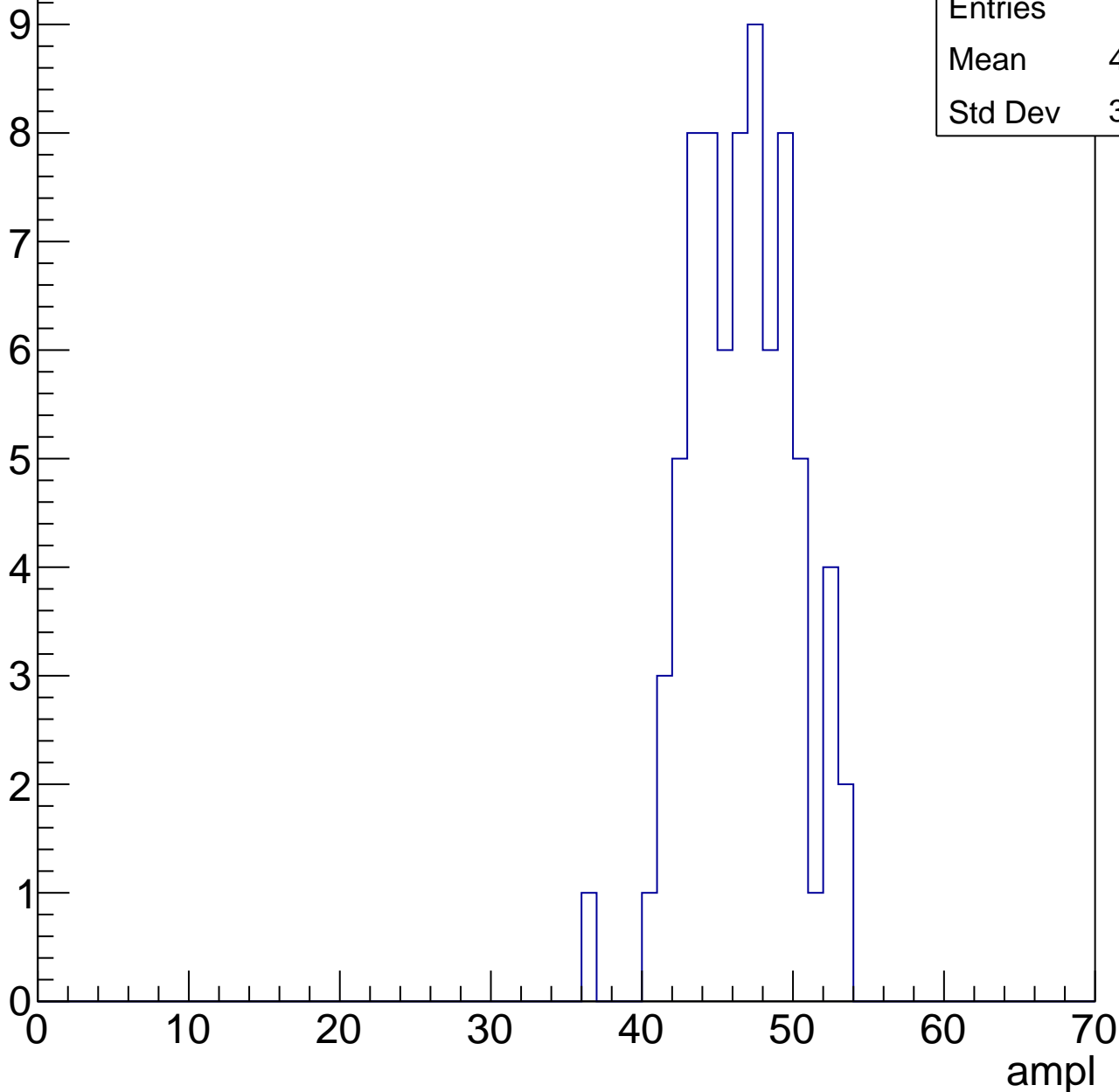
Gaus Width: 3.1266



B1L002S, U7-ch51, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

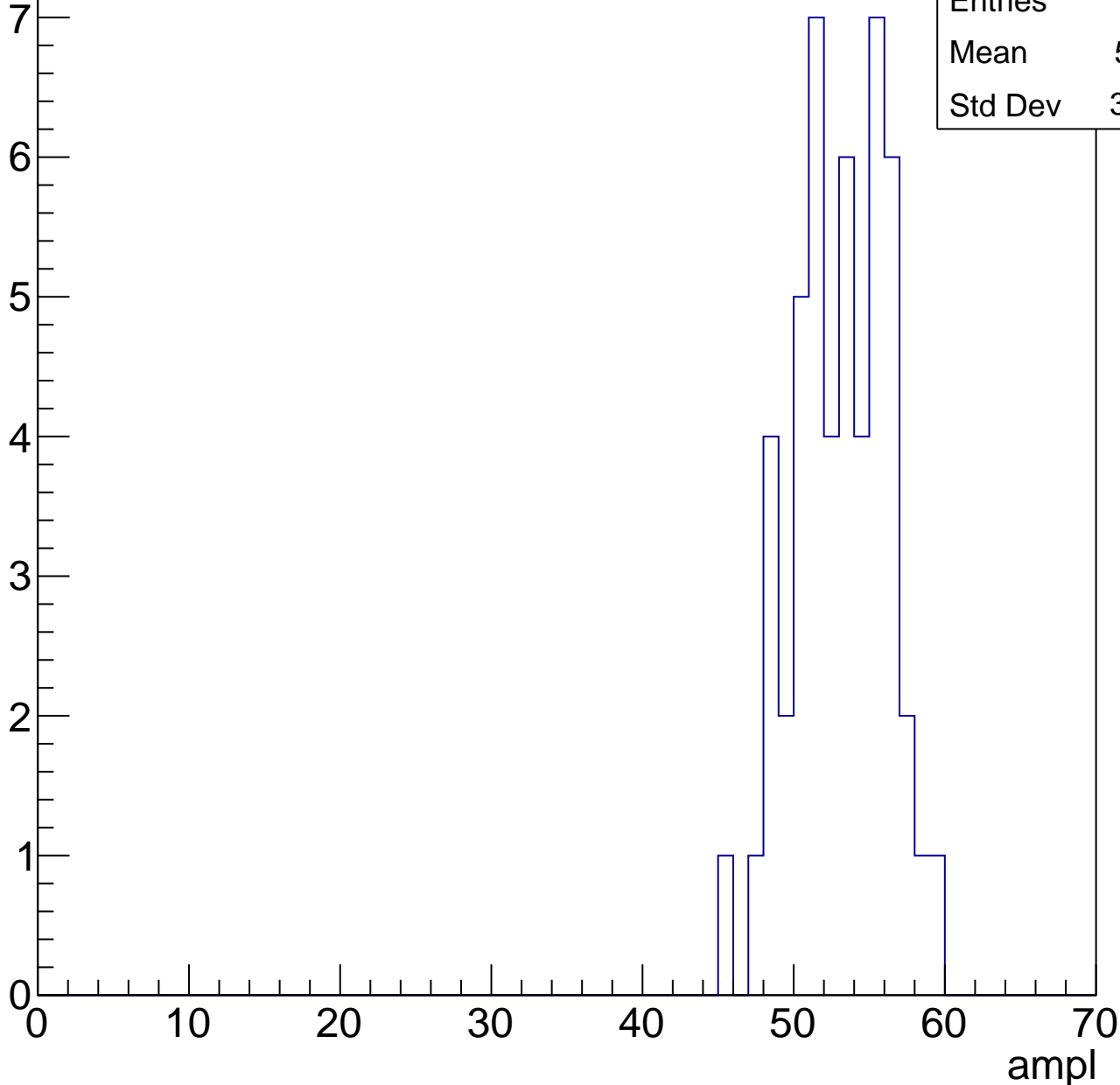


B1L002S, U7-ch51, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	52.61
Std Dev	3.068



B1L002S, U7-ch51, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

10

Entries 75

Mean 58.32

Std Dev 3.133

8

6

4

2

0

0

10

20

30

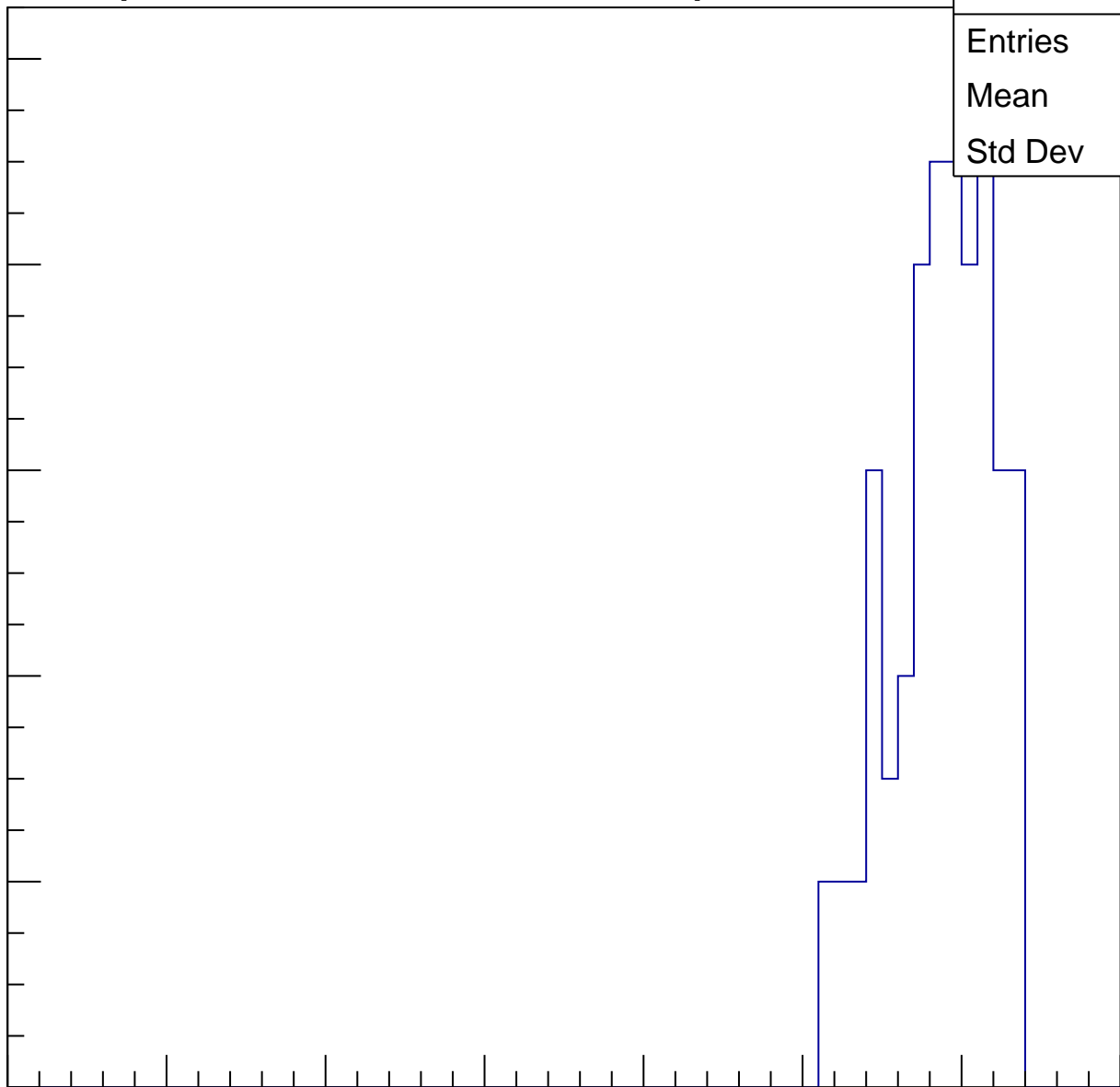
40

50

60

70

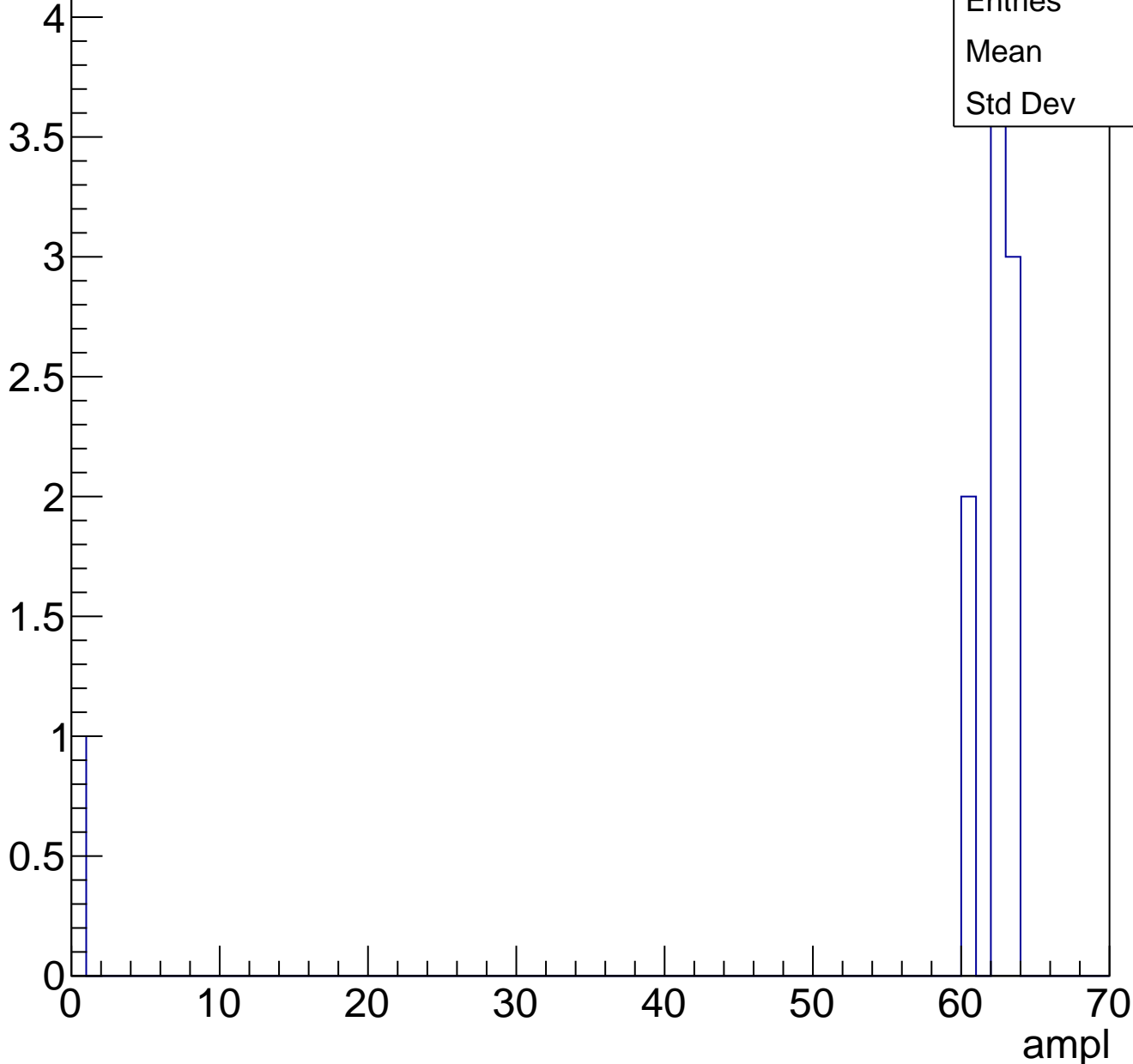
ampl



B1L002S, U7-ch51, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch51, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl

B1L002S, U7-ch52, adc0

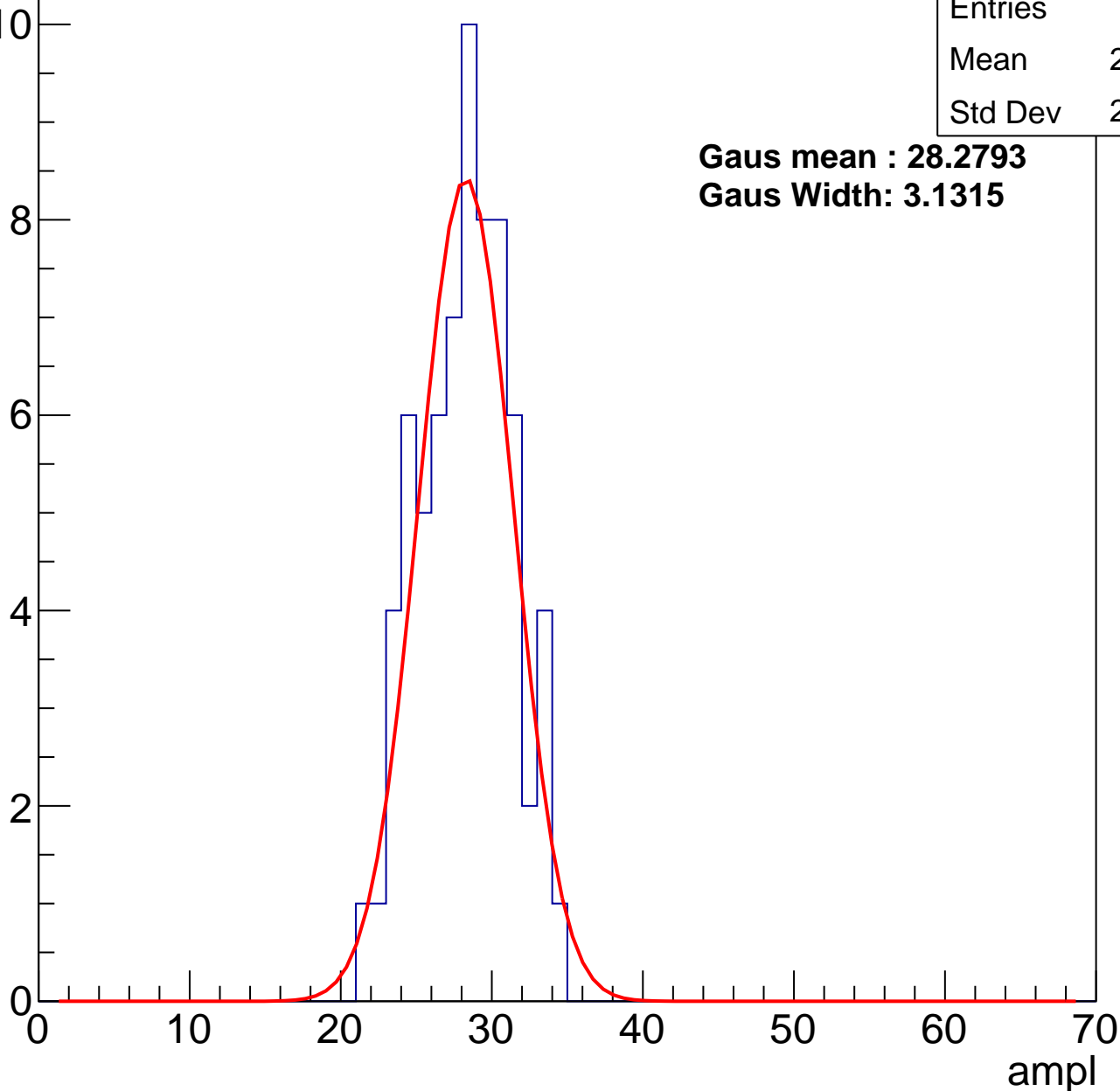
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.78
Std Dev	2.987

Gaus mean : 28.2793

Gaus Width: 3.1315



B1L002S, U7-ch52, adc1

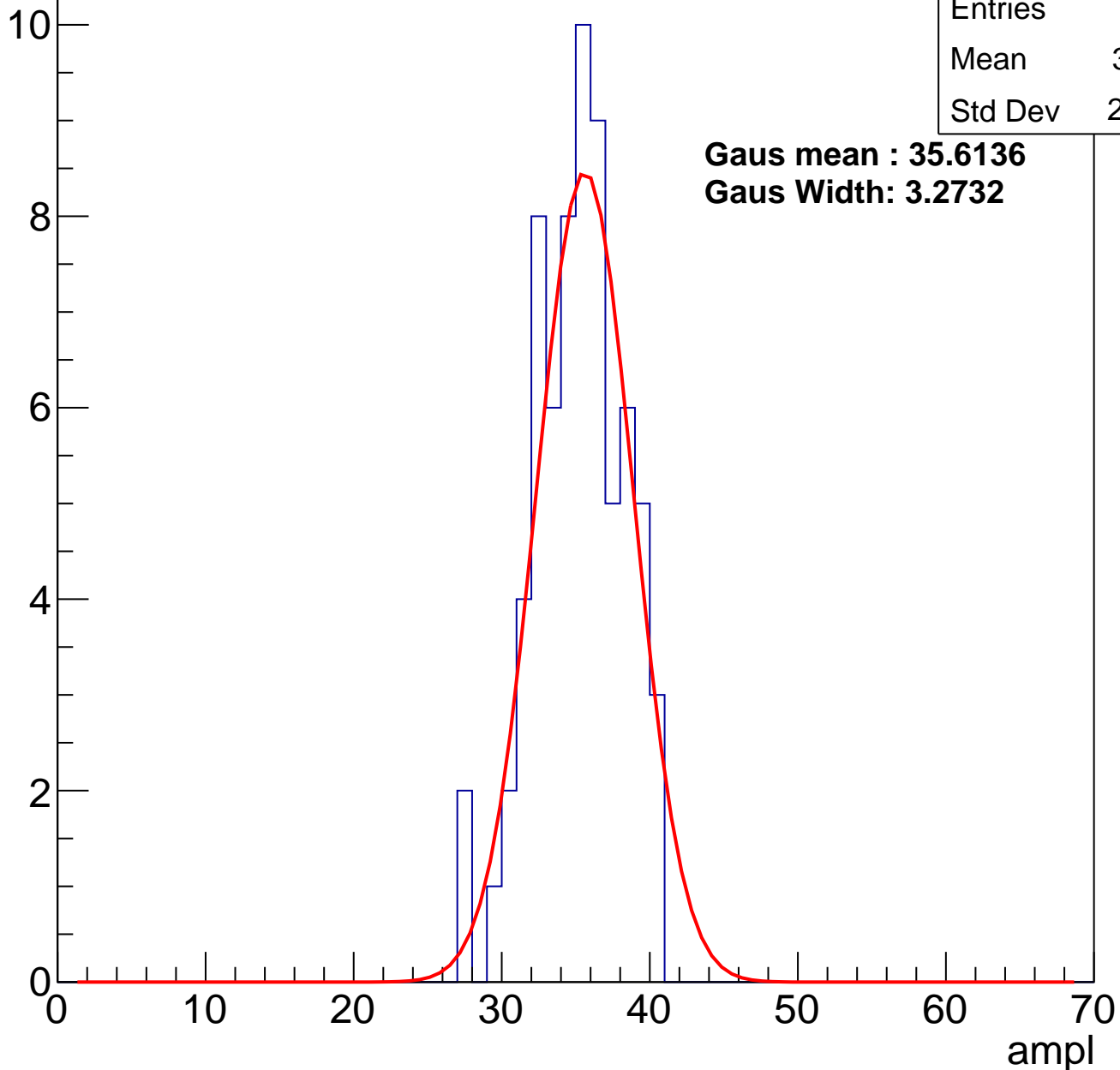
calib_packv5_042523_0143.root, FC#10, port B3

Entries	69
Mean	34.71
Std Dev	2.984

Gaus mean : 35.6136

Gaus Width: 3.2732

Entry



B1L002S, U7-ch52, adc2

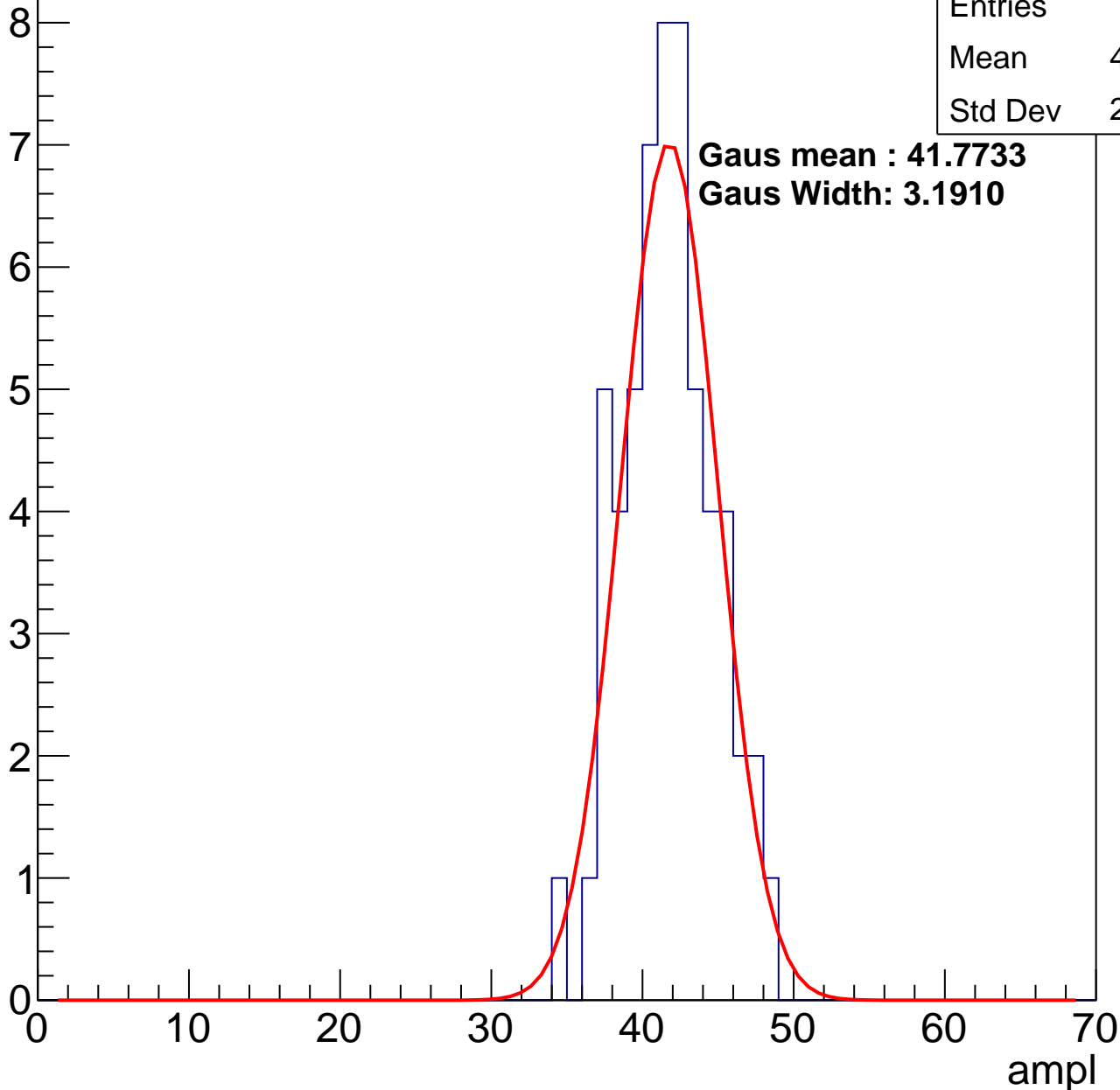
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.25
Std Dev	2.999

Gaus mean : 41.7733

Gaus Width: 3.1910

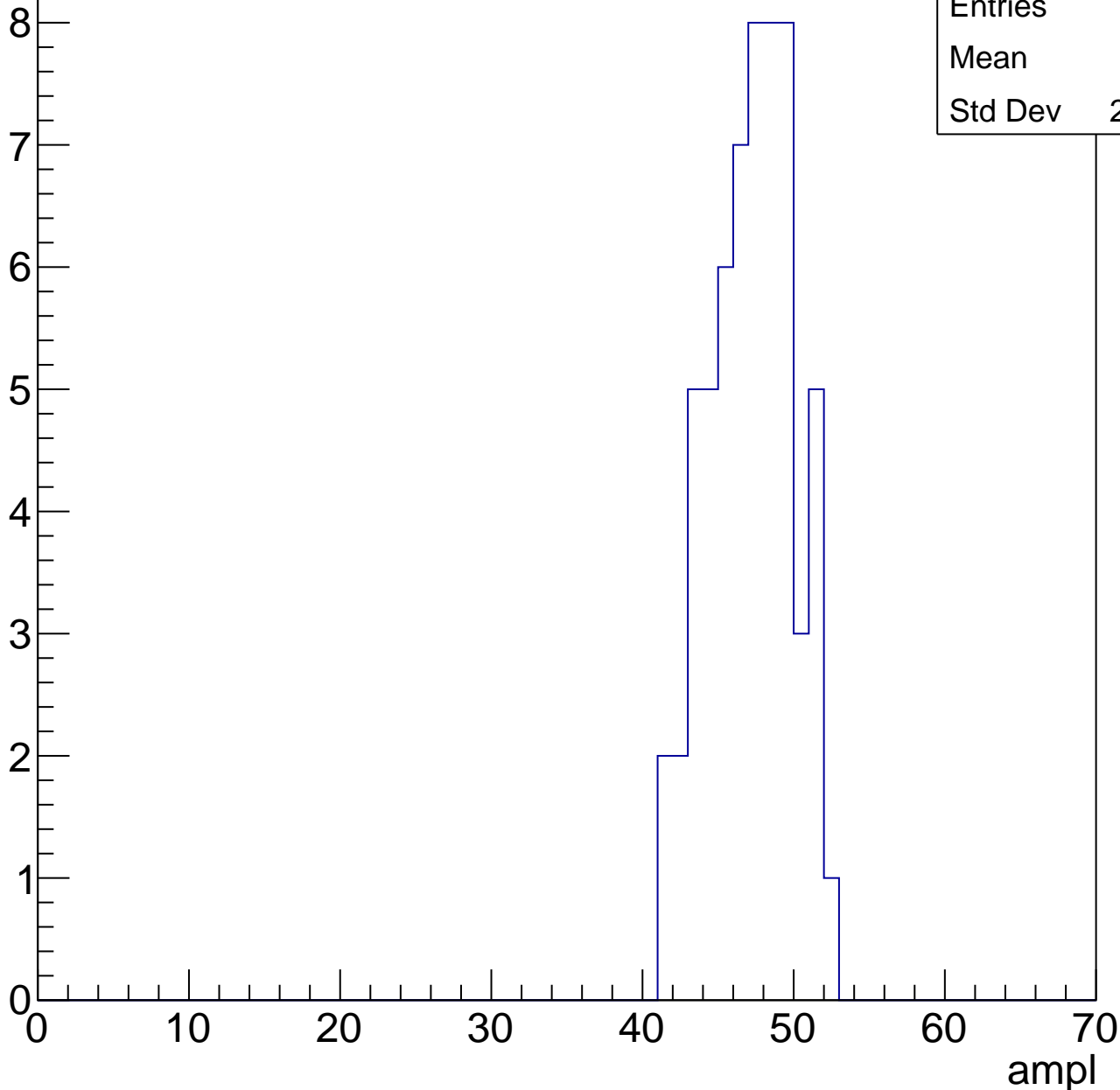


B1L002S, U7-ch52, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.7
Std Dev	2.722

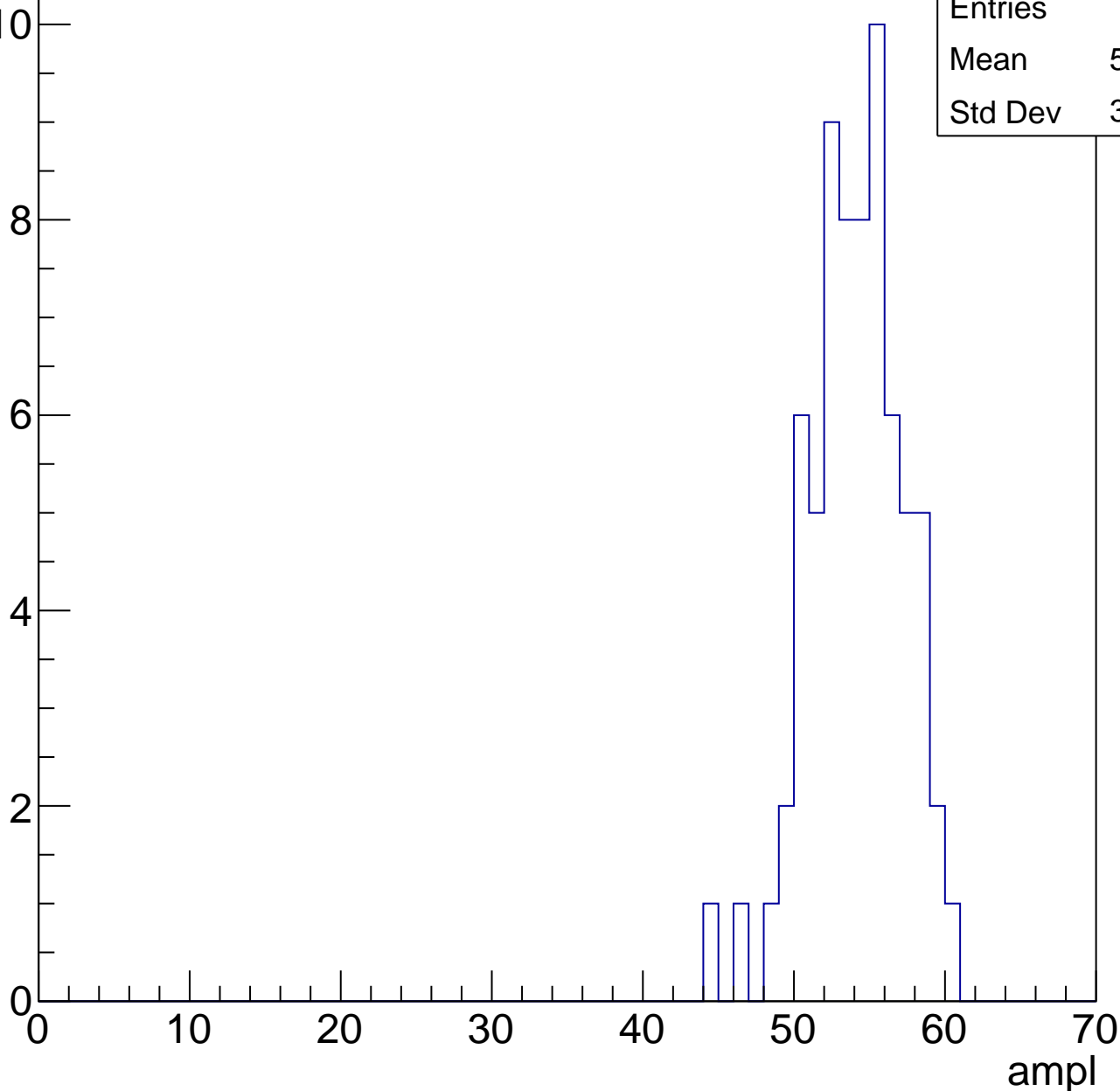


B1L002S, U7-ch52, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	53.63
Std Dev	3.095

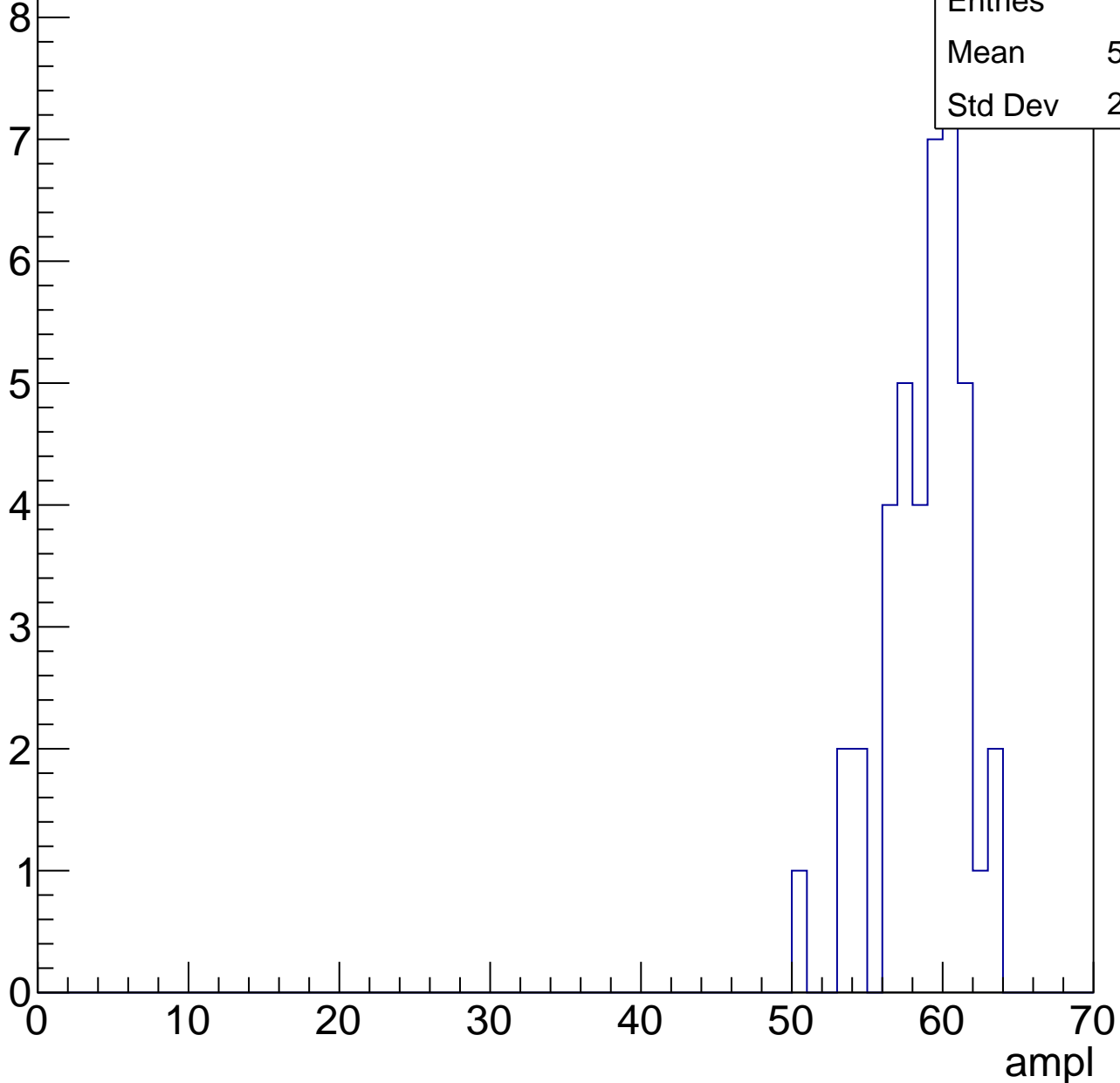


B1L002S, U7-ch52, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	41
Mean	58.32
Std Dev	2.762

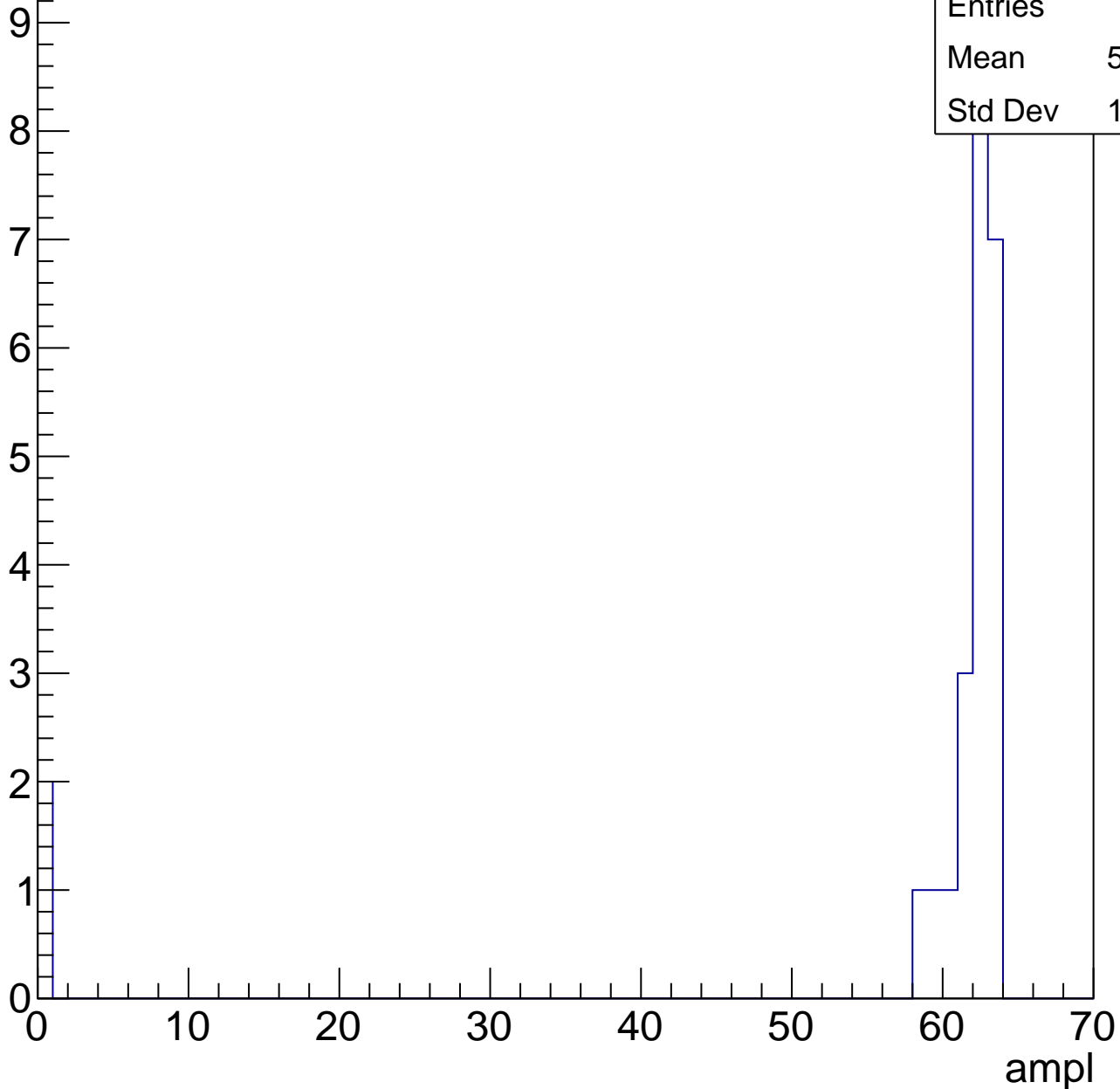


B1L002S, U7-ch52, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

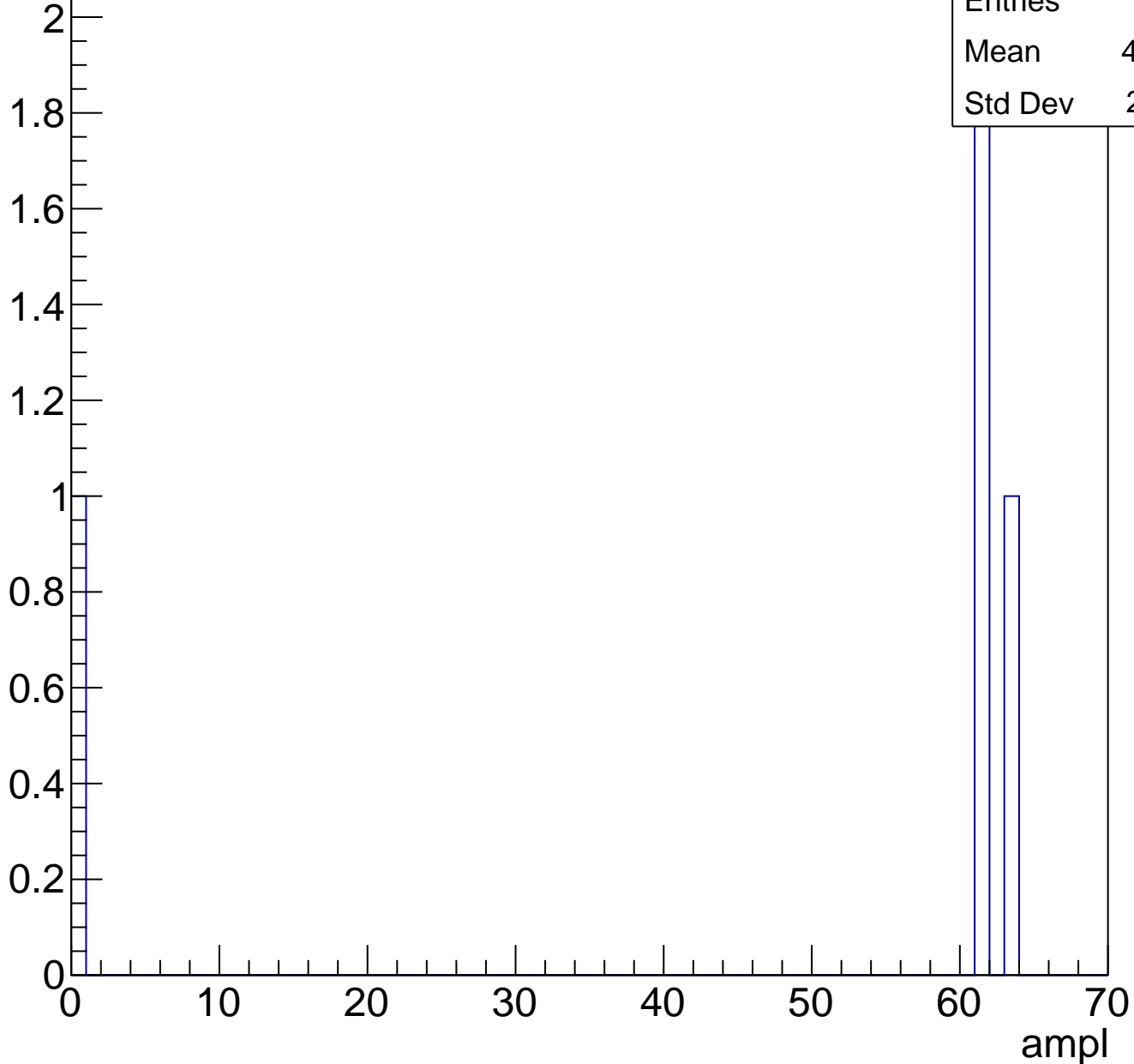
Entries	24
Mean	56.62
Std Dev	17.12



B1L002S, U7-ch52, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	46.25
Std Dev	26.71

B1L002S, U7-ch53, adc0

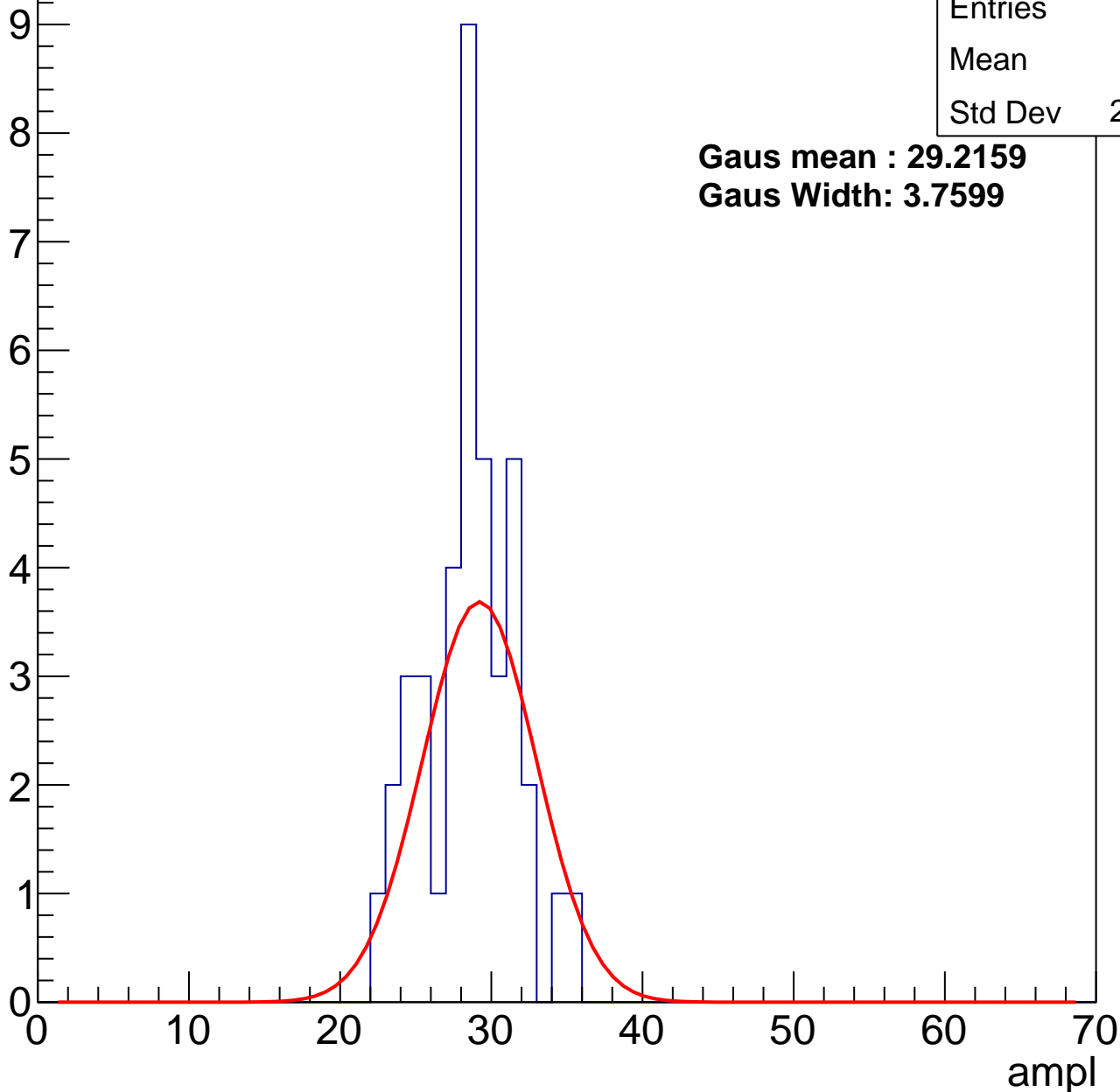
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	40
Mean	28.1
Std Dev	2.948

Gaus mean : 29.2159

Gaus Width: 3.7599



B1L002S, U7-ch53, adc1

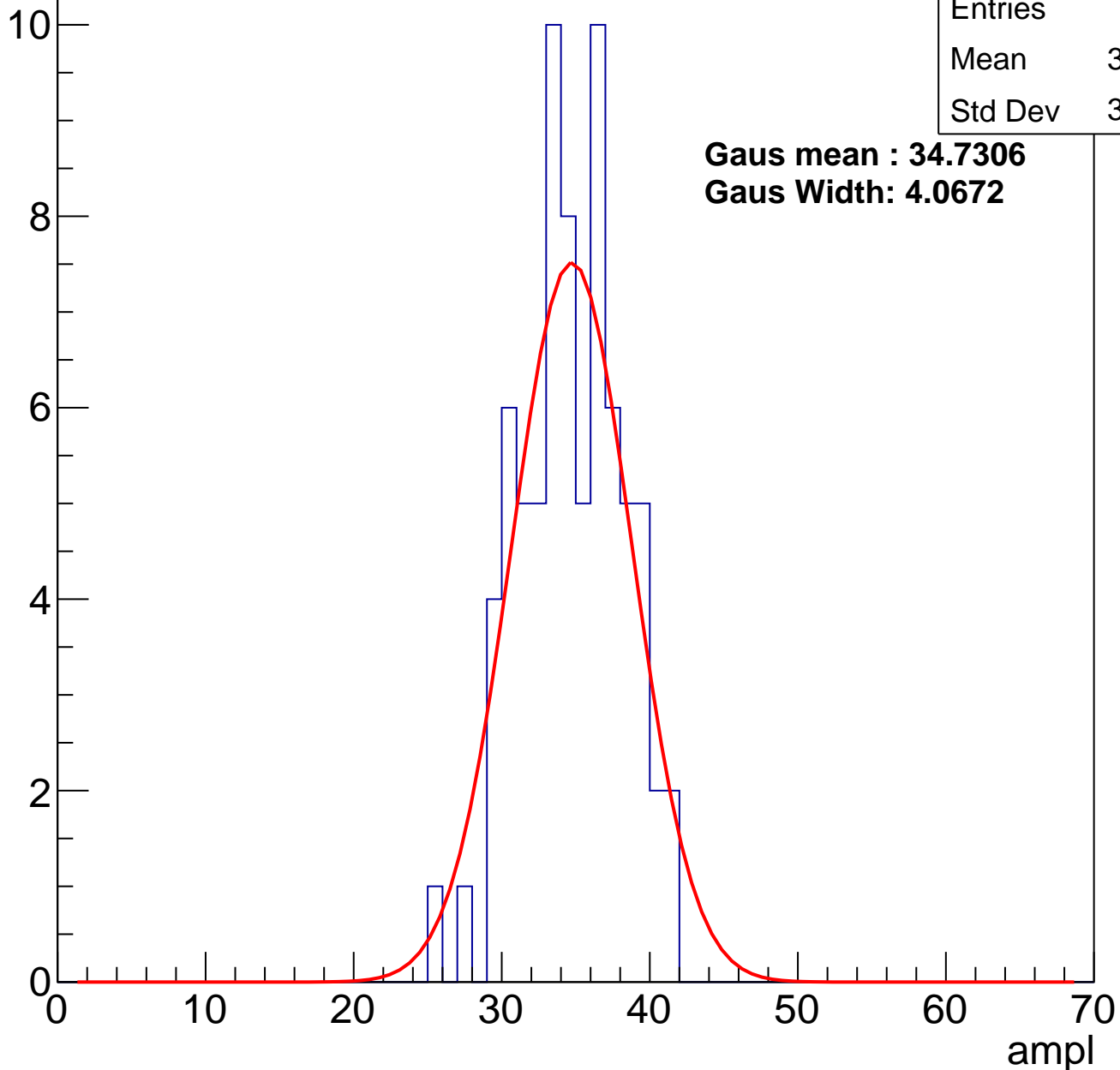
calib_packv5_042523_0143.root, FC#10, port B3

Entries	75
Mean	34.25
Std Dev	3.406

Gaus mean : 34.7306

Gaus Width: 4.0672

Entry



B1L002S, U7-ch53, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	79
Mean	41.87
Std Dev	3.523

Gaus mean : 42.3965

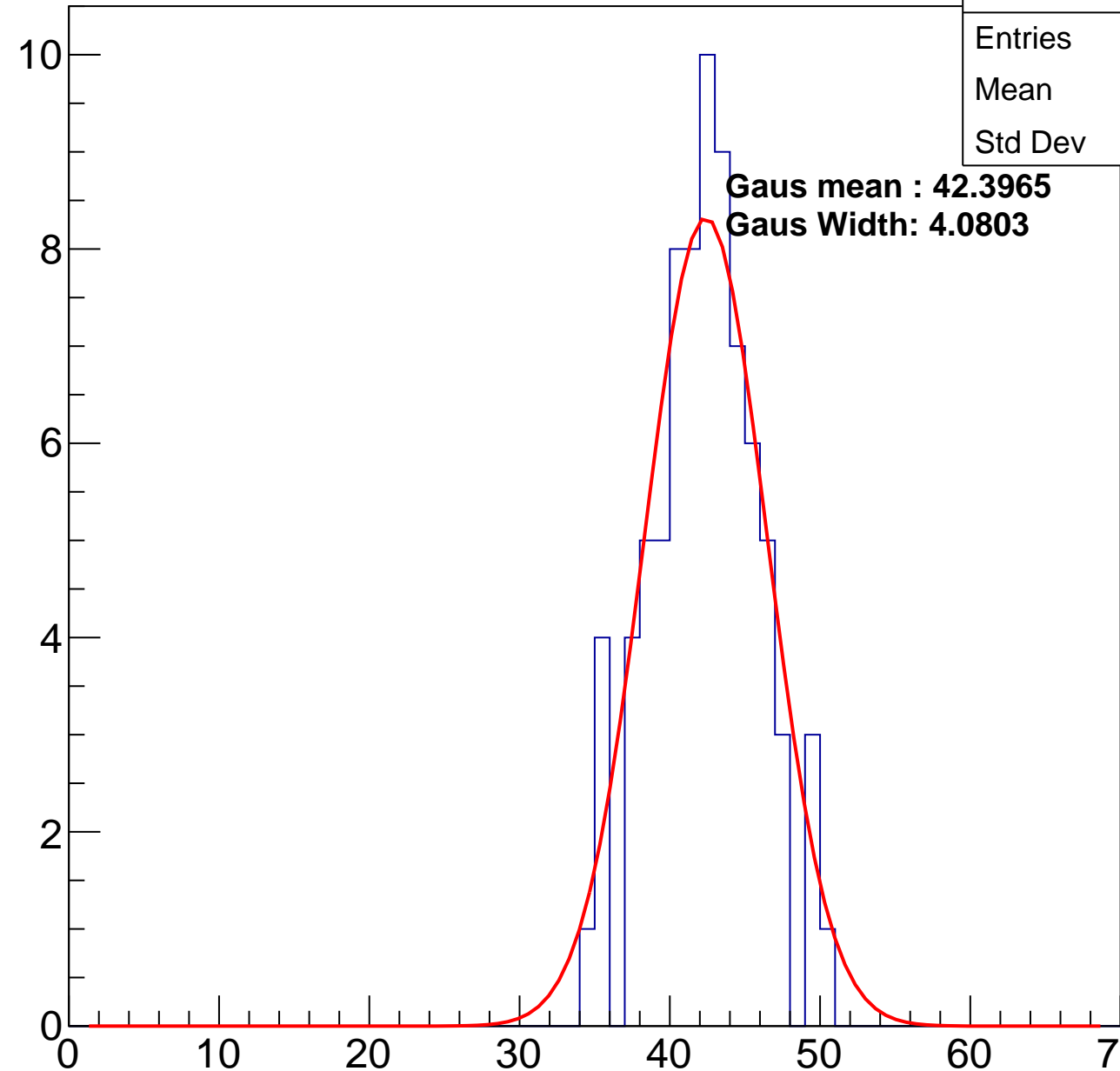
Gaus Width: 4.0803

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

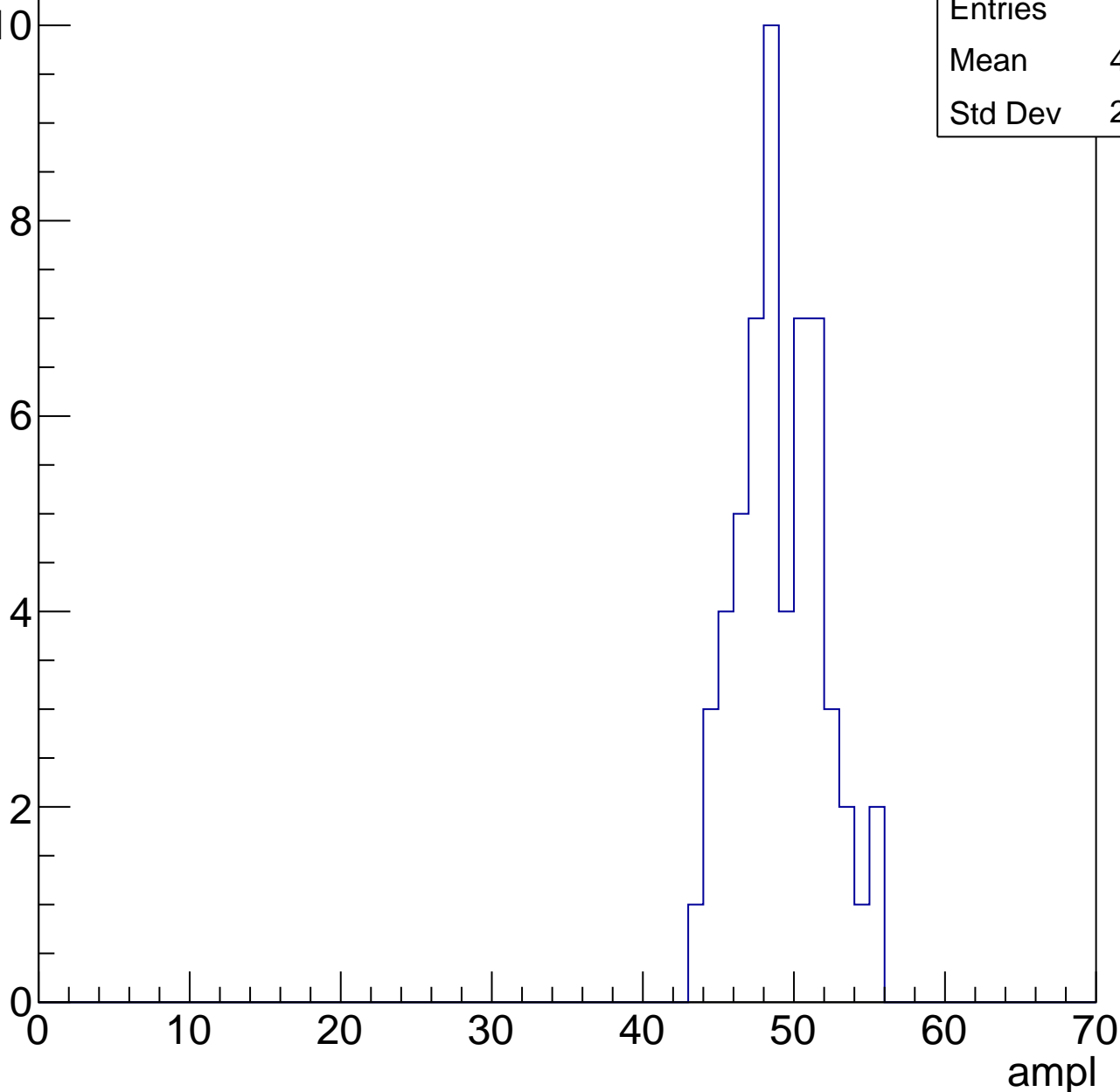


B1L002S, U7-ch53, adc3

calib_packv5_042523_0143.root, FC#10, port B3

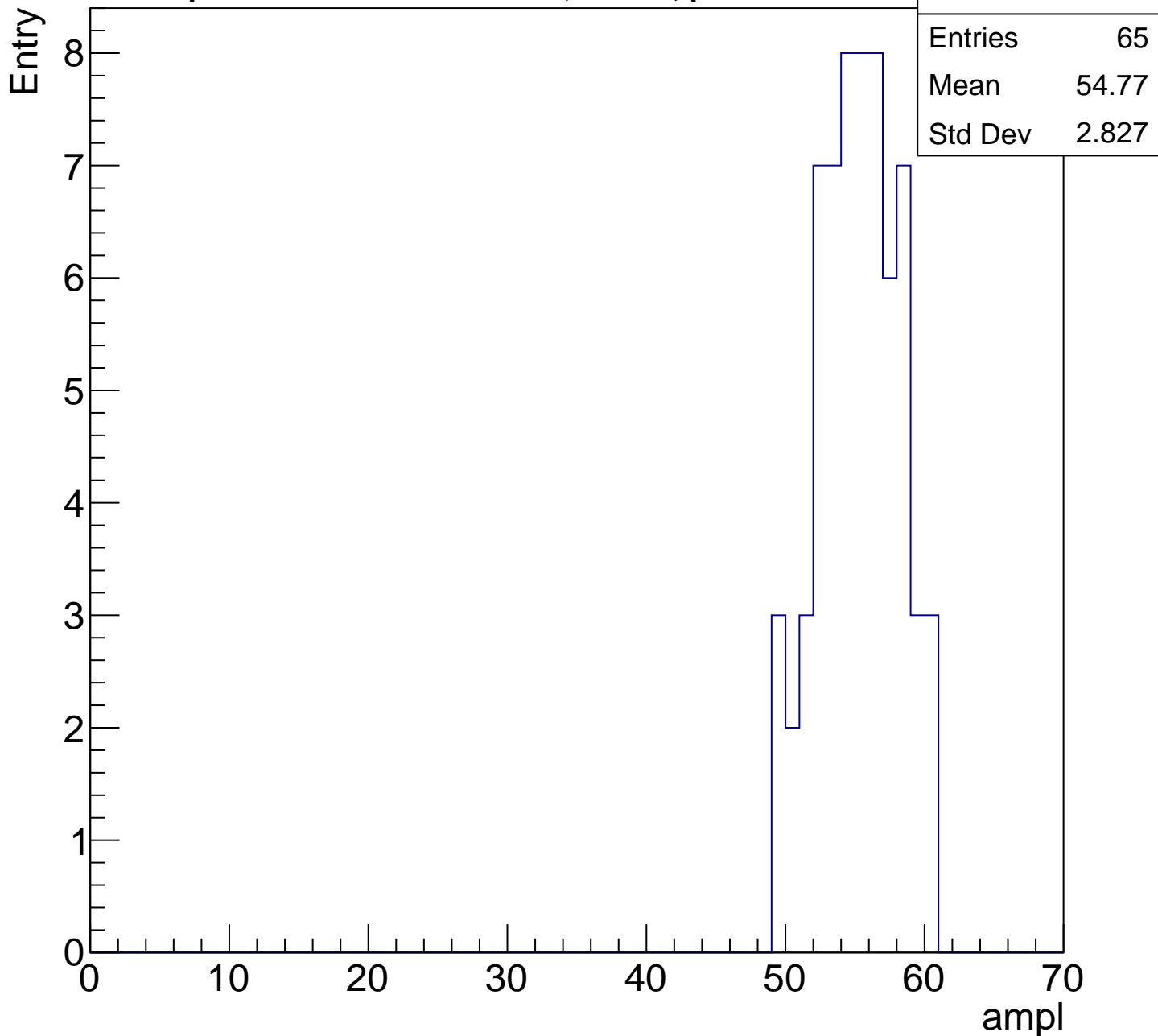
Entry

Entries	56
Mean	48.62
Std Dev	2.807



B1L002S, U7-ch53, adc4

calib_packv5_042523_0143.root, FC#10, port B3

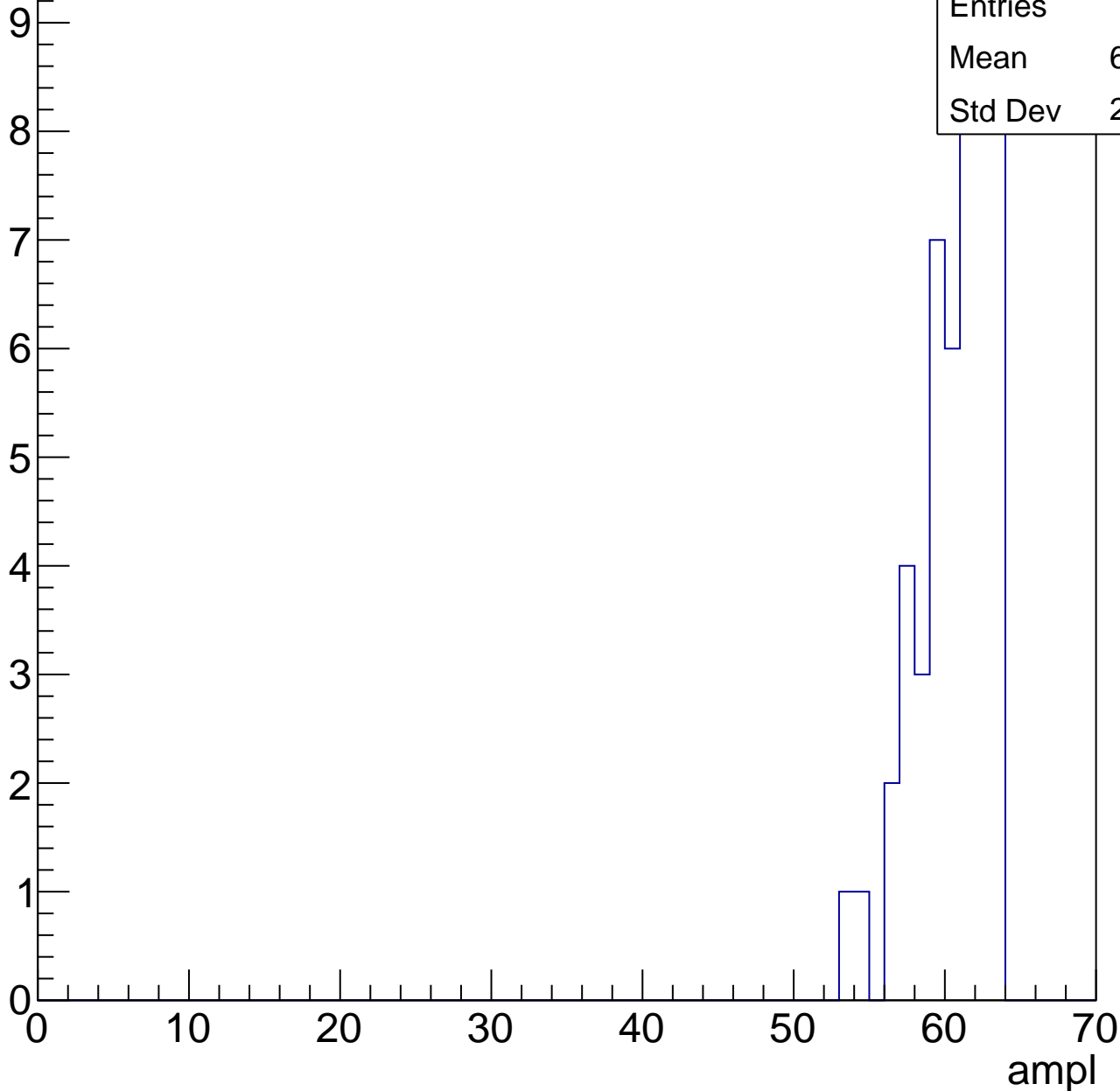


B1L002S, U7-ch53, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

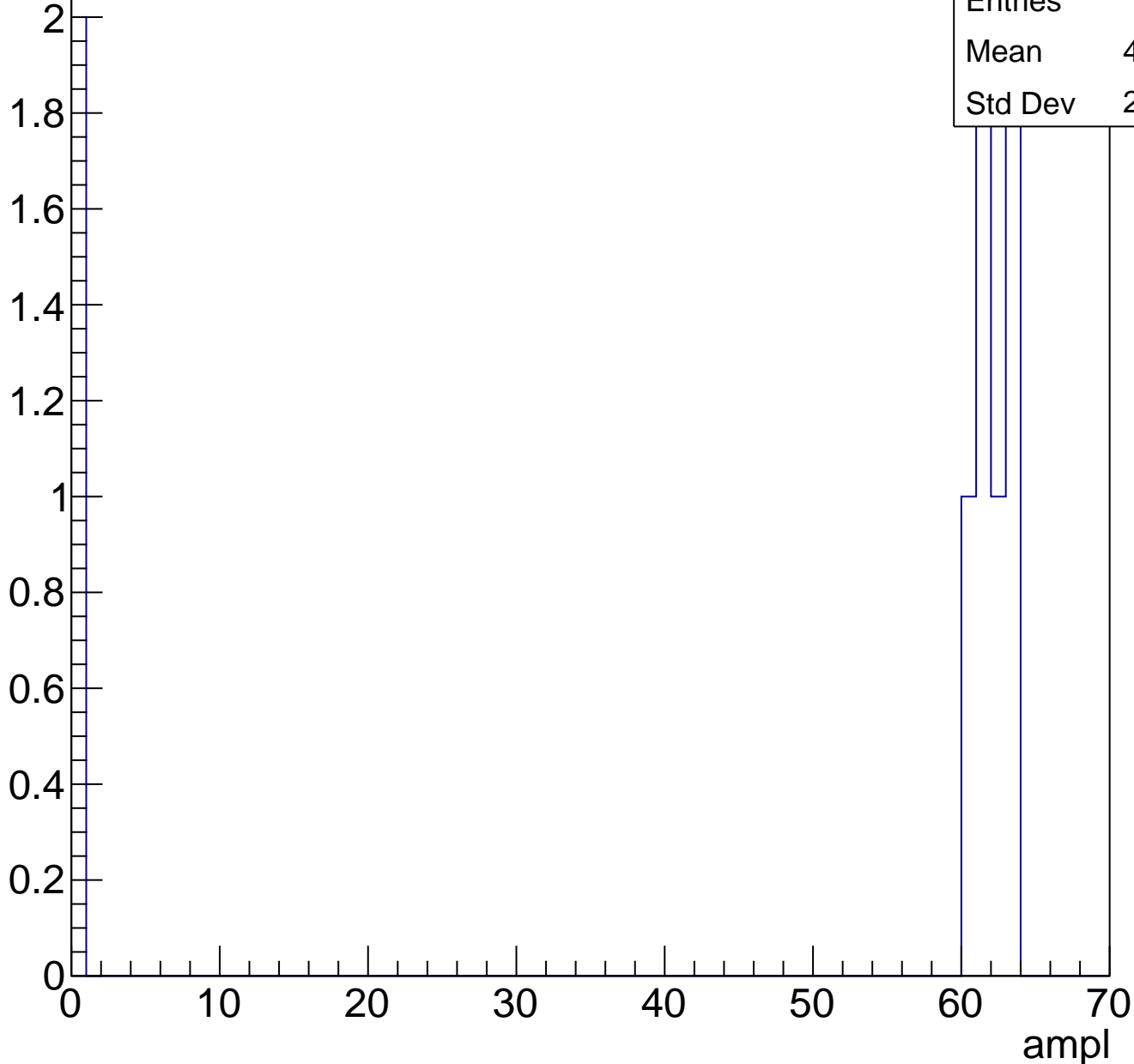
Entries	49
Mean	60.08
Std Dev	2.423



B1L002S, U7-ch53, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch53, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch54, adc0

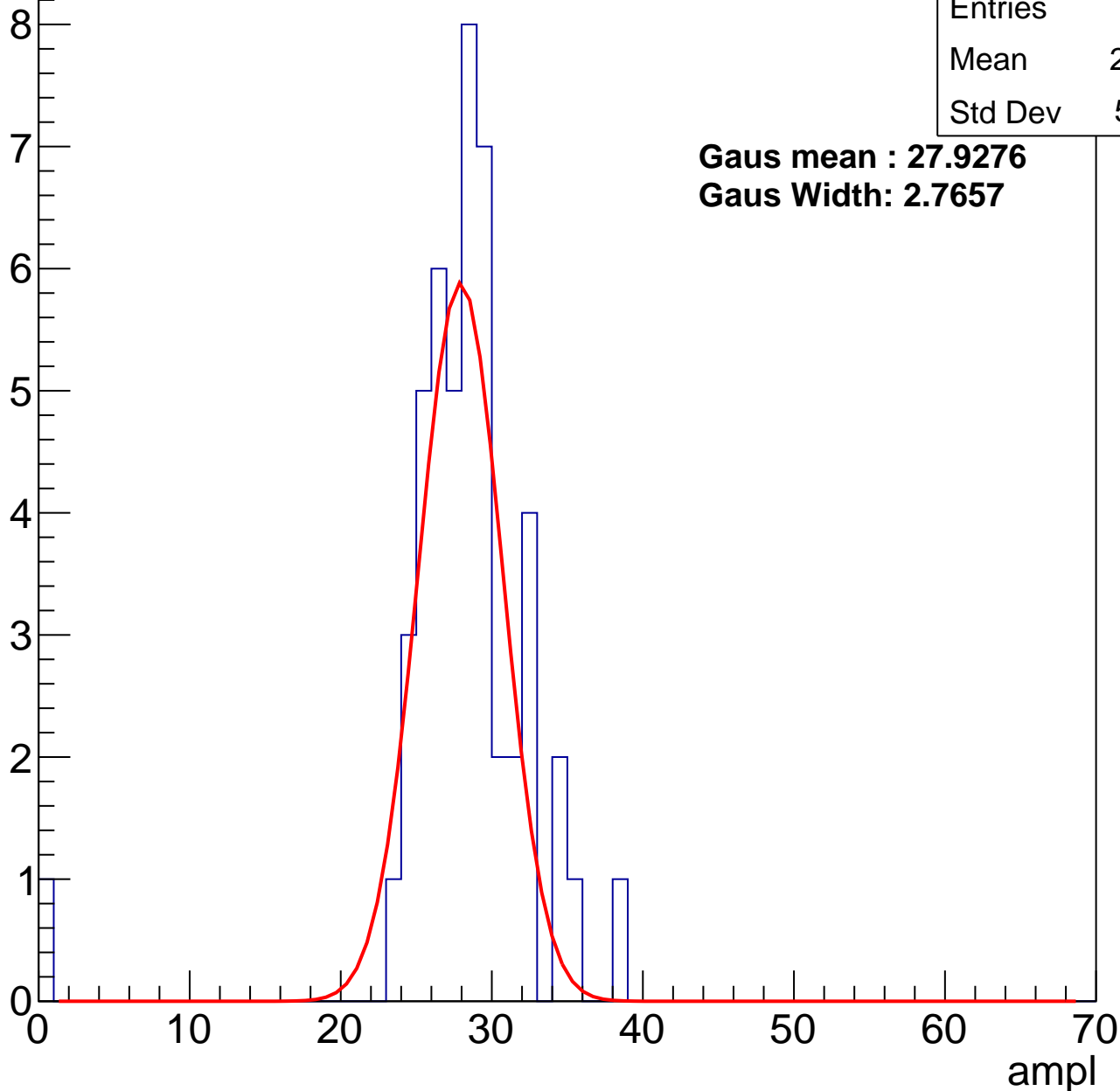
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	48
Mean	27.69
Std Dev	5.091

Gaus mean : 27.9276

Gaus Width: 2.7657



B1L002S, U7-ch54, adc1

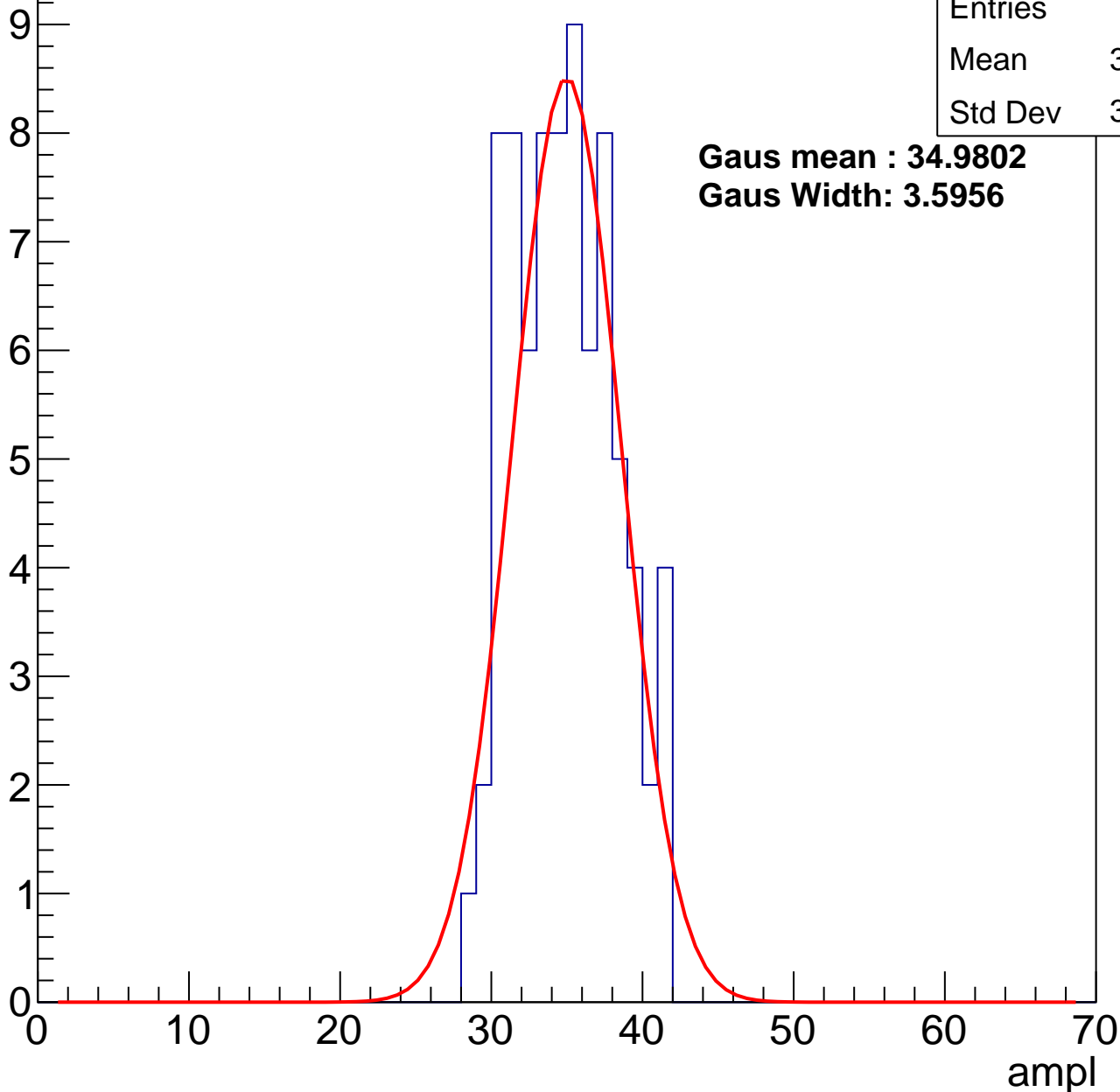
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	79
Mean	34.42
Std Dev	3.294

Gaus mean : 34.9802

Gaus Width: 3.5956



B1L002S, U7-ch54, adc2

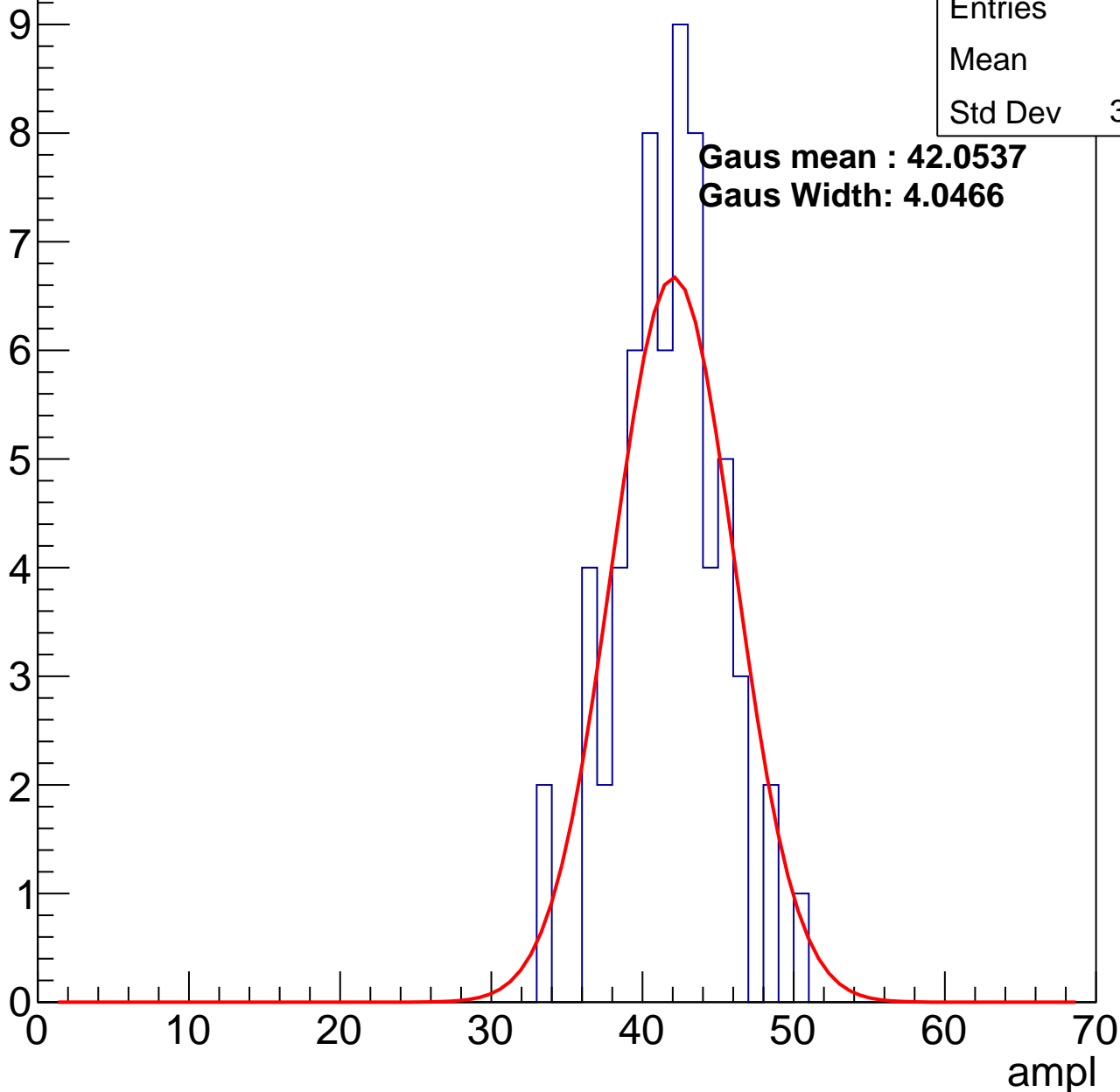
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.3
Std Dev	3.385

Gaus mean : 42.0537

Gaus Width: 4.0466

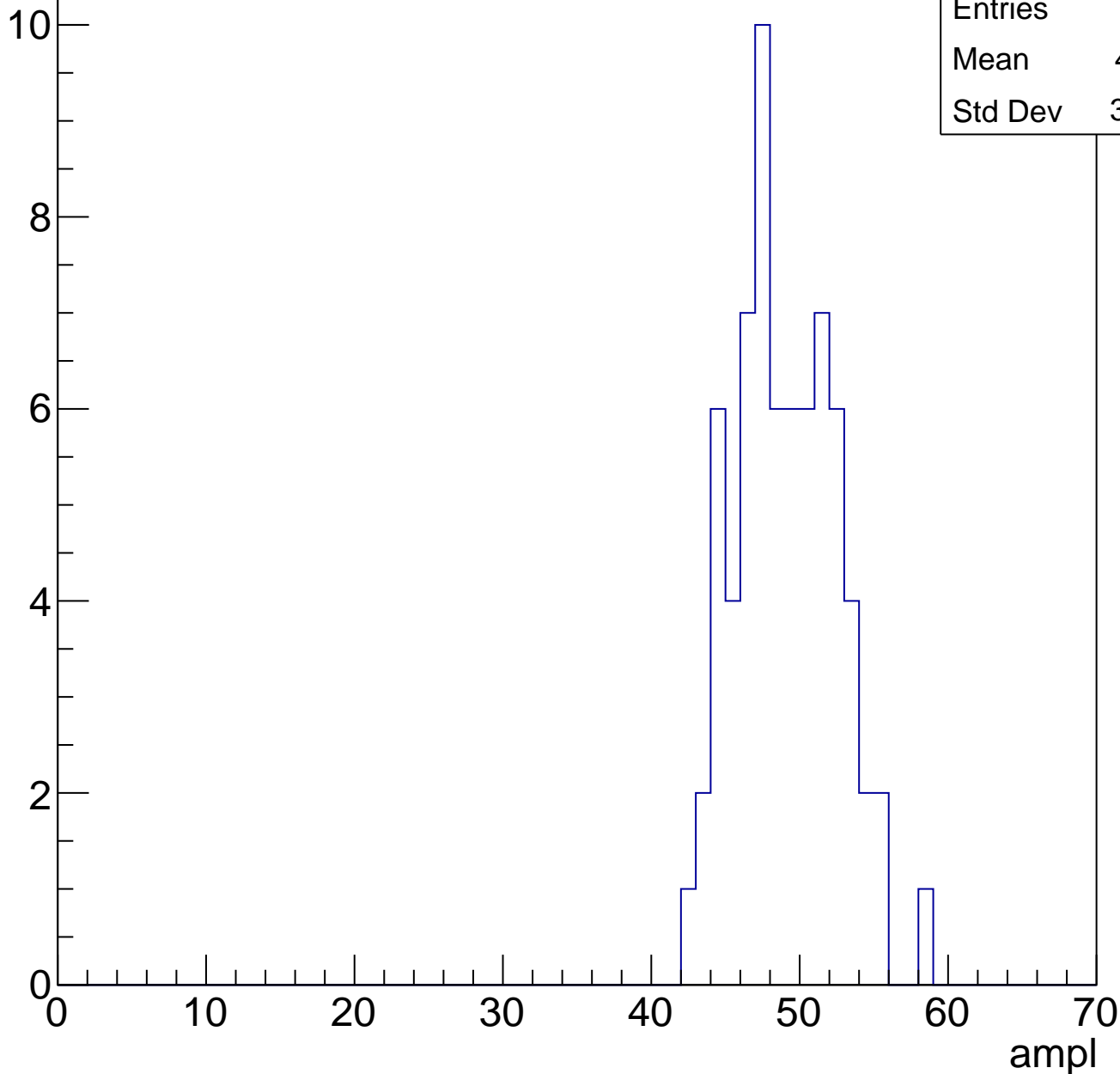


B1L002S, U7-ch54, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	48.61
Std Dev	3.356

Entry

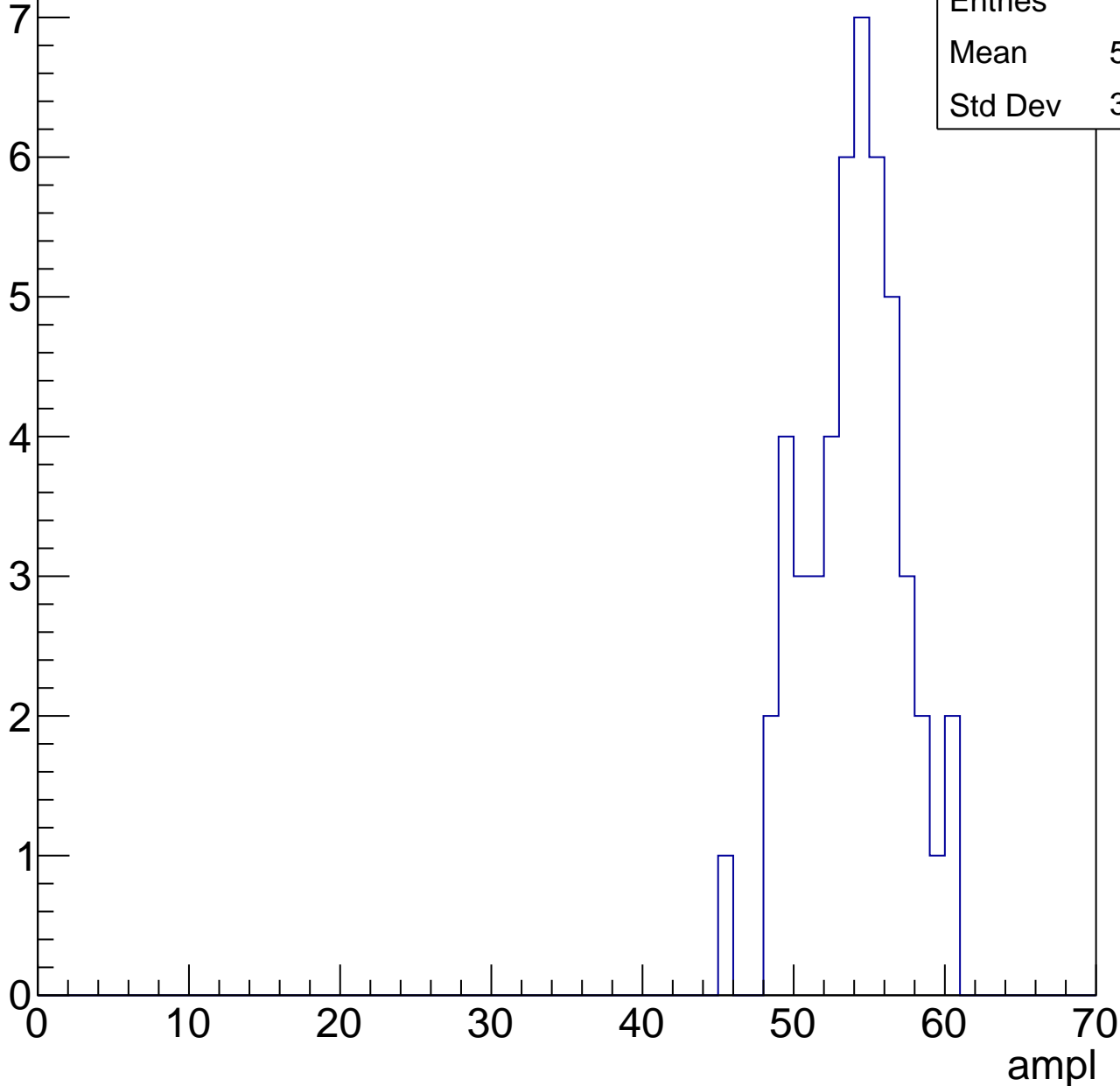


B1L002S, U7-ch54, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	53.47
Std Dev	3.252



B1L002S, U7-ch54, adc5

calib_packv5_042523_0143.root, FC#10, port B3

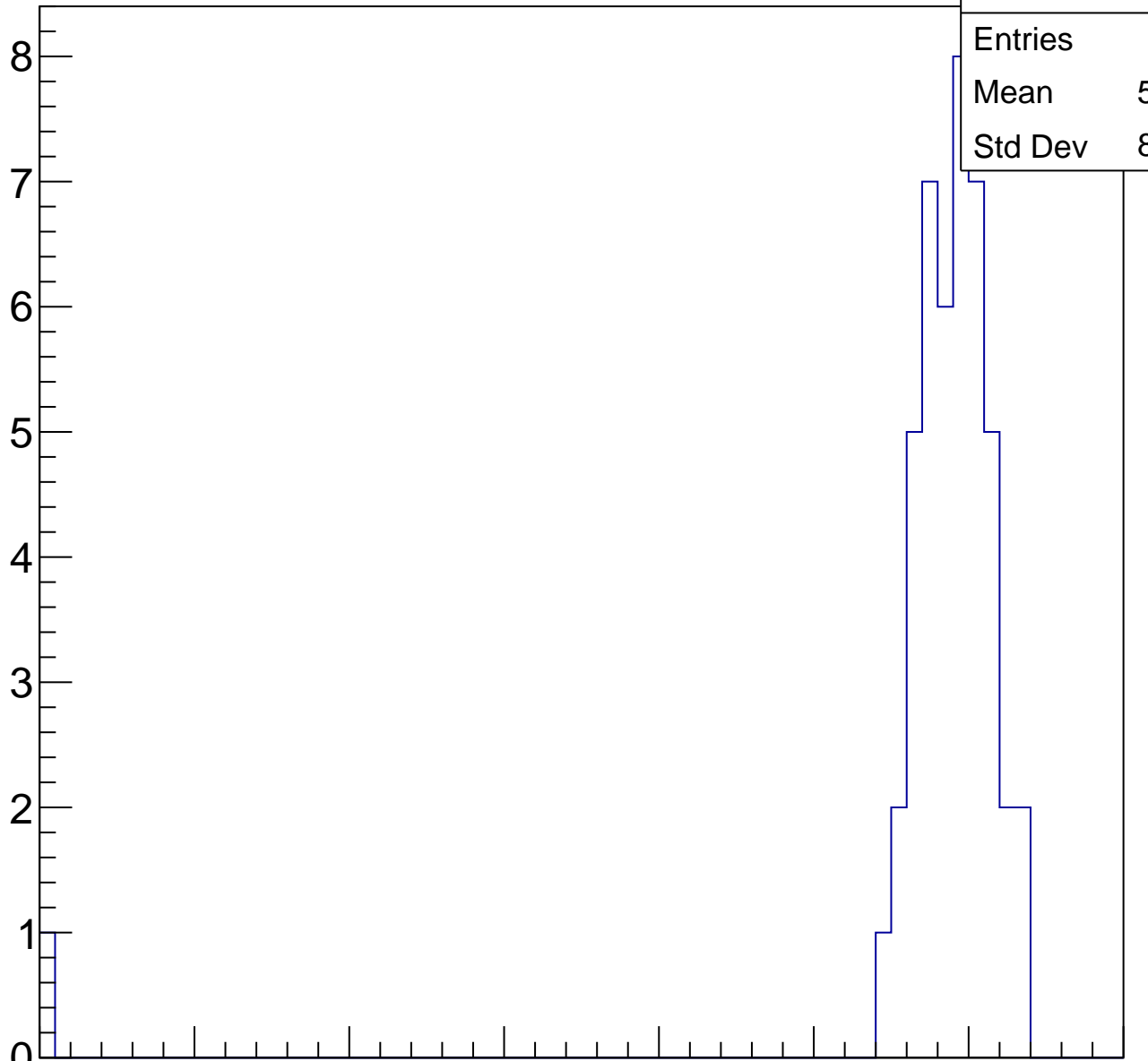
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	57.35
Std Dev	8.808

ampl

0 10 20 30 40 50 60 70

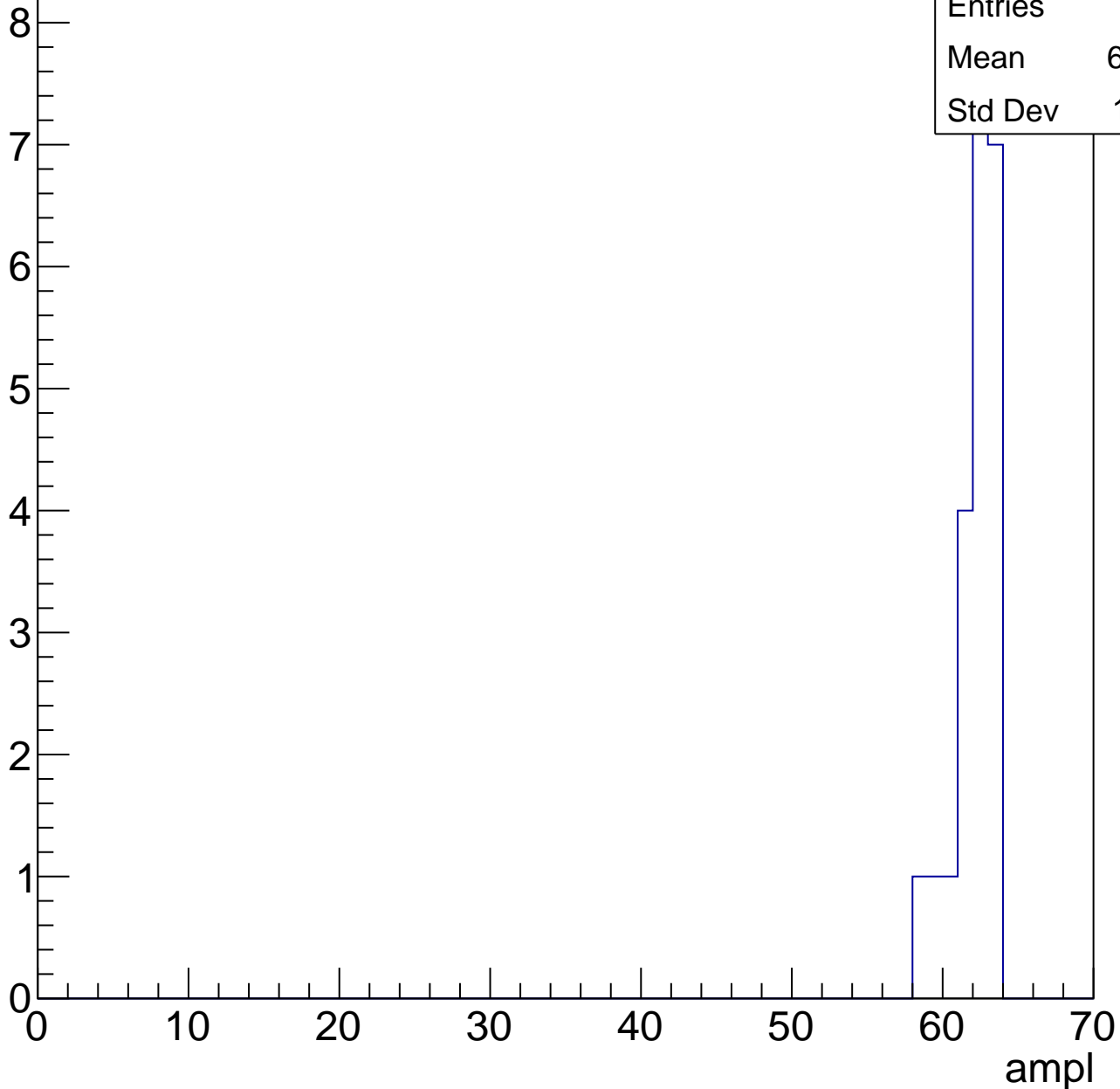


B1L002S, U7-ch54, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

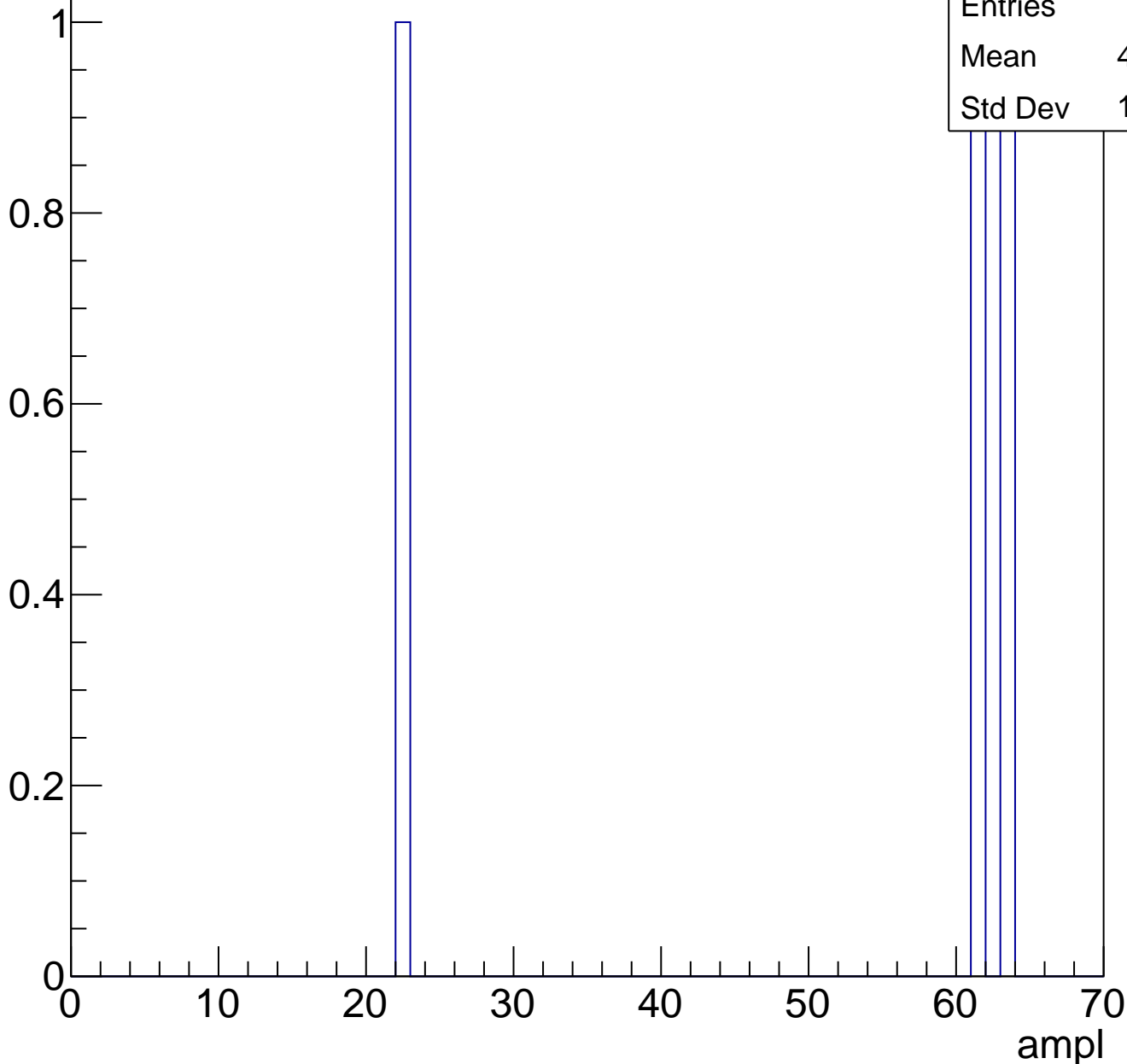
Entries	22
Mean	61.73
Std Dev	1.321



B1L002S, U7-ch54, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch55, adc0

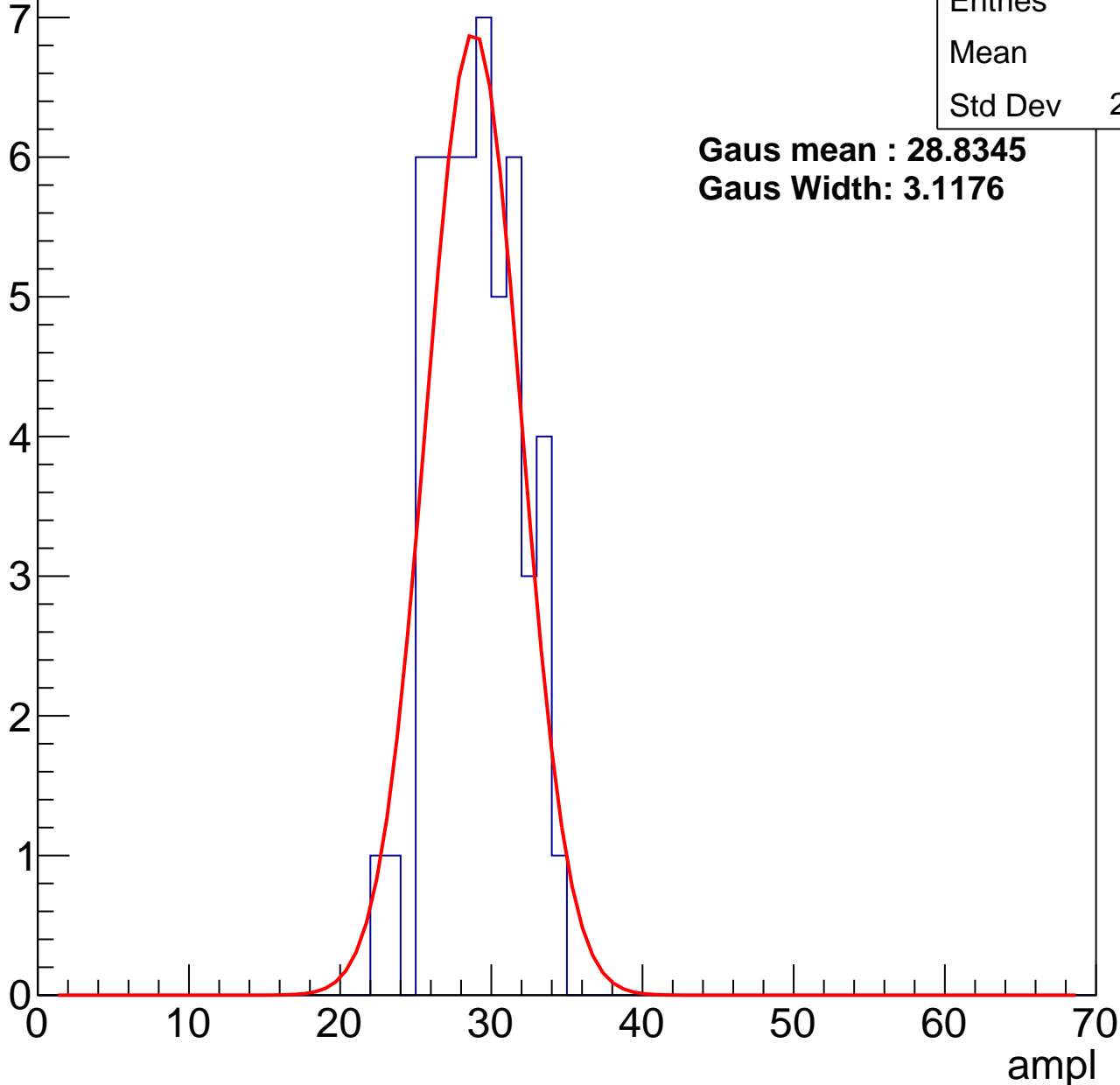
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	28.5
Std Dev	2.763

Gaus mean : 28.8345

Gaus Width: 3.1176



B1L002S, U7-ch55, adc1

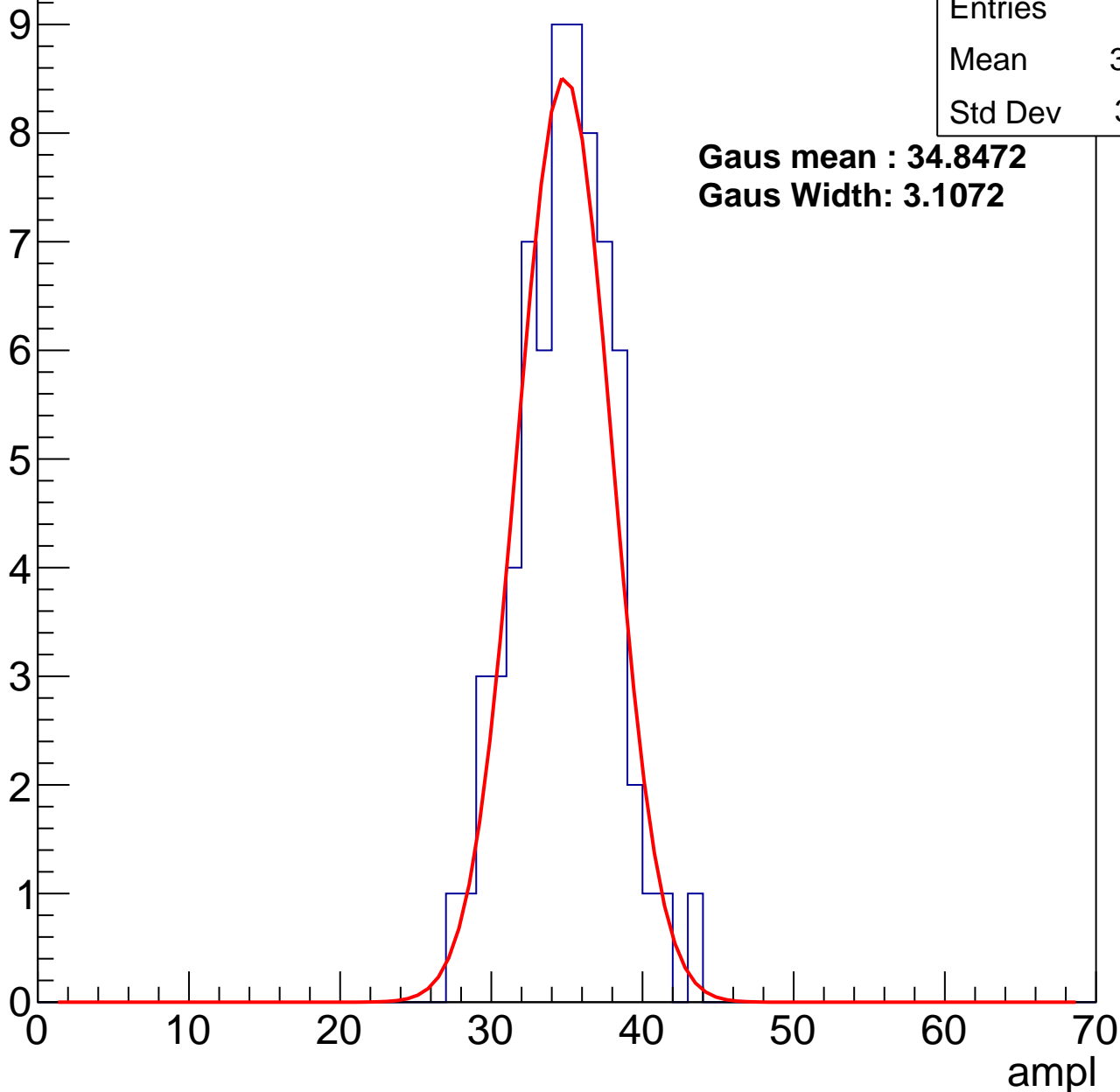
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.43
Std Dev	3.141

Gaus mean : 34.8472

Gaus Width: 3.1072



B1L002S, U7-ch55, adc2

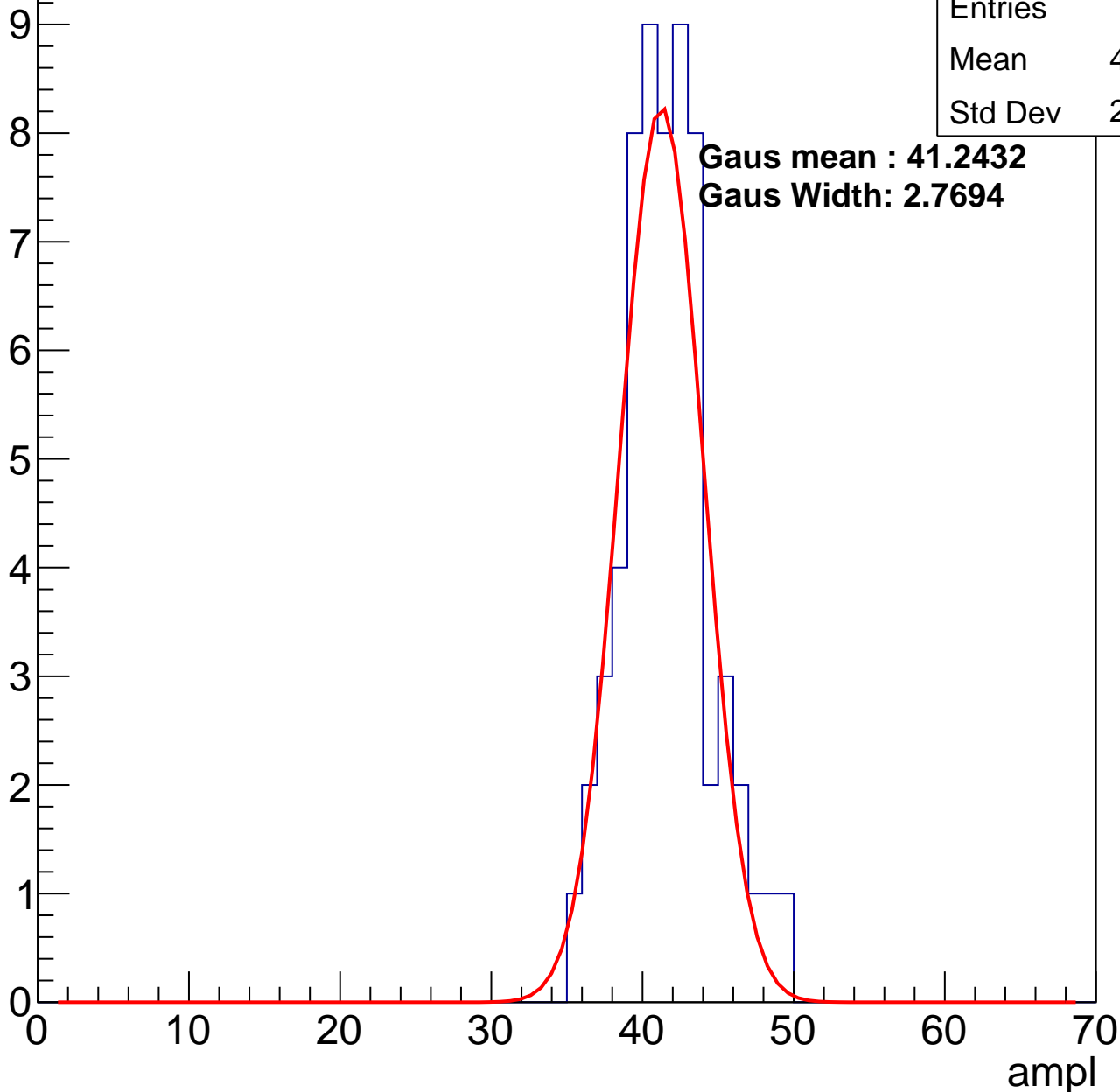
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.15
Std Dev	2.884

Gaus mean : 41.2432

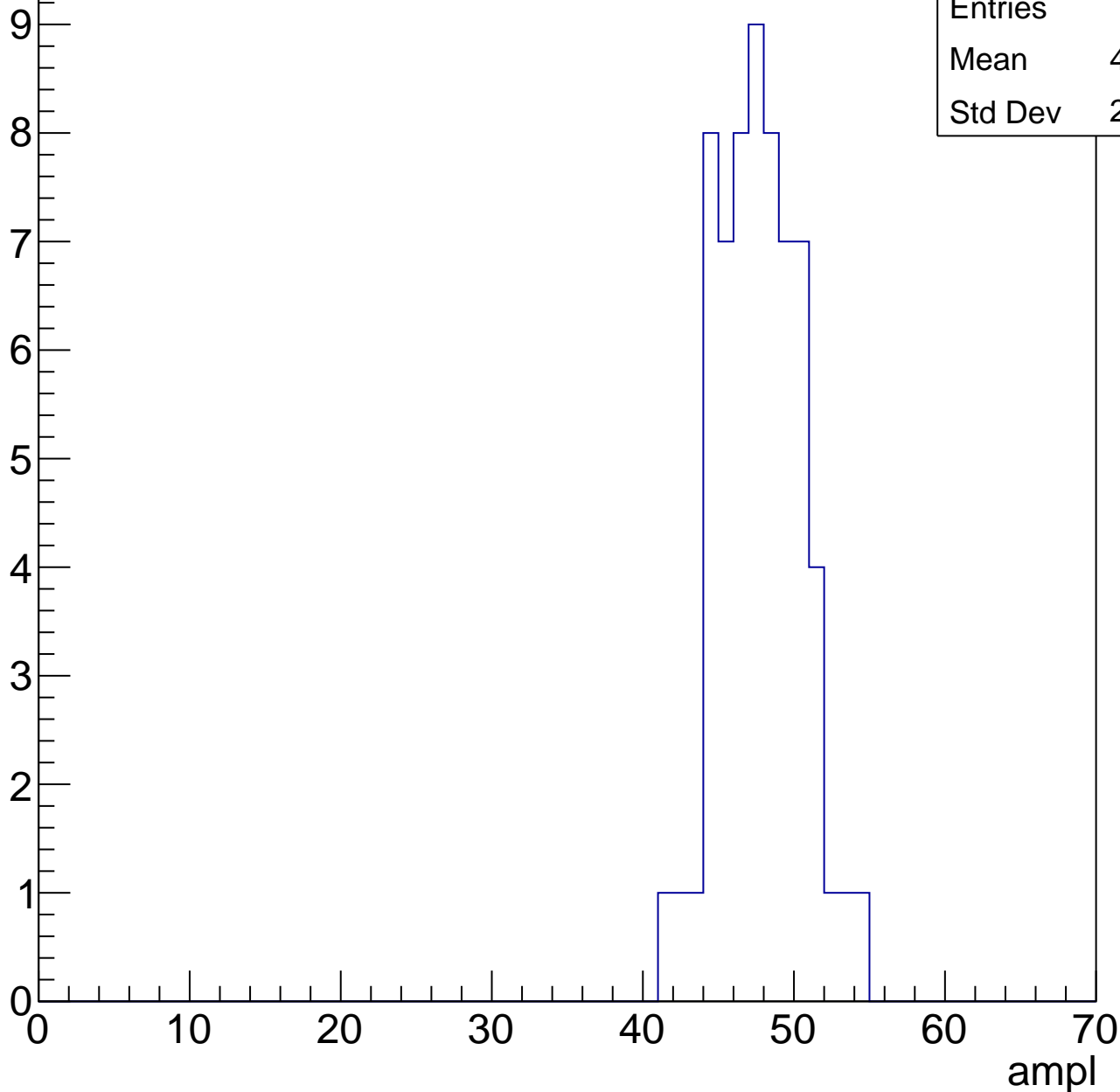
Gaus Width: 2.7694



B1L002S, U7-ch55, adc3

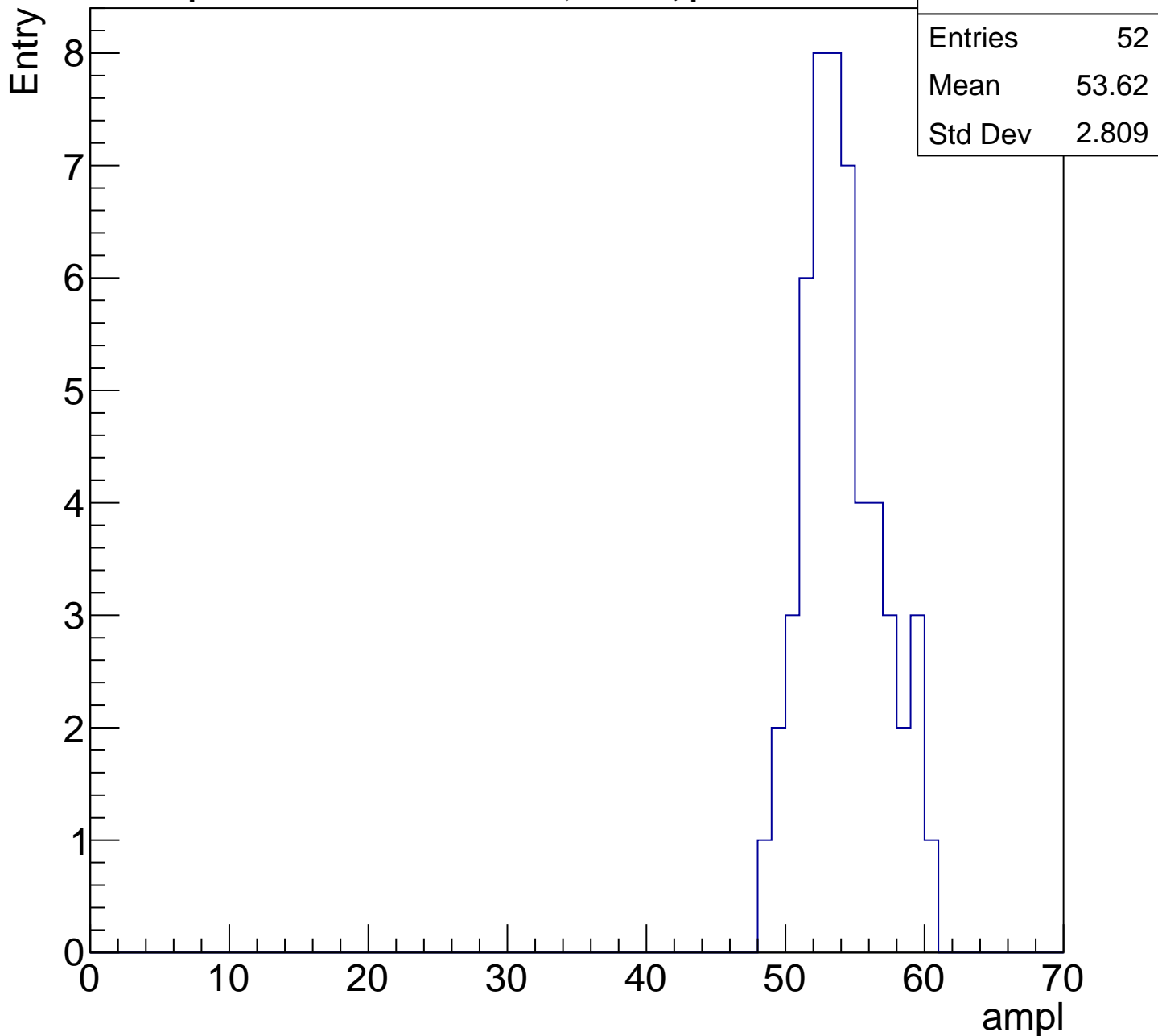
calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch55, adc4

calib_packv5_042523_0143.root, FC#10, port B3

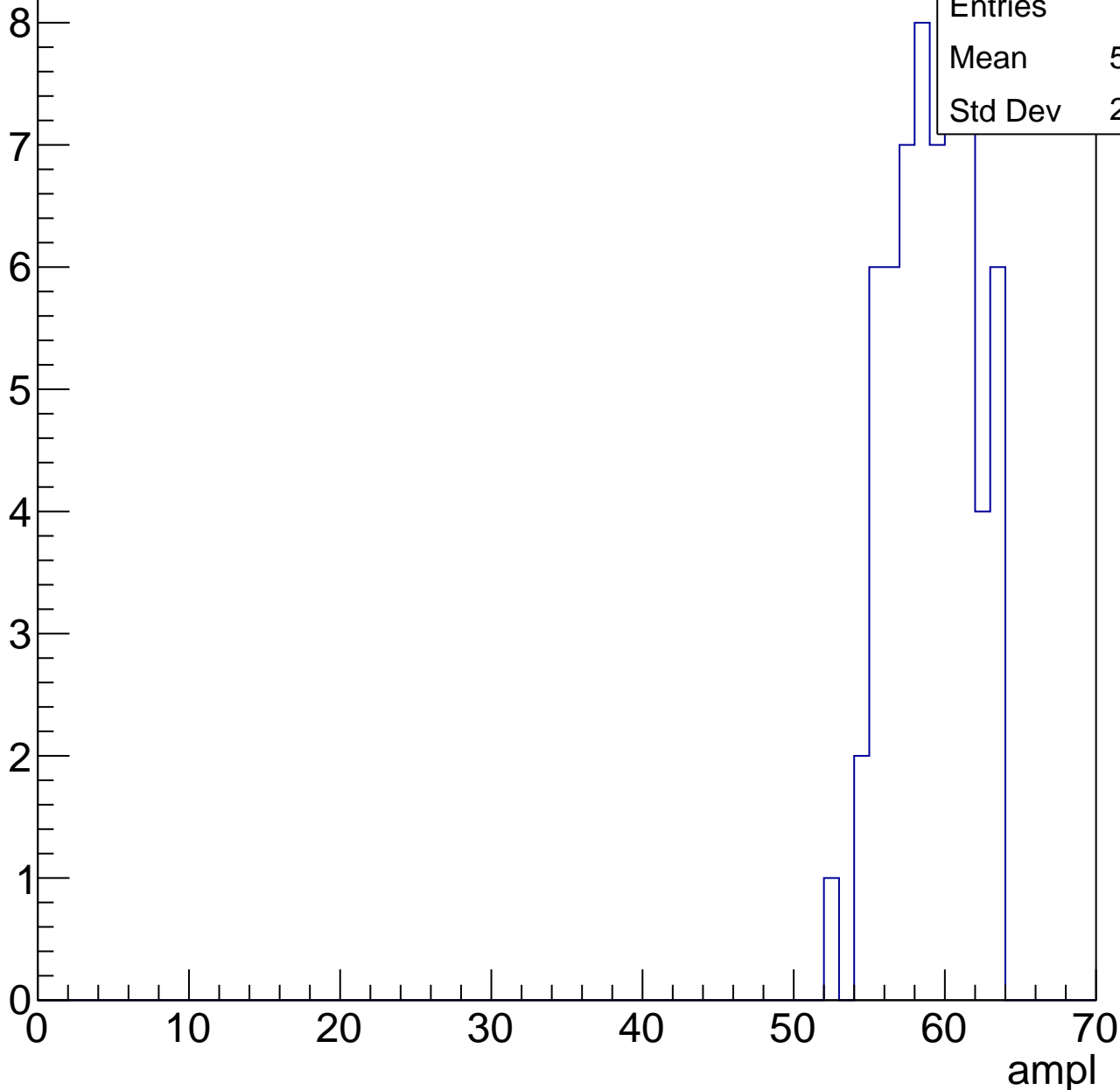


B1L002S, U7-ch55, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	58.67
Std Dev	2.673

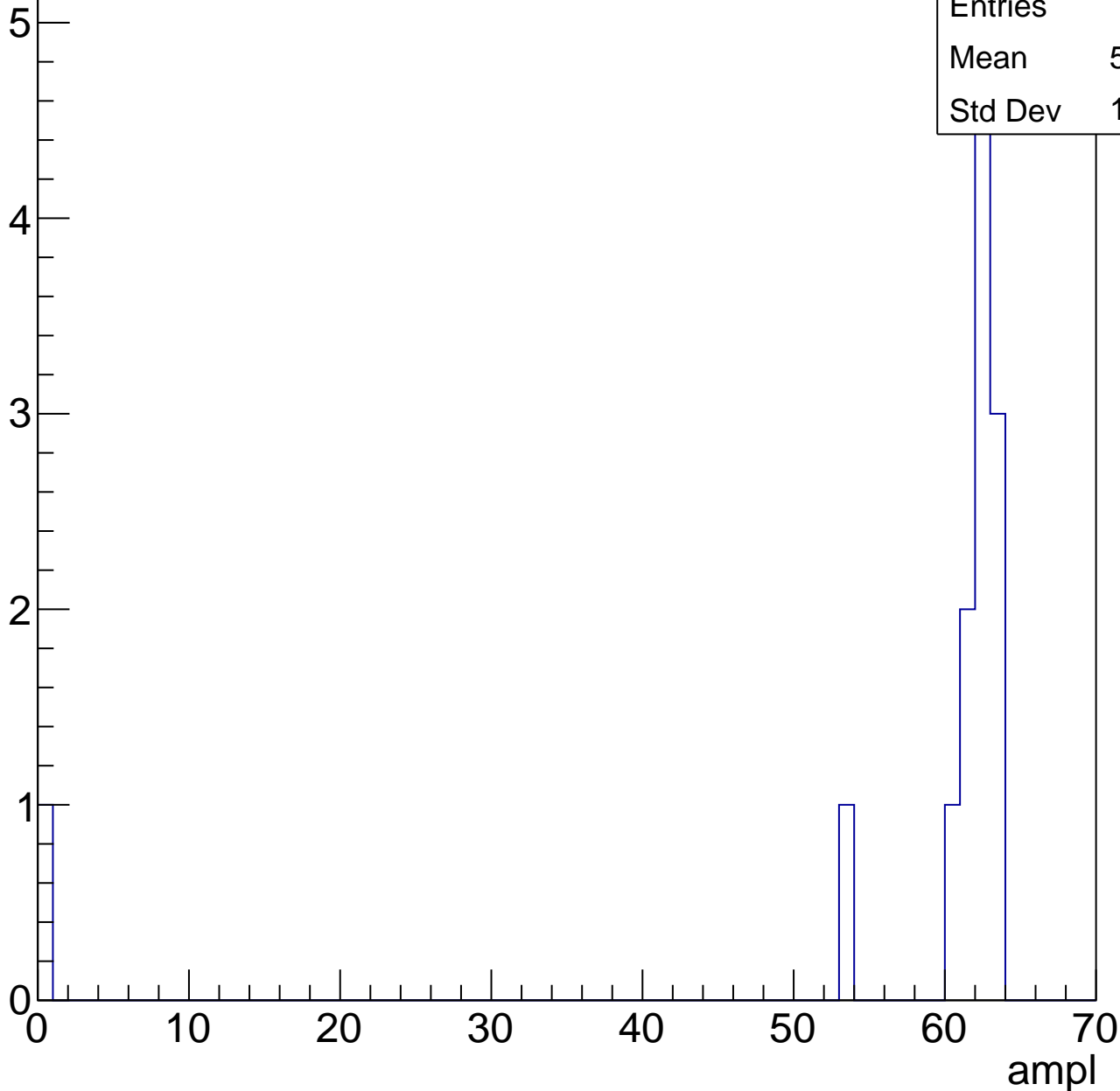


B1L002S, U7-ch55, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	13
Mean	56.46
Std Dev	16.49



B1L002S, U7-ch55, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch56, adc0

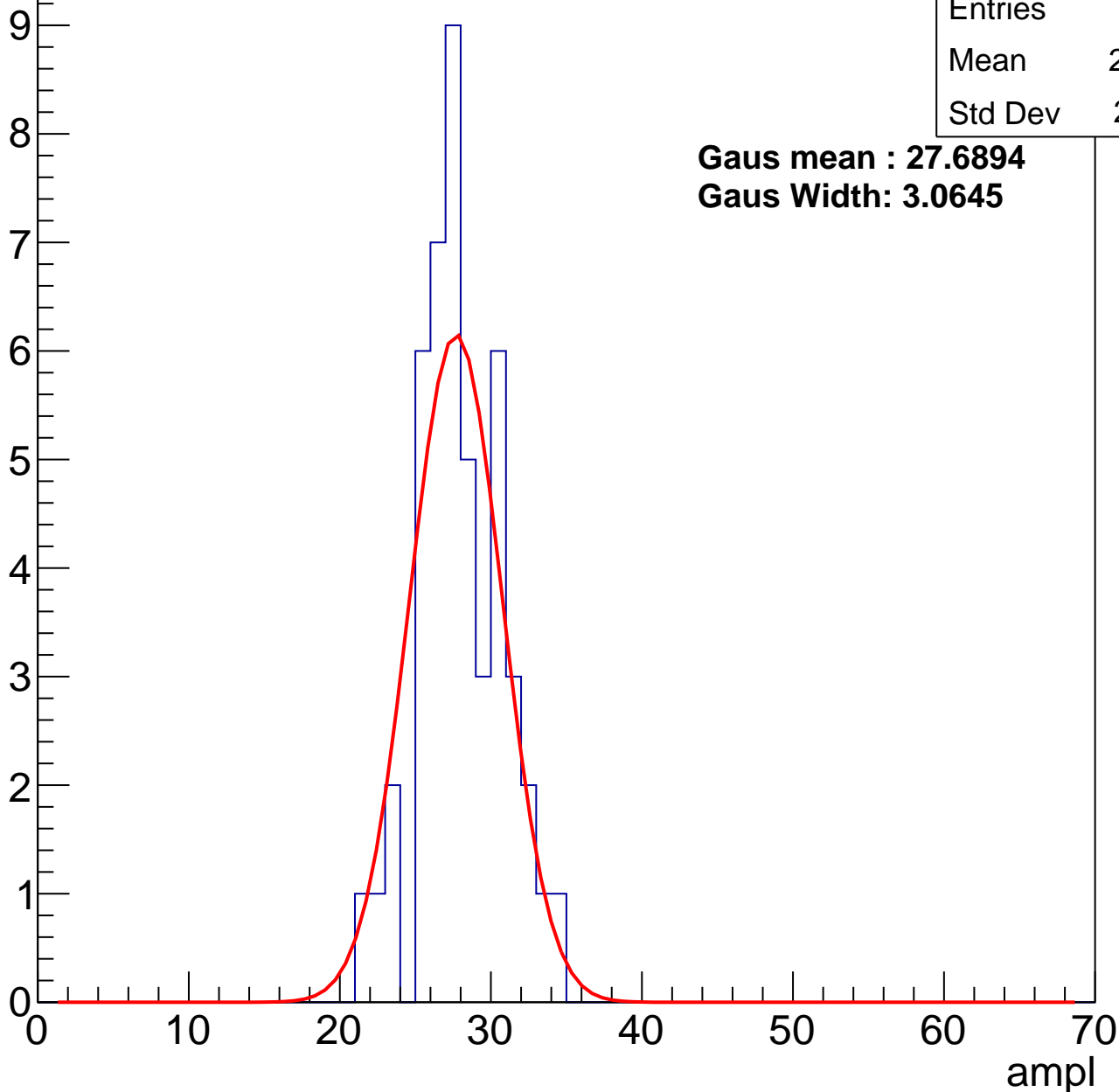
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	27.55
Std Dev	2.781

Gaus mean : 27.6894

Gaus Width: 3.0645



B1L002S, U7-ch56, adc1

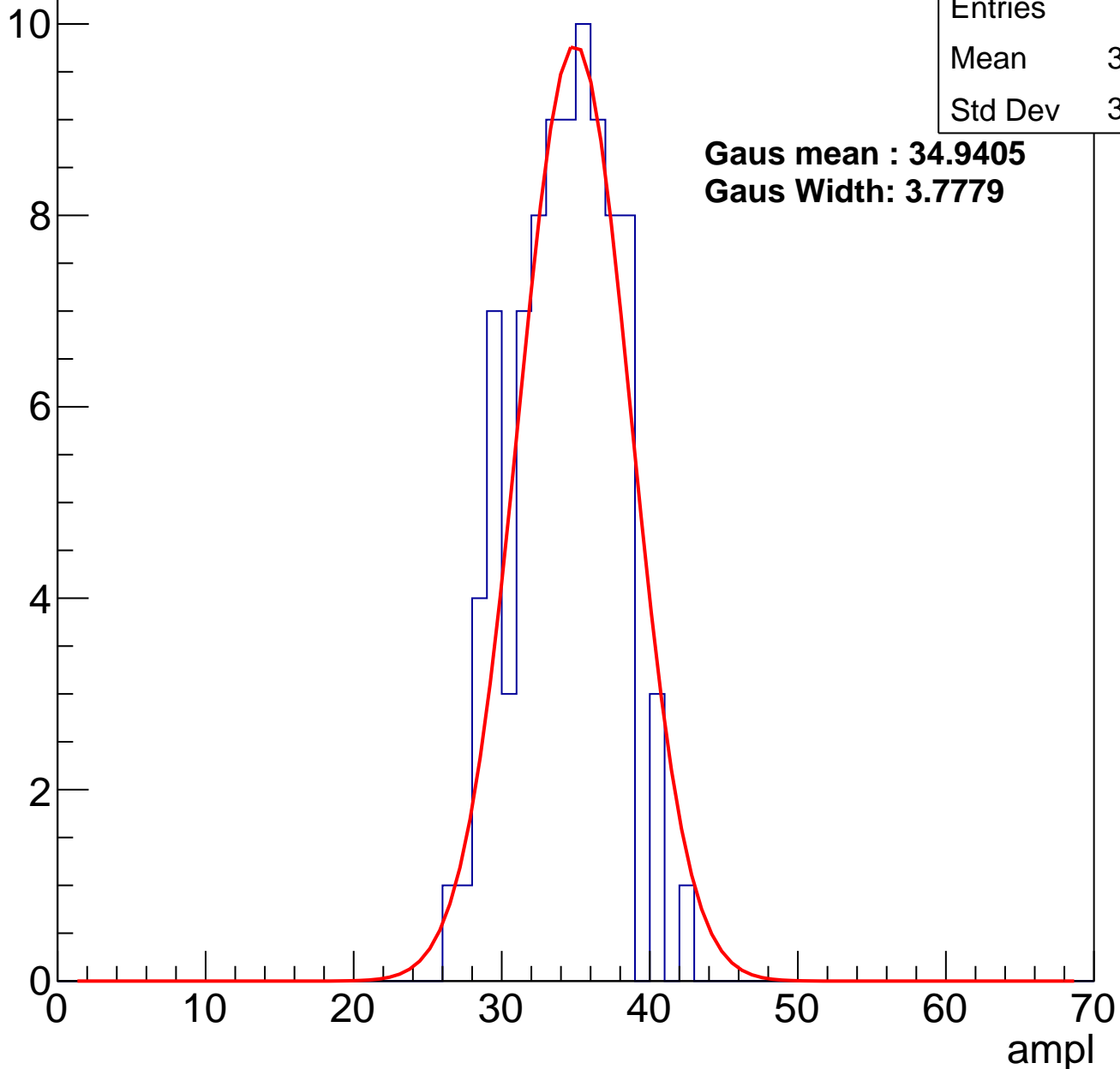
calib_packv5_042523_0143.root, FC#10, port B3

Entries	88
Mean	33.75
Std Dev	3.365

Gaus mean : 34.9405

Gaus Width: 3.7779

Entry



B1L002S, U7-ch56, adc2

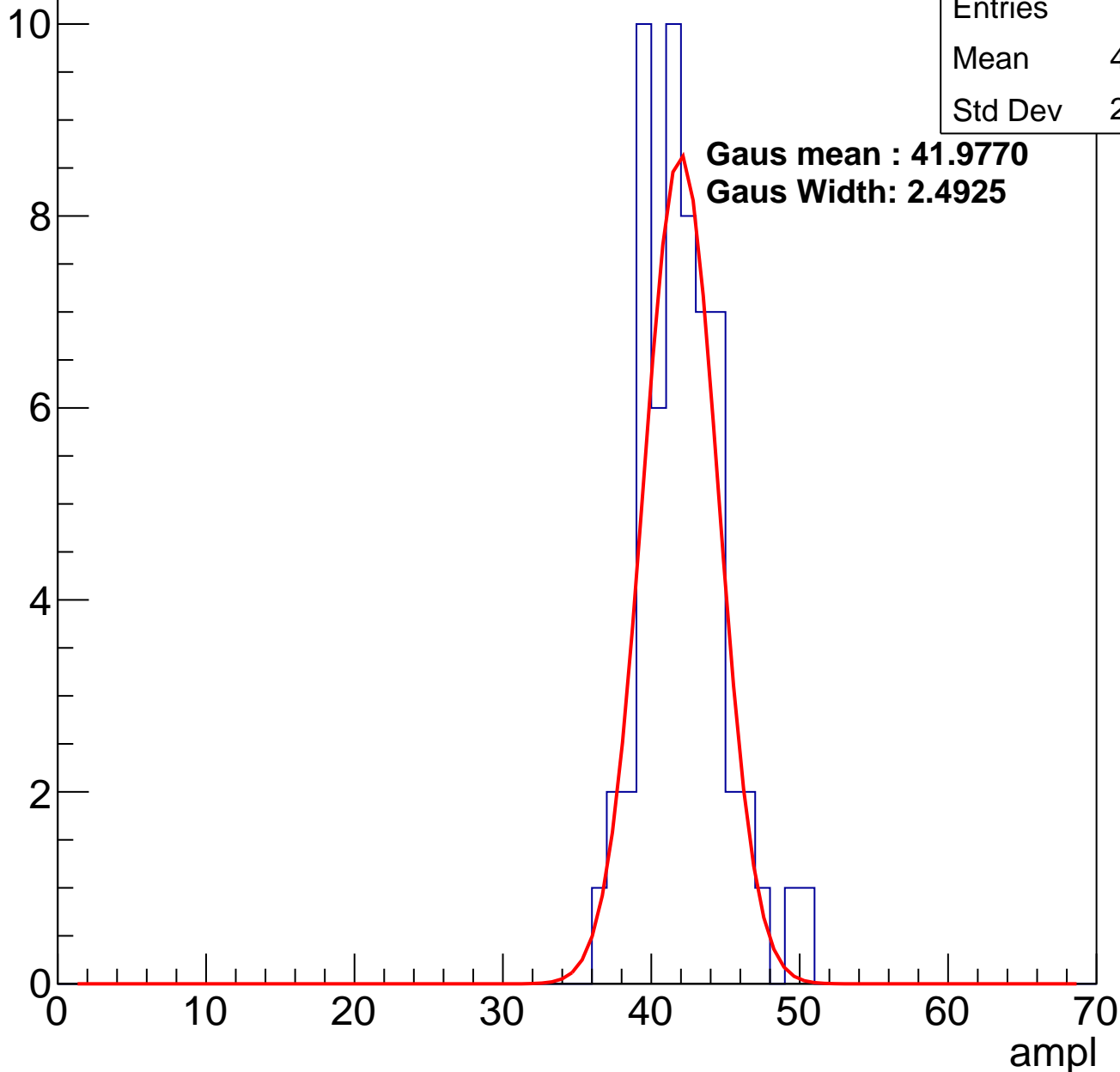
calib_packv5_042523_0143.root, FC#10, port B3

Entries	60
Mean	41.65
Std Dev	2.762

Gaus mean : 41.9770

Gaus Width: 2.4925

Entry

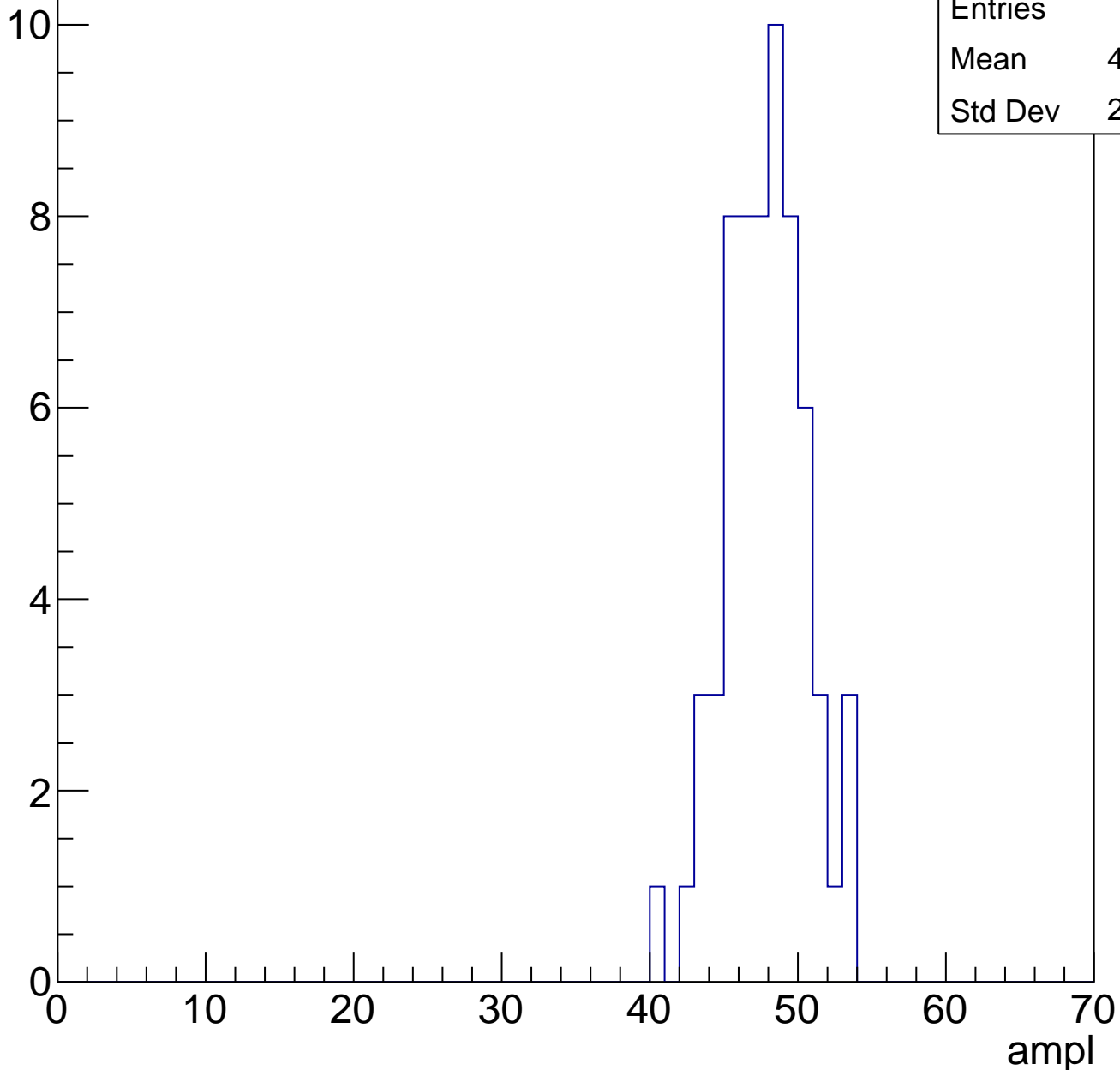


B1L002S, U7-ch56, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	63
Mean	47.35
Std Dev	2.697

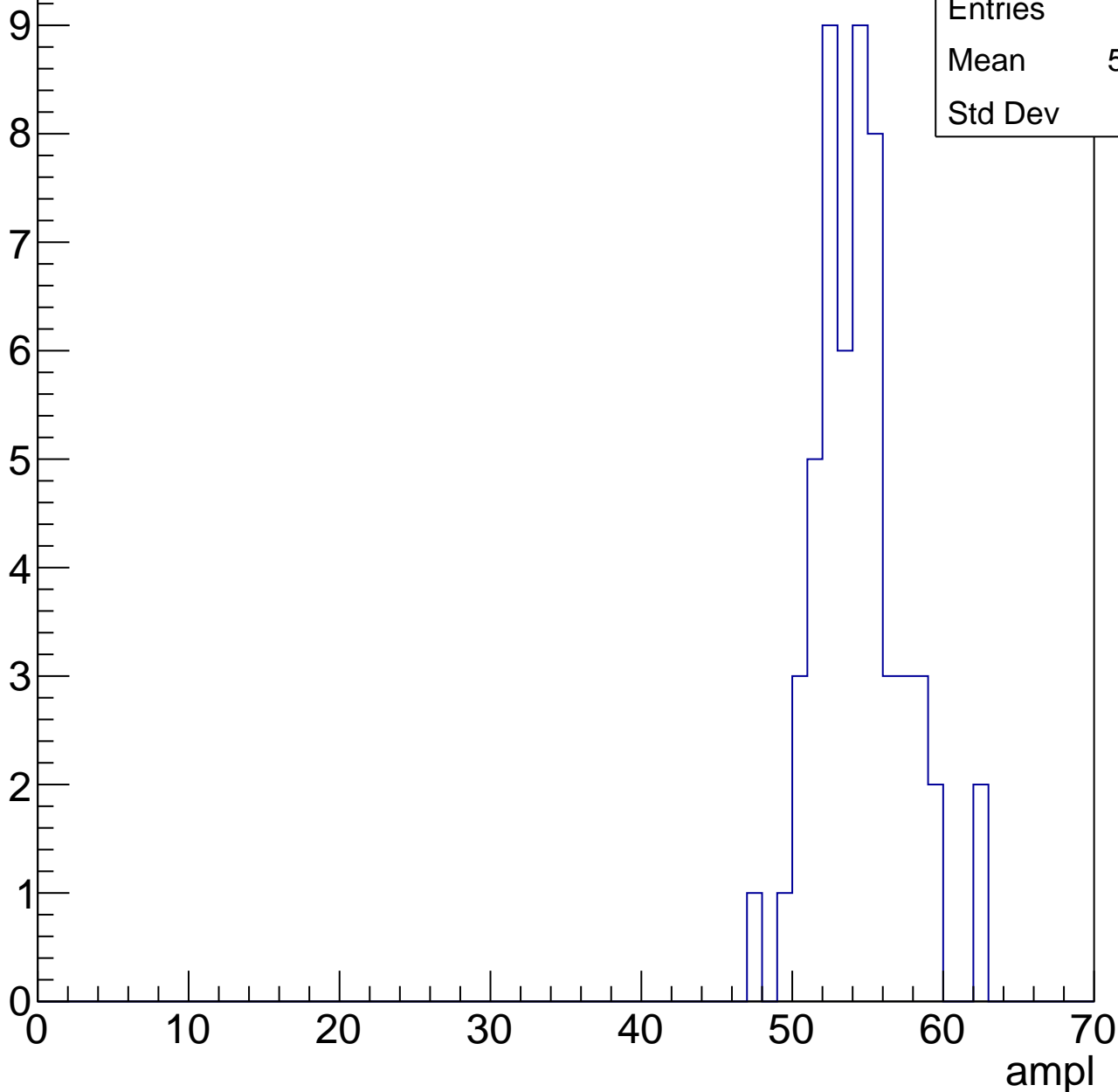
Entry



B1L002S, U7-ch56, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch56, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	58
Mean	58.41
Std Dev	2.626

Entry

10

8

6

4

2

0

0

10

20

30

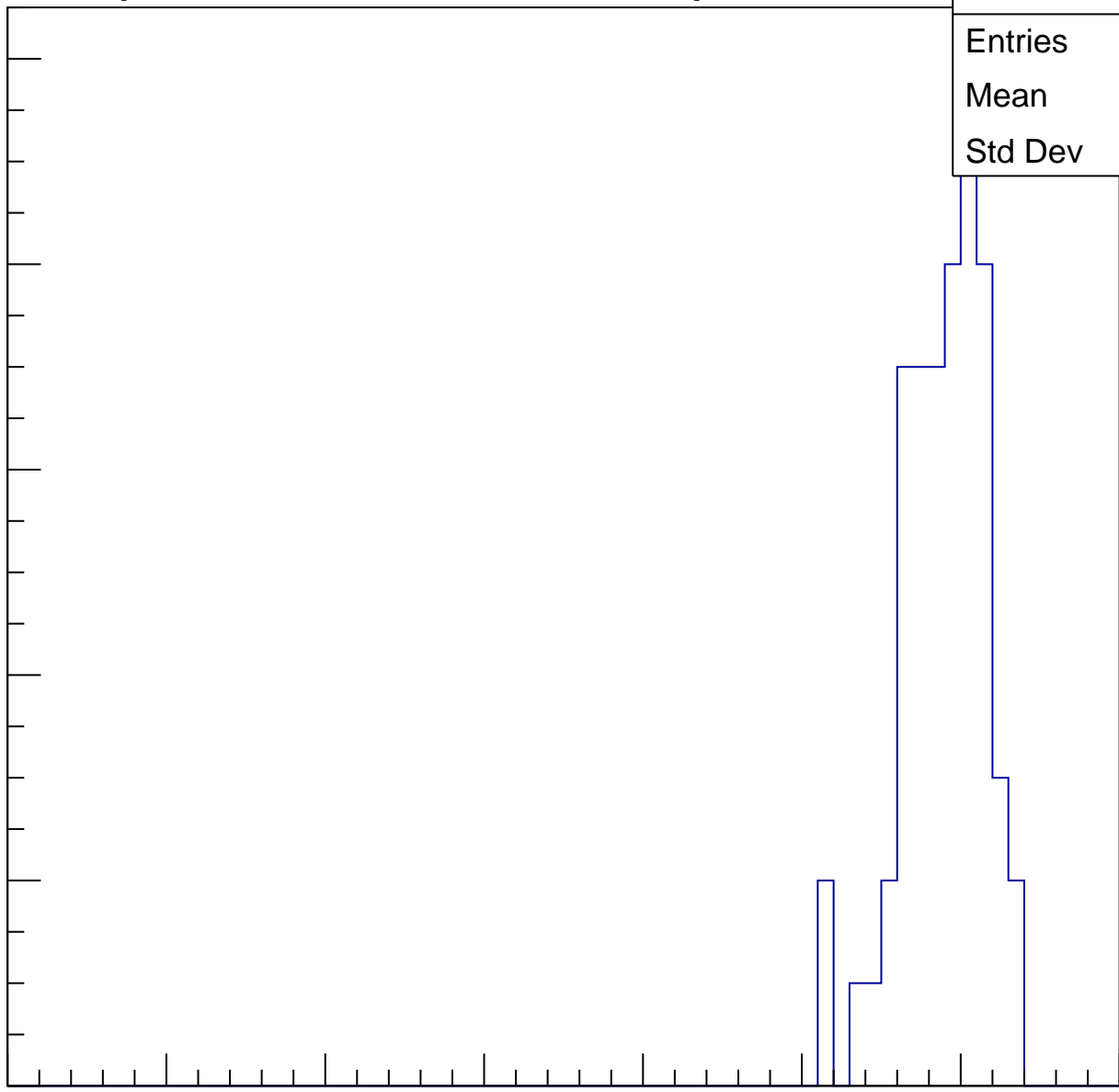
40

50

60

70

ampl

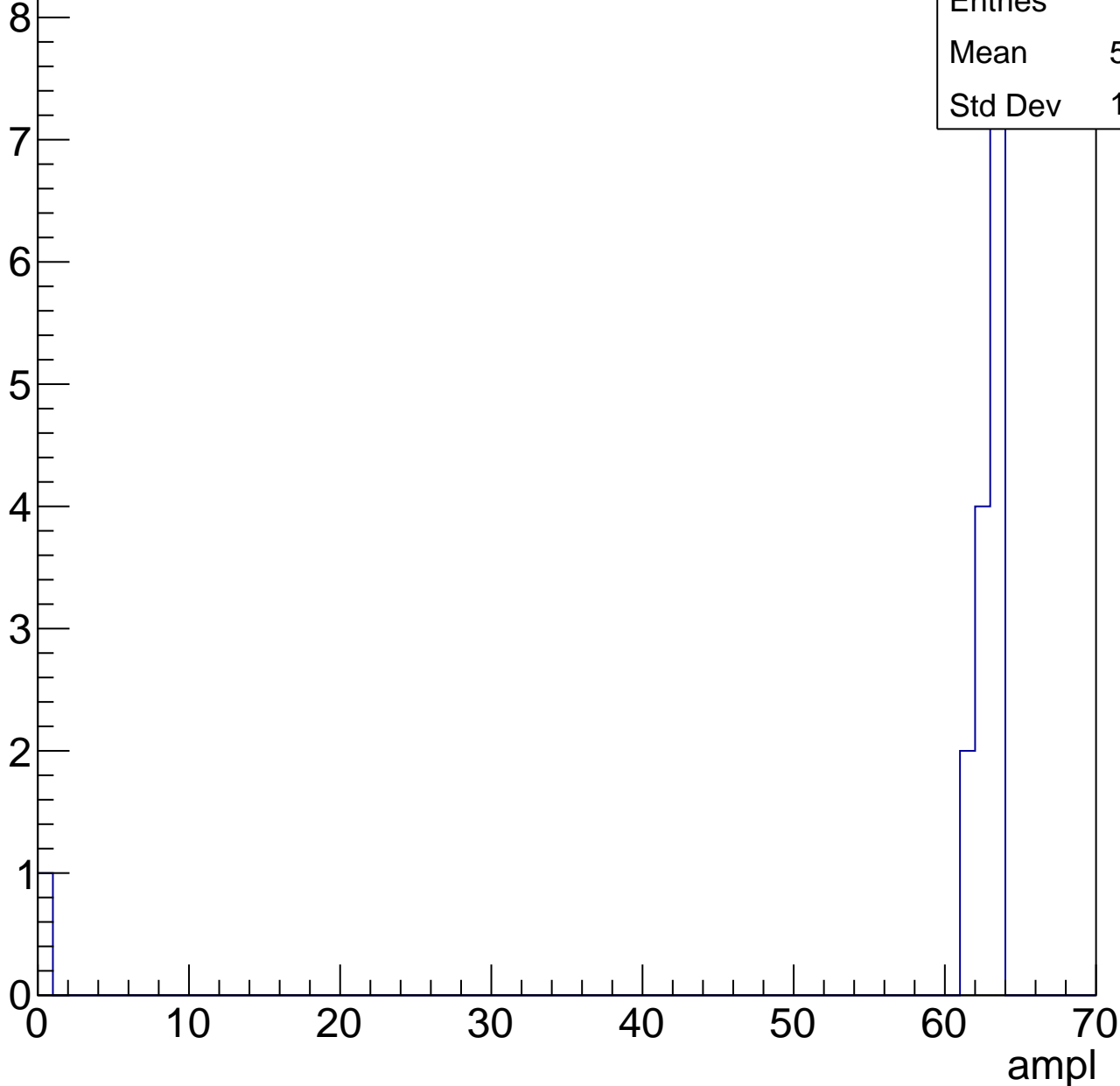


B1L002S, U7-ch56, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

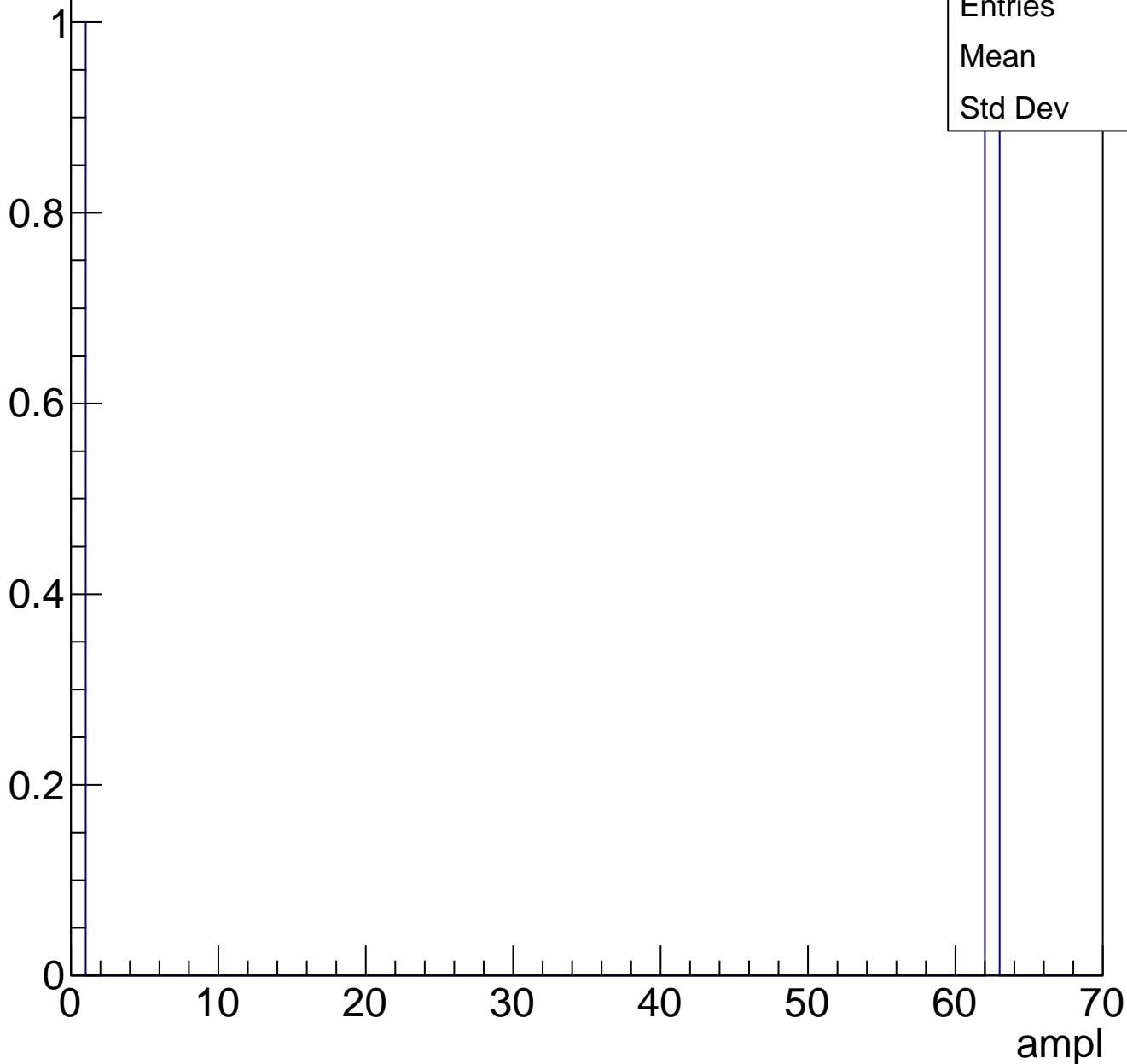
Entries	15
Mean	58.27
Std Dev	15.59



B1L002S, U7-ch56, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch57, adc0

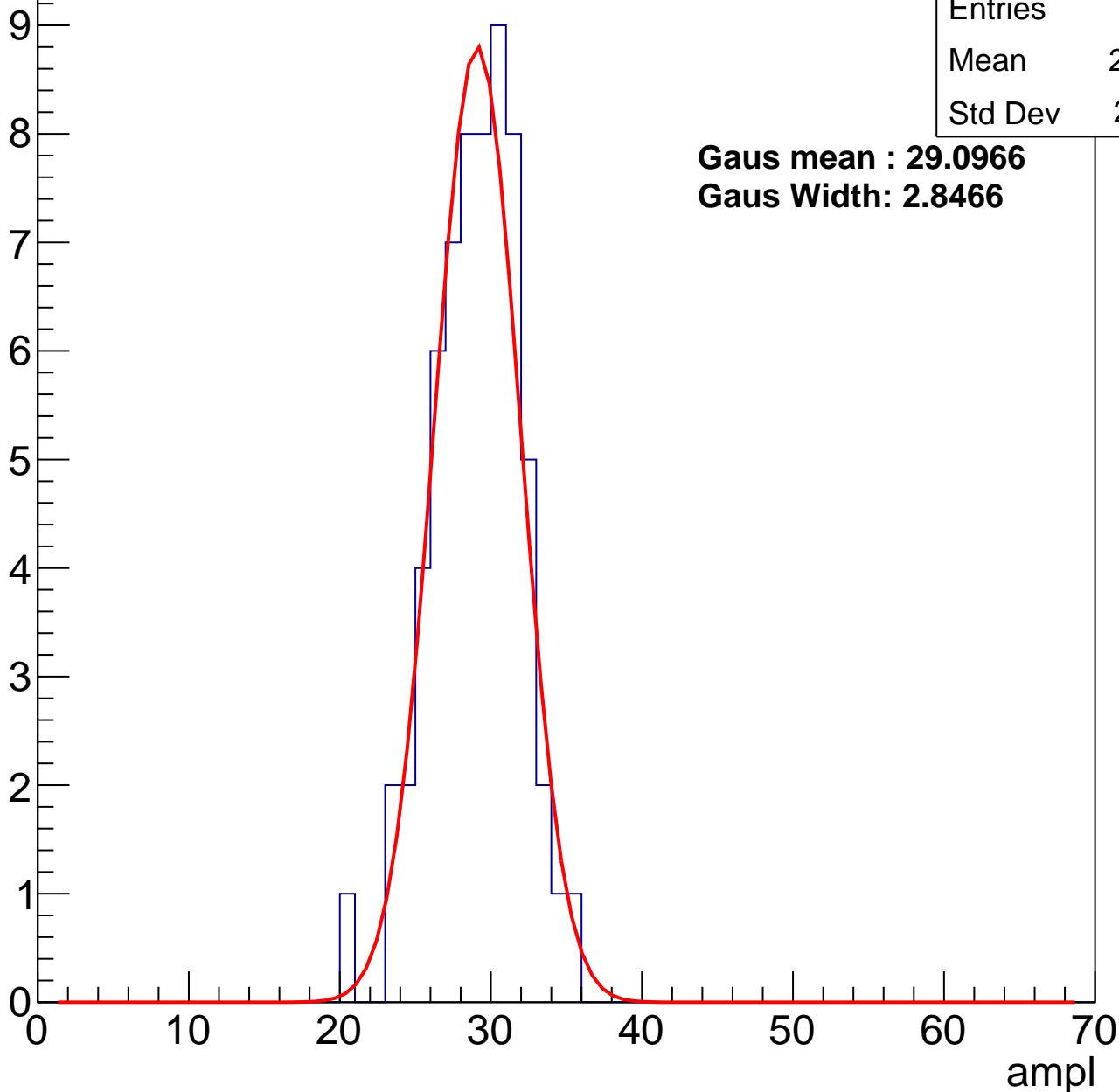
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.56
Std Dev	2.861

Gaus mean : 29.0966

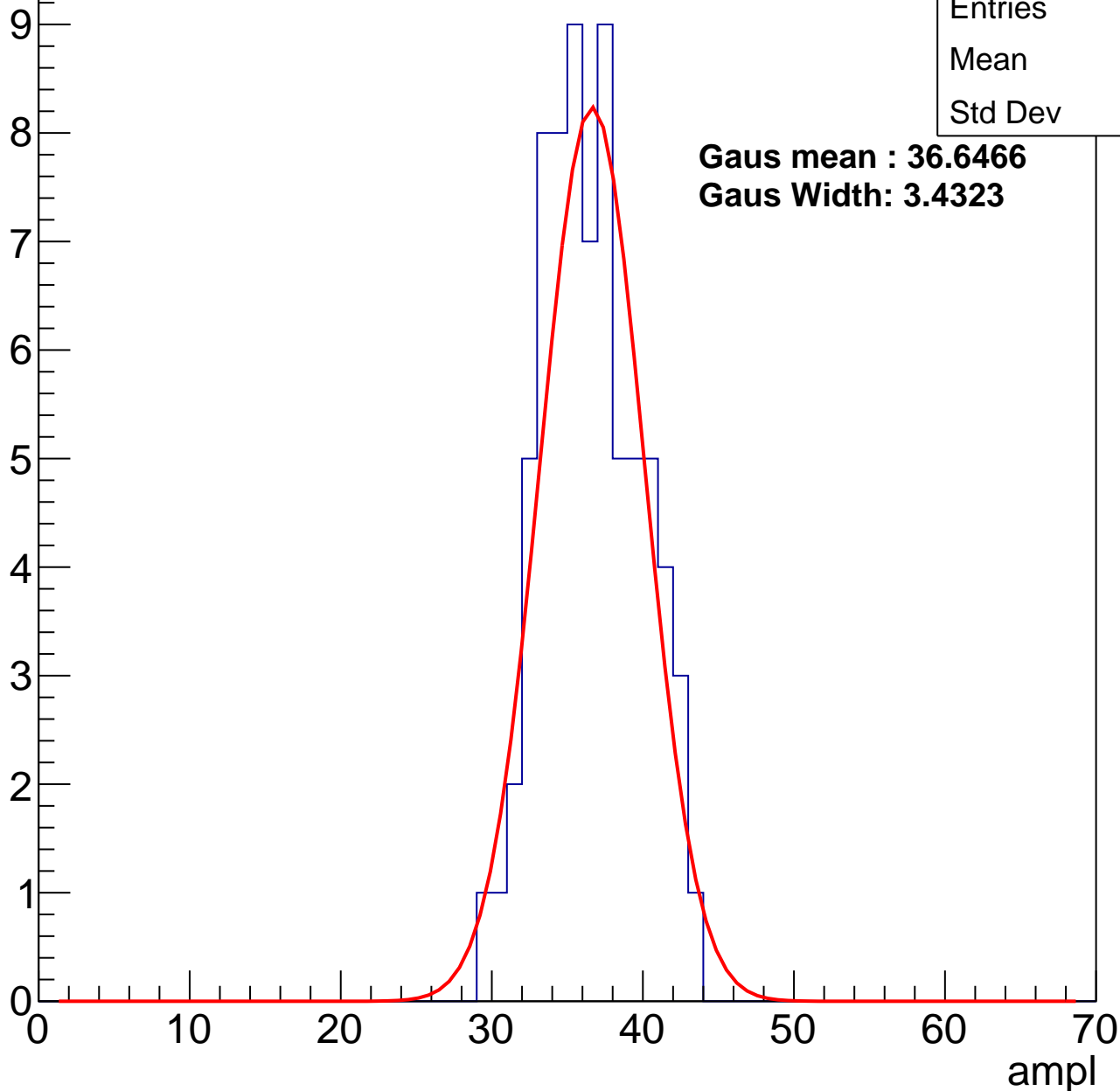
Gaus Width: 2.8466



B1L002S, U7-ch57, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch57, adc2

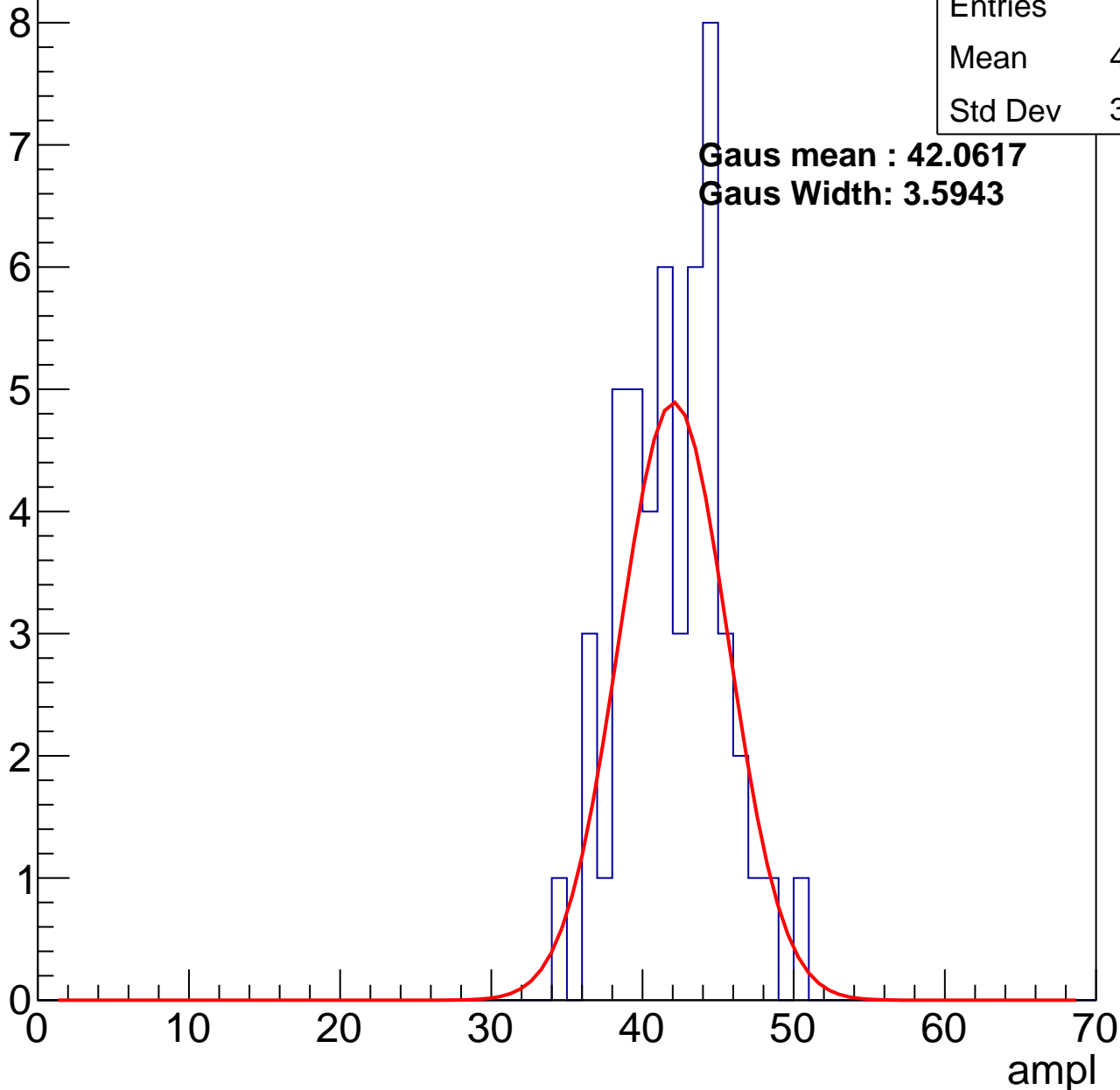
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	41.56
Std Dev	3.336

Gaus mean : 42.0617

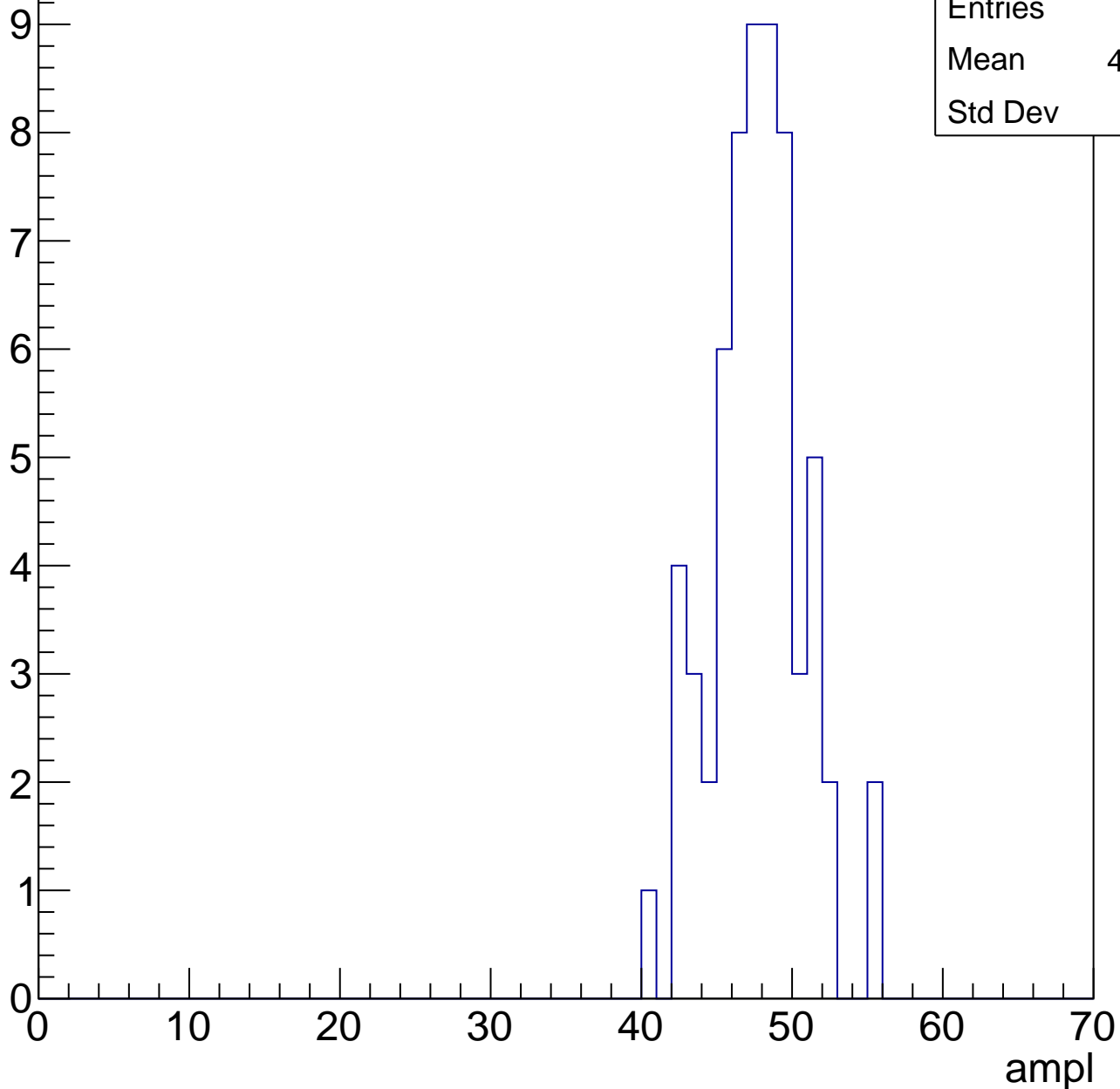
Gaus Width: 3.5943



B1L002S, U7-ch57, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	62
Mean	47.24
Std Dev	3.03

B1L002S, U7-ch57, adc4

calib_packv5_042523_0143.root, FC#10, port B3

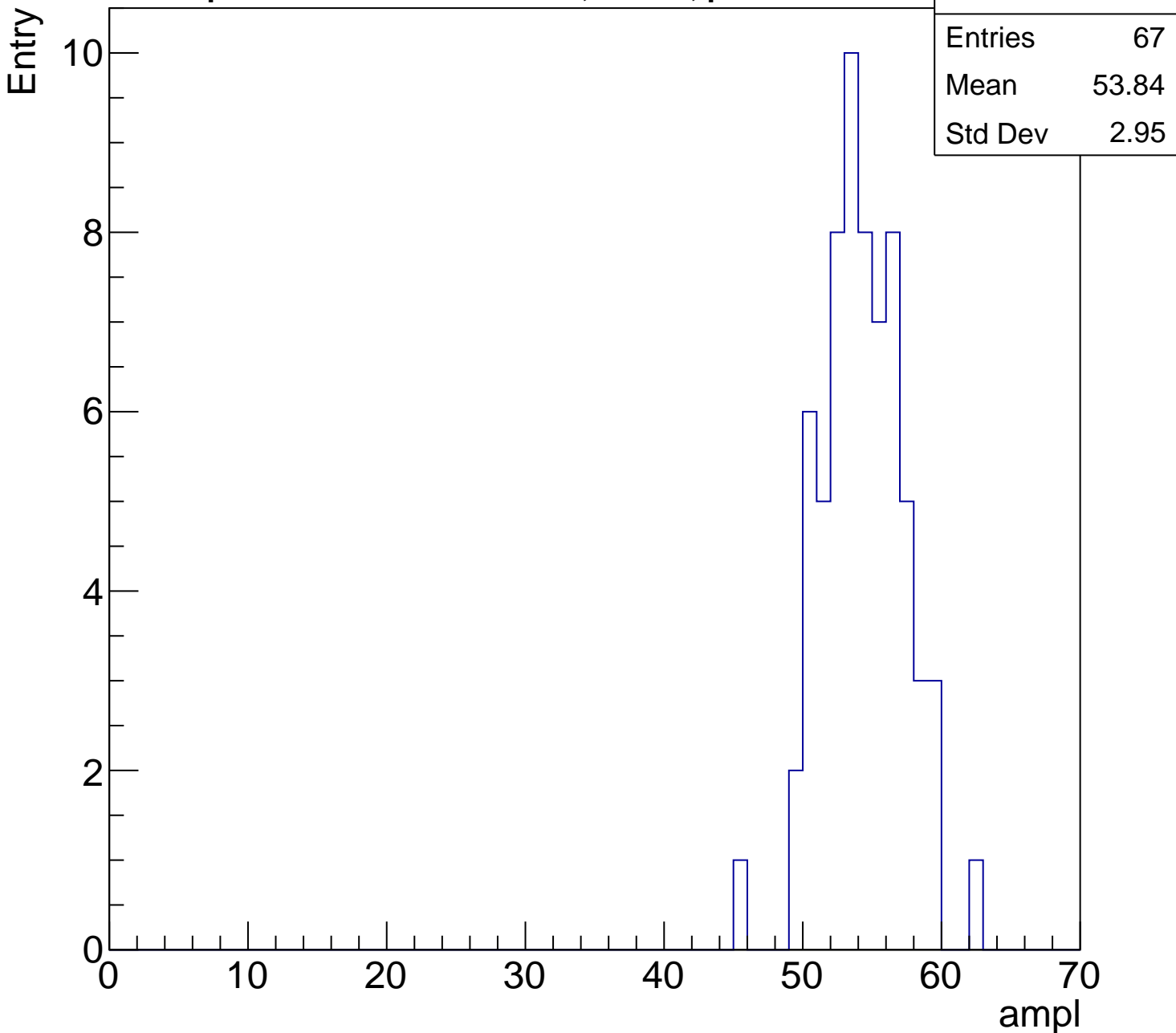
Entries	67
Mean	53.84
Std Dev	2.95

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

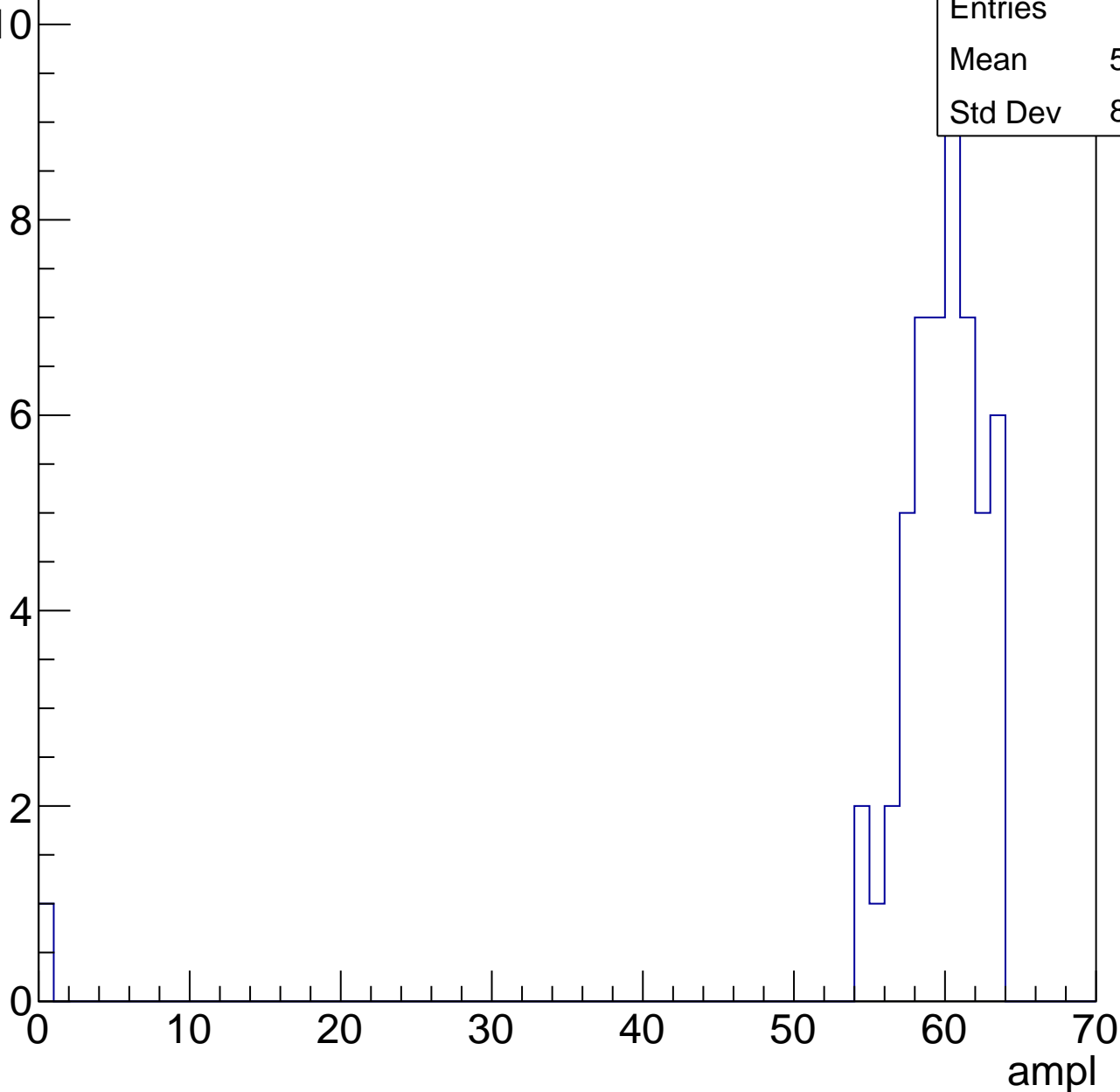


B1L002S, U7-ch57, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

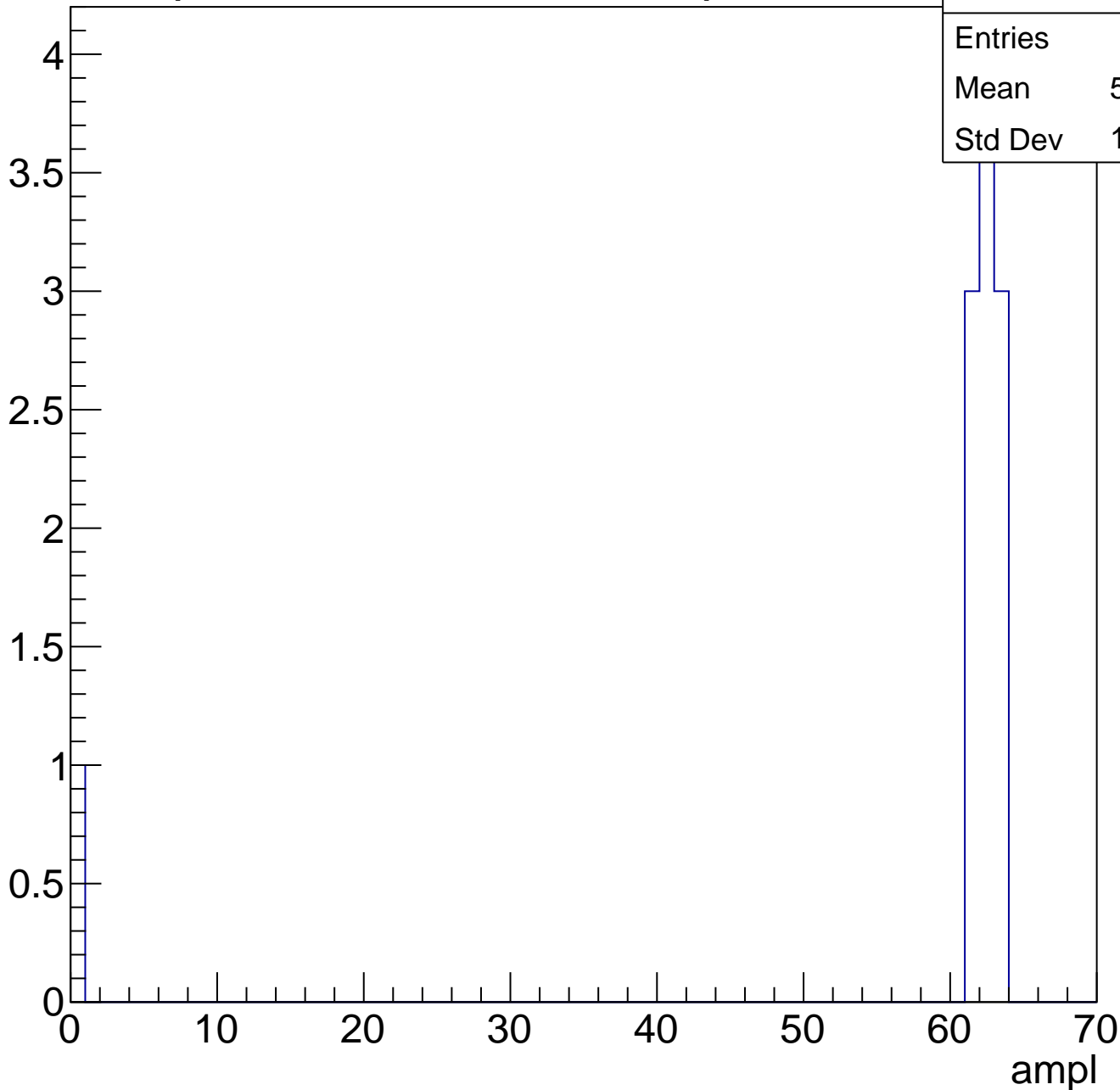
Entries	53
Mean	58.38
Std Dev	8.412



B1L002S, U7-ch57, adc6

calib_packv5_042523_0143.root, FC#10, port B3

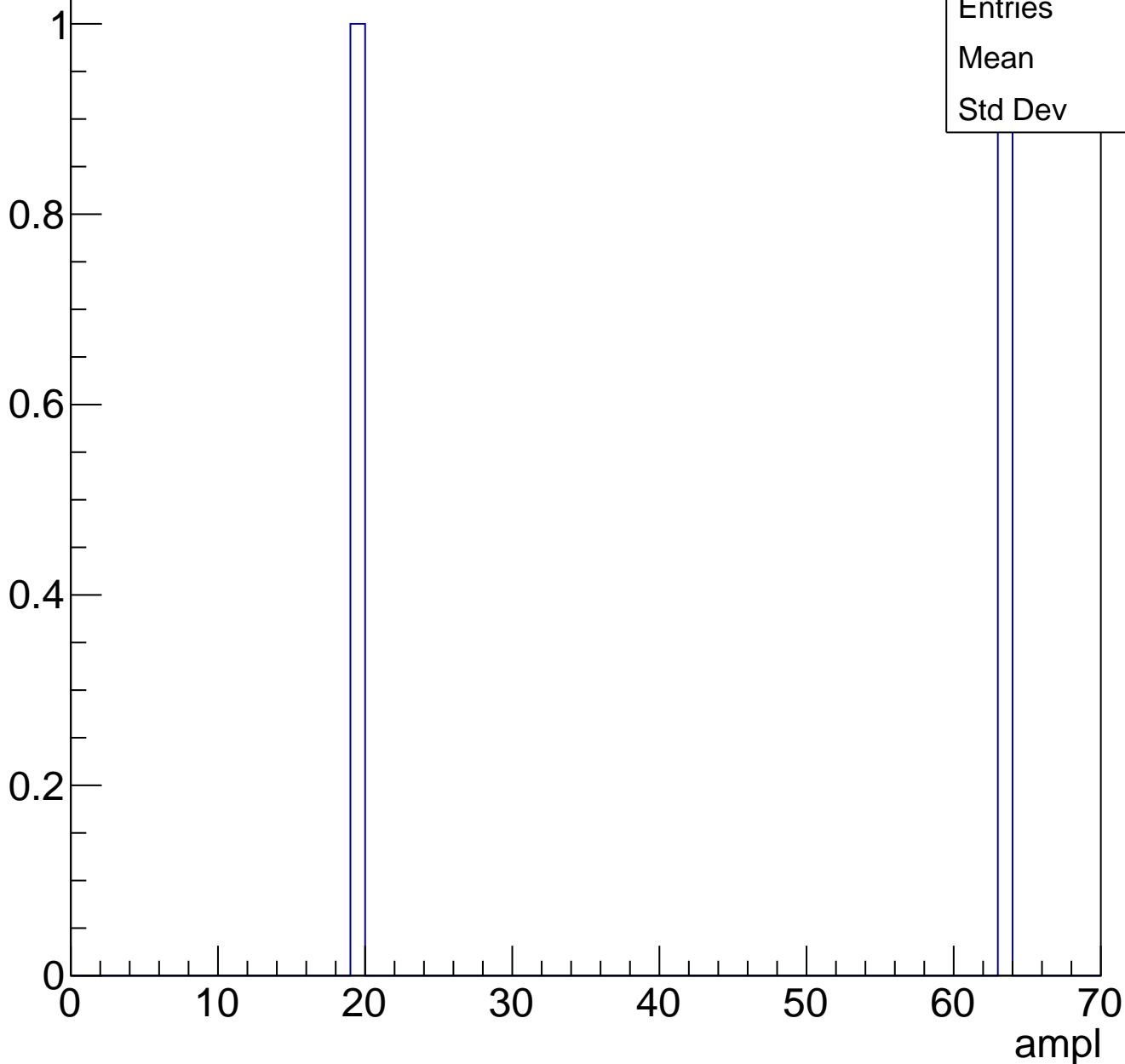
Entry



B1L002S, U7-ch57, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch58, adc0

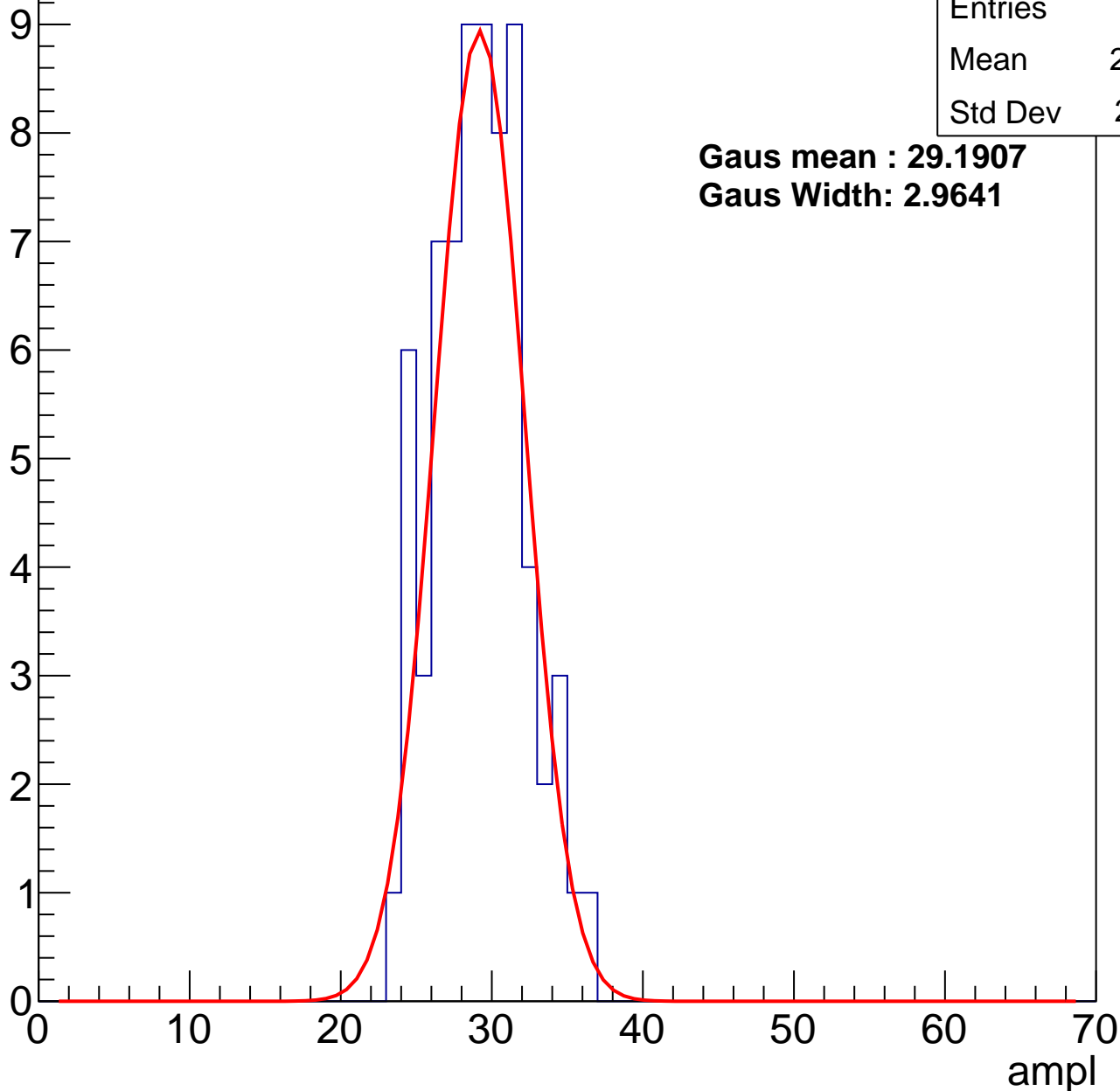
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.74
Std Dev	2.931

Gaus mean : 29.1907

Gaus Width: 2.9641



B1L002S, U7-ch58, adc1

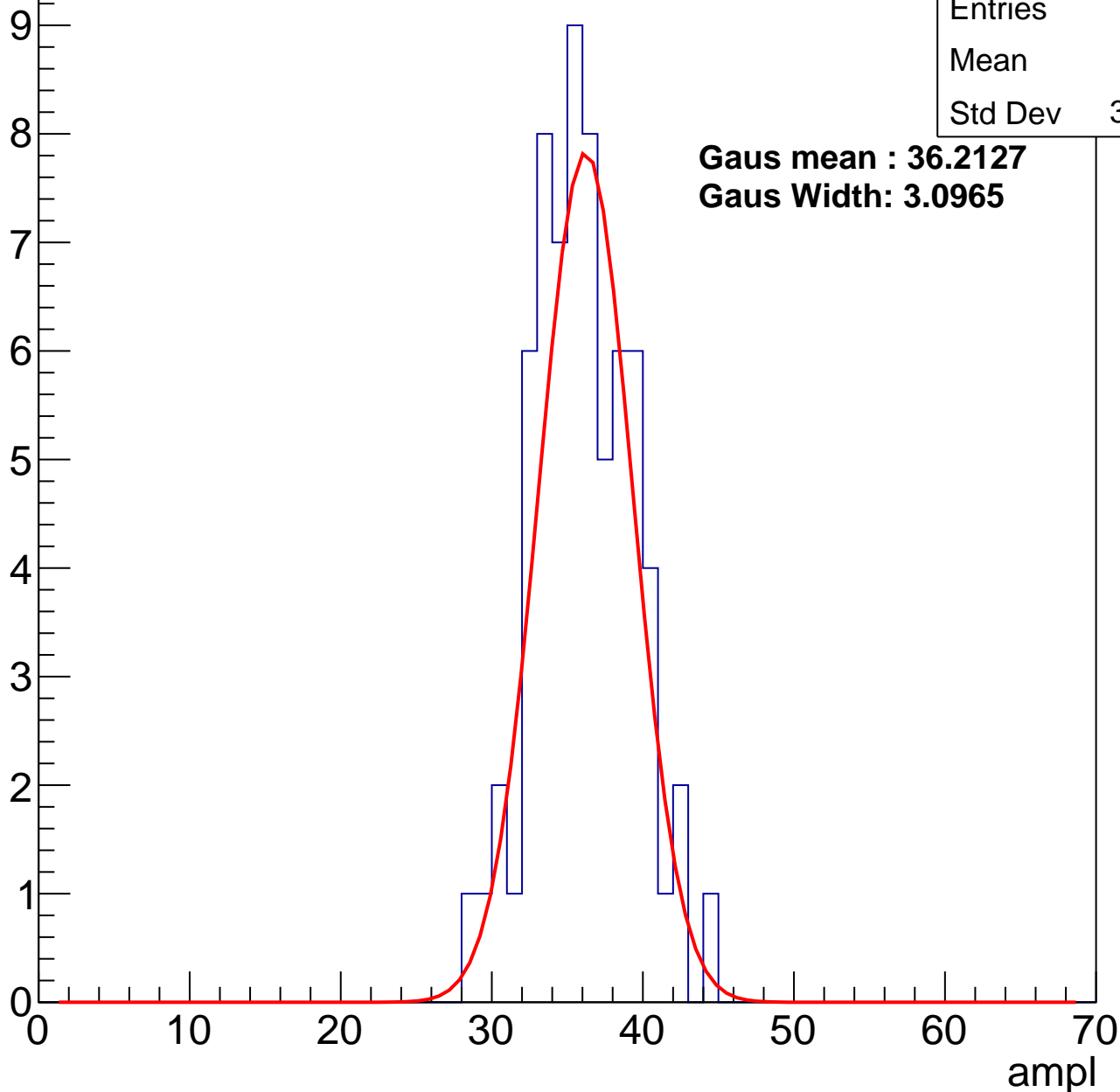
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.6
Std Dev	3.236

Gaus mean : 36.2127

Gaus Width: 3.0965



B1L002S, U7-ch58, adc2

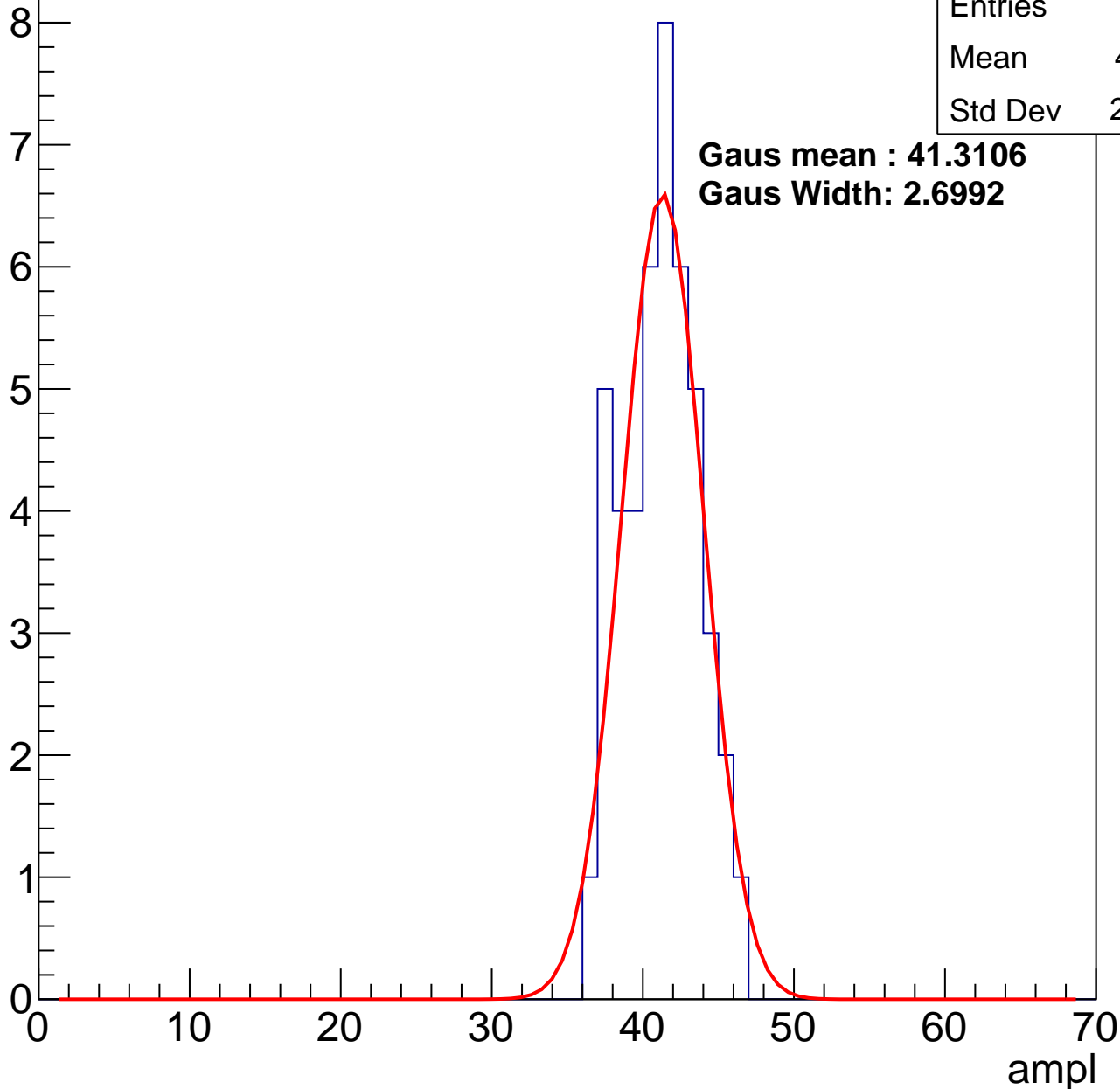
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	40.71
Std Dev	2.446

Gaus mean : 41.3106

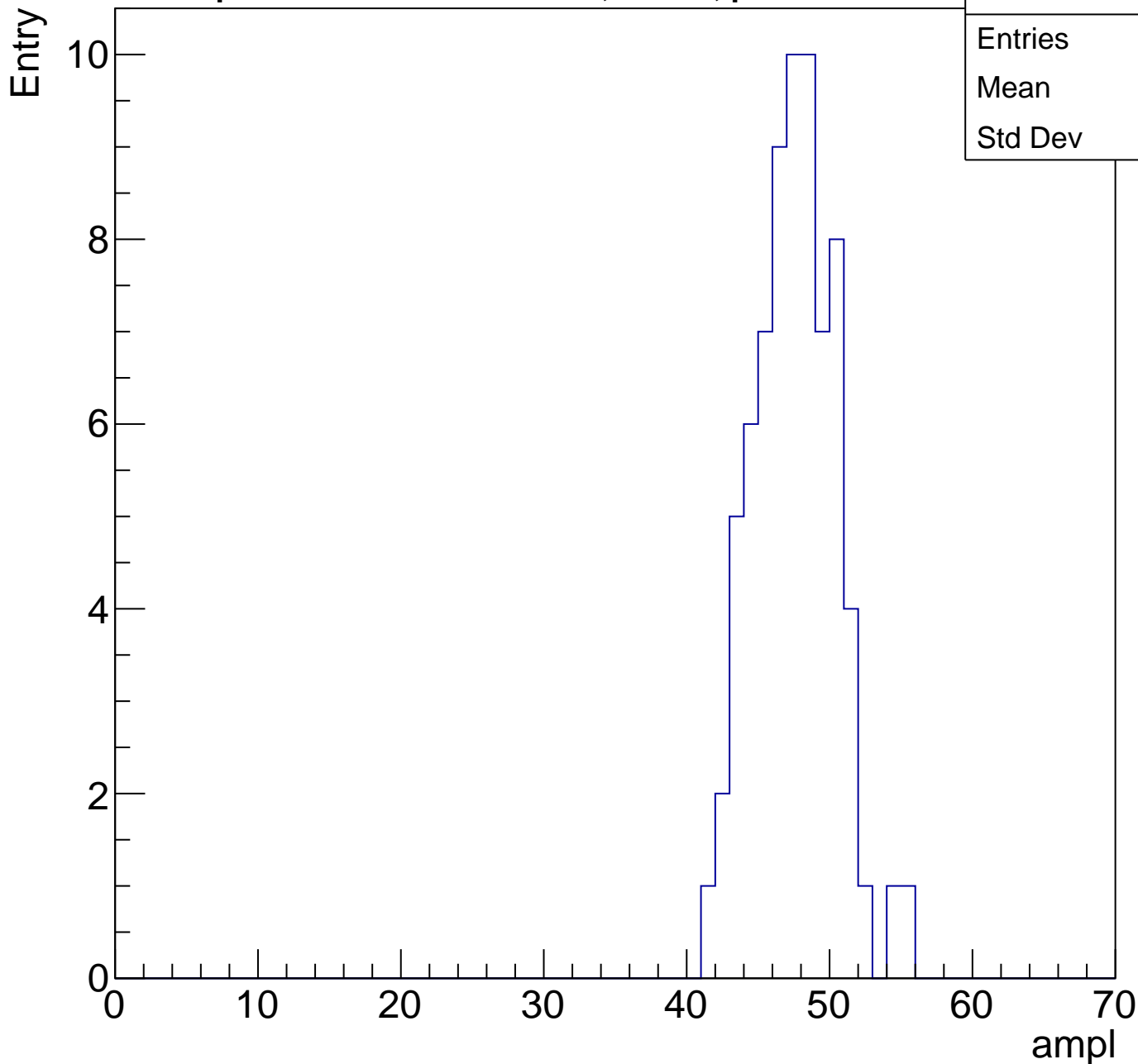
Gaus Width: 2.6992



B1L002S, U7-ch58, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	72
Mean	47.1
Std Dev	2.81

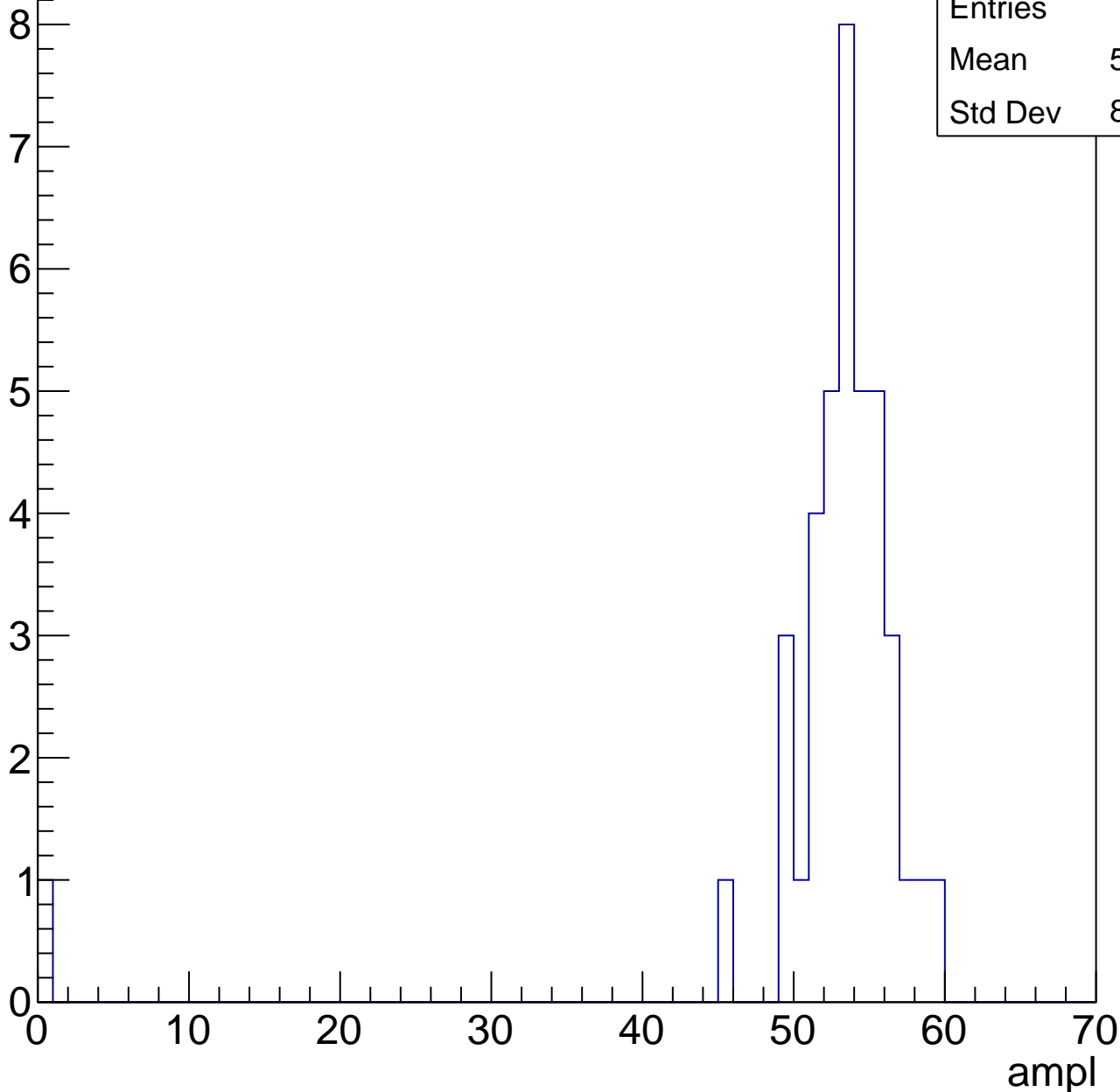


B1L002S, U7-ch58, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

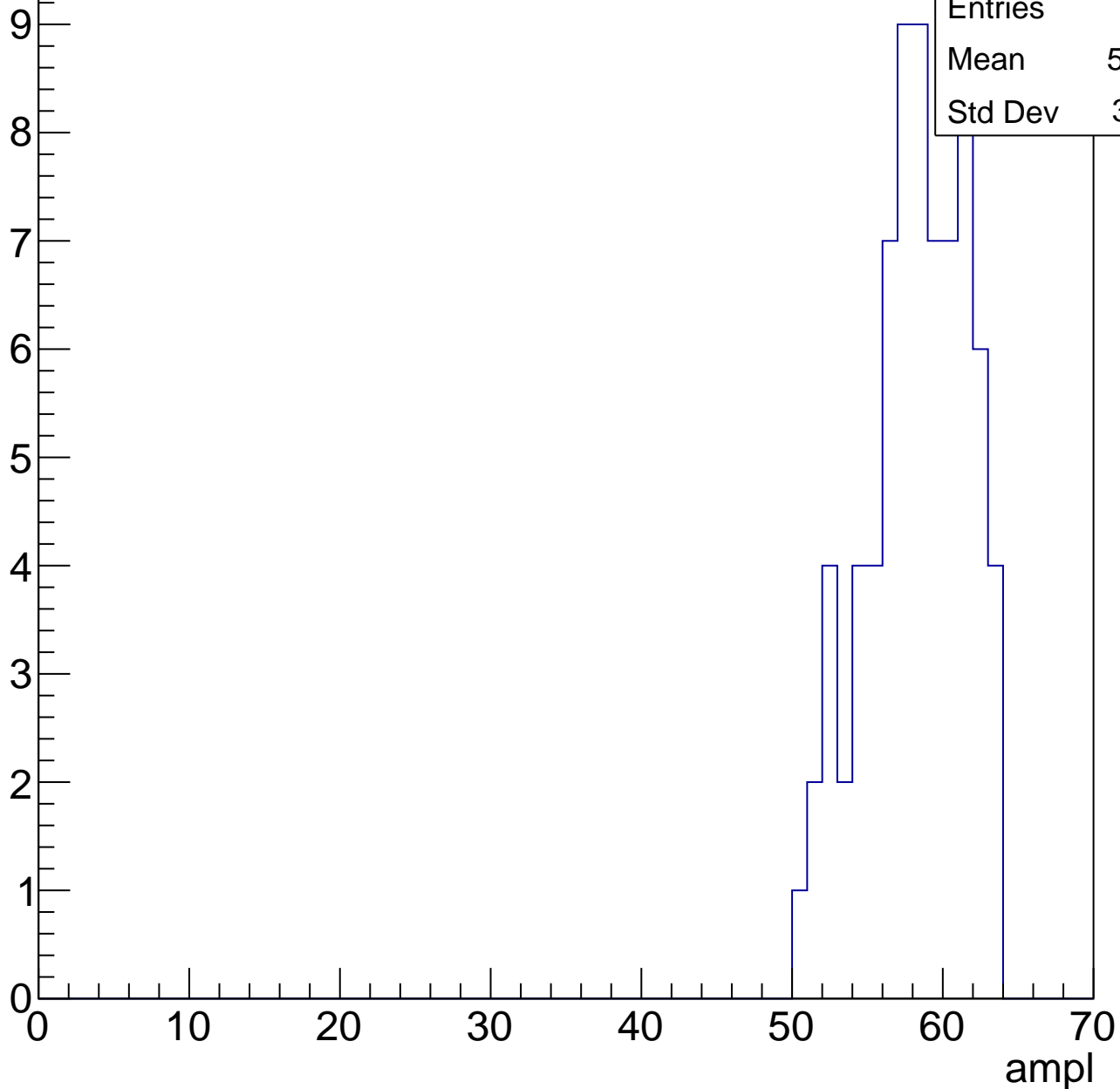
Entries	39
Mean	51.72
Std Dev	8.794



B1L002S, U7-ch58, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

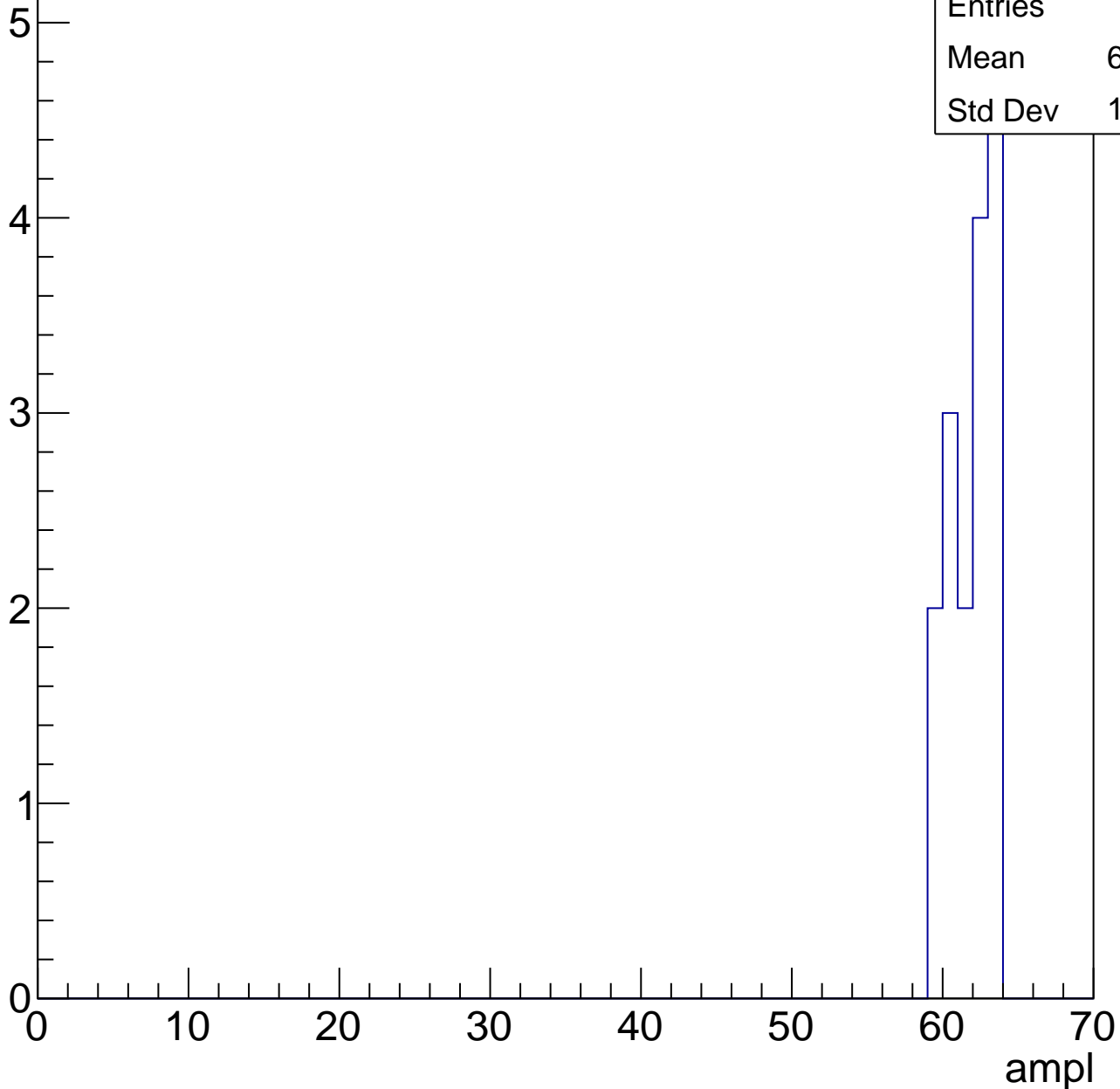


B1L002S, U7-ch58, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

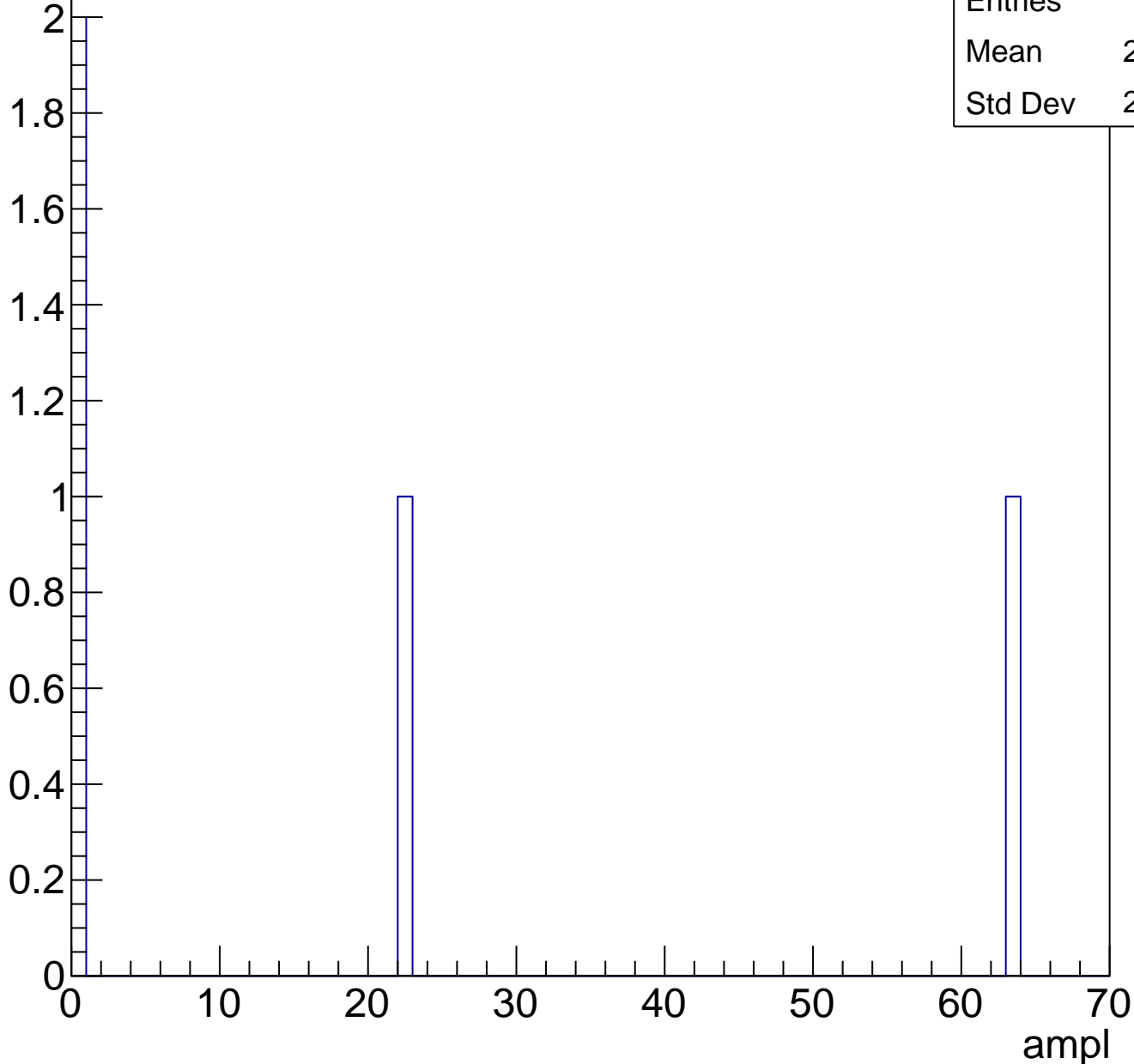
Entries	16
Mean	61.44
Std Dev	1.413



B1L002S, U7-ch58, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B1L002S, U7-ch59, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	77
Mean	27.58
Std Dev	3.11

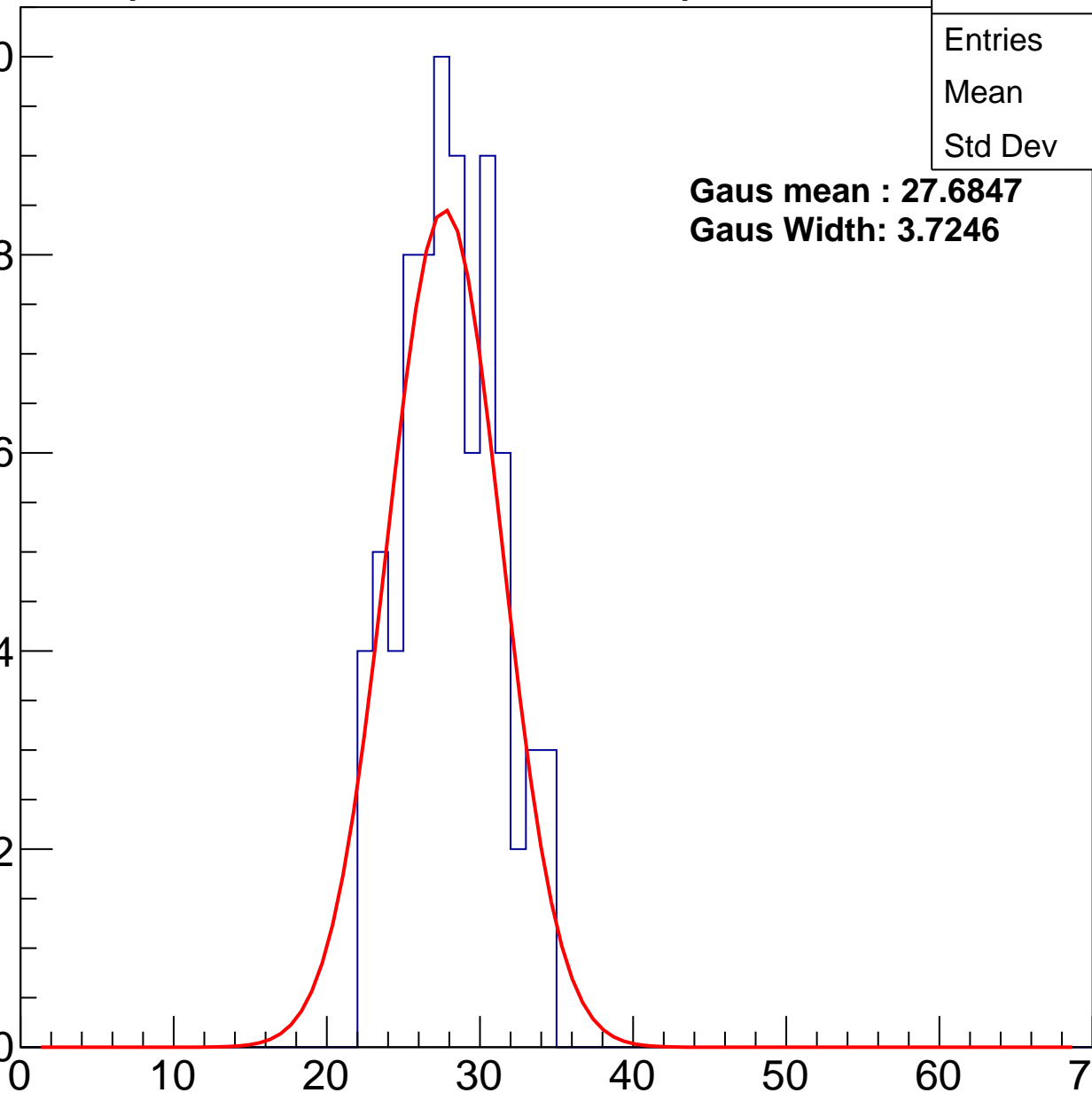
Gaus mean : 27.6847

Gaus Width: 3.7246

Entry

10
8
6
4
2
0

ampl



B1L002S, U7-ch59, adc1

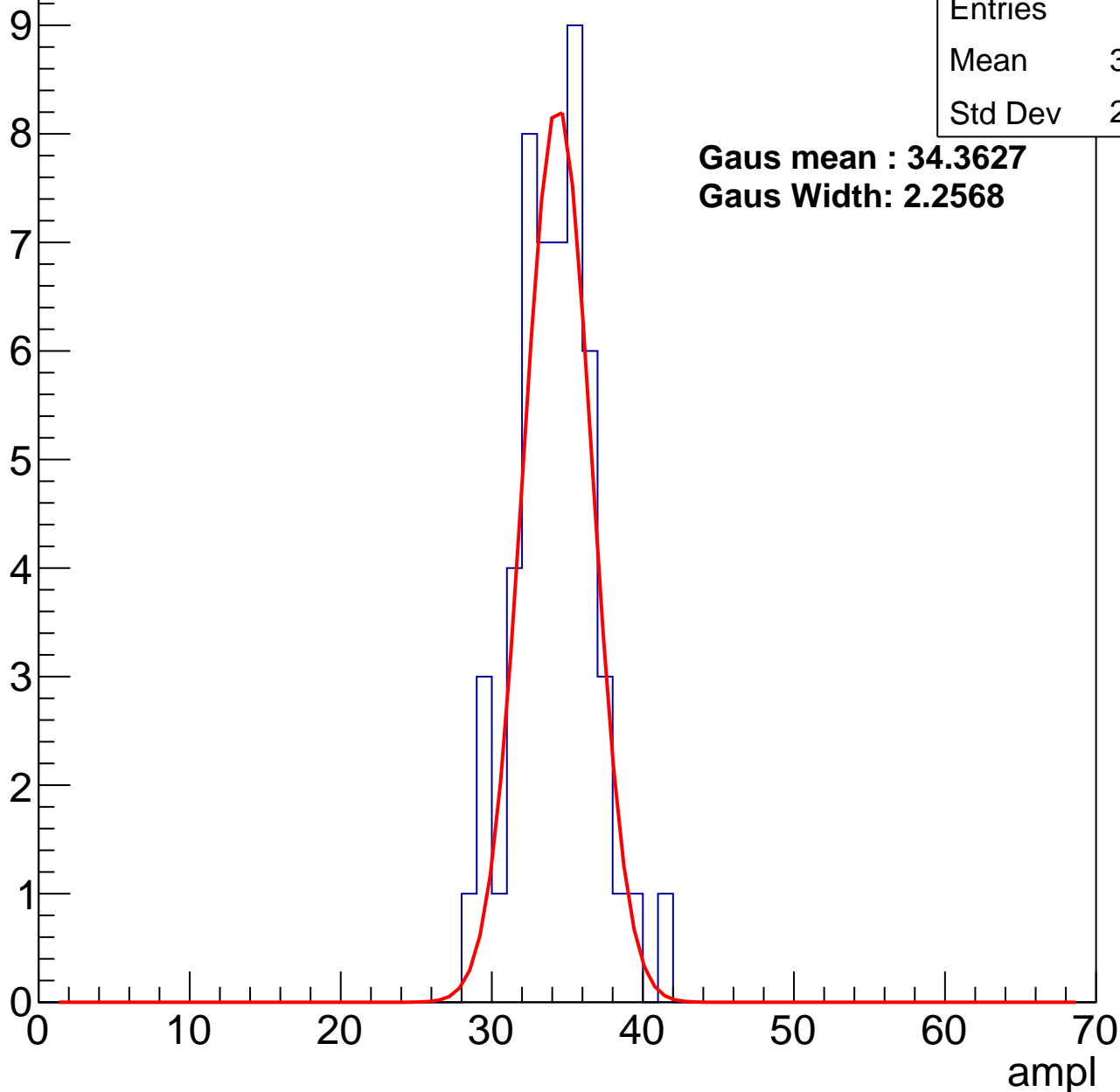
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.73
Std Dev	2.588

Gaus mean : 34.3627

Gaus Width: 2.2568



B1L002S, U7-ch59, adc2

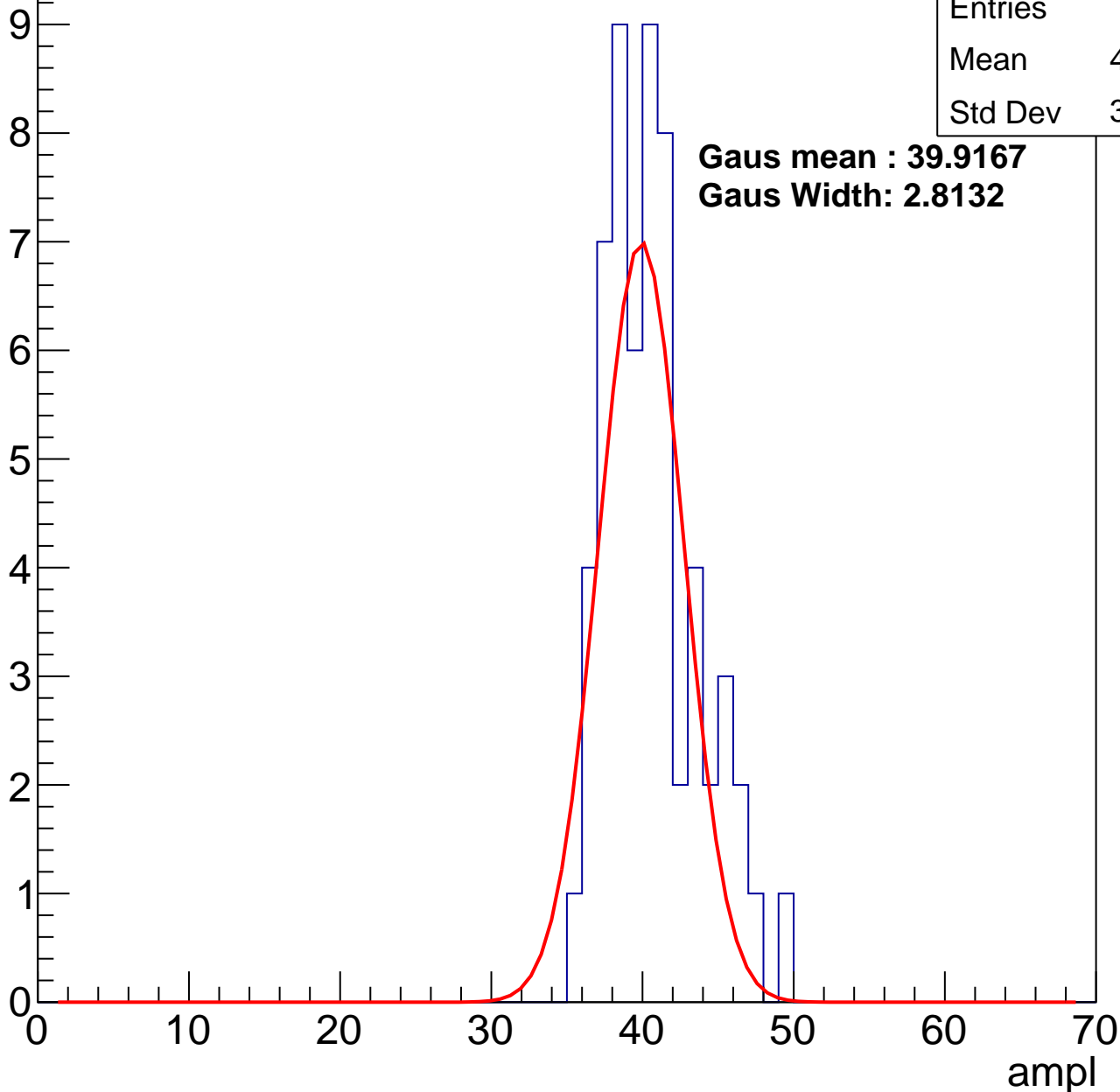
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.15
Std Dev	3.063

Gaus mean : 39.9167

Gaus Width: 2.8132

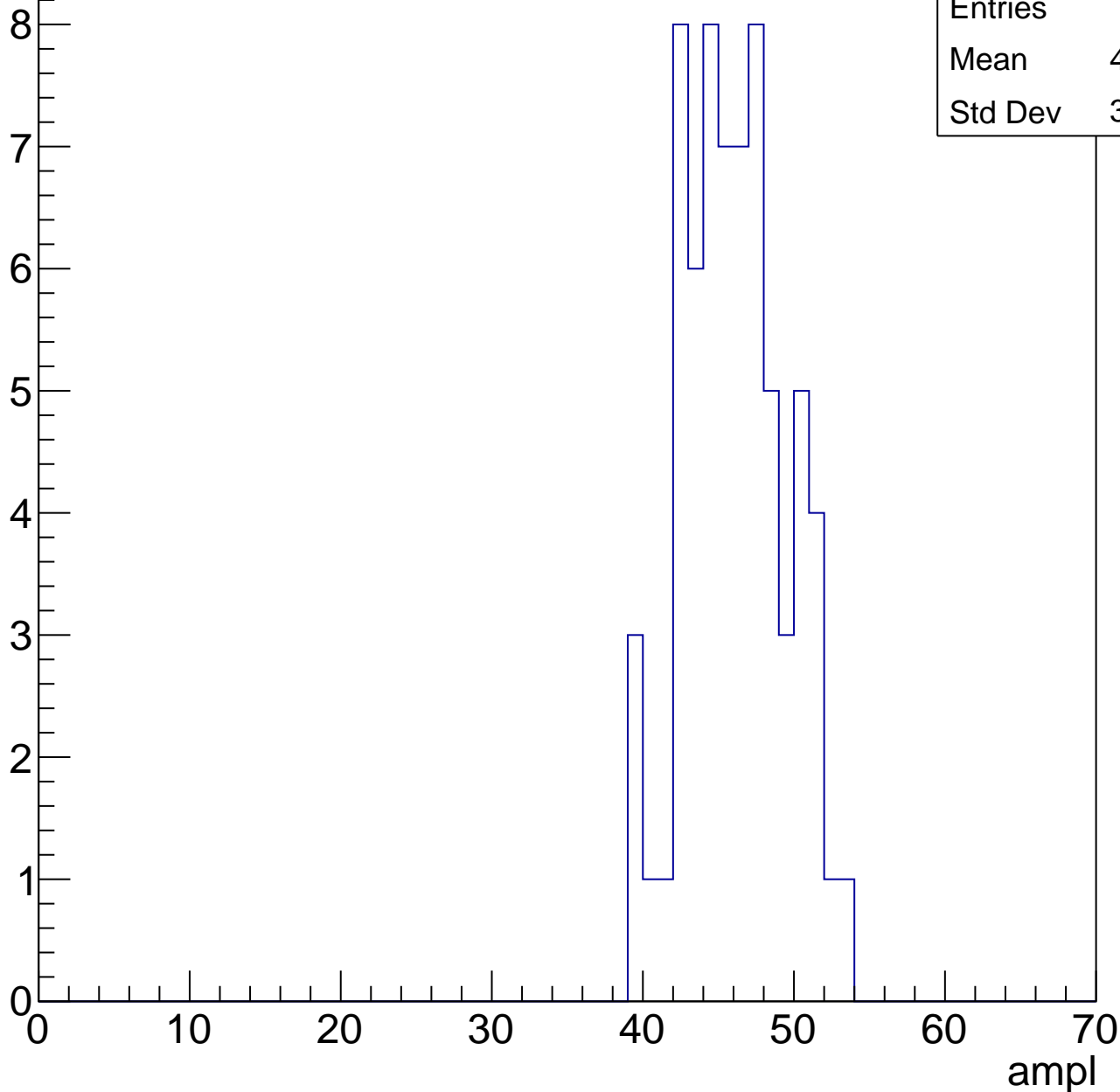


B1L002S, U7-ch59, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

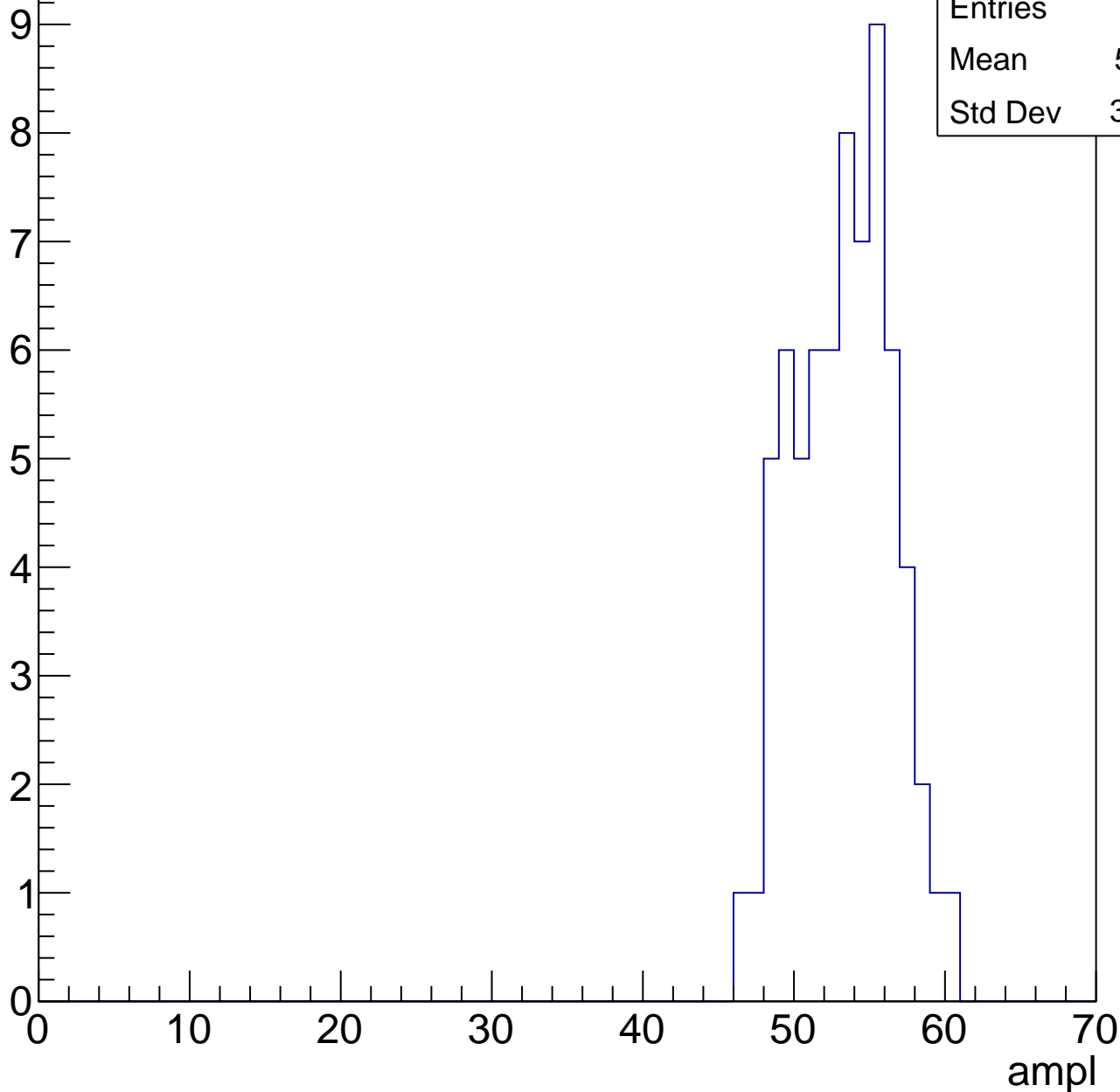
Entries	68
Mean	45.63
Std Dev	3.298



B1L002S, U7-ch59, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

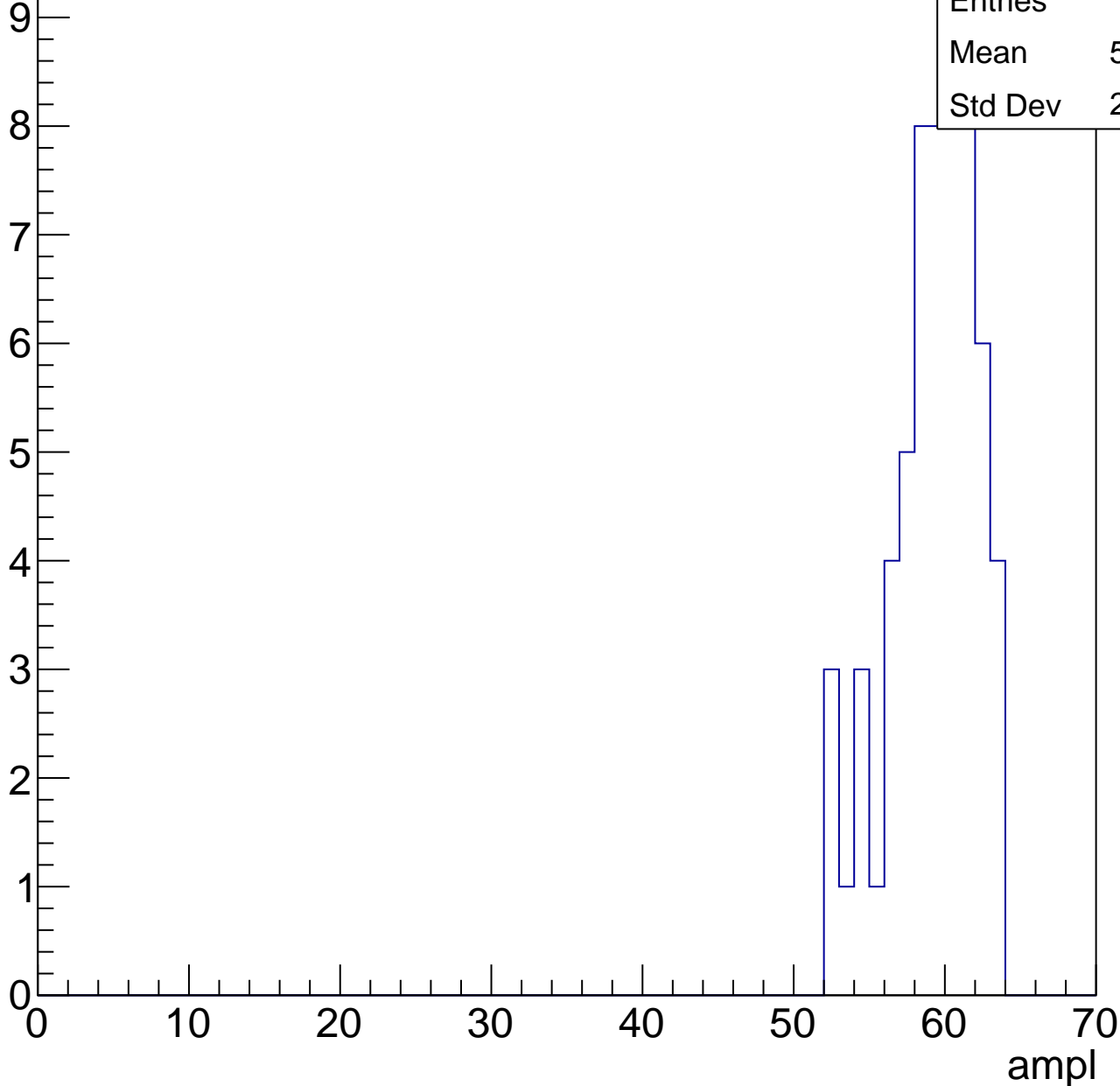


B1L002S, U7-ch59, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.72
Std Dev	2.864

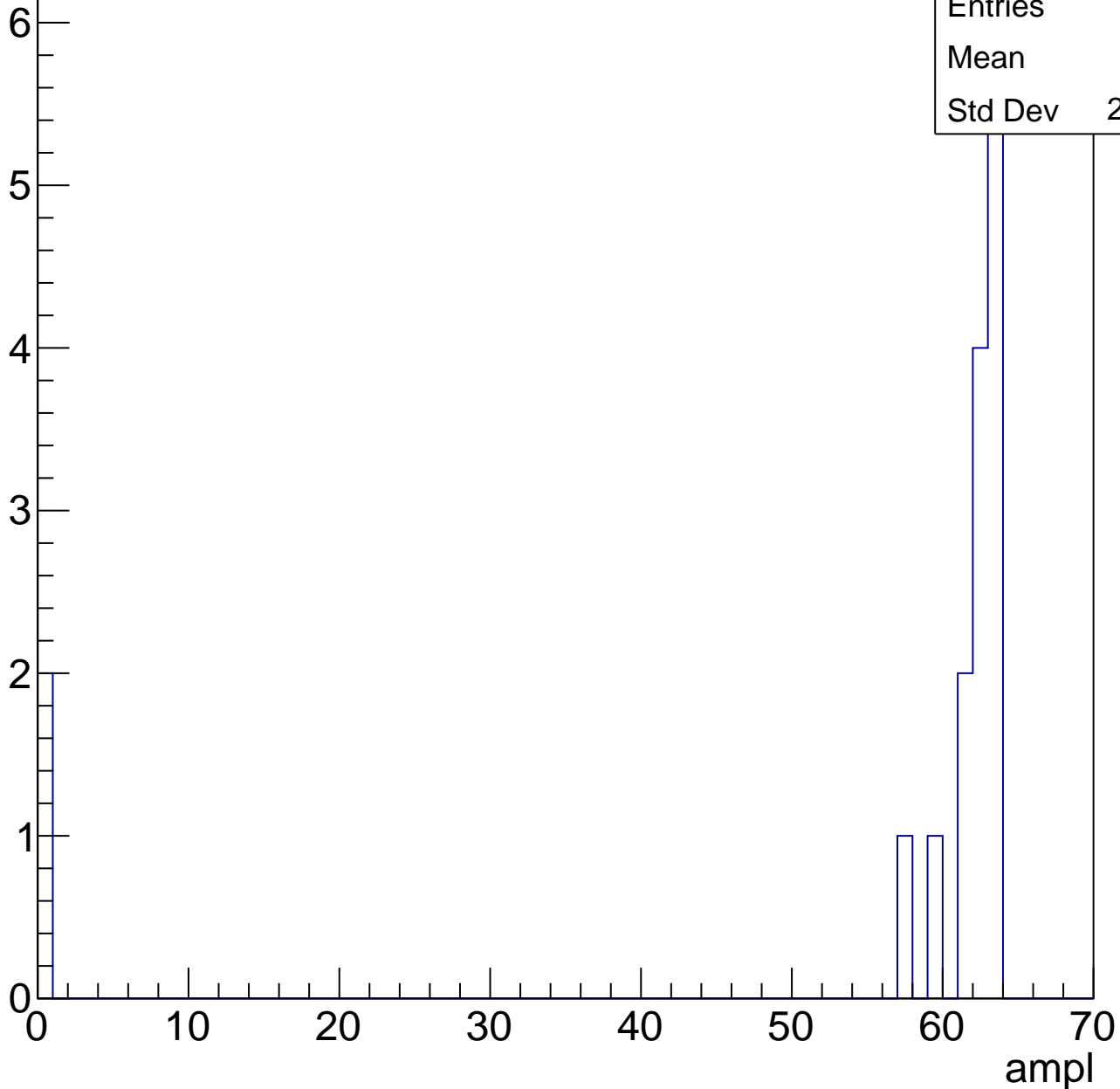


B1L002S, U7-ch59, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

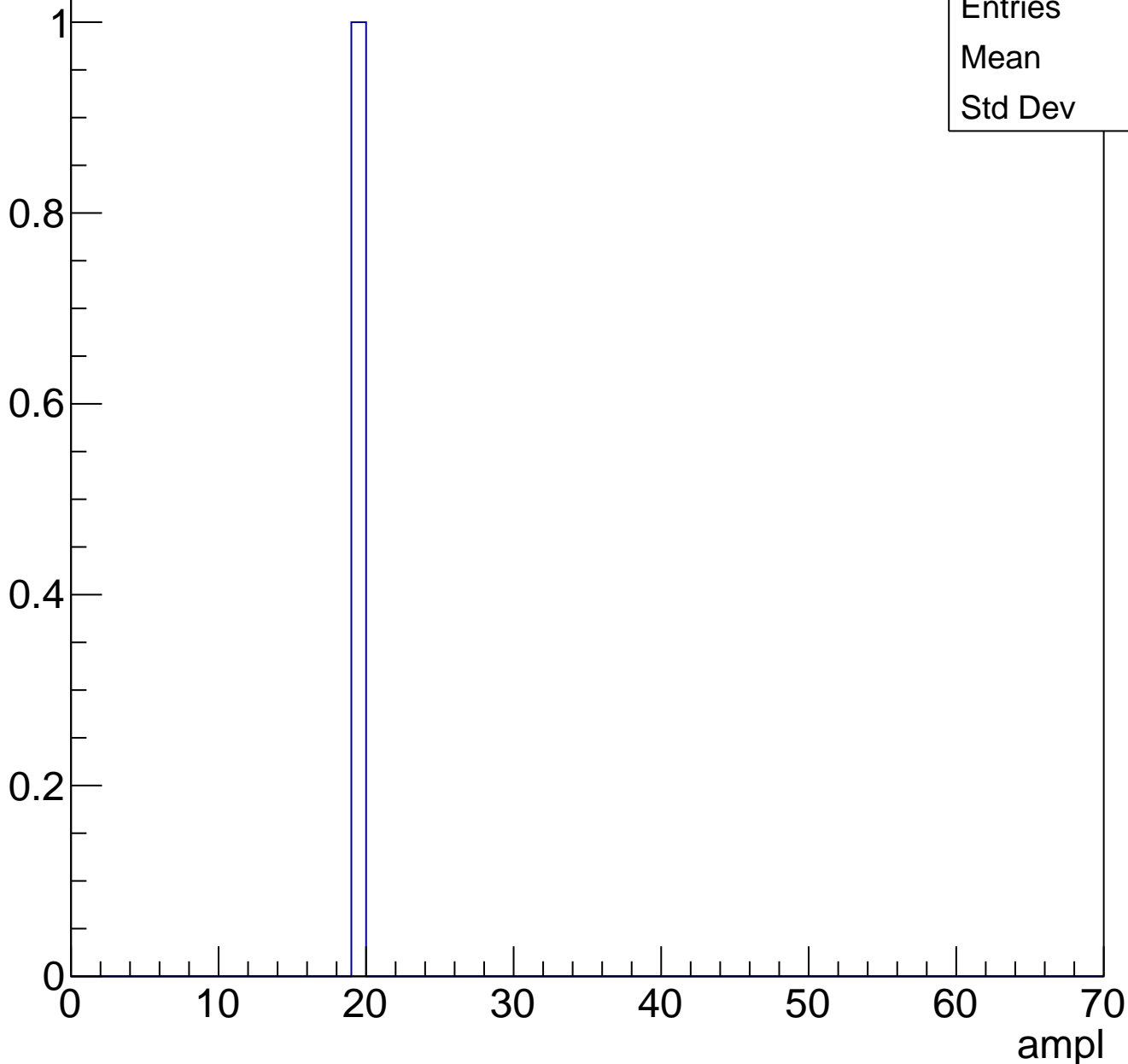
Entries	16
Mean	54
Std Dev	20.47



B1L002S, U7-ch59, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch60, adc0

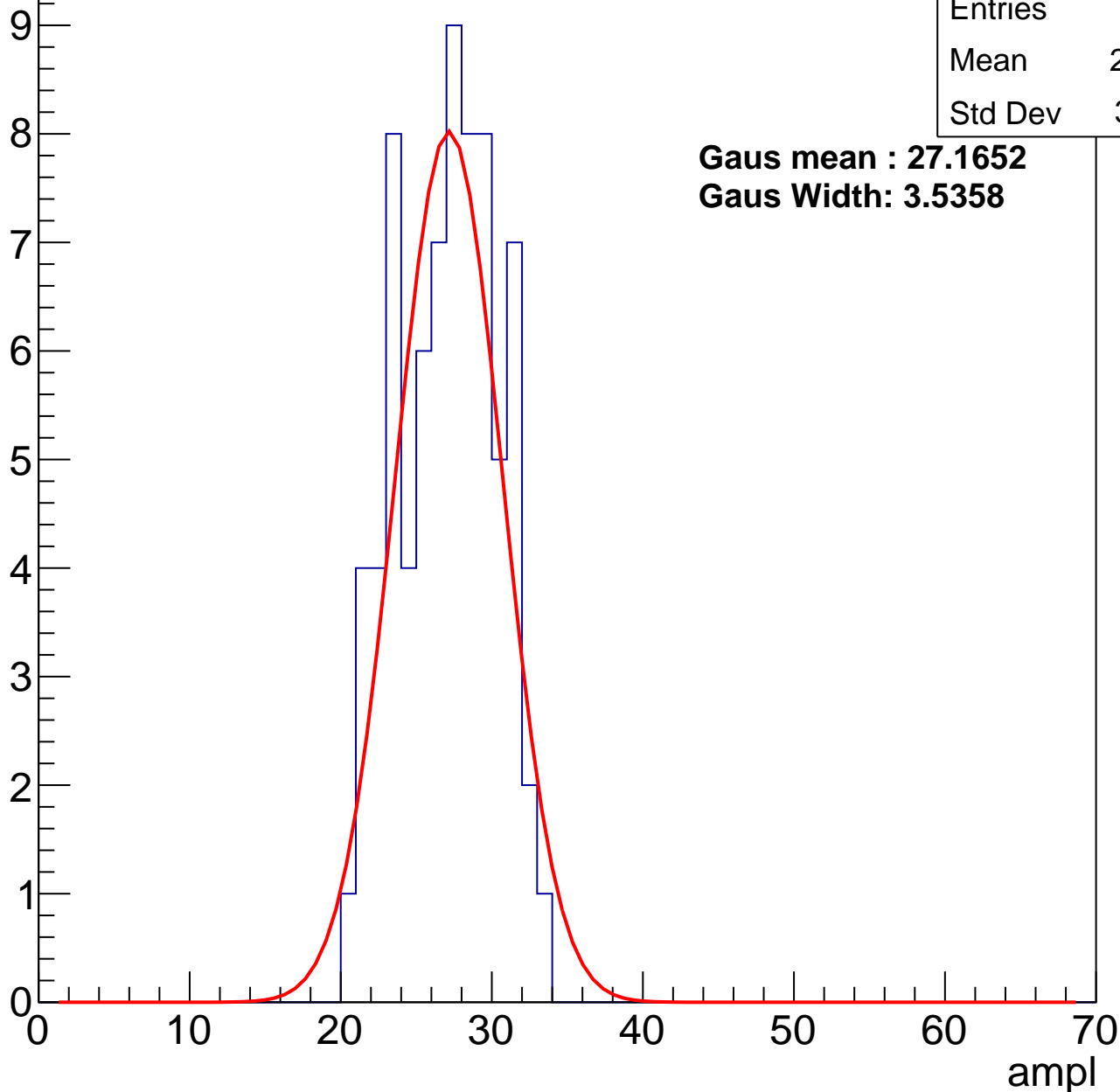
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	26.58
Std Dev	3.201

Gaus mean : 27.1652

Gaus Width: 3.5358



B1L002S, U7-ch60, adc1

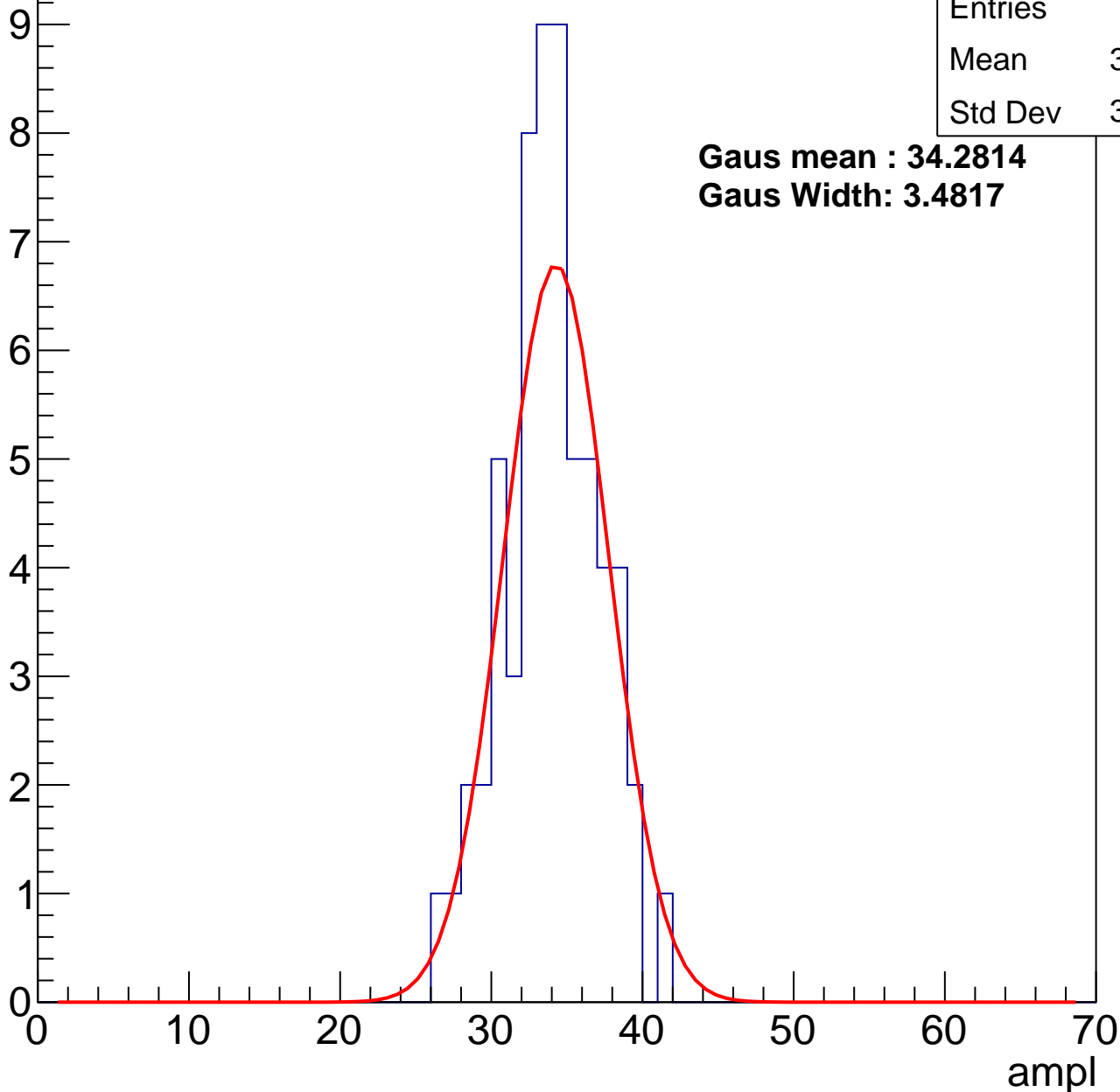
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	33.49
Std Dev	3.113

Gaus mean : 34.2814

Gaus Width: 3.4817



B1L002S, U7-ch60, adc2

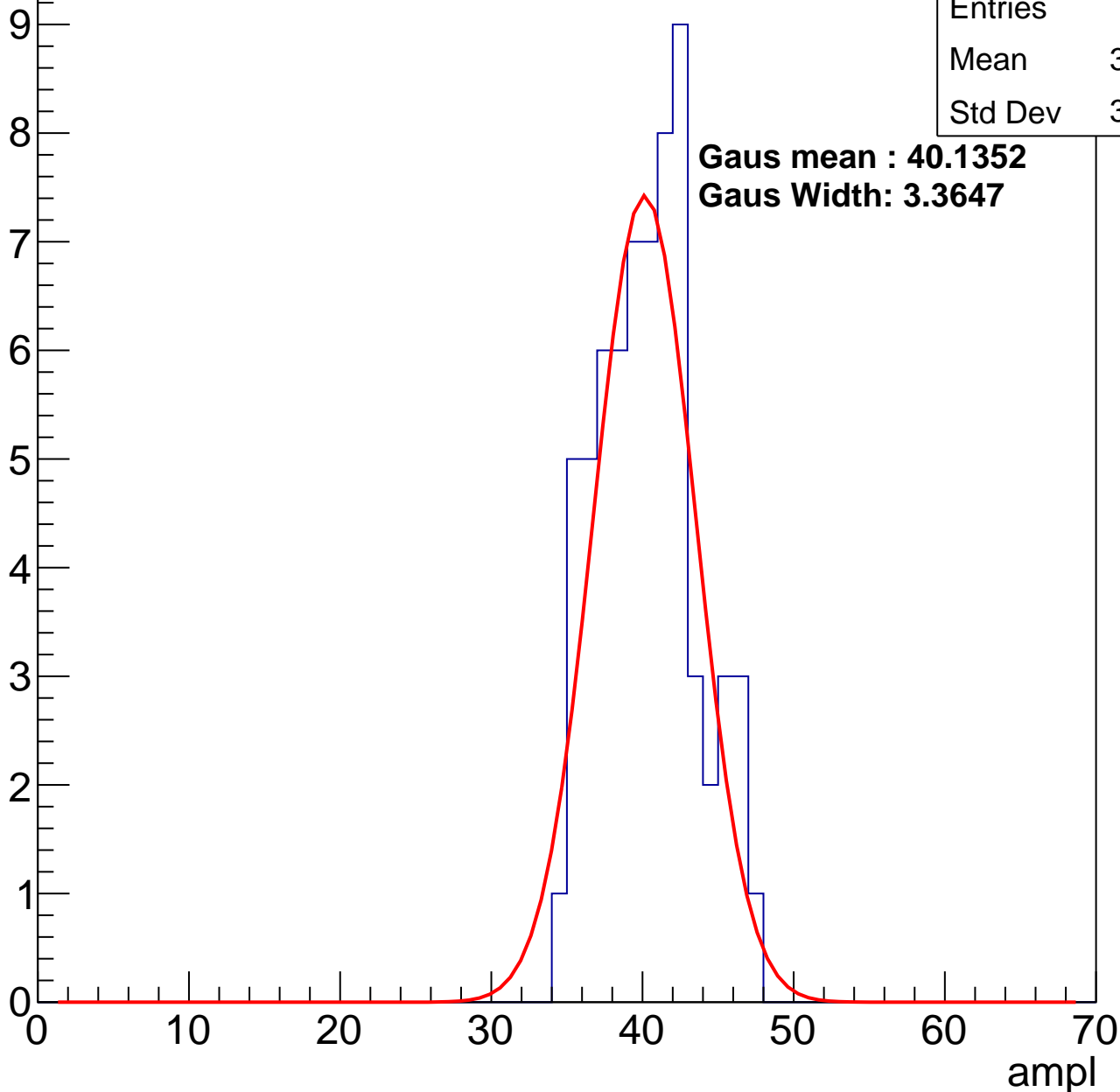
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	39.92
Std Dev	3.164

Gaus mean : 40.1352

Gaus Width: 3.3647



B1L002S, U7-ch60, adc3

calib_packv5_042523_0143.root, FC#10, port B3

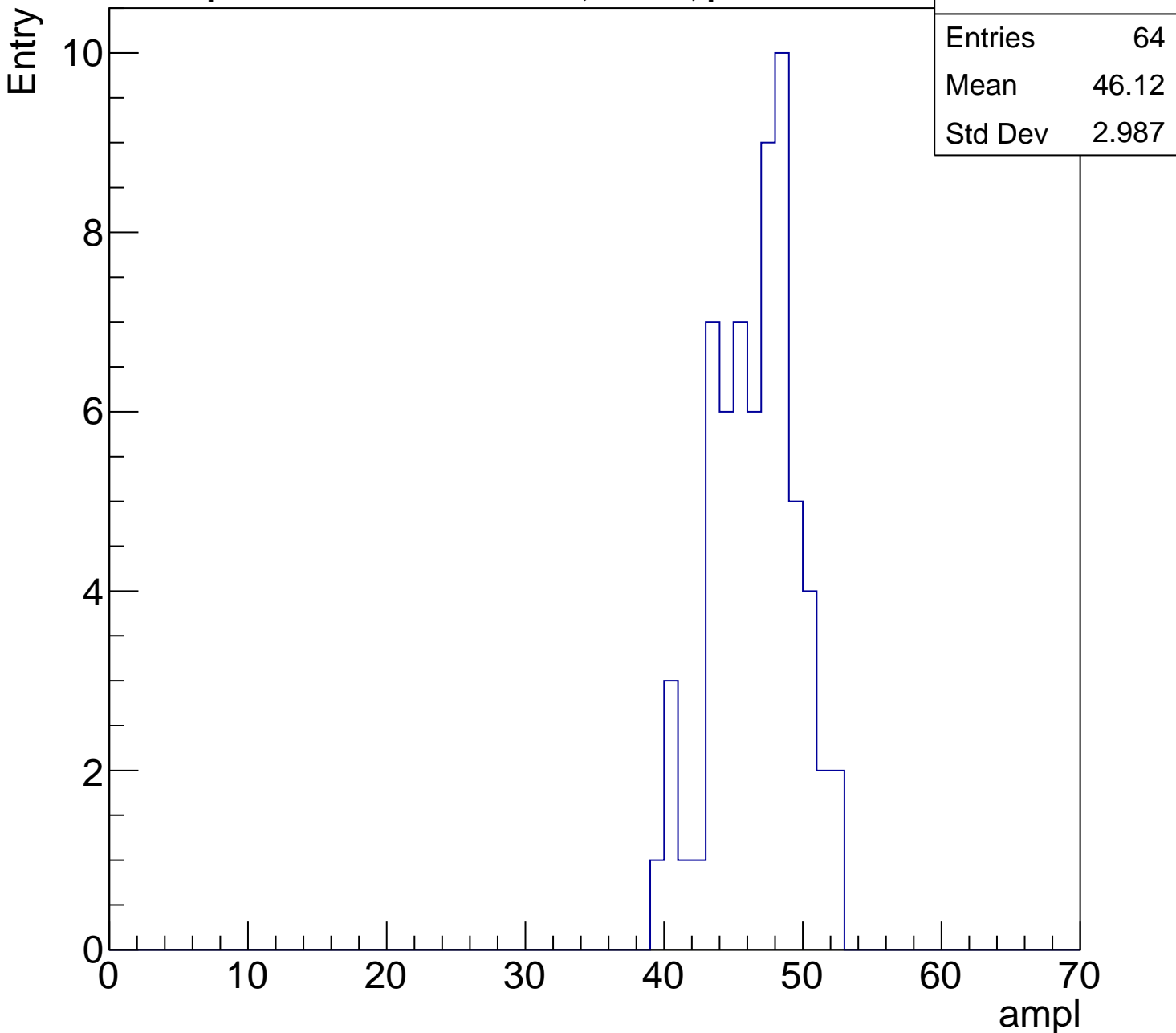
Entries	64
Mean	46.12
Std Dev	2.987

Entry

10
8
6
4
2
0

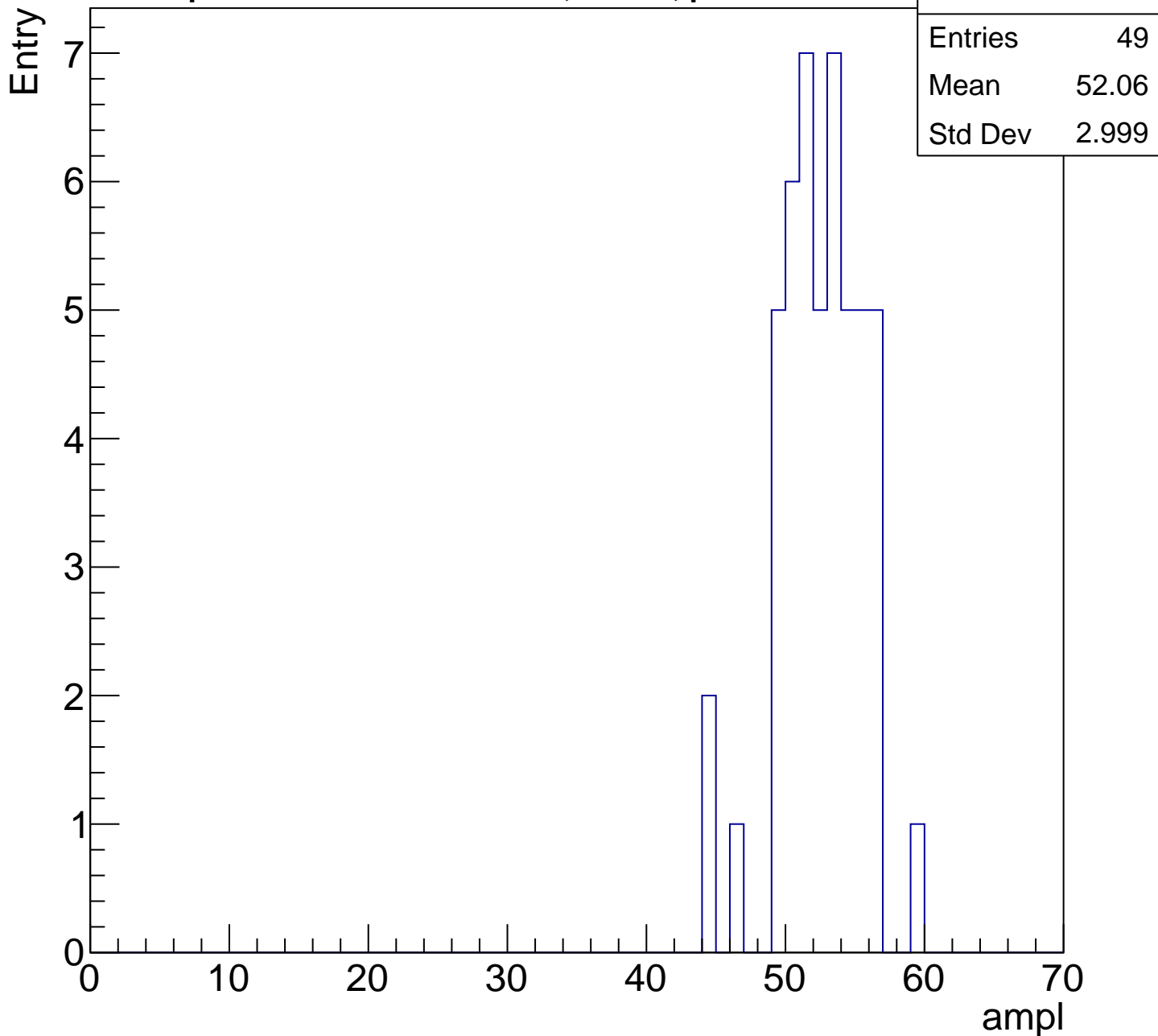
0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch60, adc4

calib_packv5_042523_0143.root, FC#10, port B3

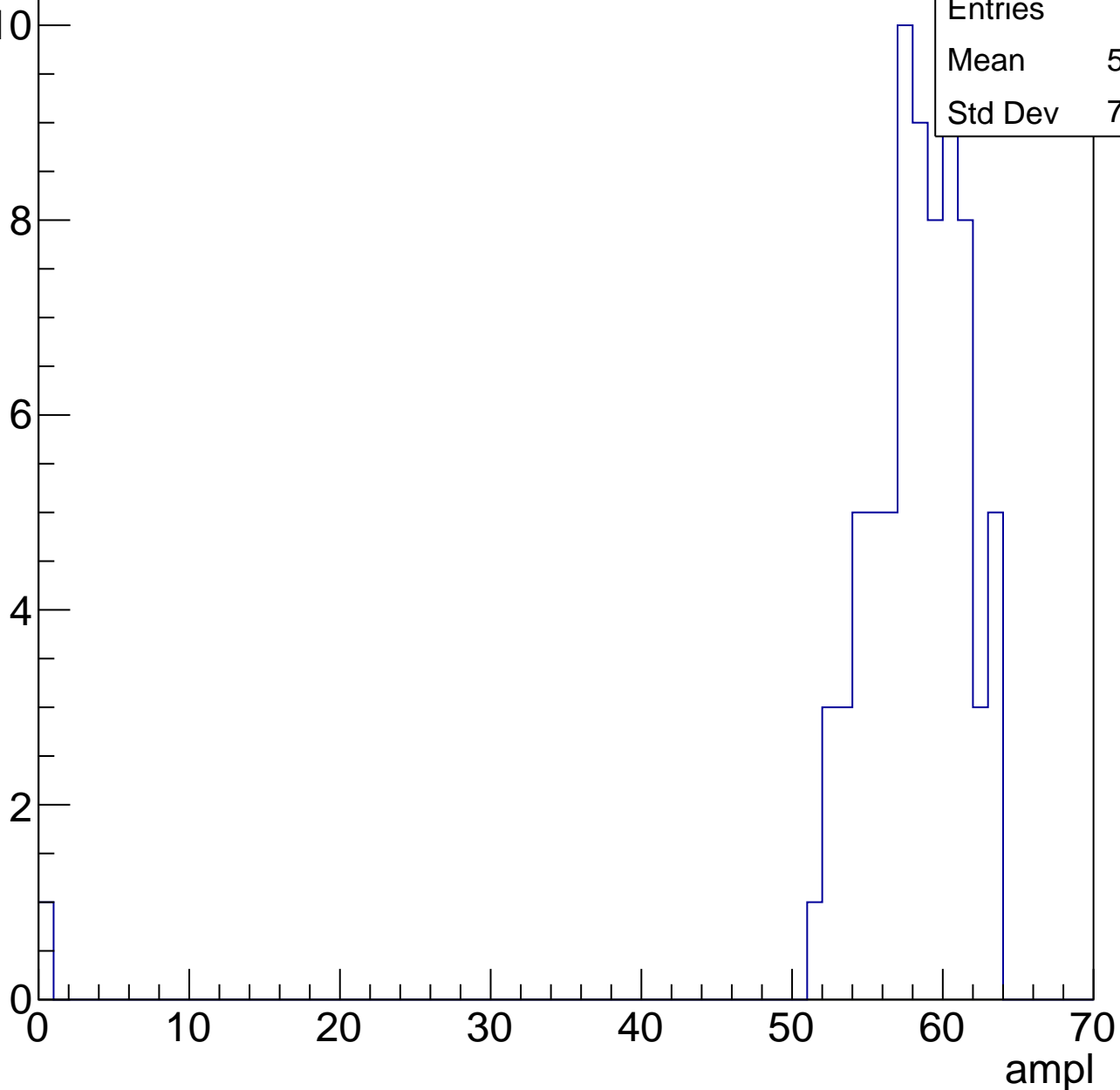


B1L002S, U7-ch60, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	57.12
Std Dev	7.286

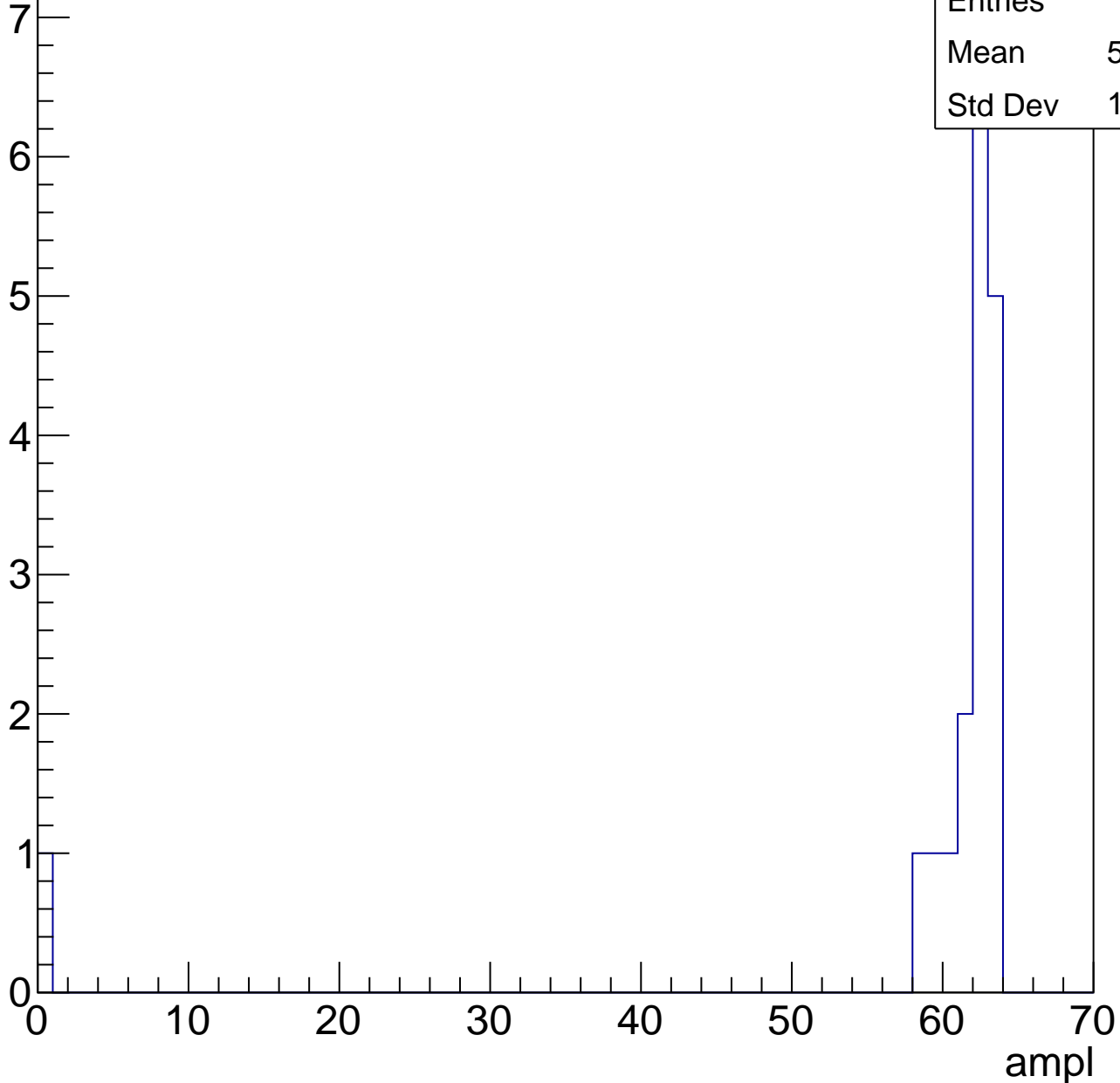


B1L002S, U7-ch60, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.22
Std Dev	14.19



B1L002S, U7-ch60, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch61, adc0

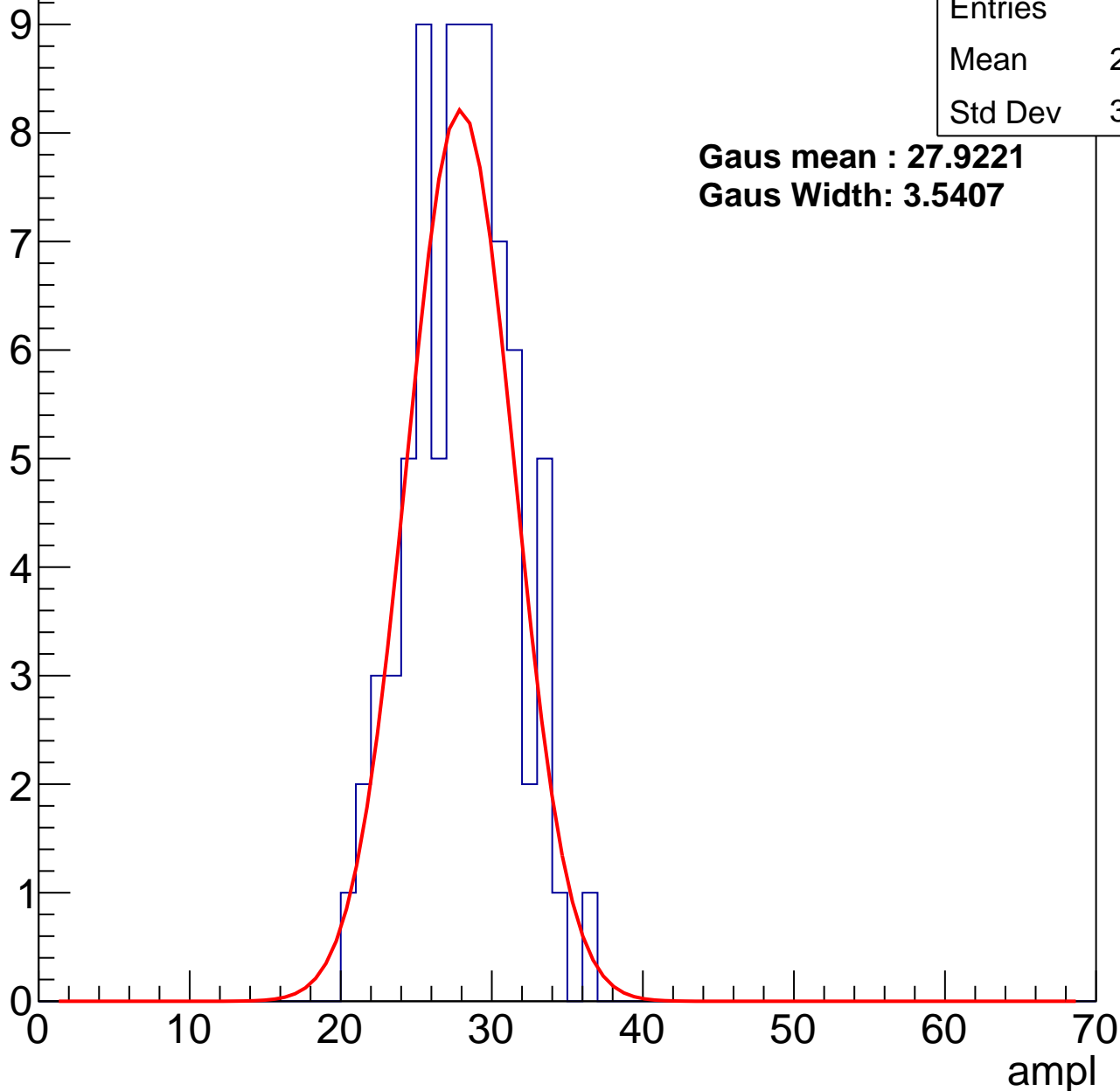
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	27.57
Std Dev	3.359

Gaus mean : 27.9221

Gaus Width: 3.5407



B1L002S, U7-ch61, adc1

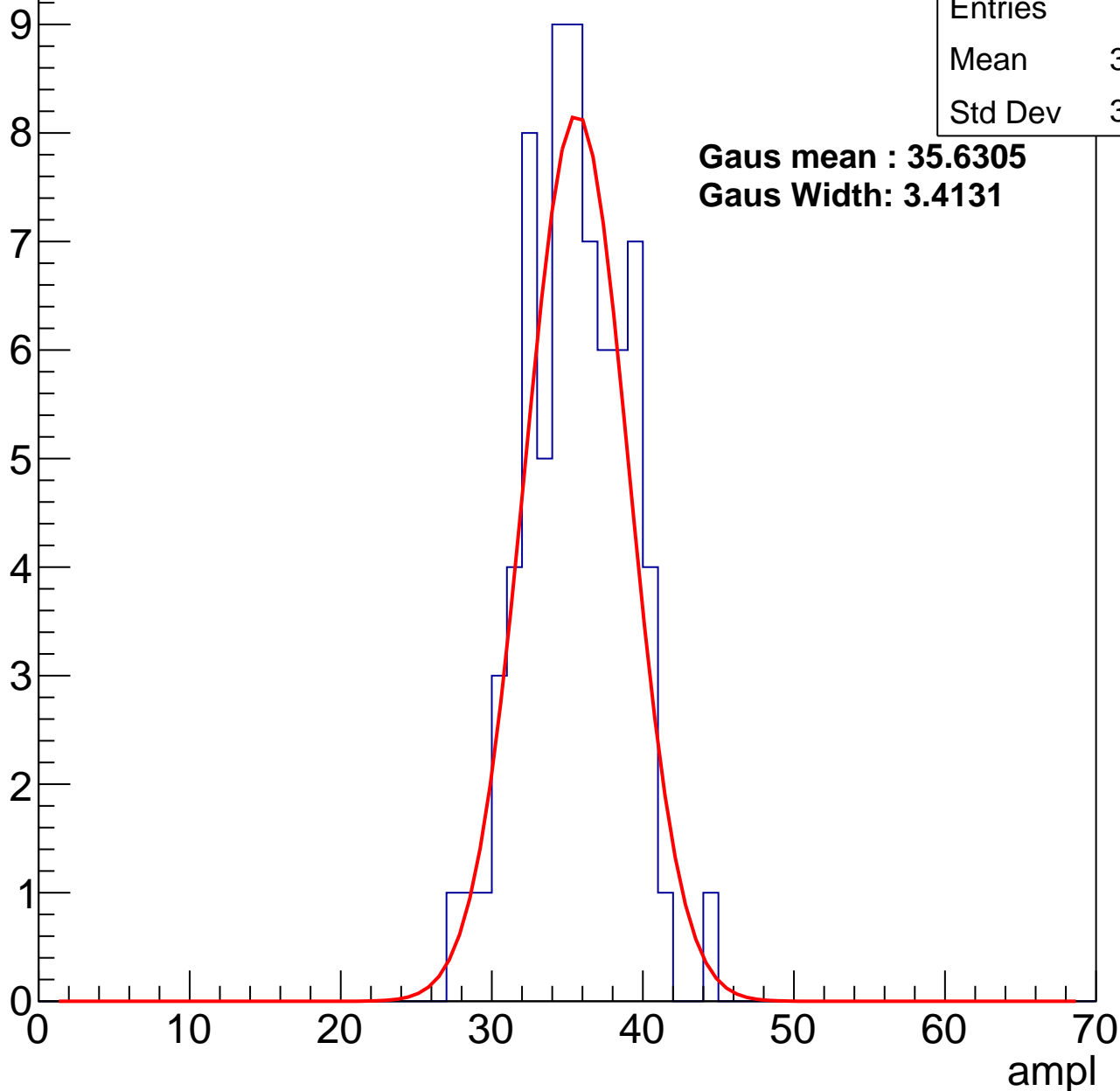
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	35.07
Std Dev	3.316

Gaus mean : 35.6305

Gaus Width: 3.4131



B1L002S, U7-ch61, adc2

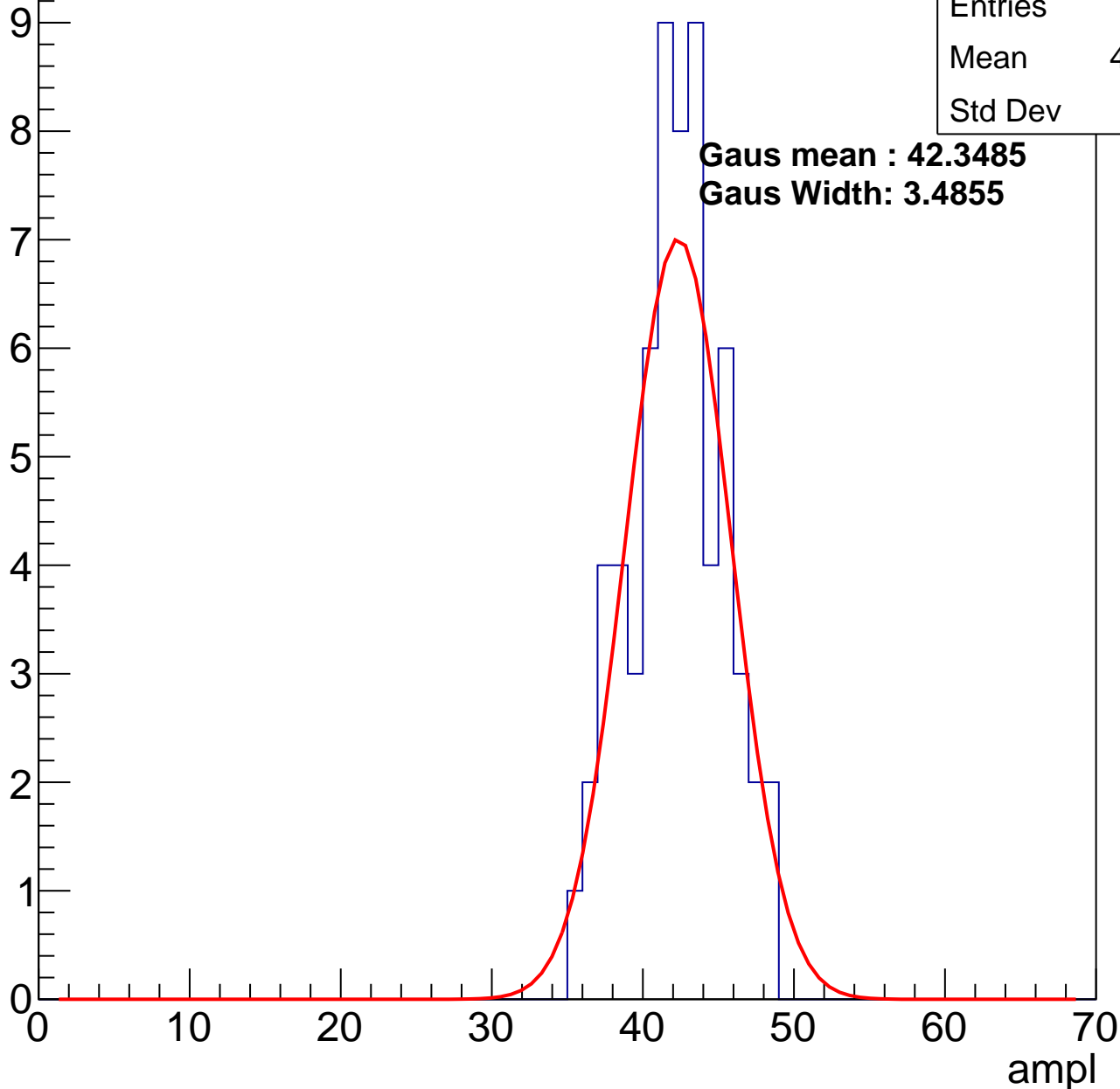
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.75
Std Dev	3.06

Gaus mean : 42.3485

Gaus Width: 3.4855

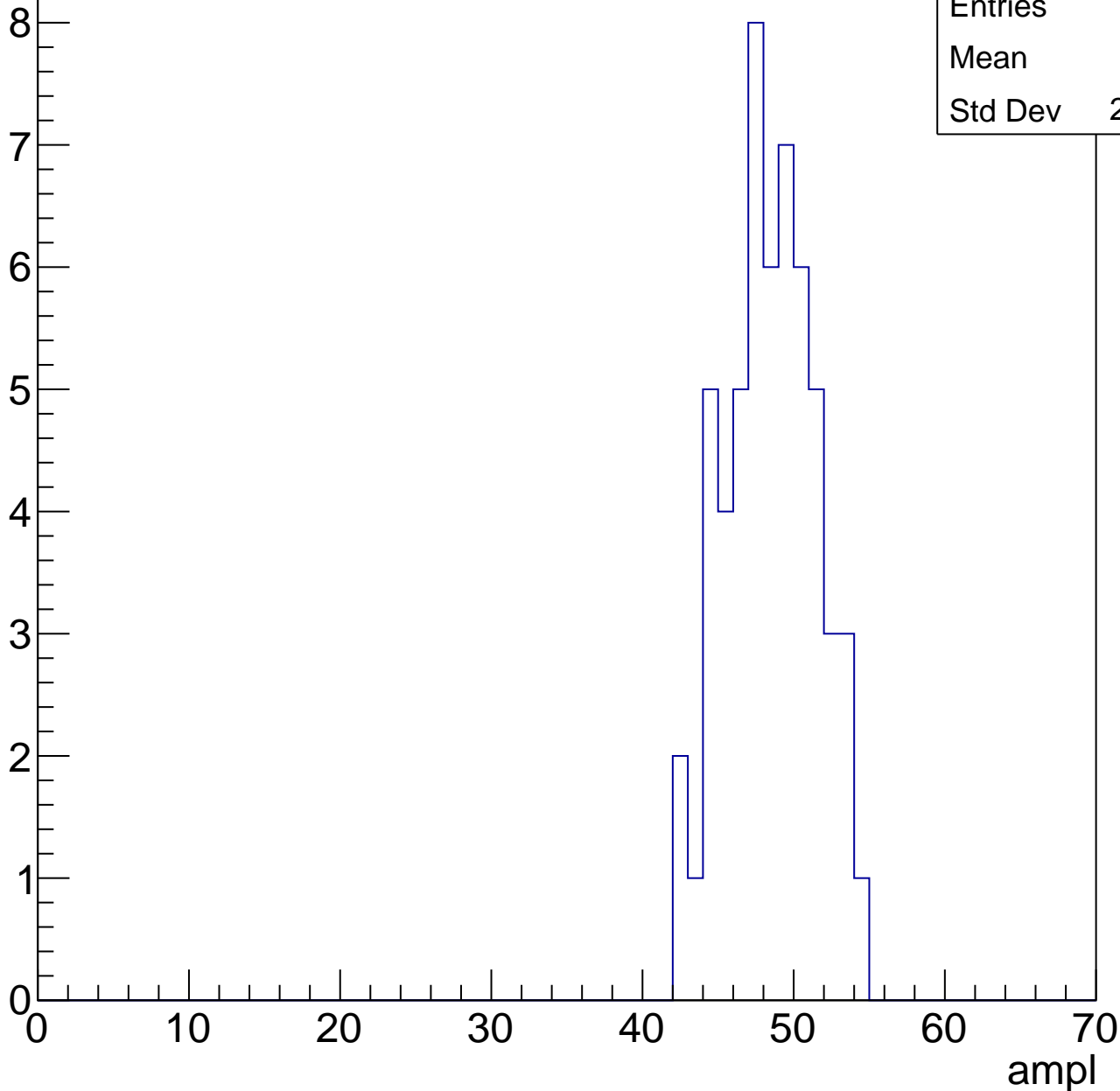


B1L002S, U7-ch61, adc3

calib_packv5_042523_0143.root, FC#10, port B3

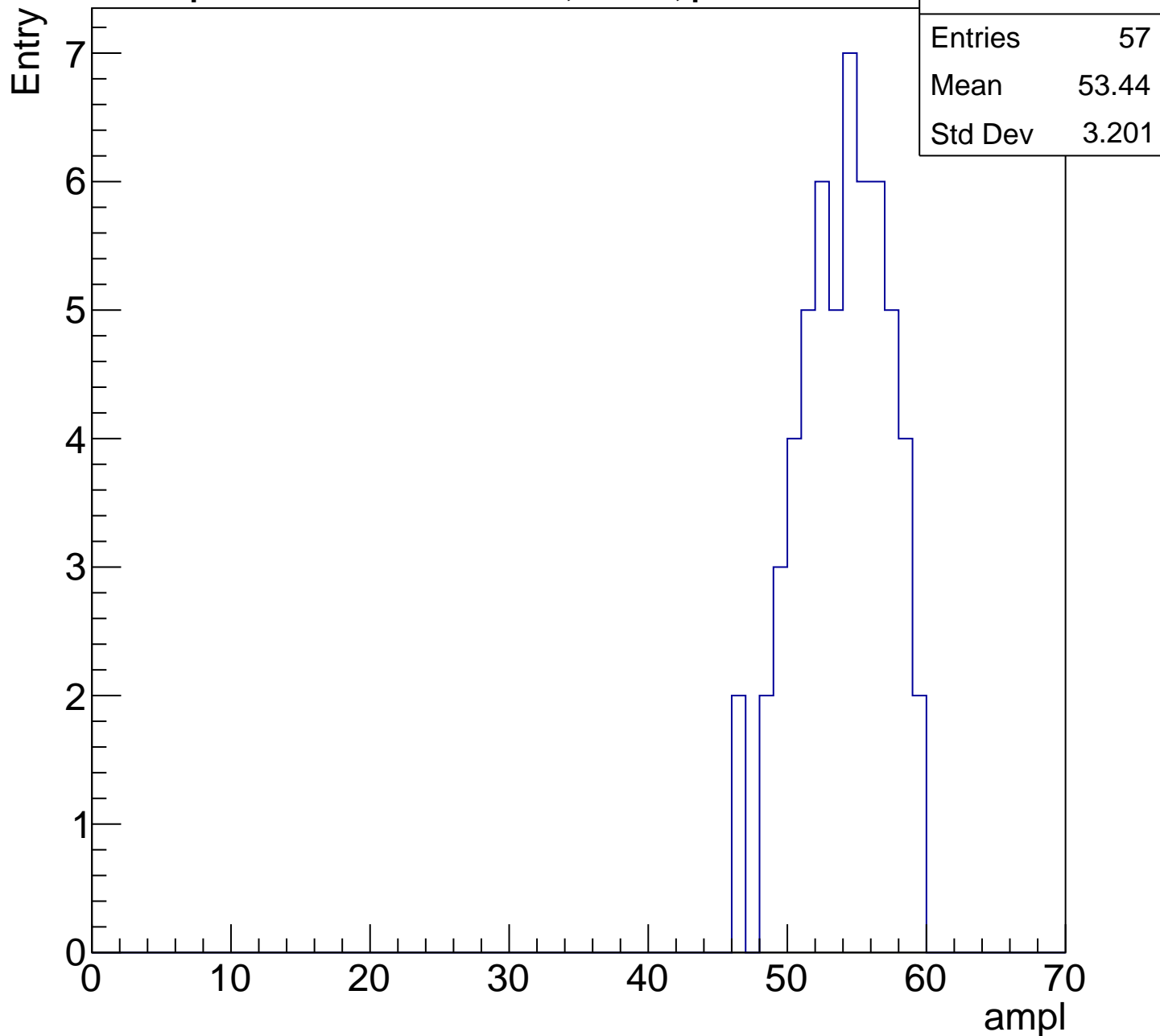
Entry

Entries	56
Mean	48
Std Dev	2.915



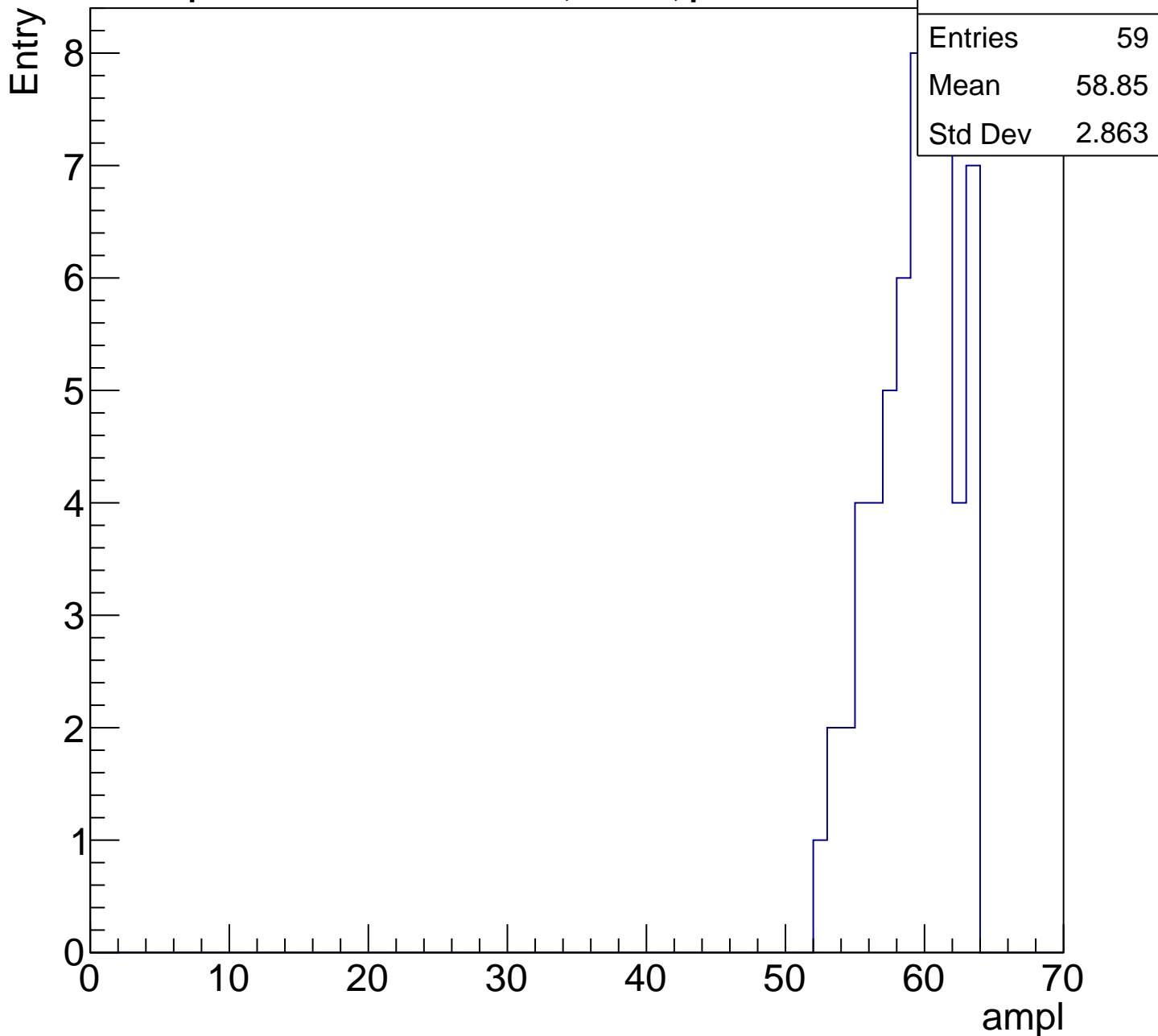
B1L002S, U7-ch61, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch61, adc5

calib_packv5_042523_0143.root, FC#10, port B3

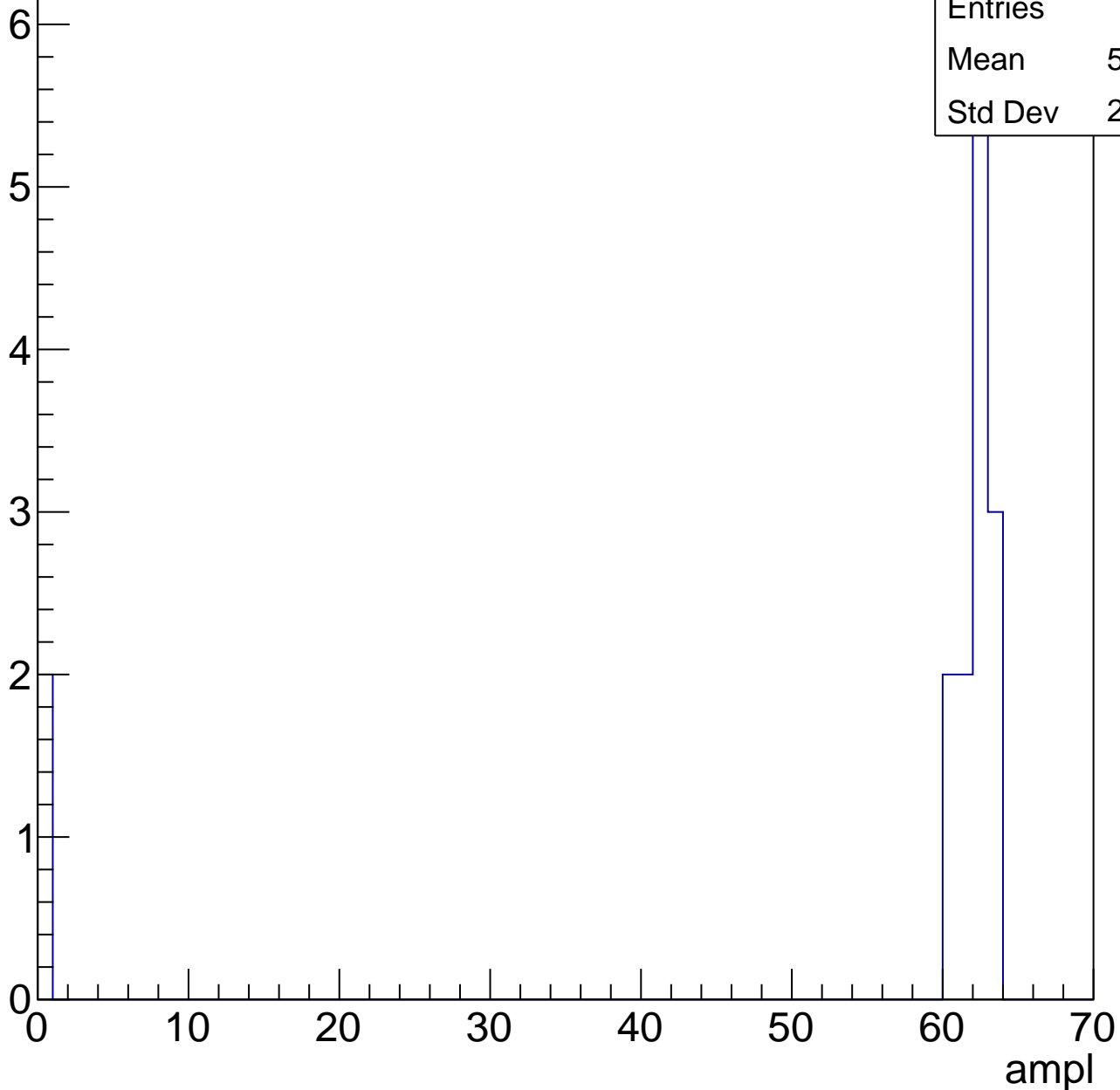


B1L002S, U7-ch61, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	15
Mean	53.53
Std Dev	21.02



B1L002S, U7-ch61, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	19
Std Dev	0

ampl

B1L002S, U7-ch62, adc0

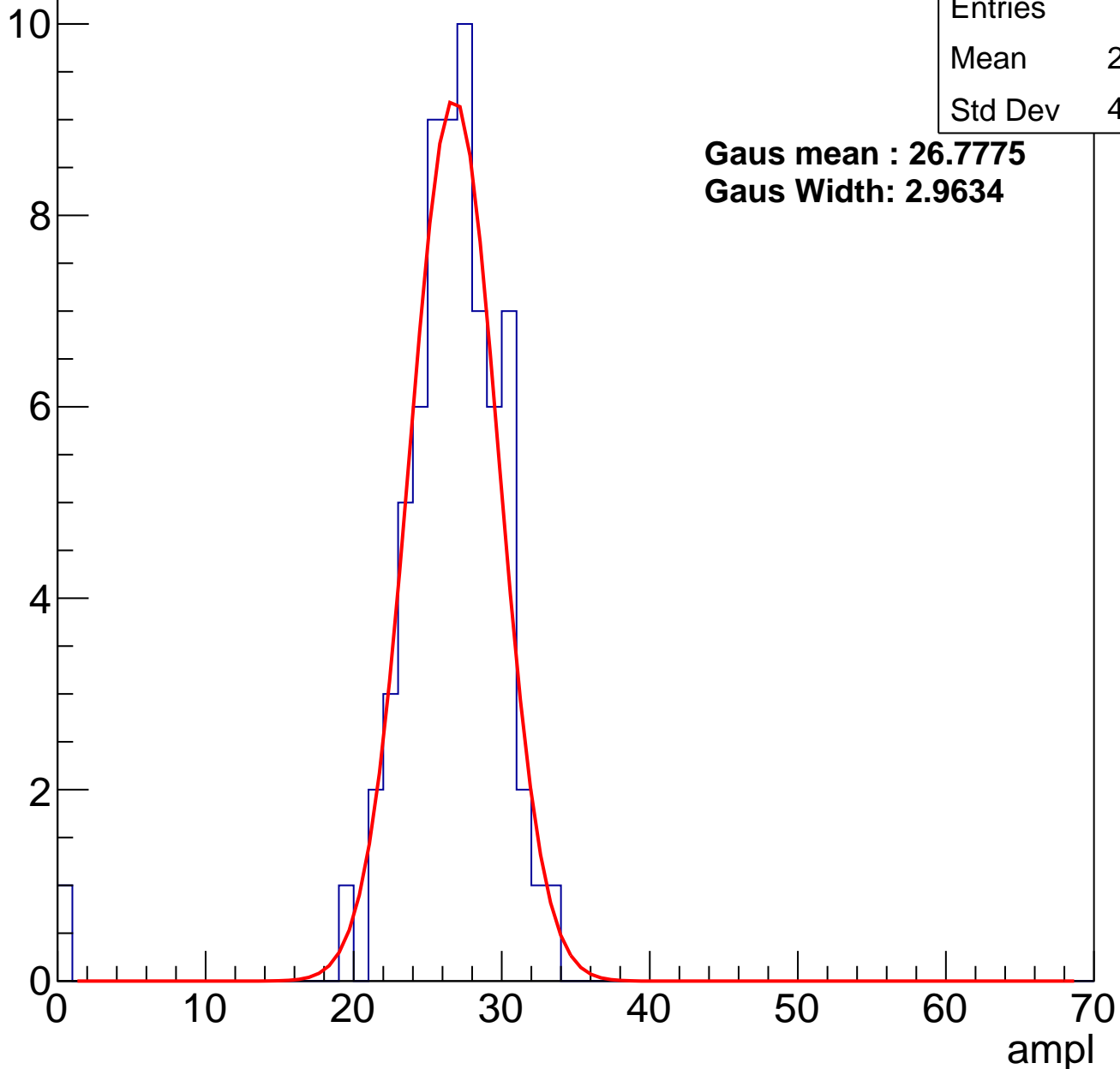
calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	26.03
Std Dev	4.209

Gaus mean : 26.7775

Gaus Width: 2.9634

Entry



B1L002S, U7-ch62, adc1

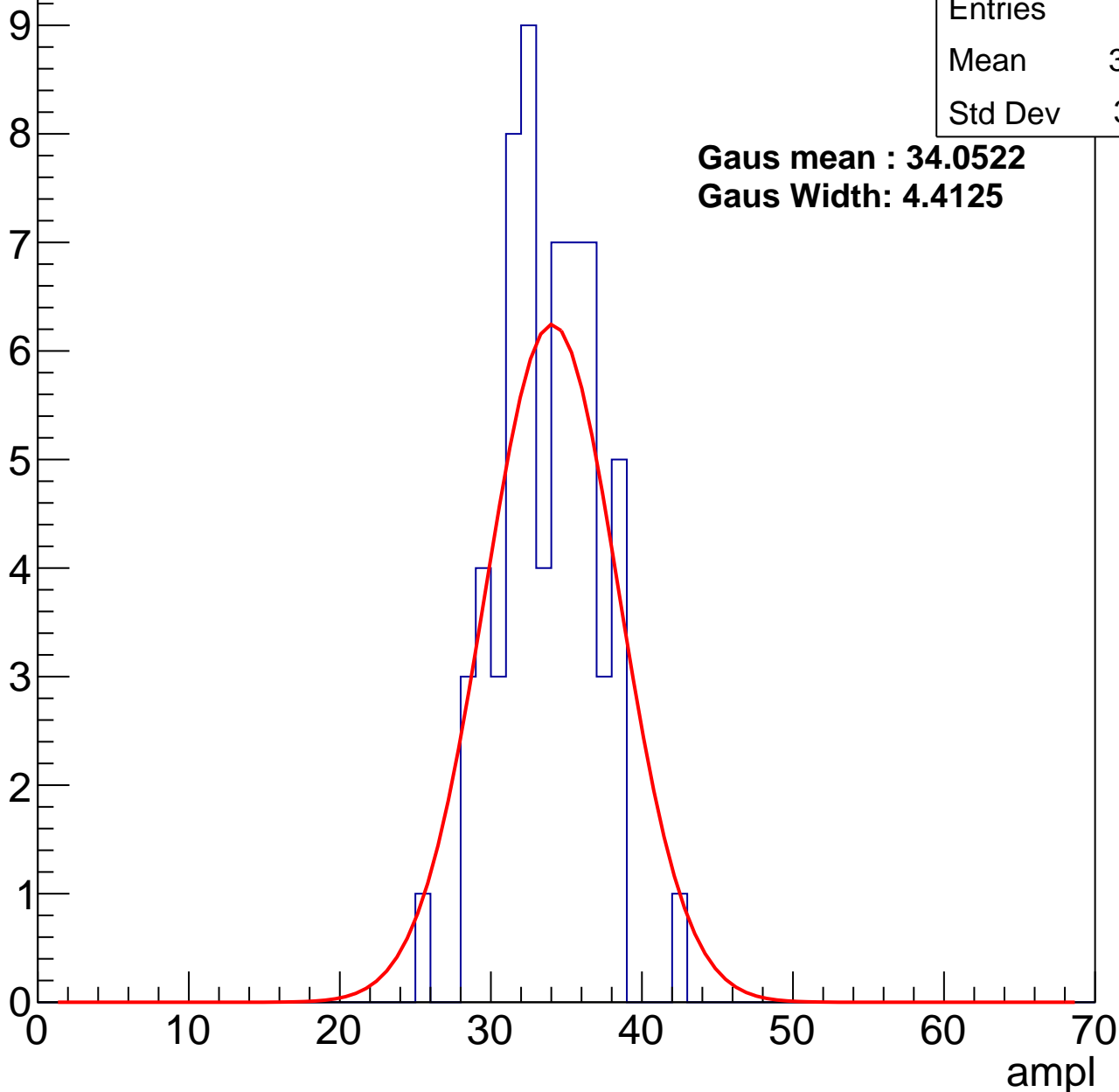
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.24
Std Dev	3.161

Gaus mean : 34.0522

Gaus Width: 4.4125



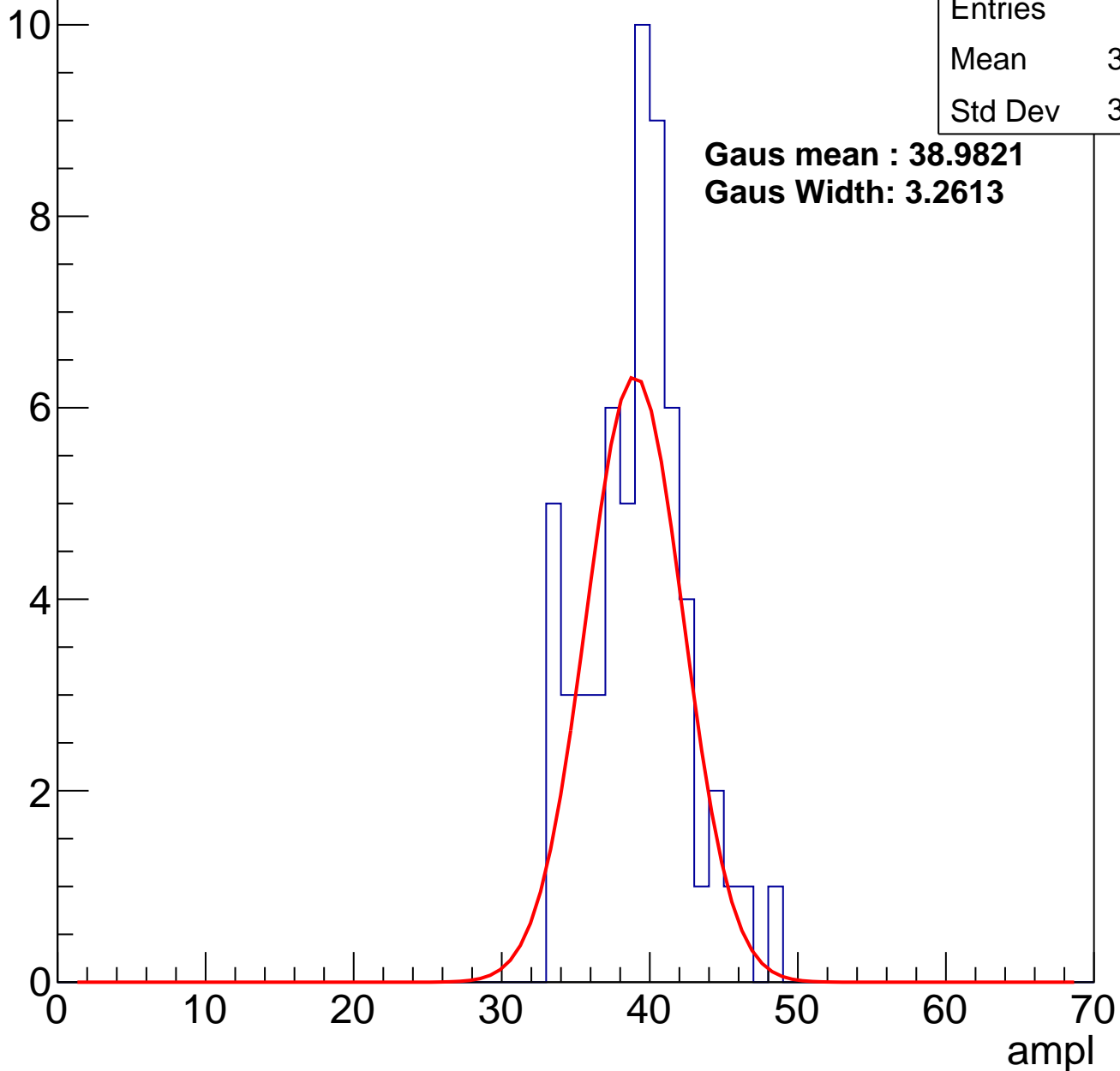
B1L002S, U7-ch62, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	60
Mean	38.77
Std Dev	3.308

Gaus mean : 38.9821
Gaus Width: 3.2613

Entry

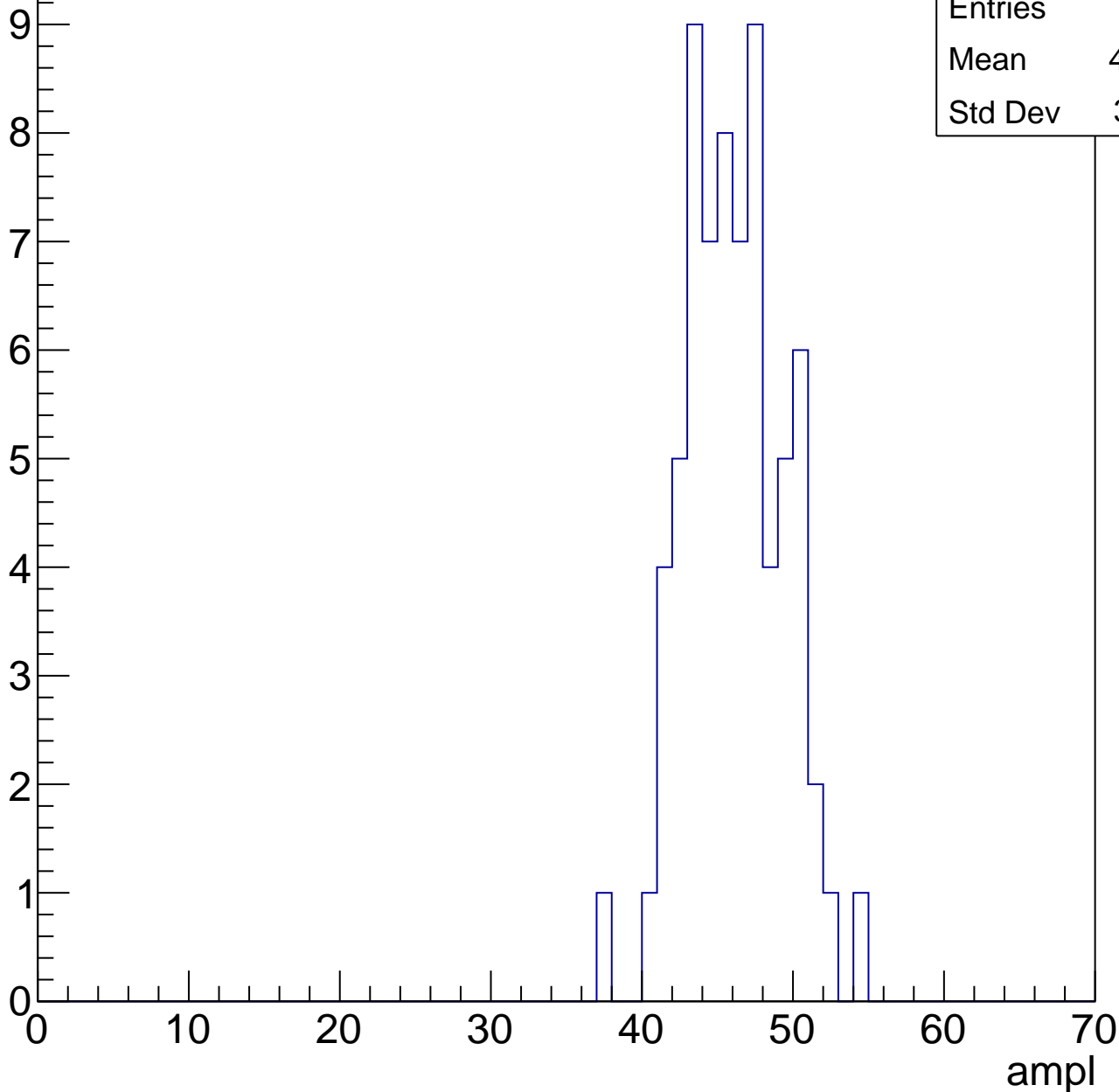


B1L002S, U7-ch62, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	45.66
Std Dev	3.211

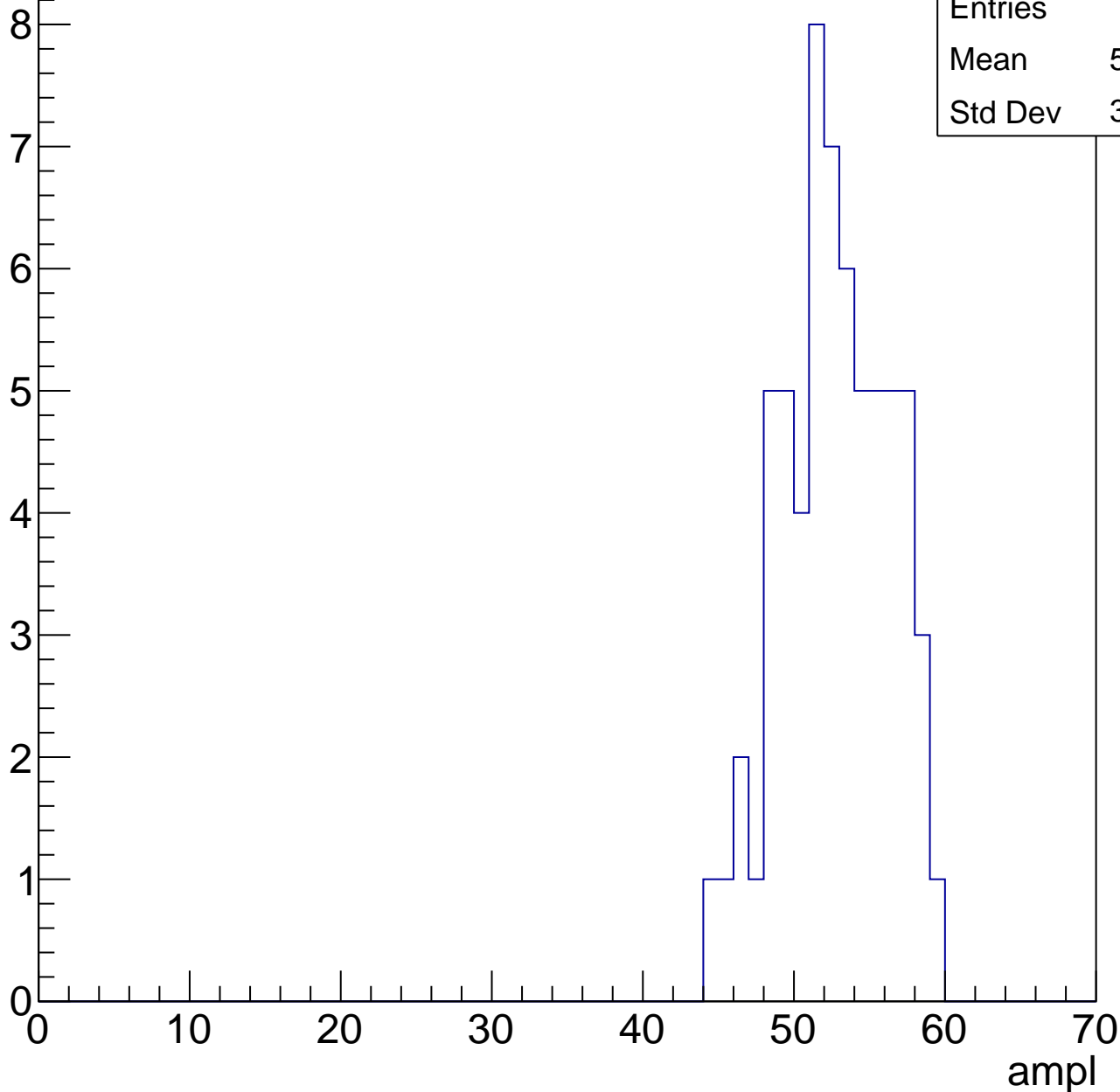


B1L002S, U7-ch62, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	52.28
Std Dev	3.507

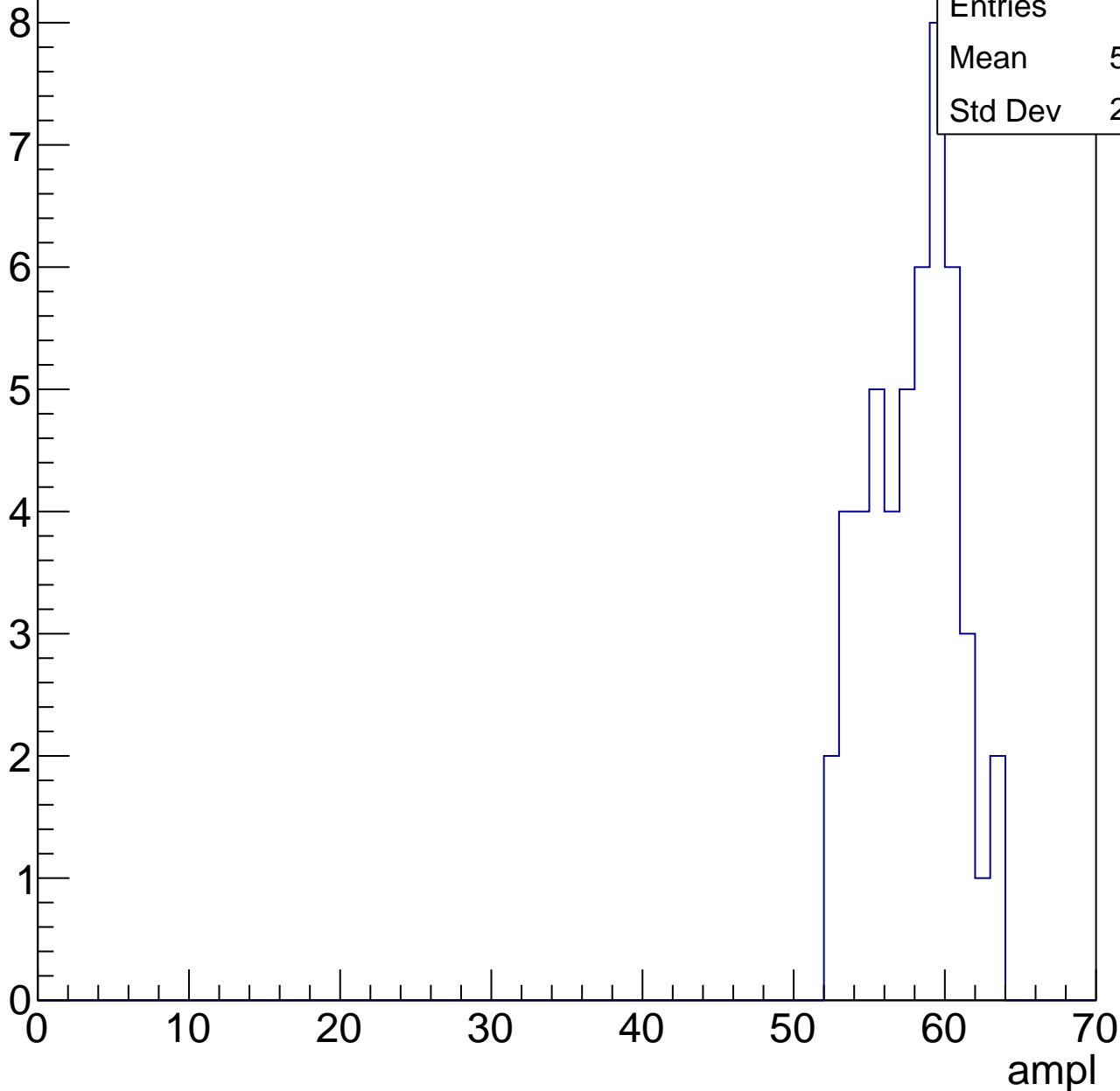


B1L002S, U7-ch62, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	57.34
Std Dev	2.847

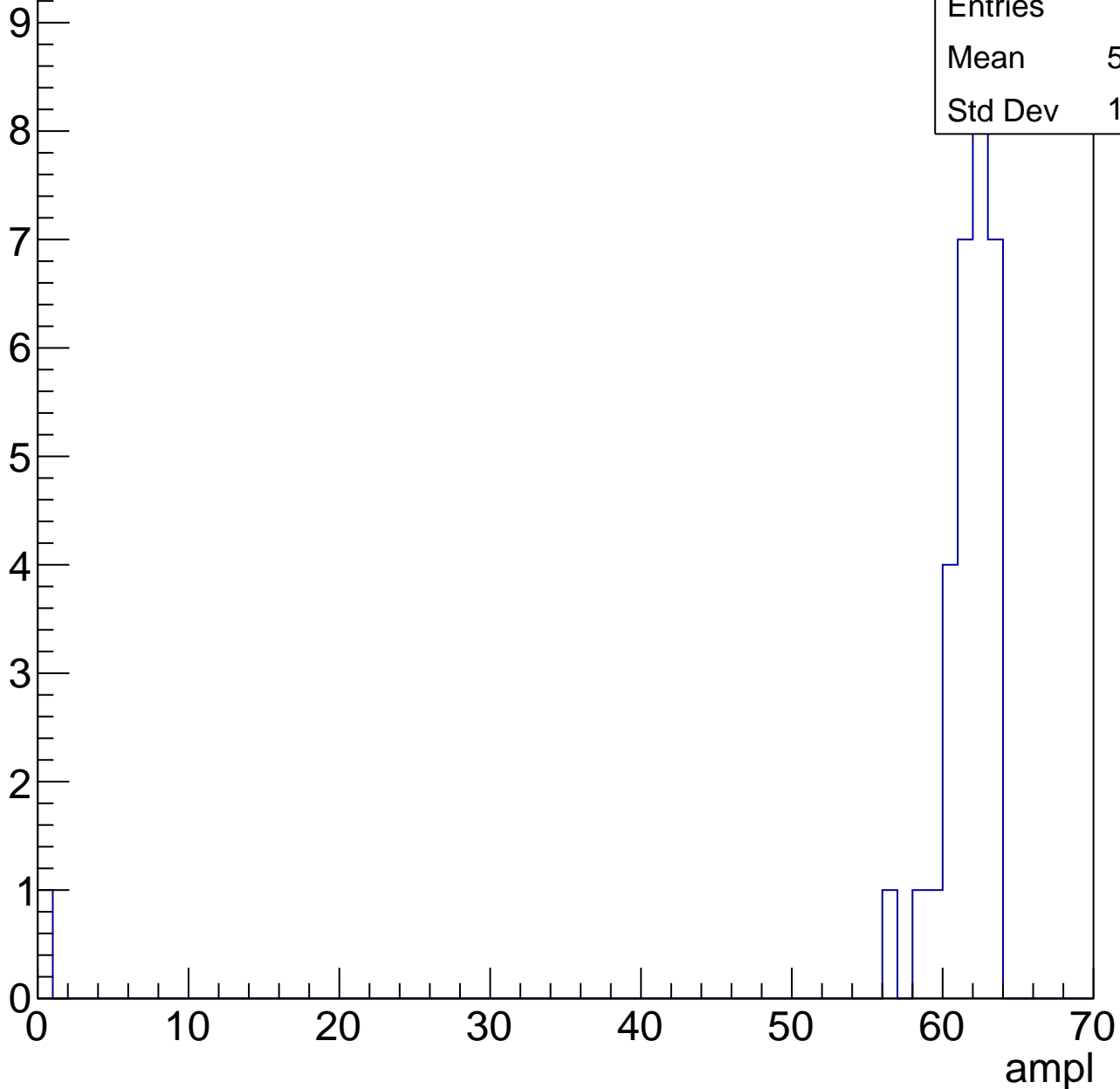


B1L002S, U7-ch62, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	31
Mean	59.32
Std Dev	10.94



B1L002S, U7-ch62, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch63, adc0

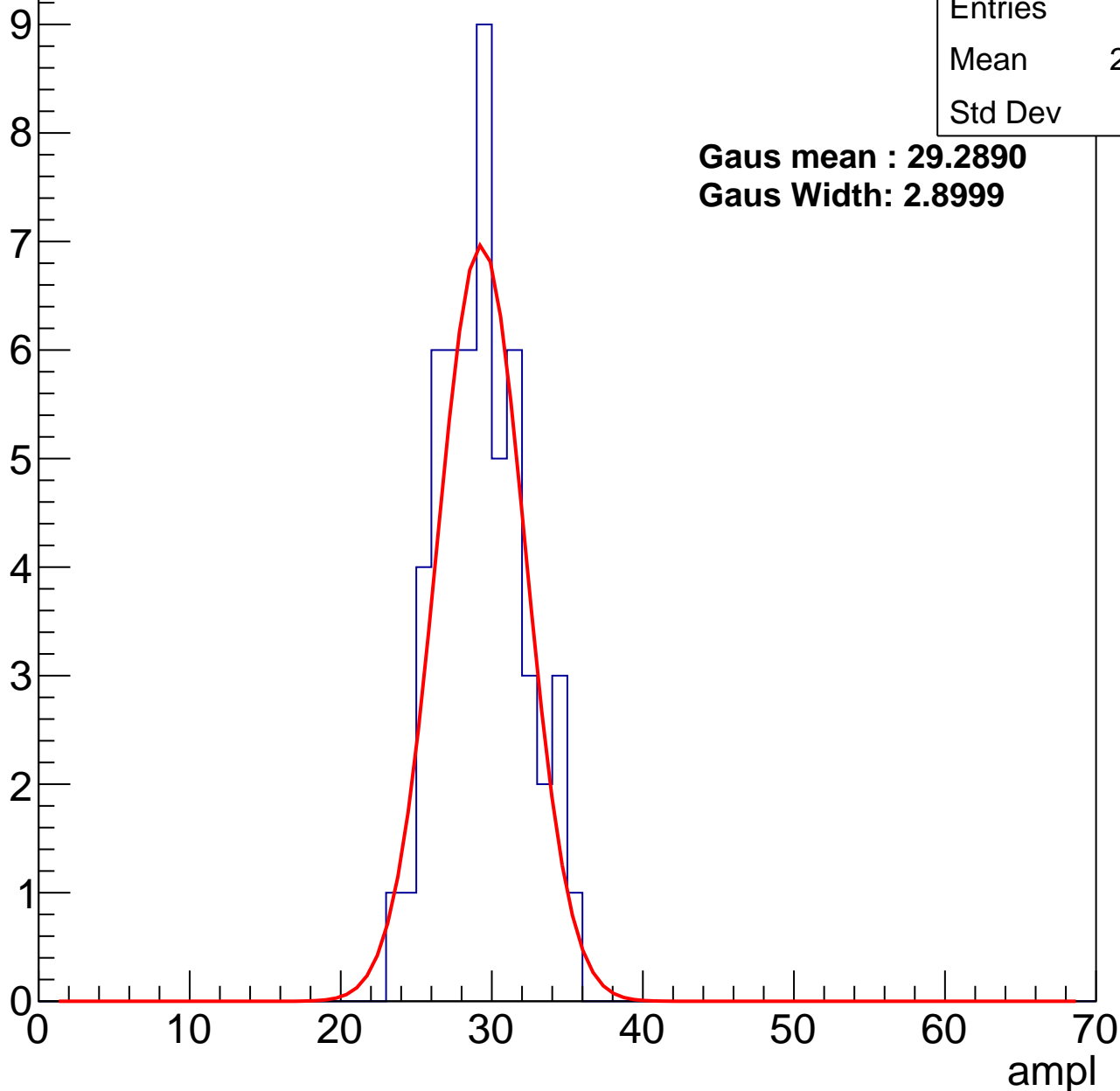
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.85
Std Dev	2.77

Gaus mean : 29.2890

Gaus Width: 2.8999



B1L002S, U7-ch63, adc1

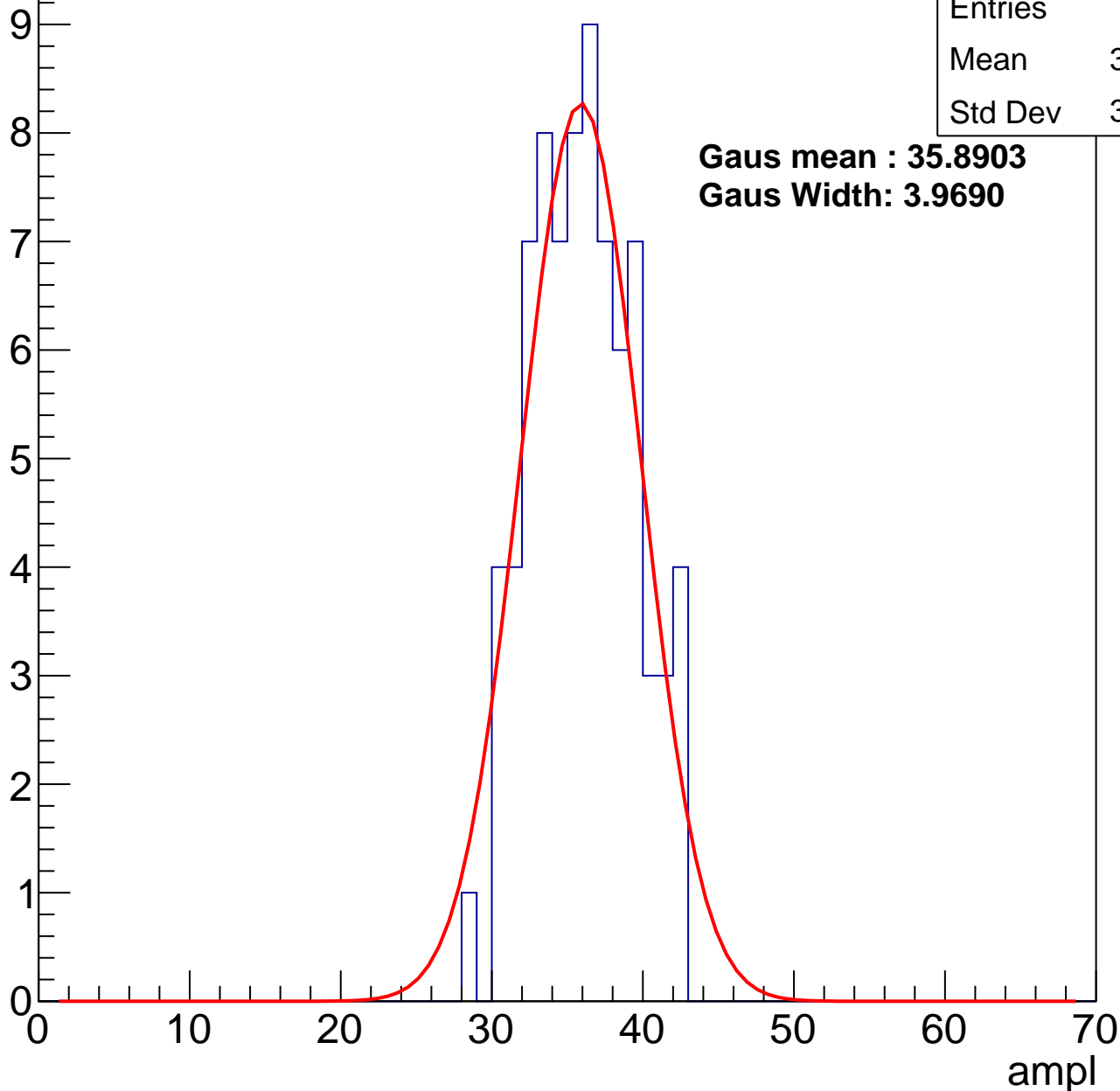
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	78
Mean	35.55
Std Dev	3.346

Gaus mean : 35.8903

Gaus Width: 3.9690



B1L002S, U7-ch63, adc2

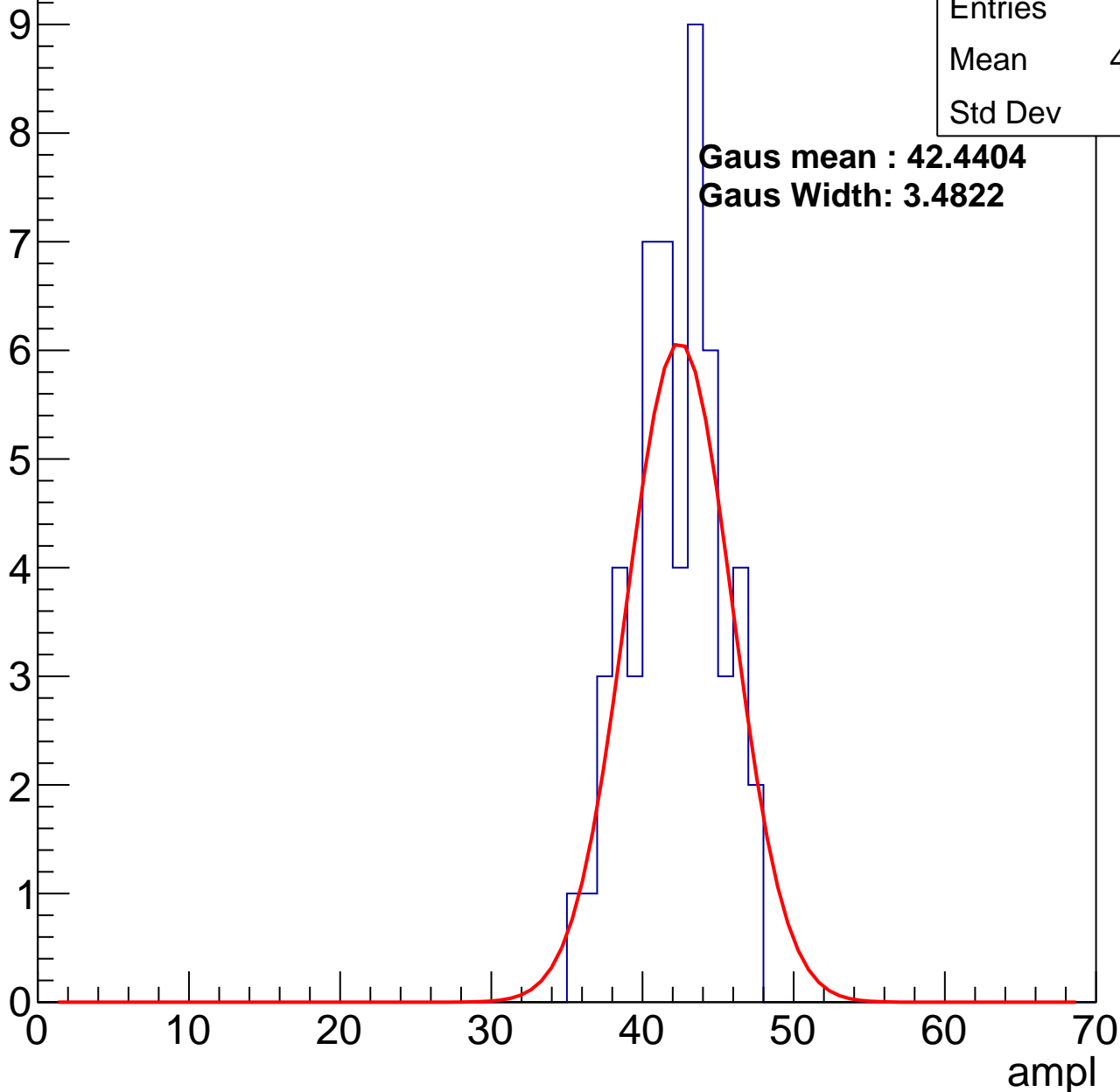
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	41.67
Std Dev	2.9

Gaus mean : 42.4404

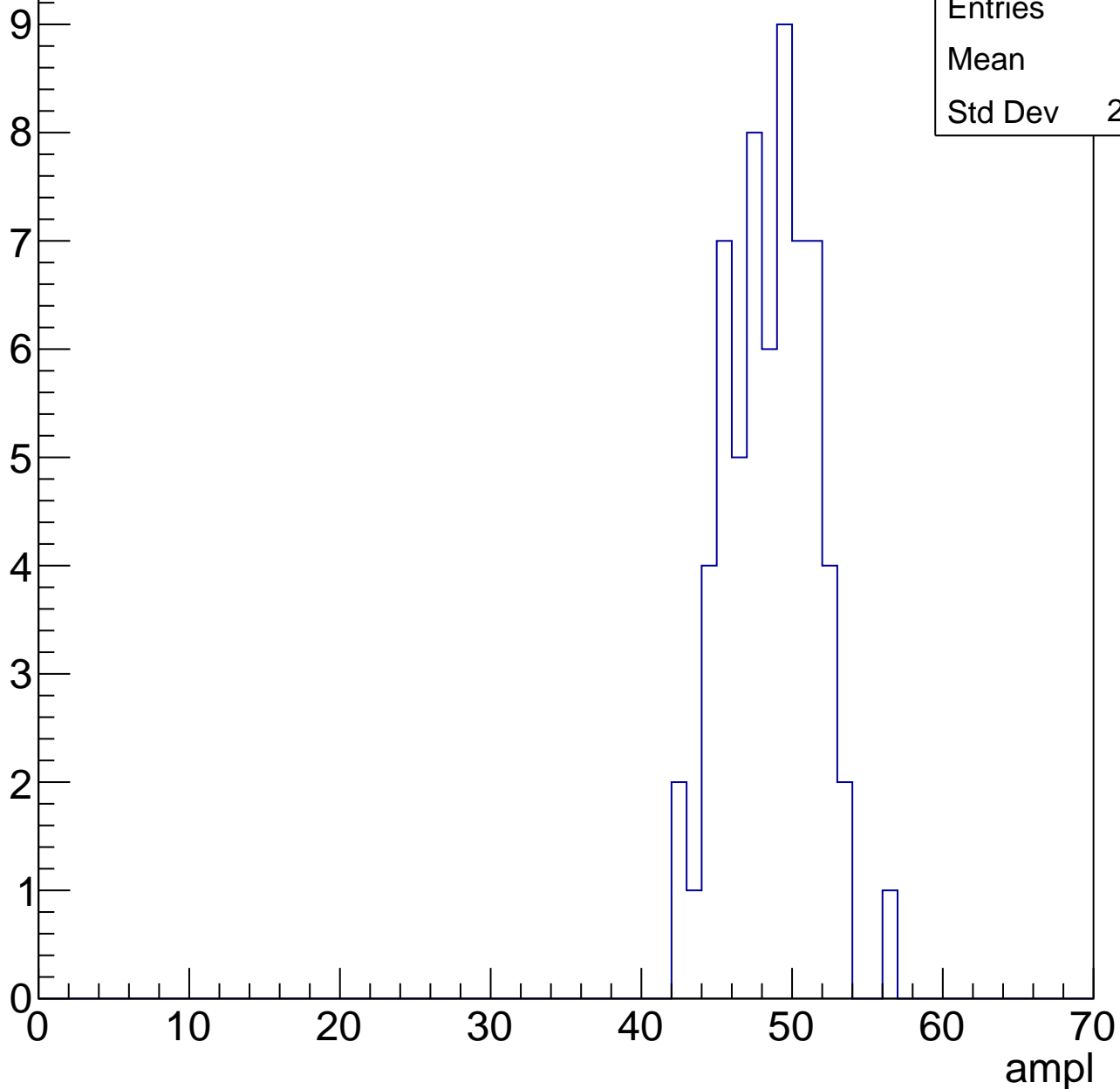
Gaus Width: 3.4822



B1L002S, U7-ch63, adc3

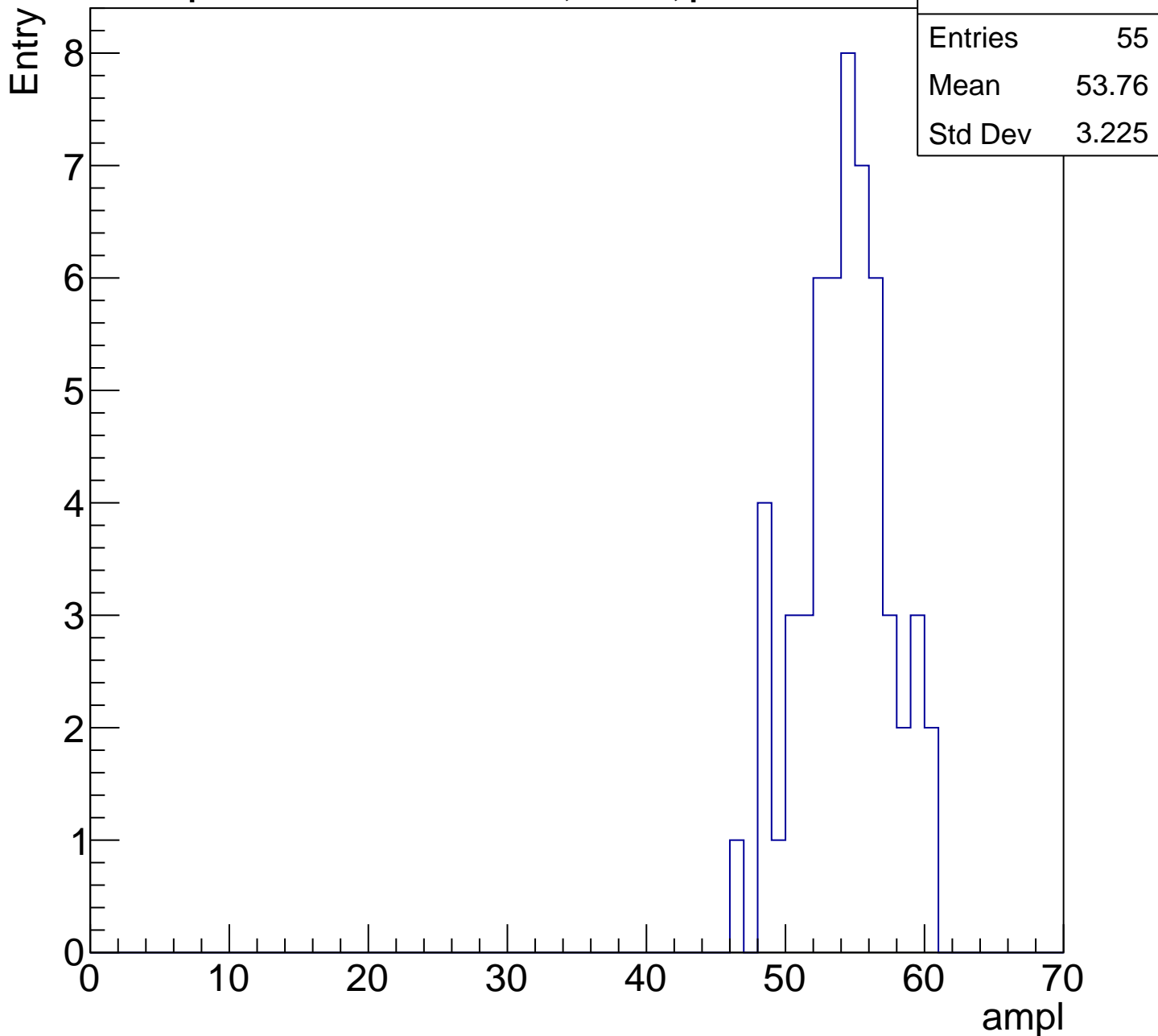
calib_packv5_042523_0143.root, FC#10, port B3

Entry



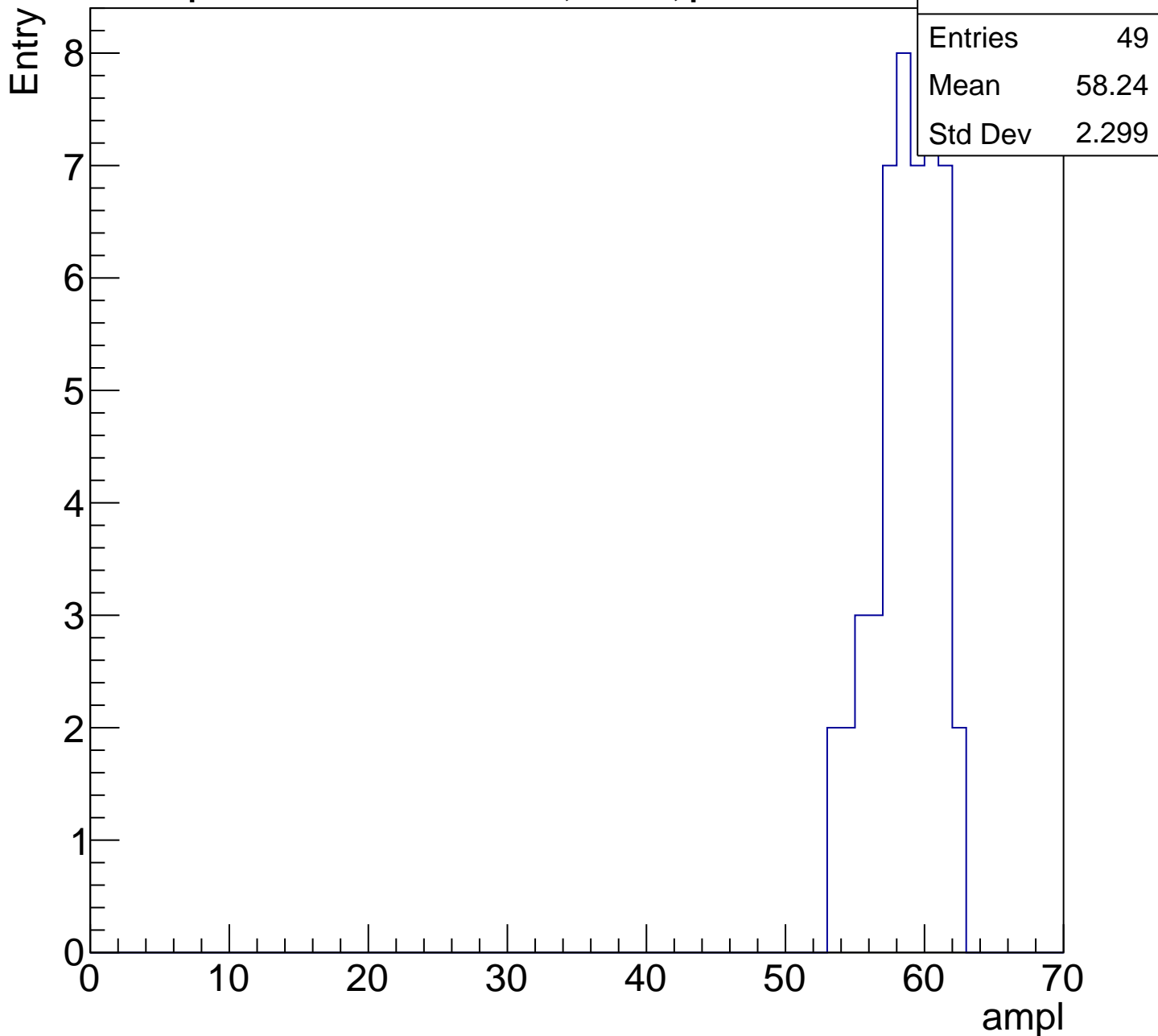
B1L002S, U7-ch63, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch63, adc5

calib_packv5_042523_0143.root, FC#10, port B3

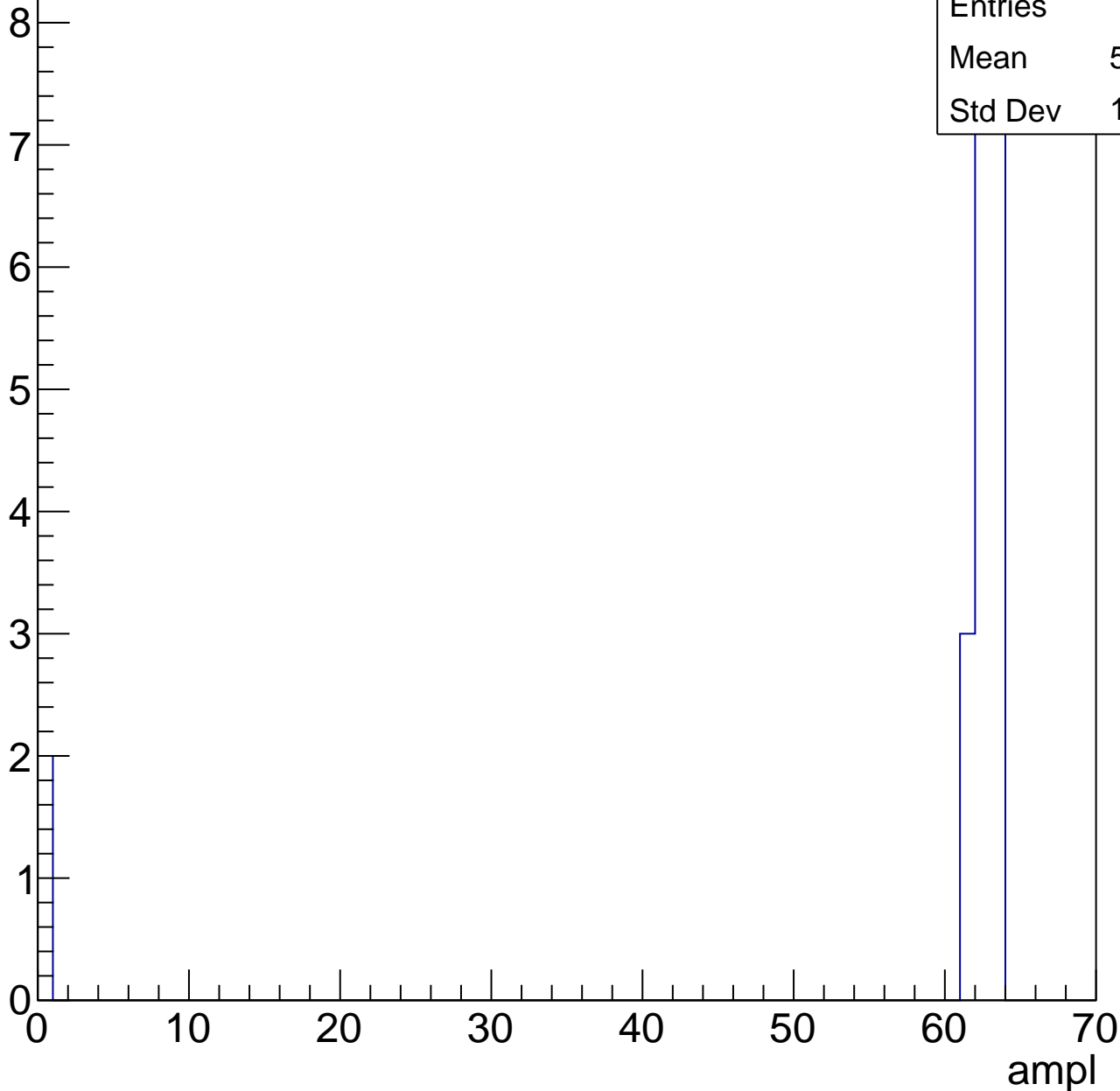


B1L002S, U7-ch63, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	21
Mean	56.33
Std Dev	18.29



B1L002S, U7-ch63, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch64, adc0

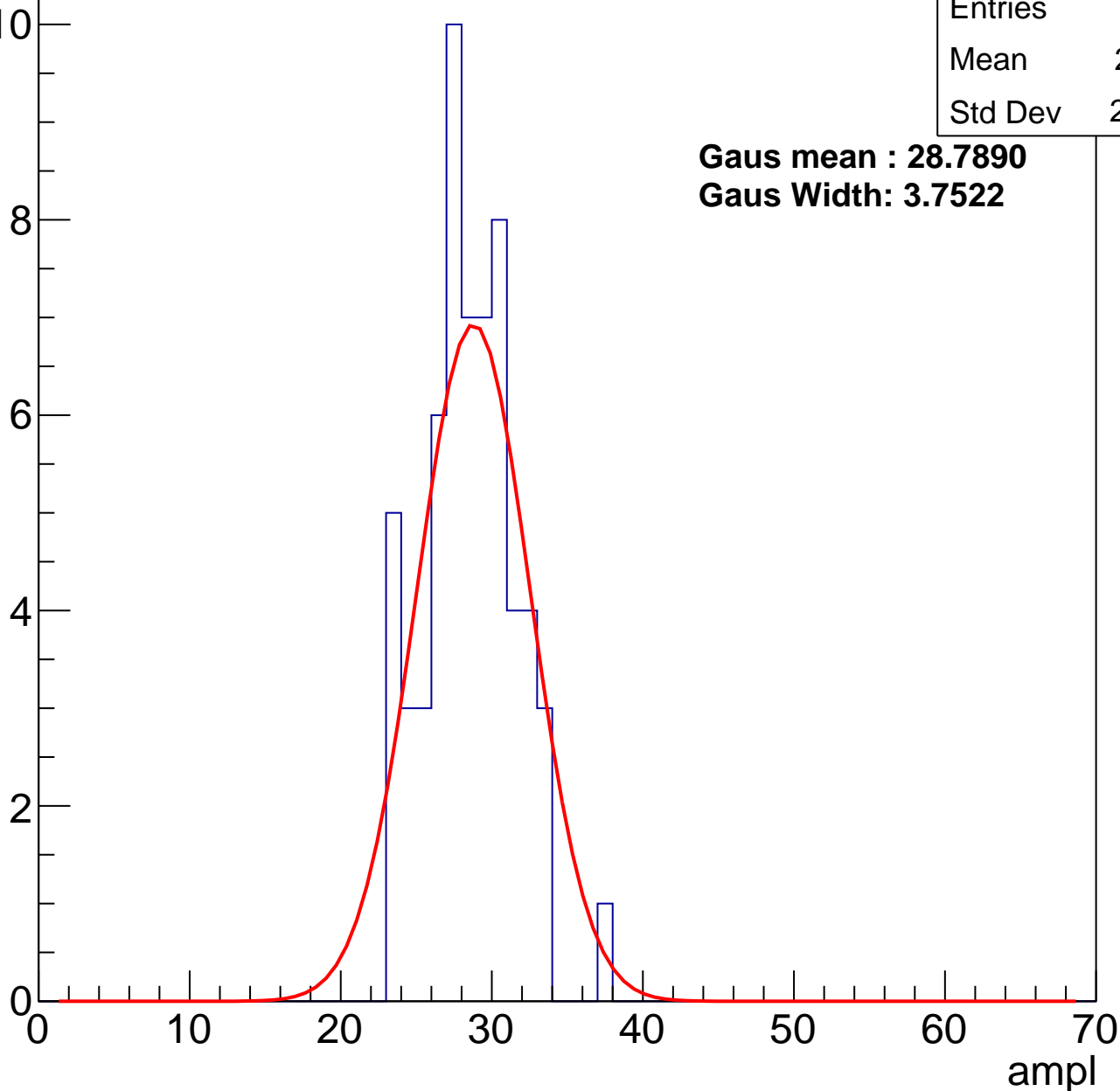
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.11
Std Dev	2.943

Gaus mean : 28.7890

Gaus Width: 3.7522



B1L002S, U7-ch64, adc1

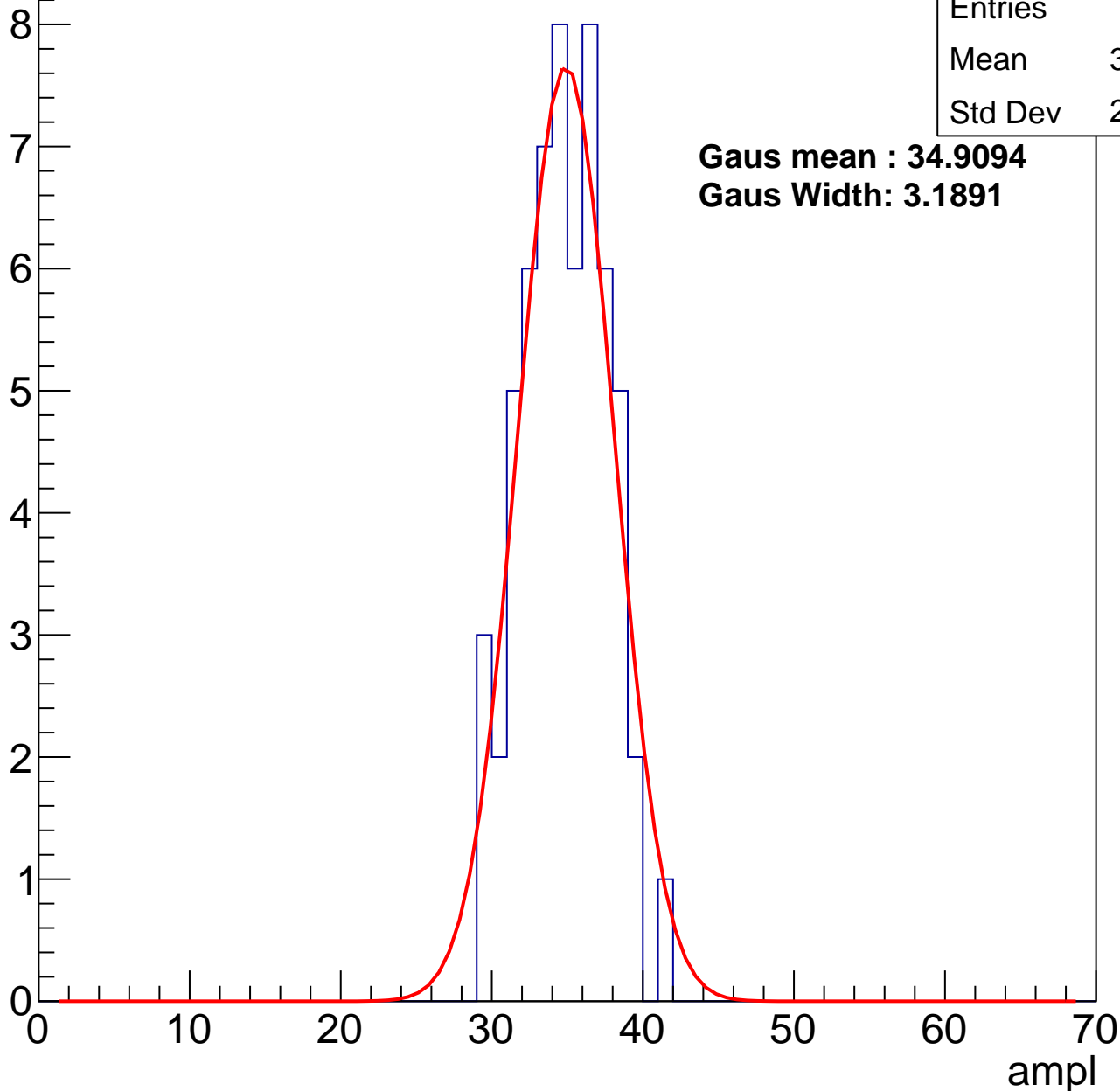
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.34
Std Dev	2.753

Gaus mean : 34.9094

Gaus Width: 3.1891



B1L002S, U7-ch64, adc2

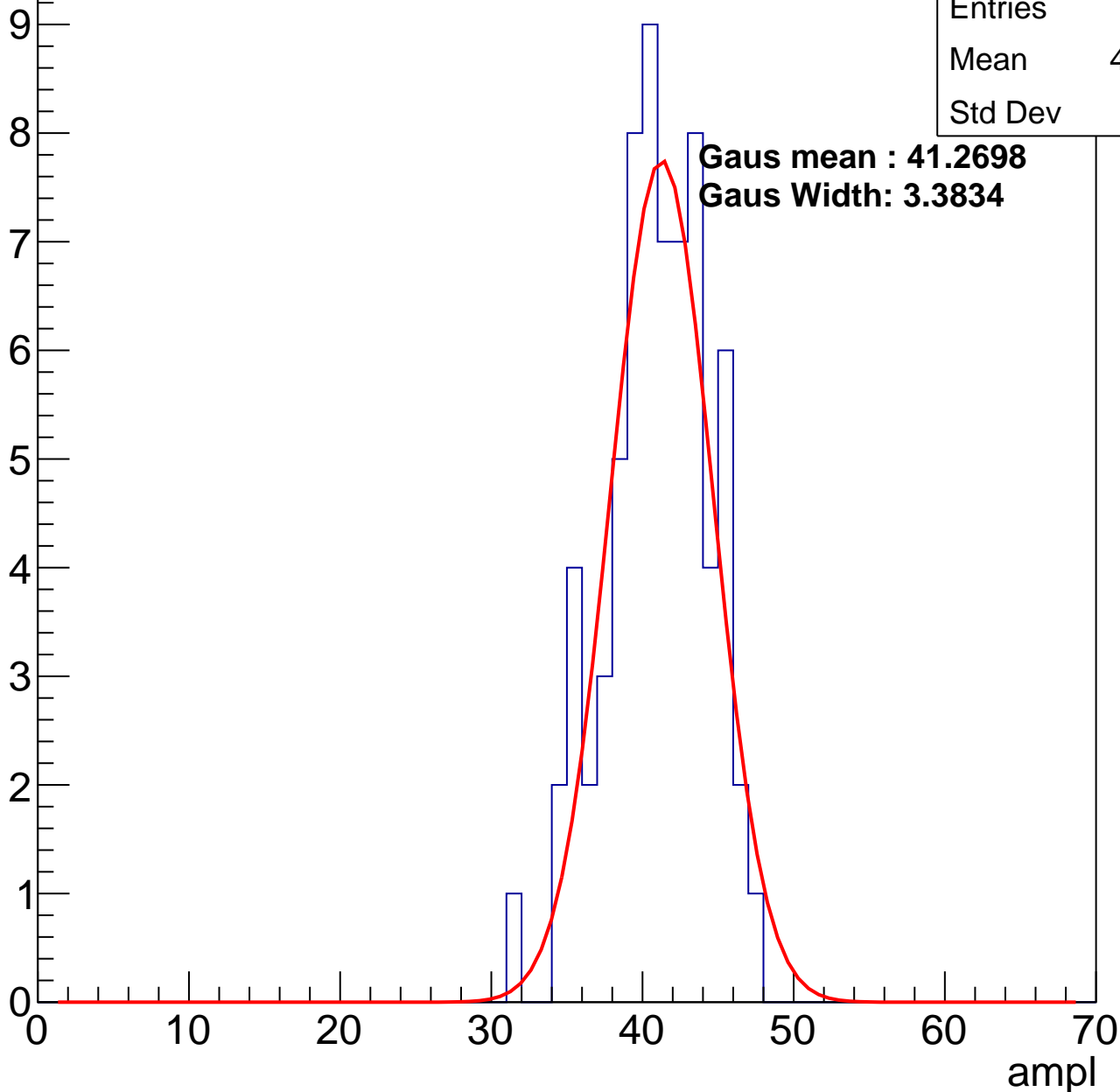
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.49
Std Dev	3.33

Gaus mean : 41.2698

Gaus Width: 3.3834

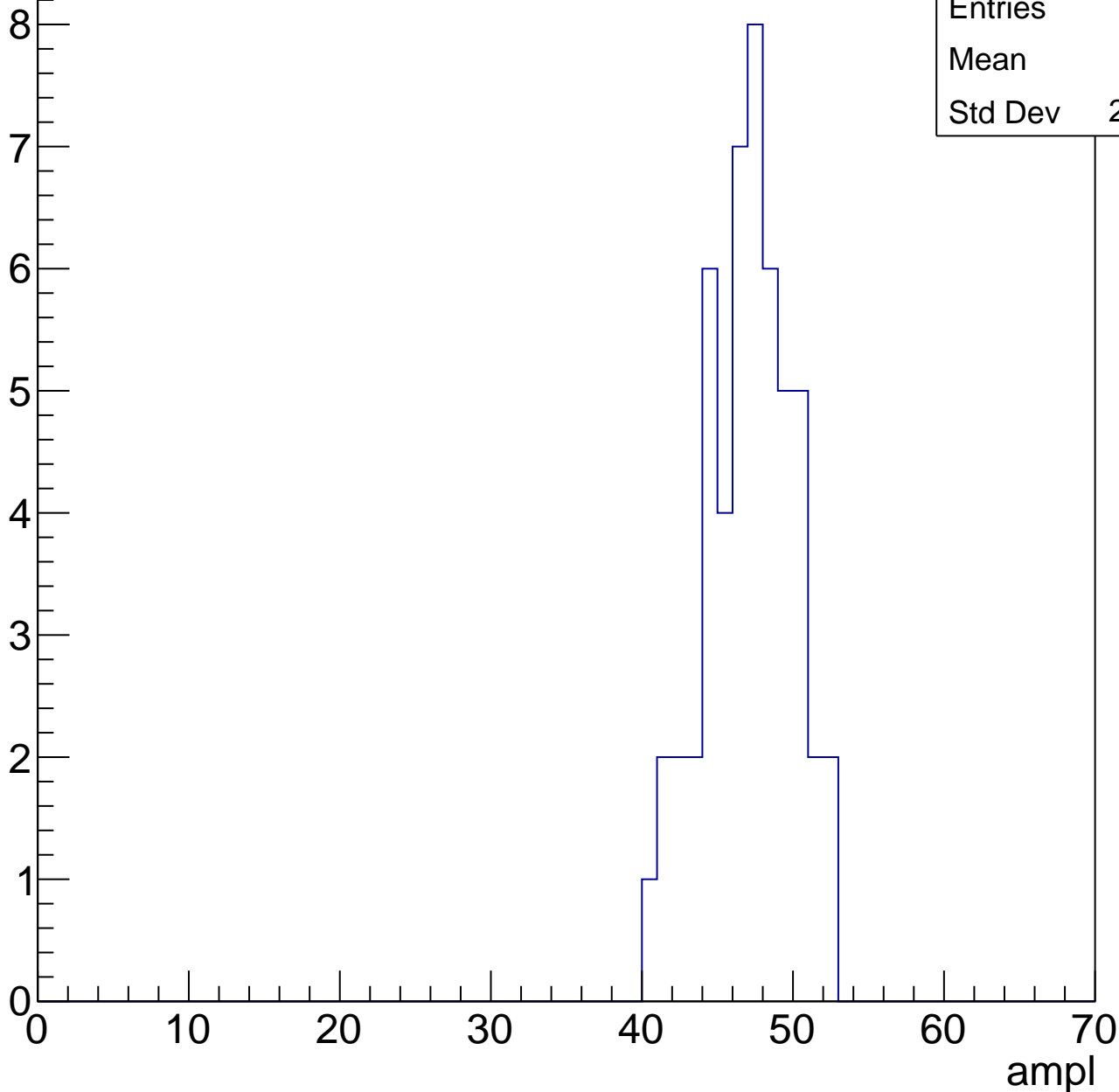


B1L002S, U7-ch64, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	46.6
Std Dev	2.857

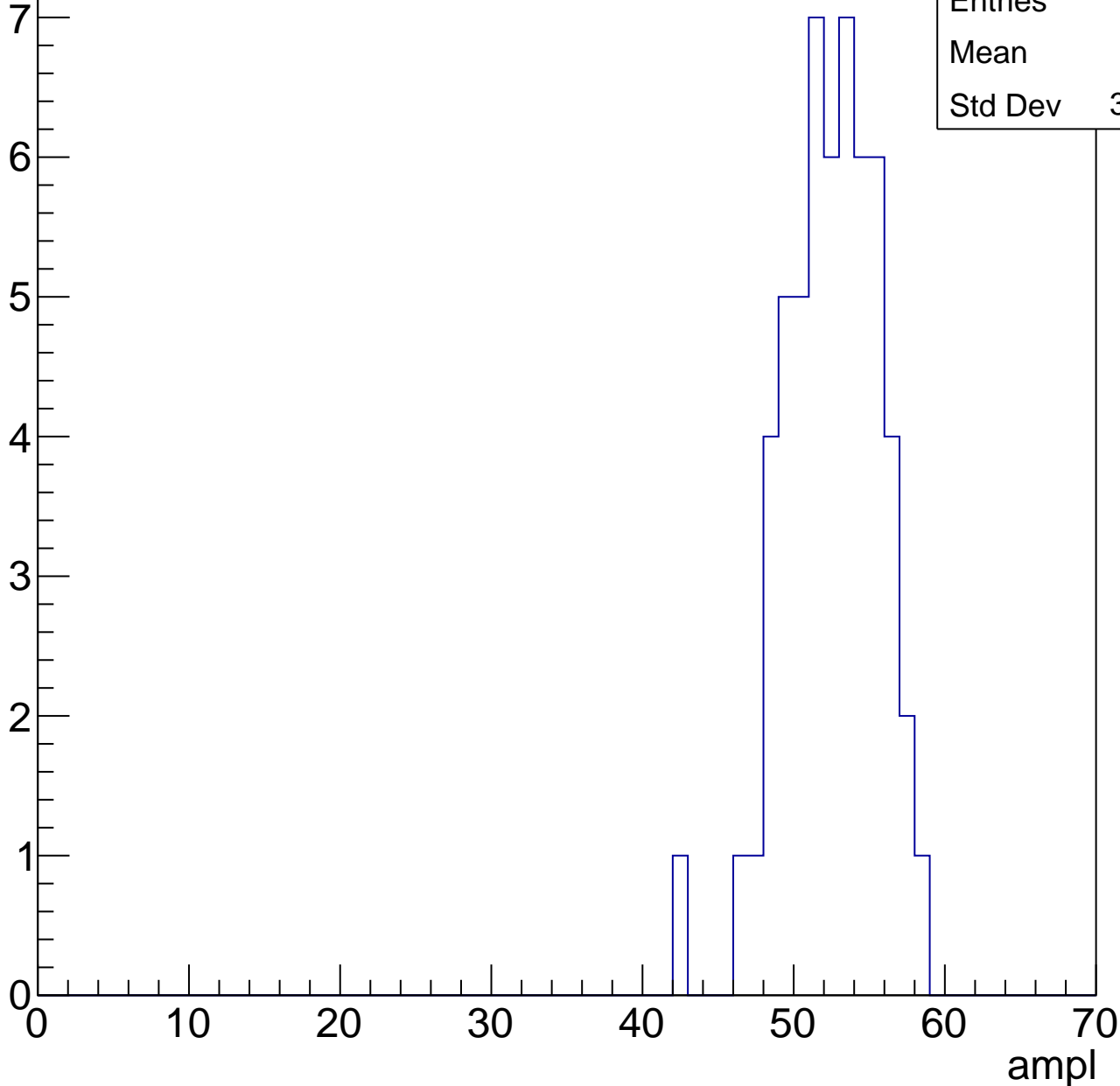


B1L002S, U7-ch64, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

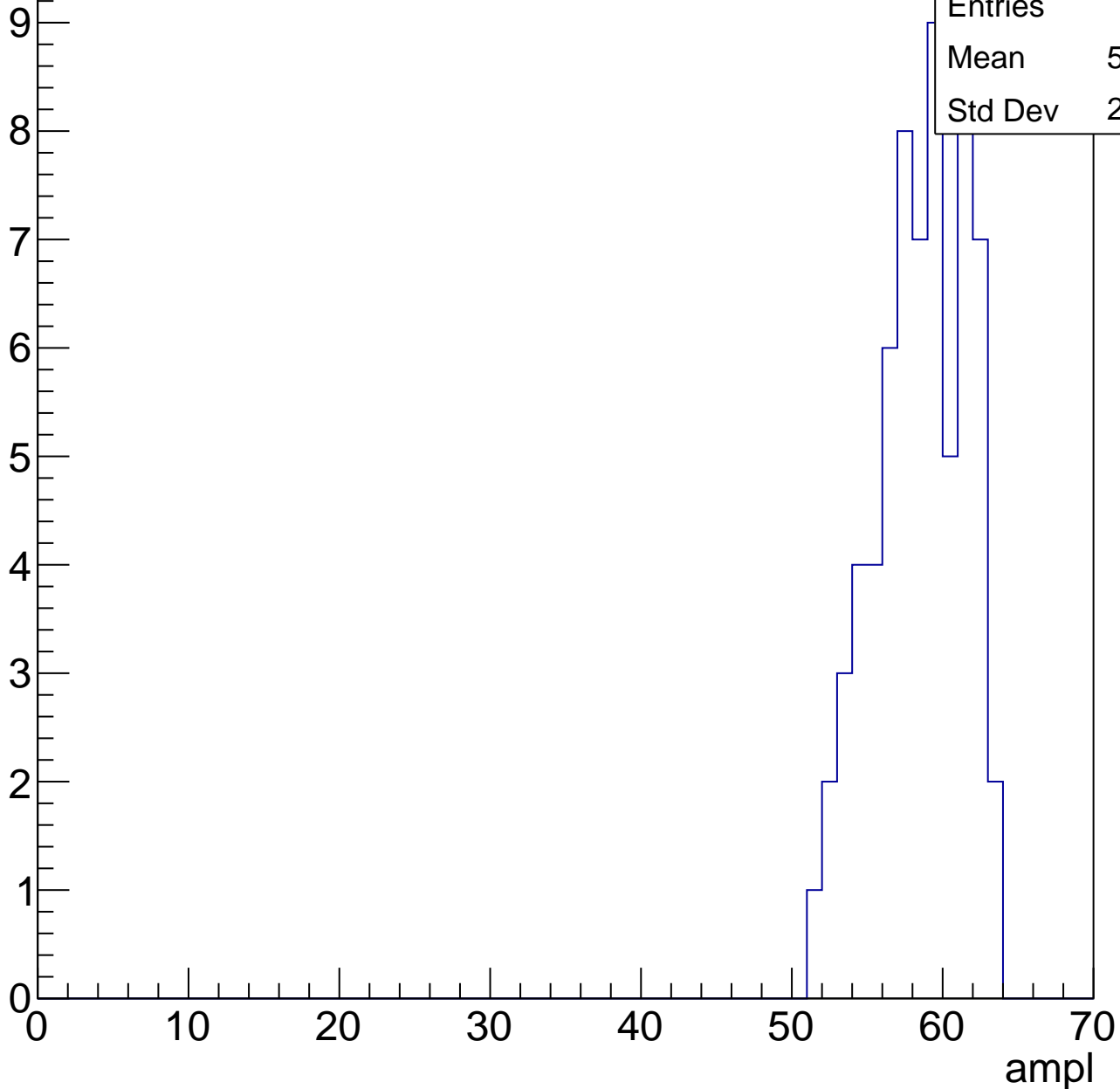
Entries	56
Mean	52
Std Dev	3.082



B1L002S, U7-ch64, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

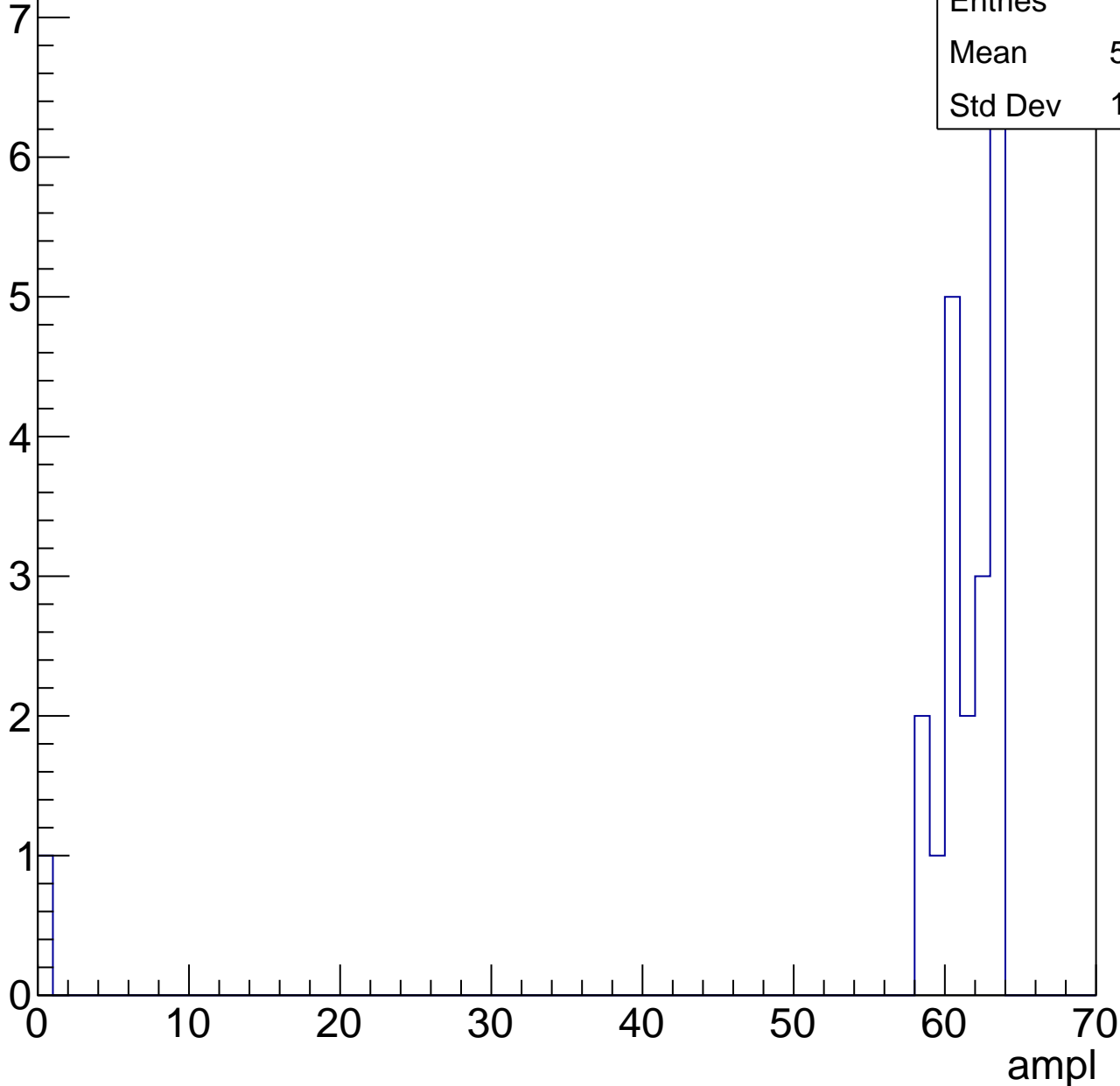


B1L002S, U7-ch64, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

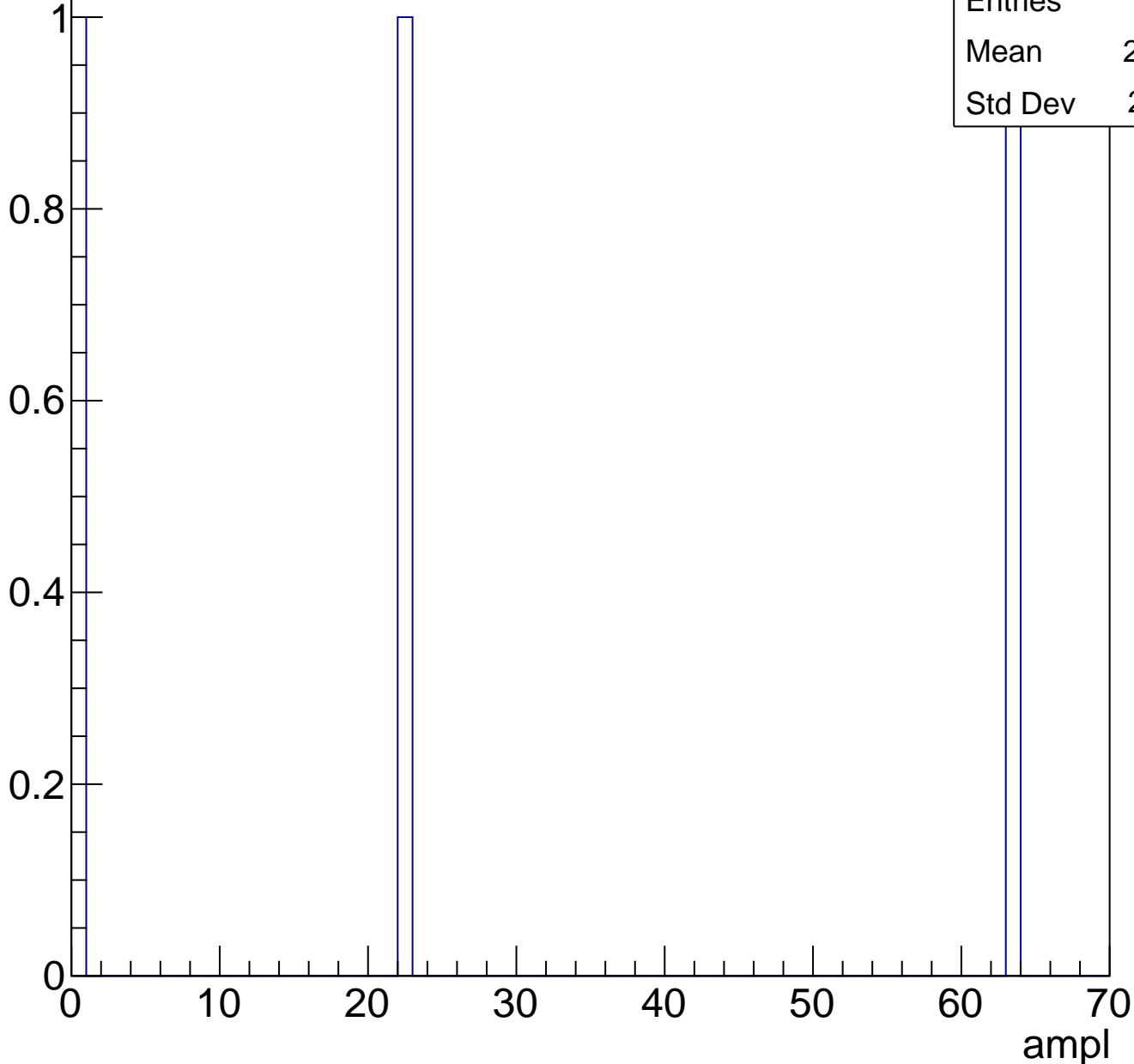
Entries	21
Mean	58.29
Std Dev	13.14



B1L002S, U7-ch64, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L002S, U7-ch65, adc0

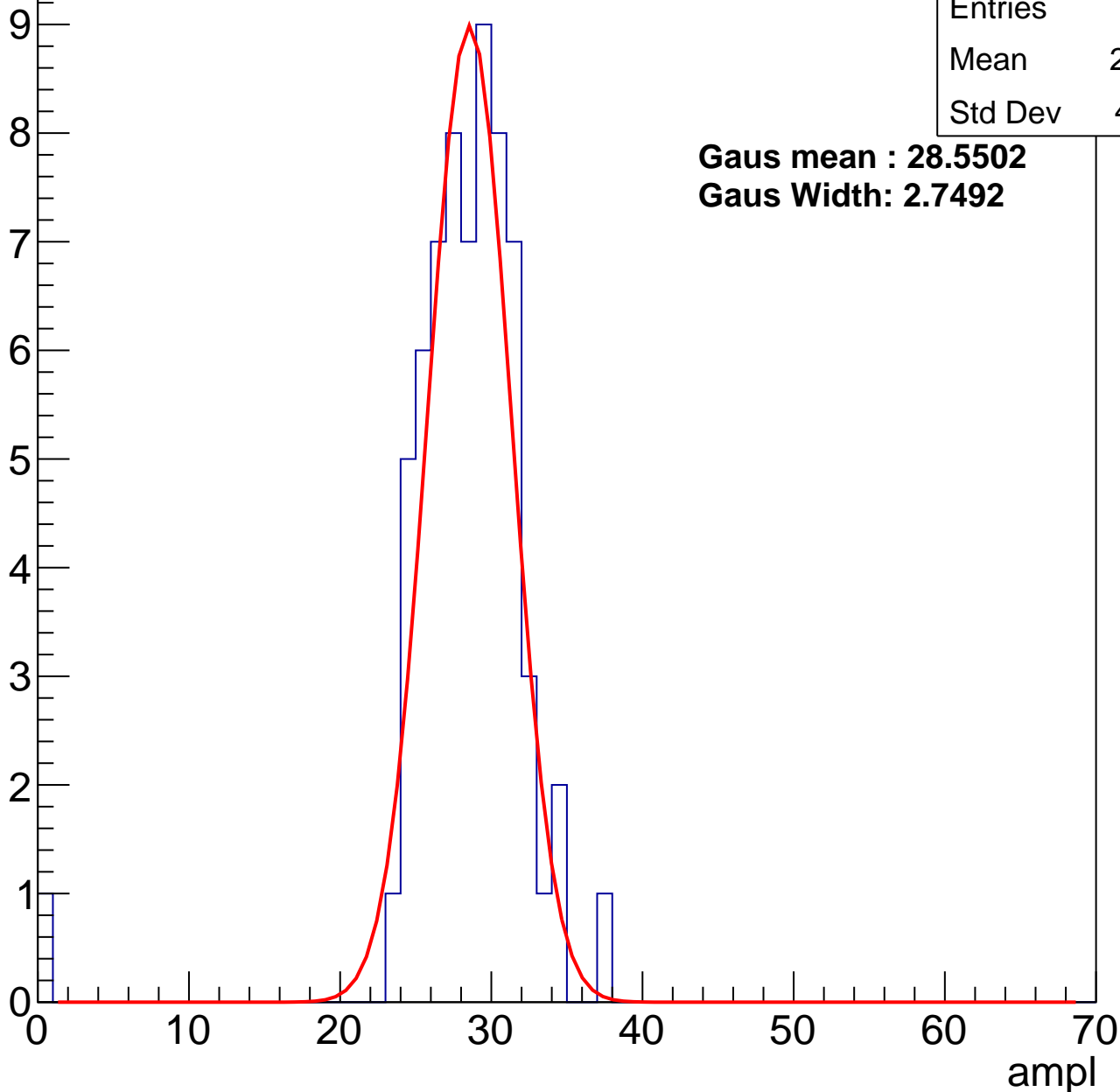
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.86
Std Dev	4.451

Gaus mean : 28.5502

Gaus Width: 2.7492



B1L002S, U7-ch65, adc1

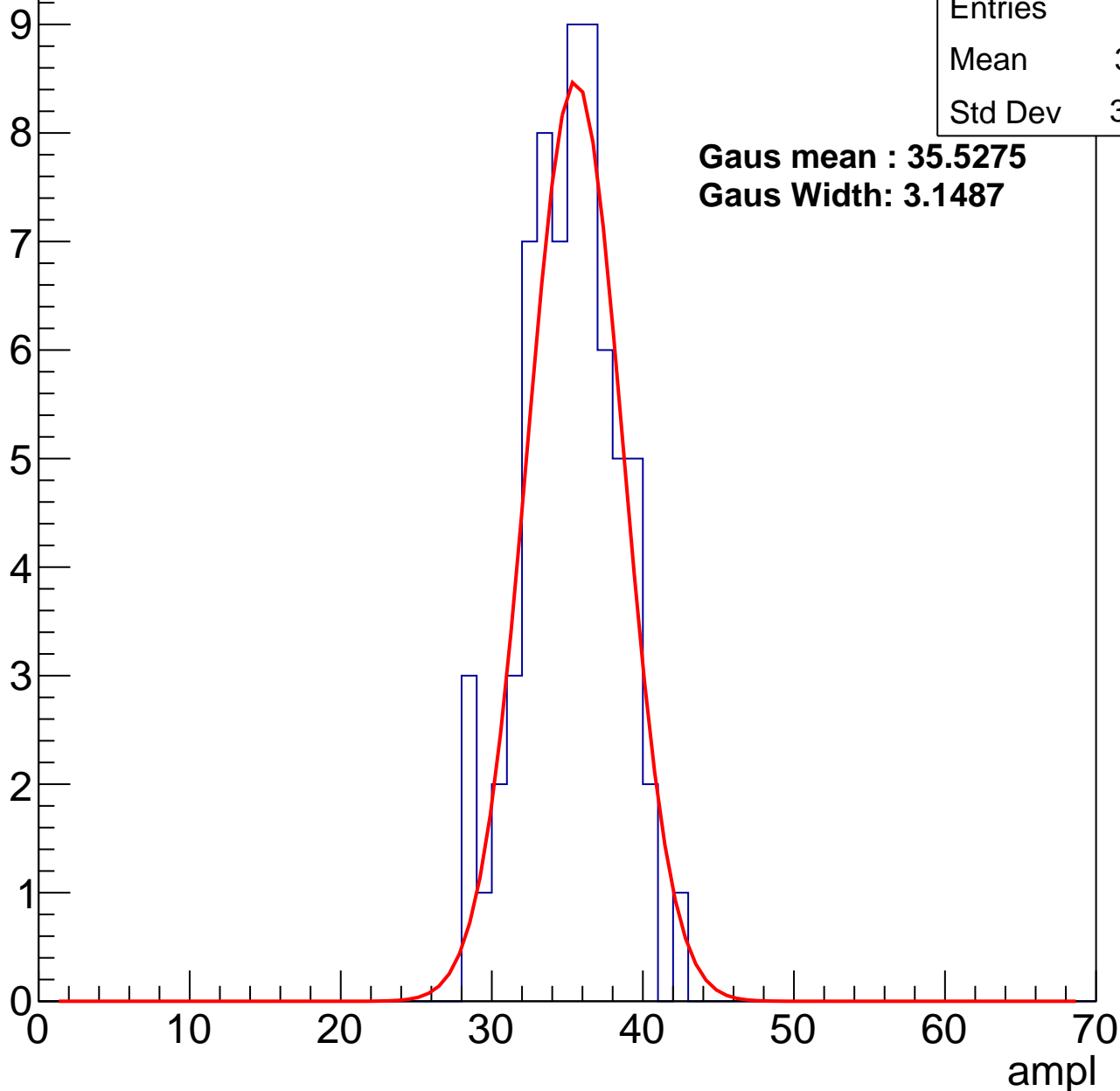
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.71
Std Dev	3.054

Gaus mean : 35.5275

Gaus Width: 3.1487



B1L002S, U7-ch65, adc2

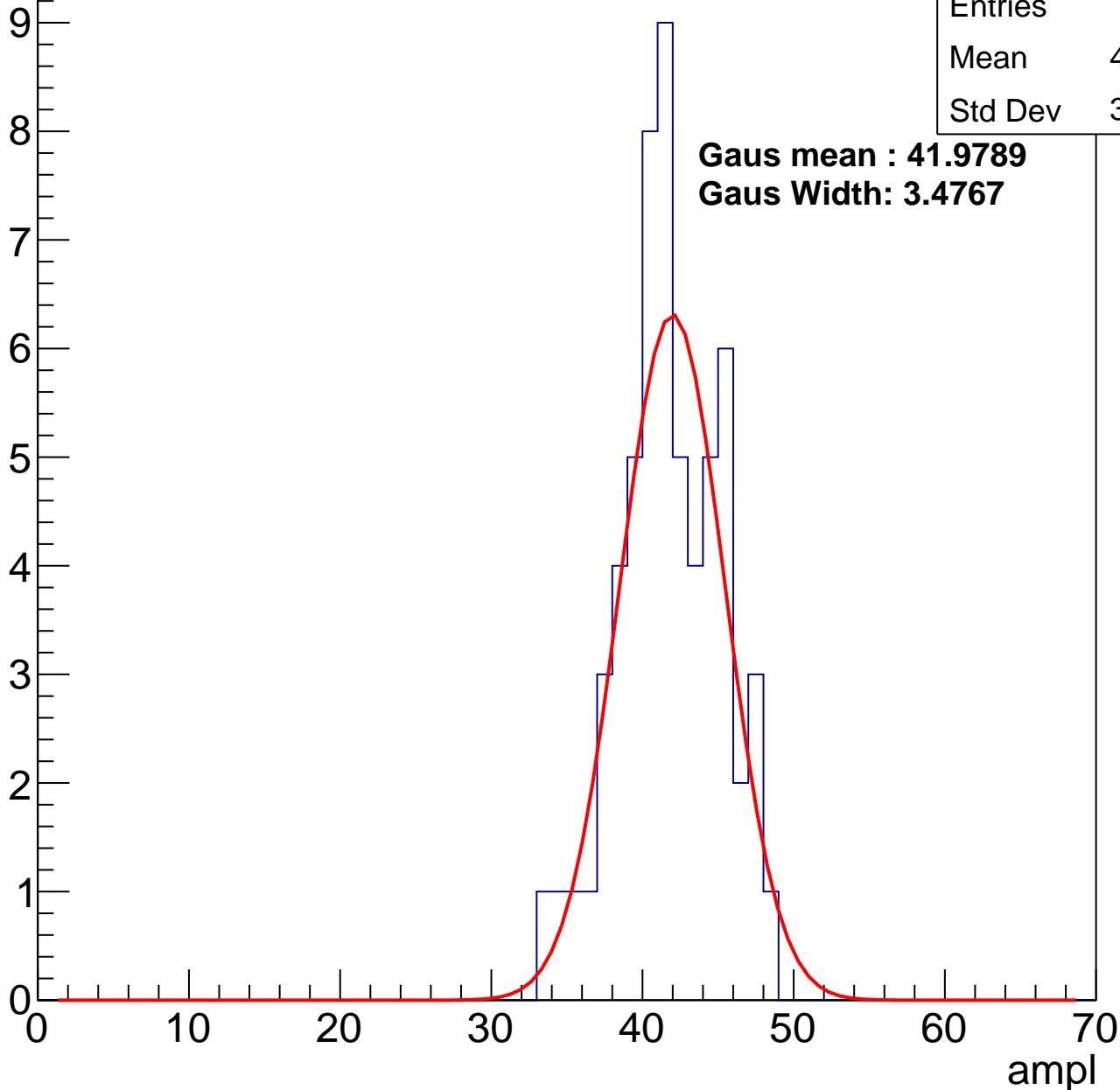
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.32
Std Dev	3.316

Gaus mean : 41.9789

Gaus Width: 3.4767

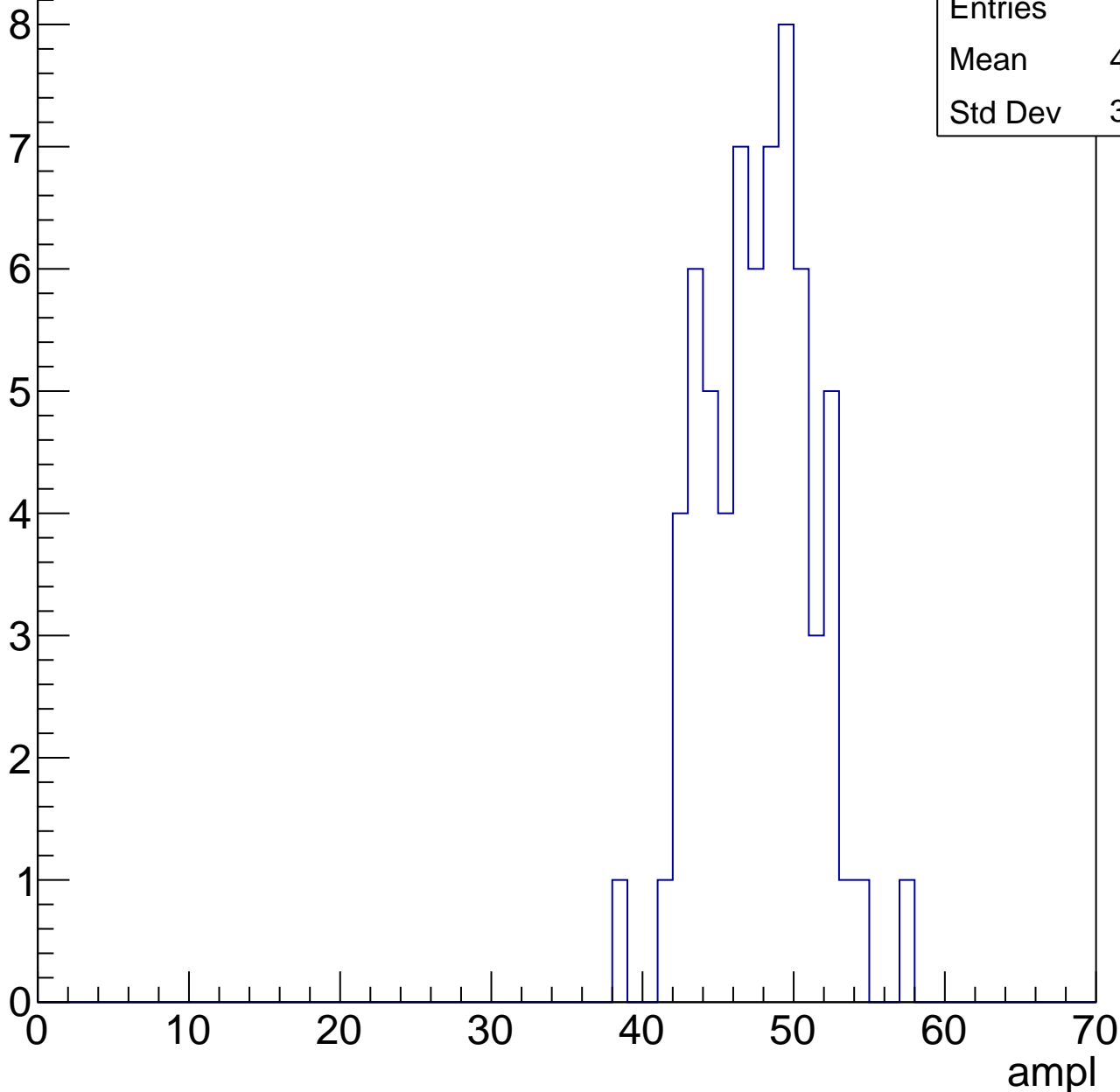


B1L002S, U7-ch65, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

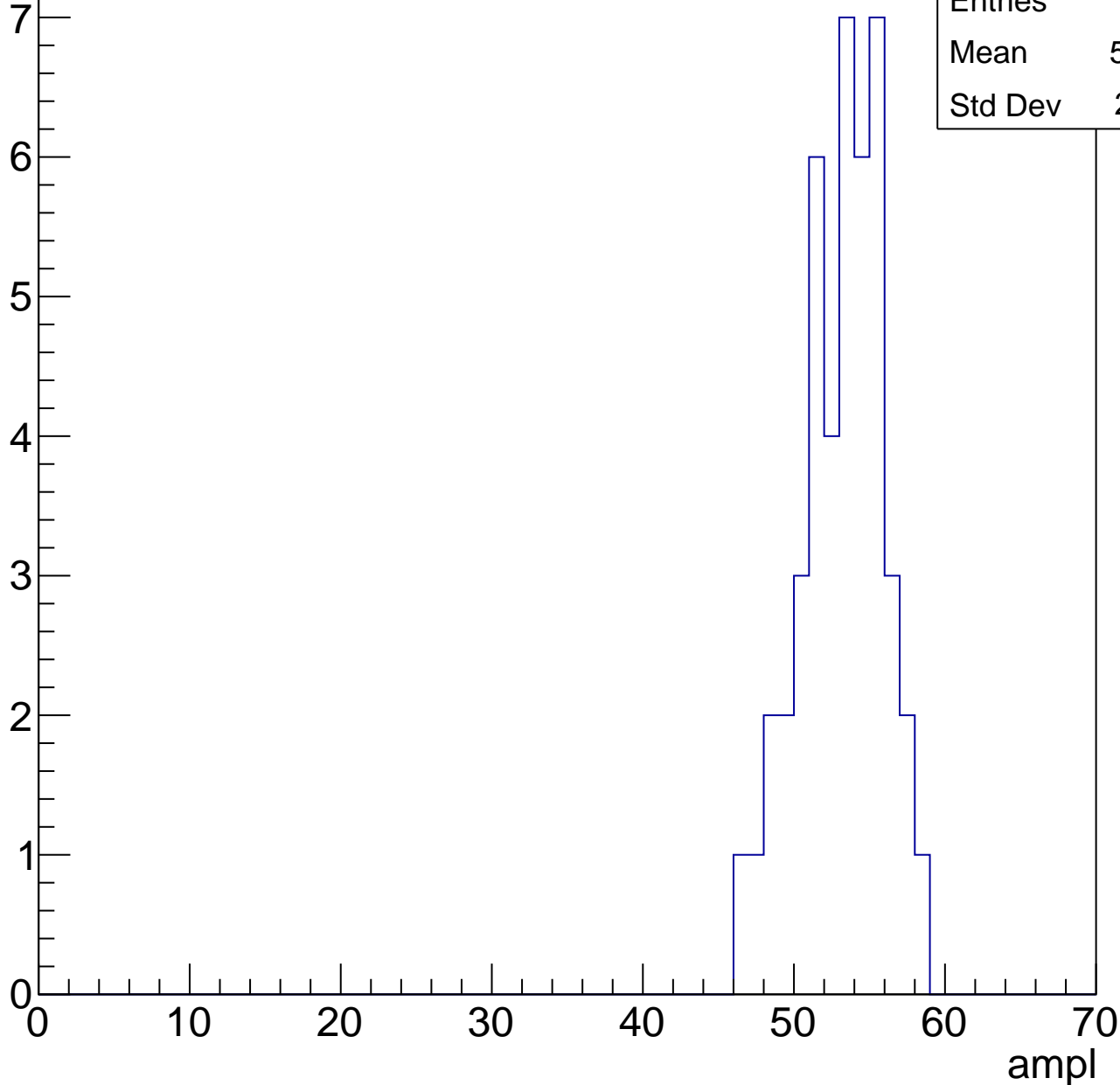
Entries	66
Mean	47.18
Std Dev	3.546



B1L002S, U7-ch65, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	45
Mean	52.69
Std Dev	2.731

B1L002S, U7-ch65, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

10

Entries 78

Mean 57.94

Std Dev 7.277

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L002S, U7-ch65, adc6

calib_packv5_042523_0143.root, FC#10, port B3

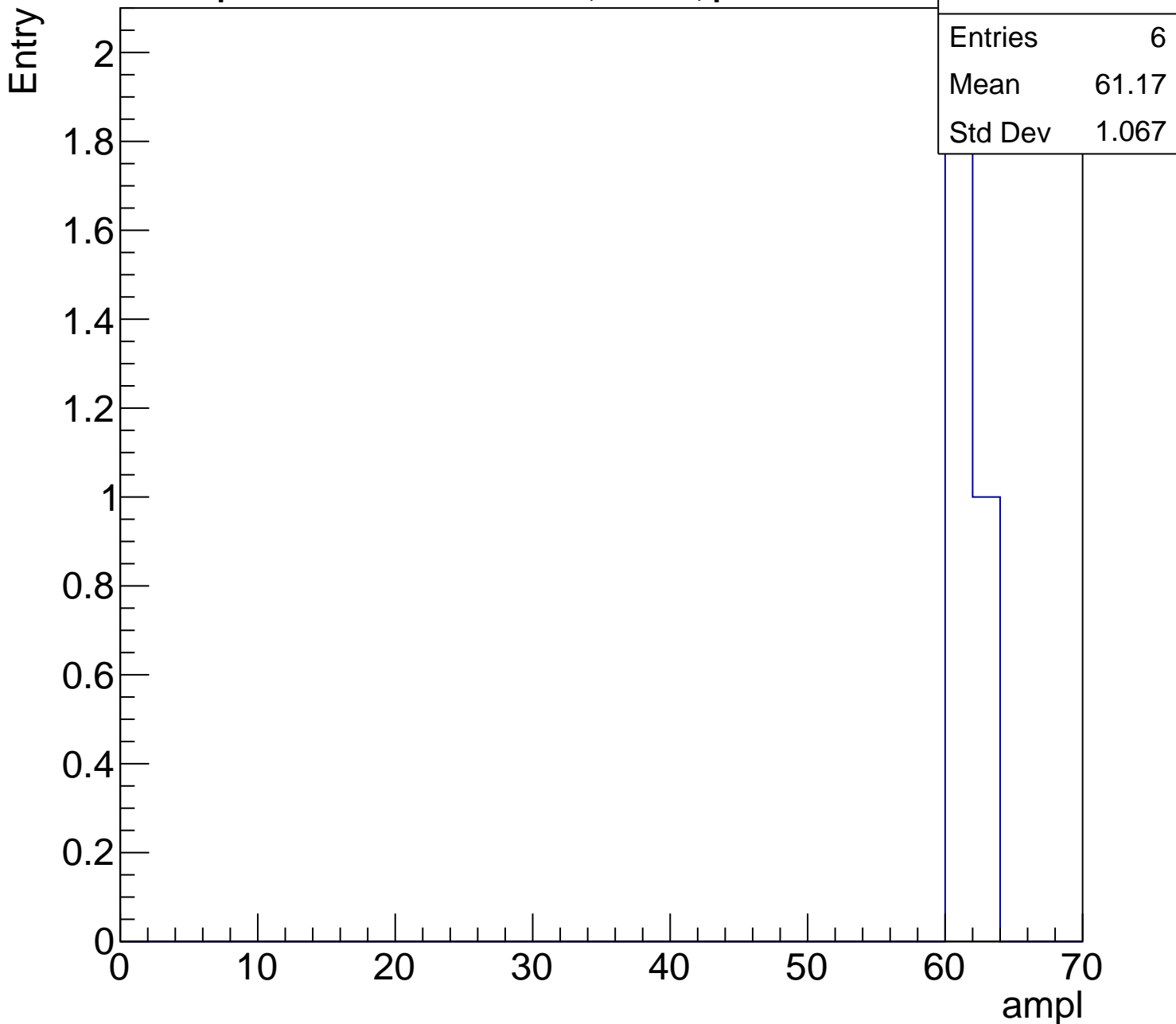
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.067

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch65, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch66, adc0

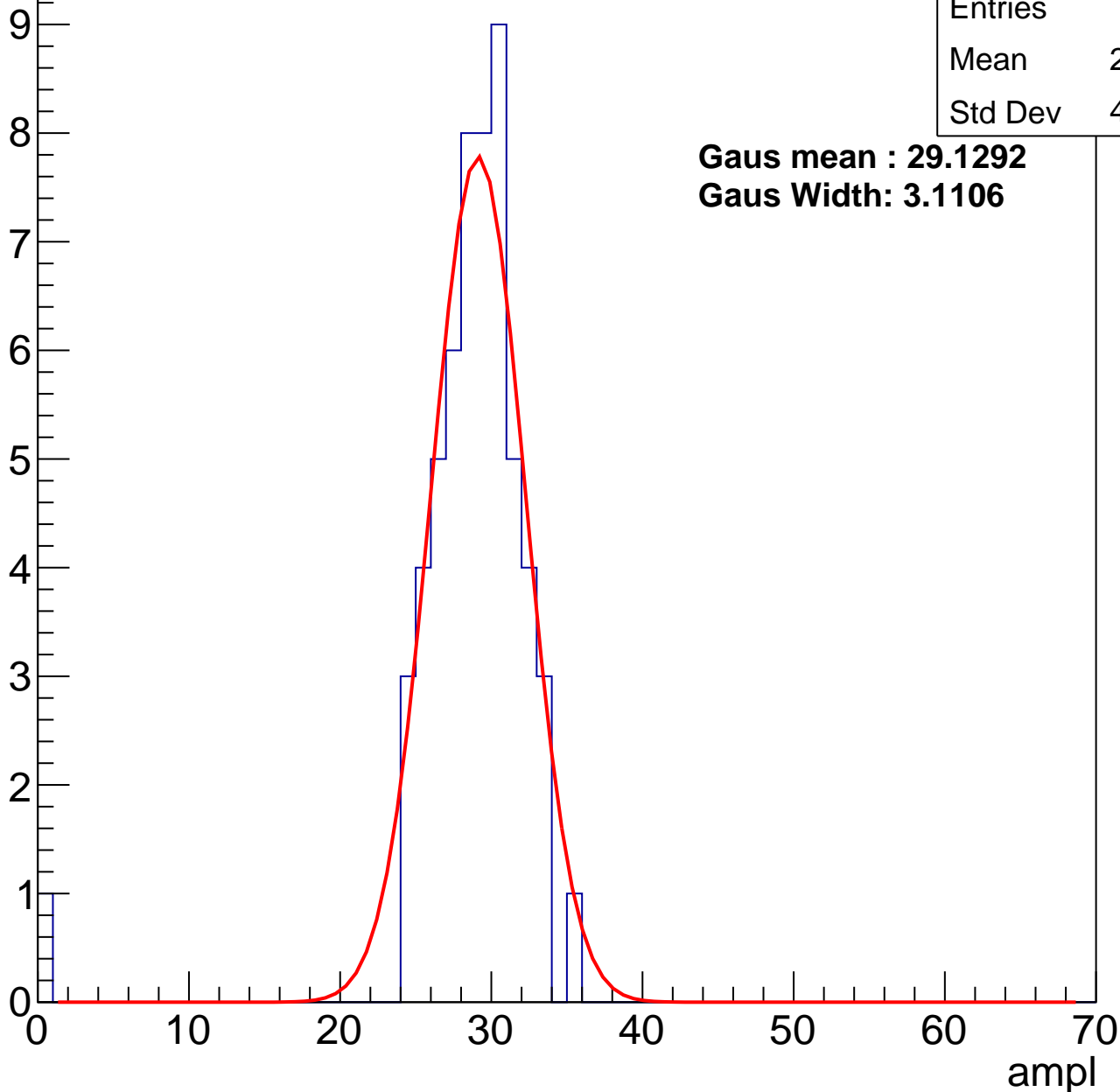
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.19
Std Dev	4.528

Gaus mean : 29.1292

Gaus Width: 3.1106



B1L002S, U7-ch66, adc1

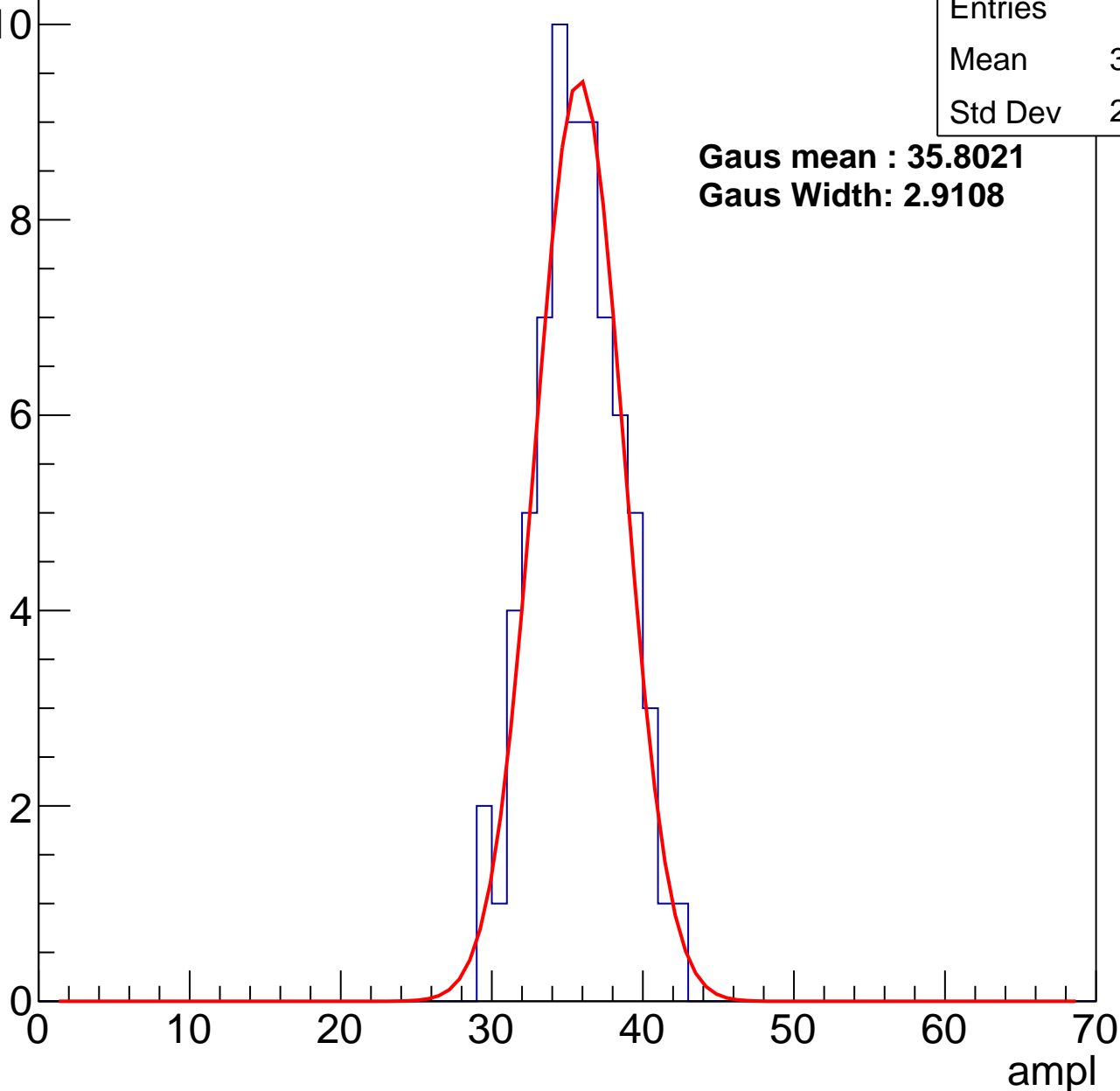
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	35.24
Std Dev	2.856

Gaus mean : 35.8021

Gaus Width: 2.9108



B1L002S, U7-ch66, adc2

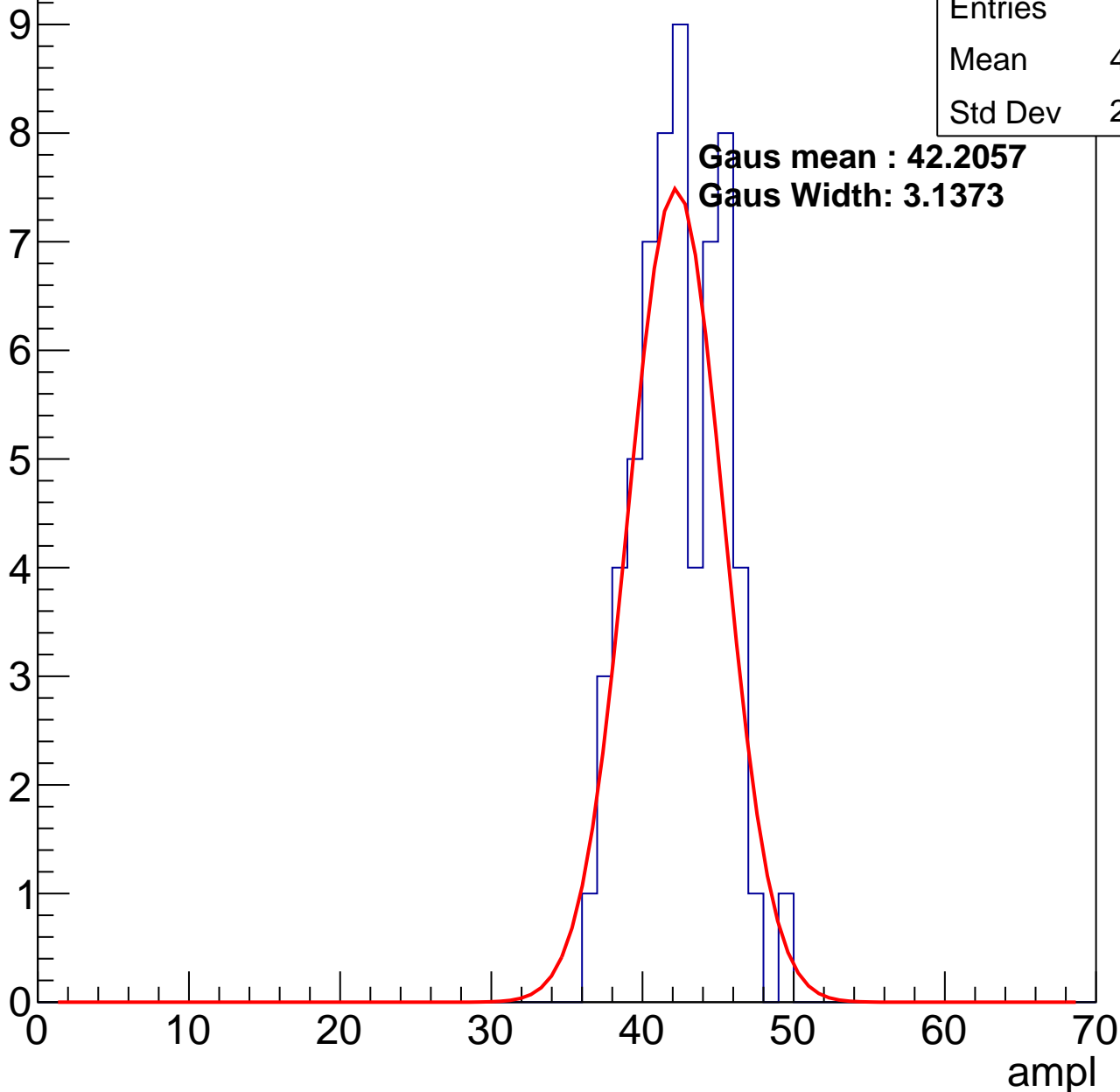
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.94
Std Dev	2.833

Gaus mean : 42.2057

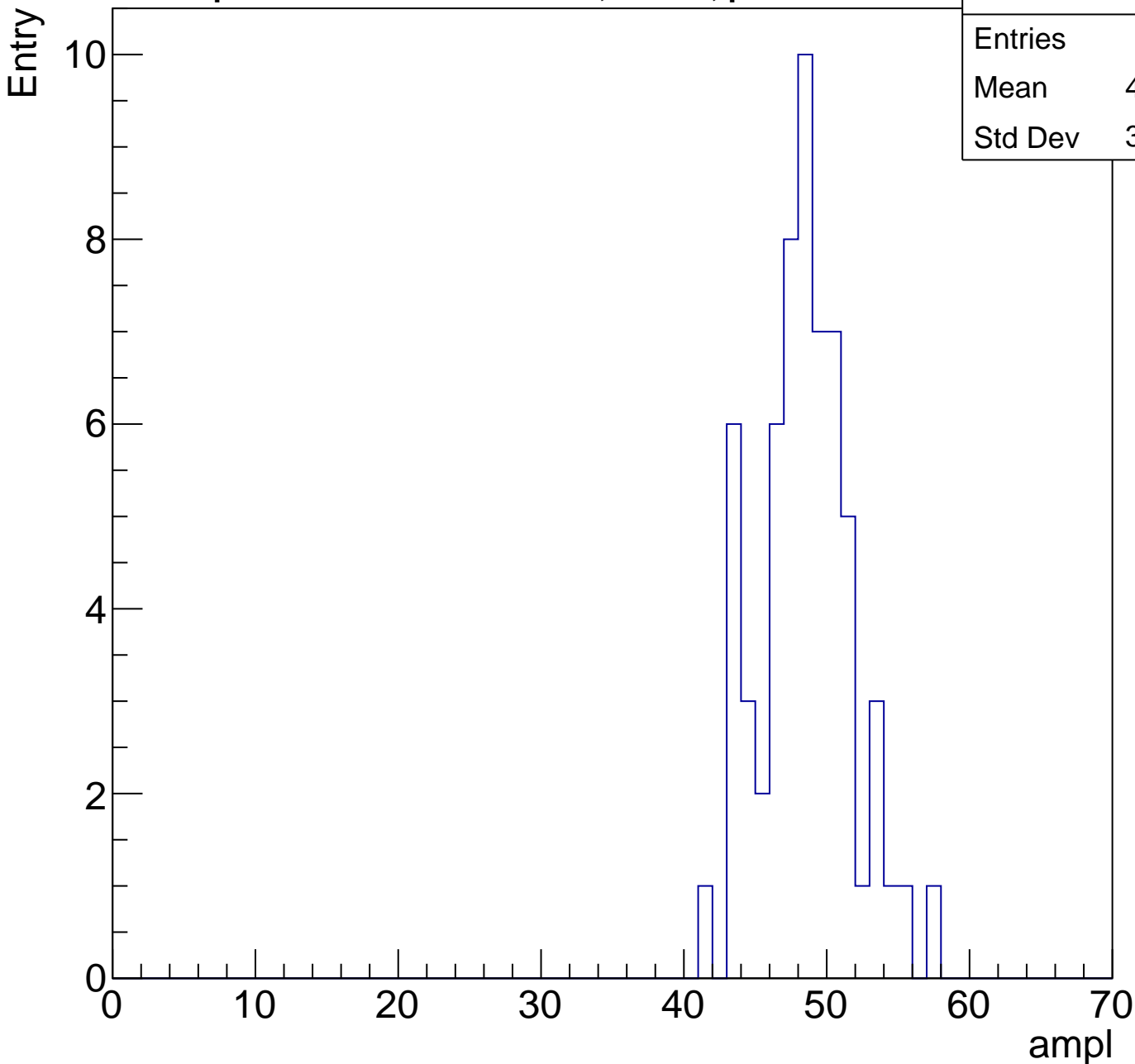
Gaus Width: 3.1373



B1L002S, U7-ch66, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	62
Mean	48.03
Std Dev	3.198

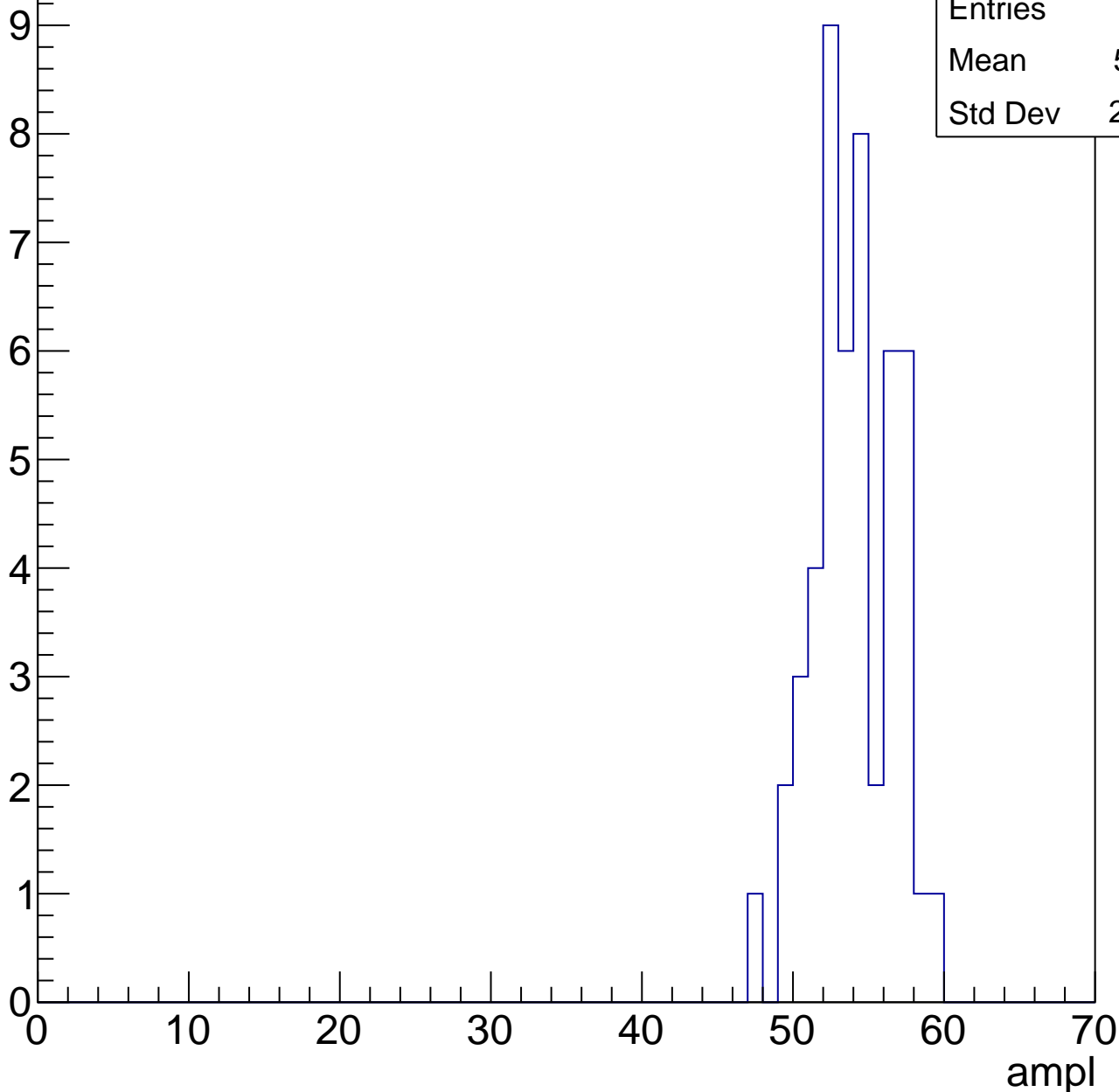


B1L002S, U7-ch66, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	53.51
Std Dev	2.612

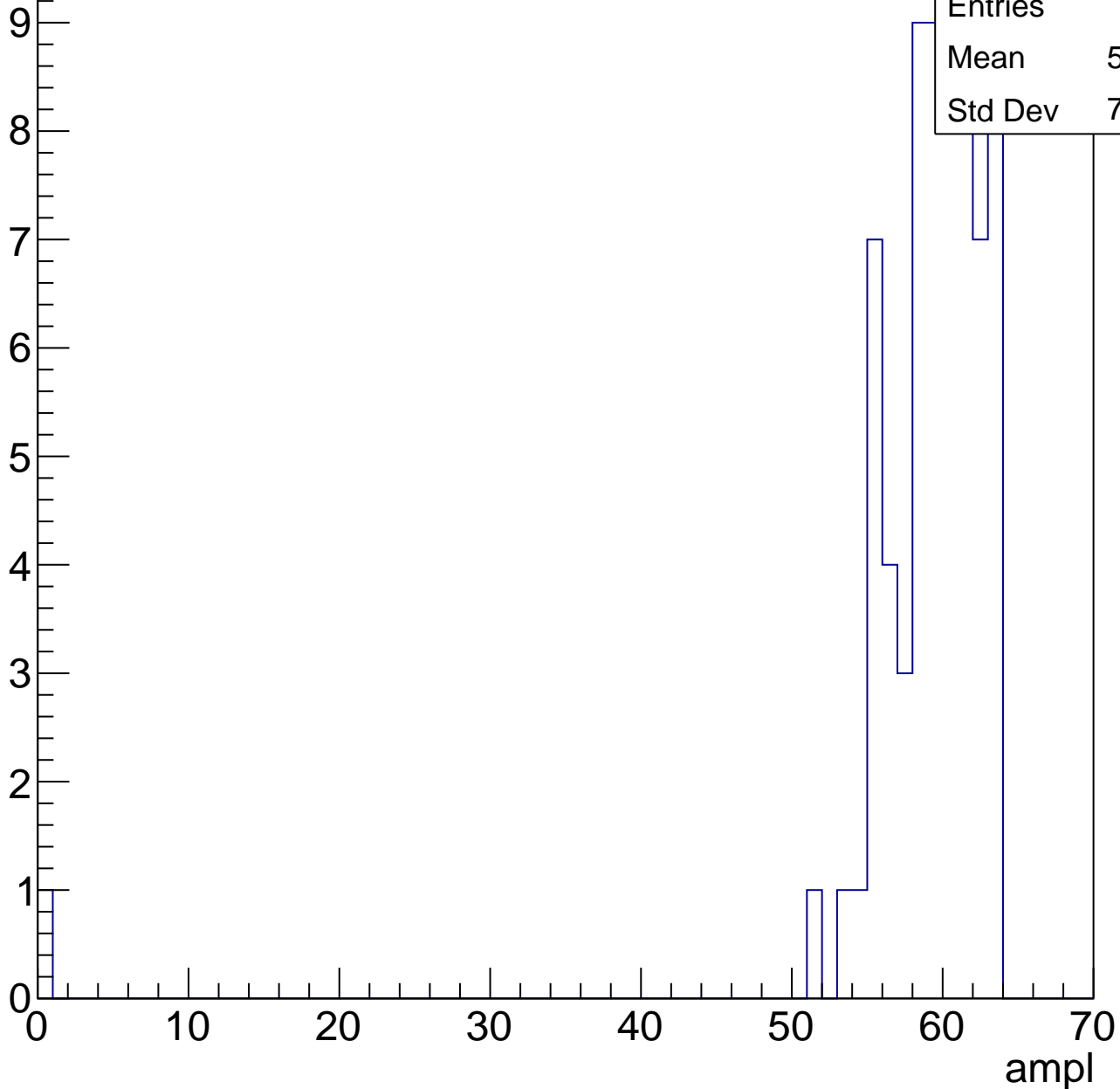


B1L002S, U7-ch66, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	58.19
Std Dev	7.634



B1L002S, U7-ch66, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

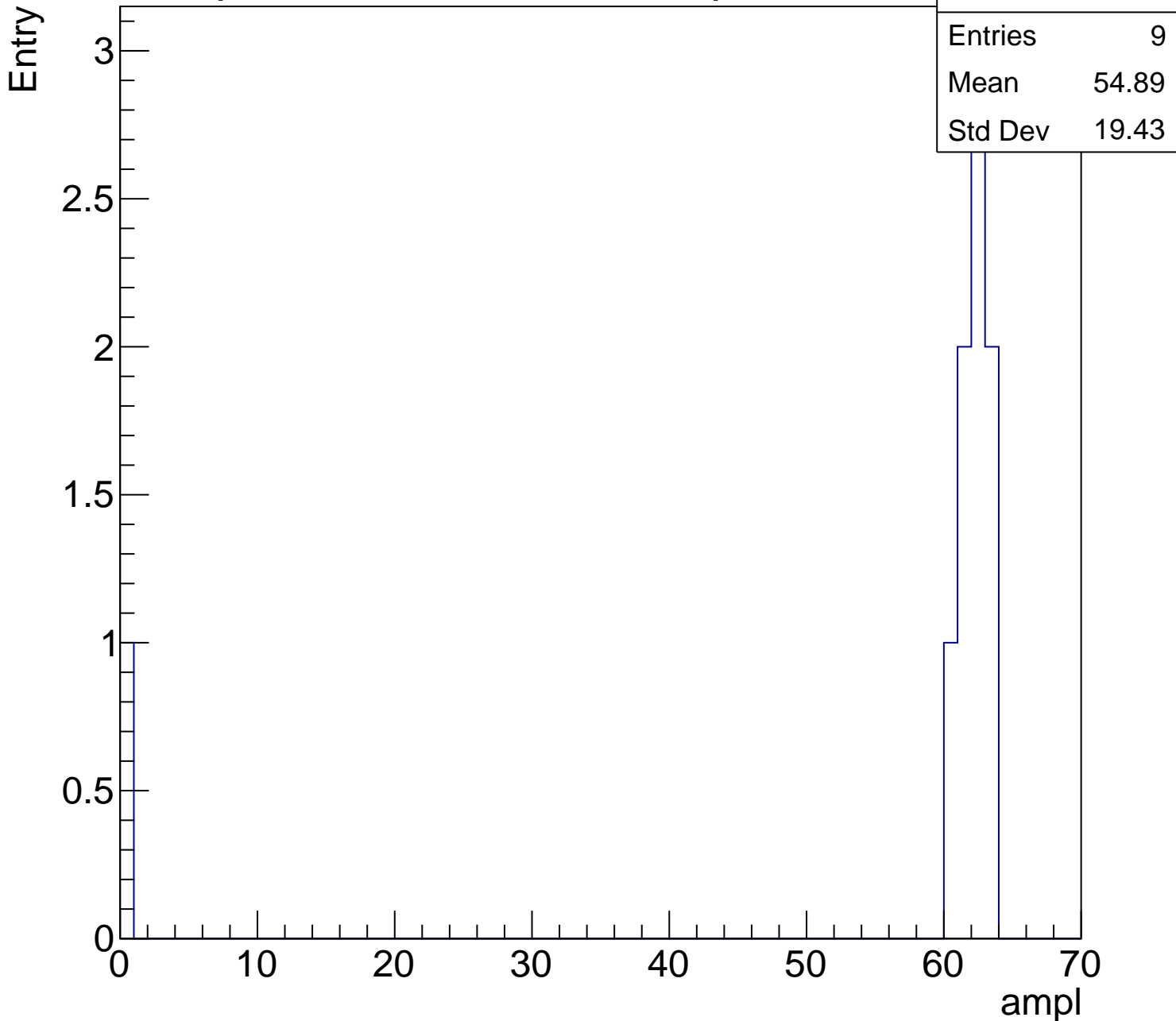
Mean

54.89

Std Dev

19.43

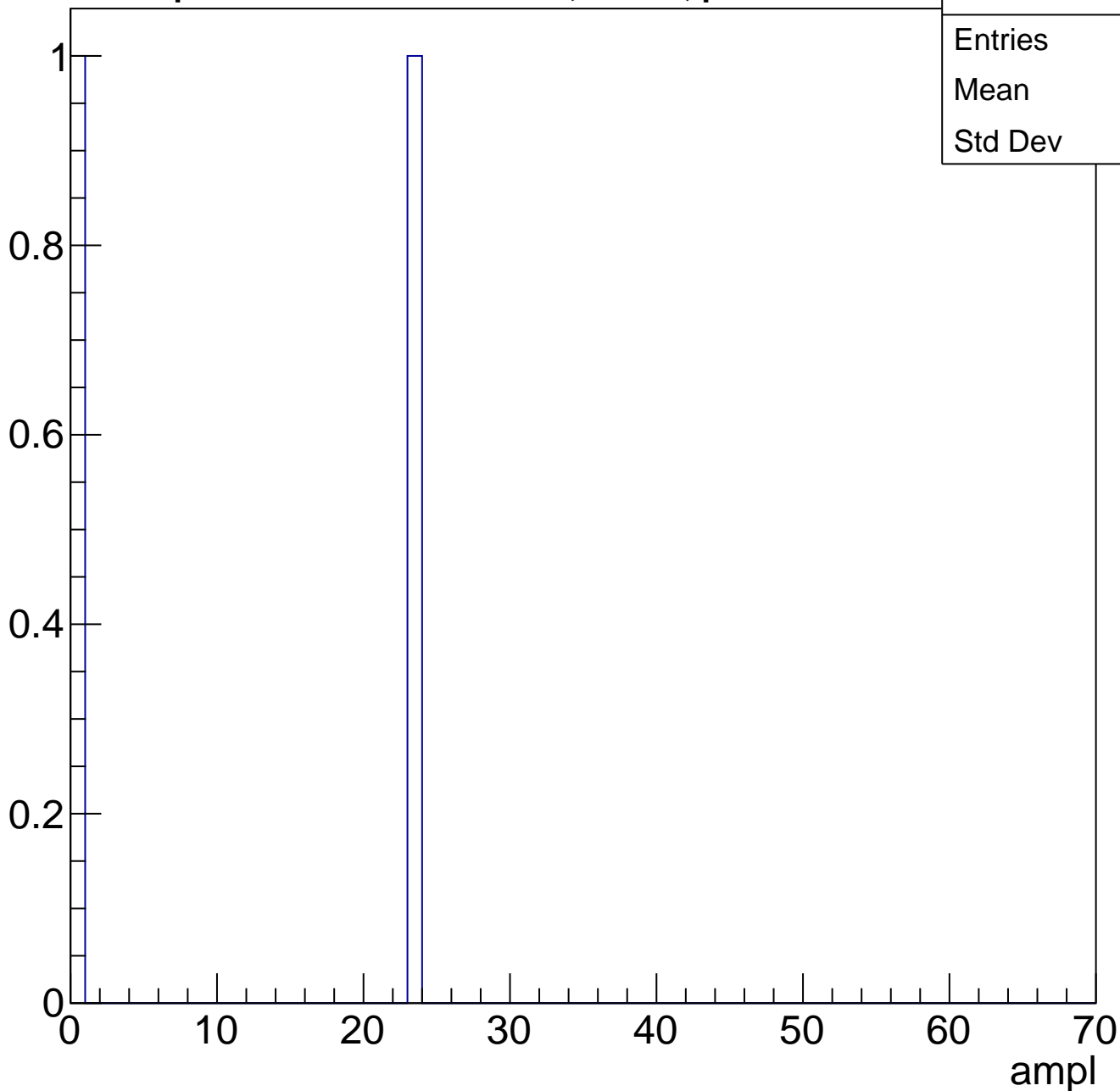
ampl



B1L002S, U7-ch66, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L002S, U7-ch67, adc0

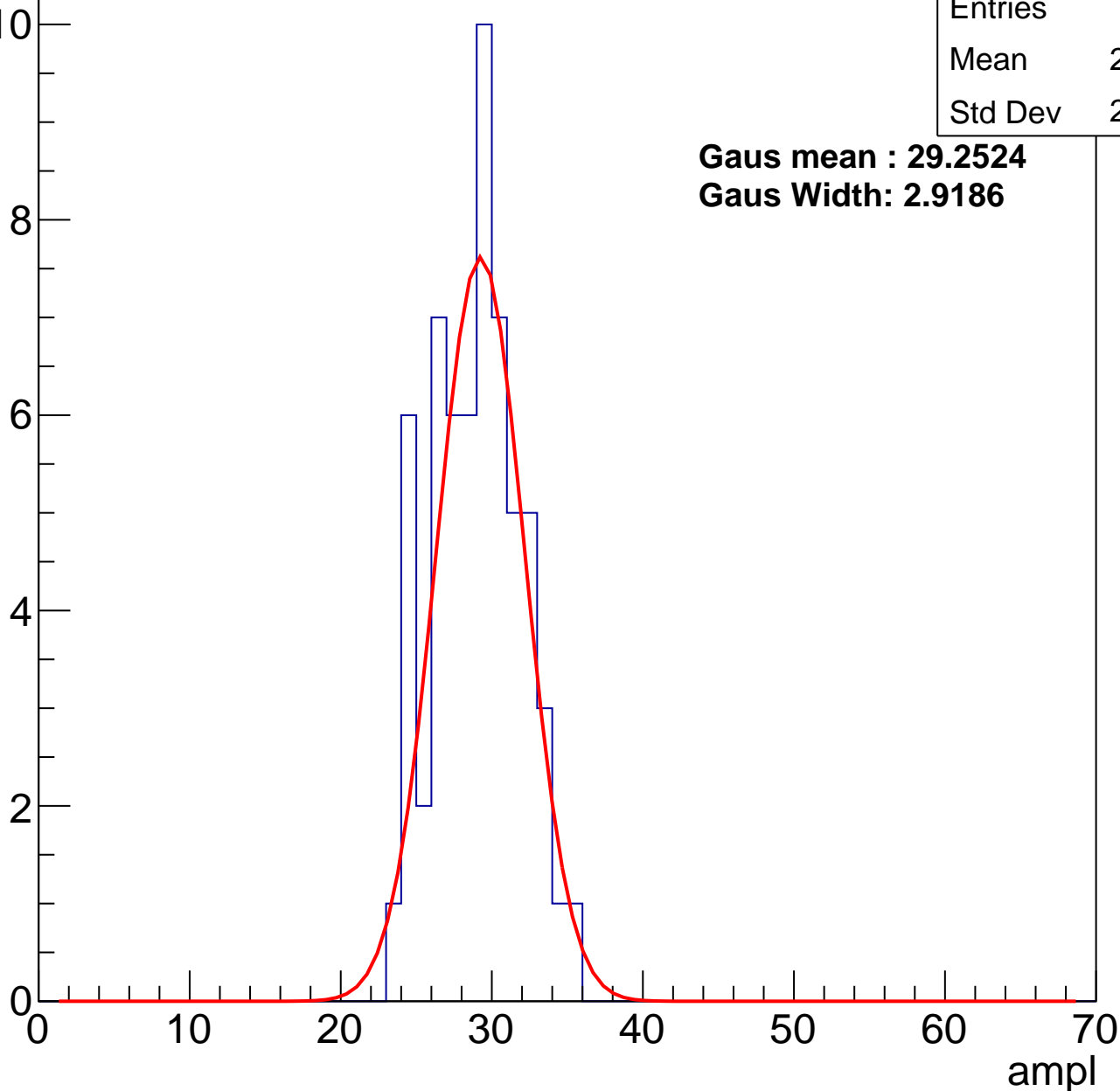
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.53
Std Dev	2.825

Gaus mean : 29.2524

Gaus Width: 2.9186



B1L002S, U7-ch67, adc1

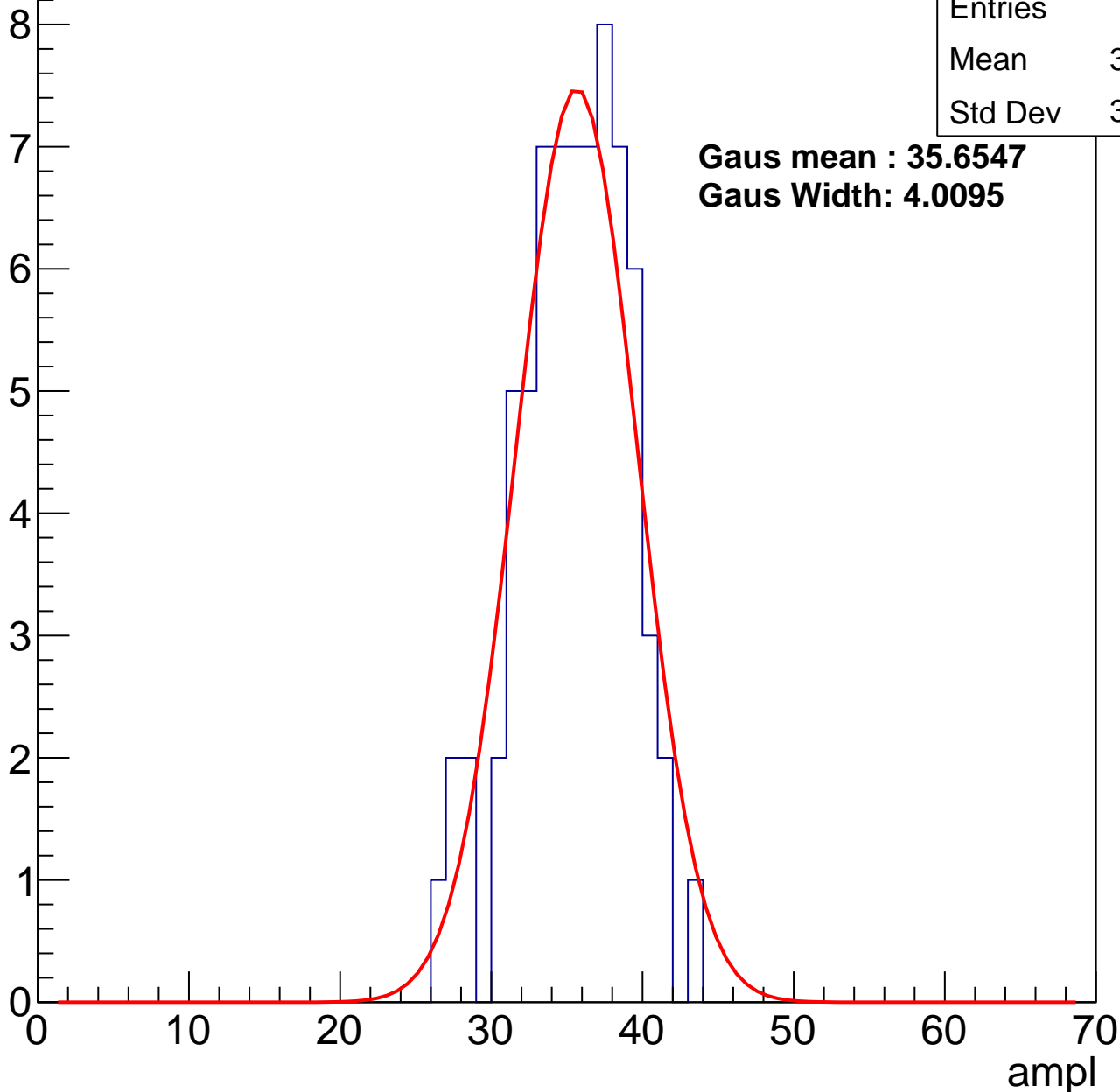
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.97
Std Dev	3.586

Gaus mean : 35.6547

Gaus Width: 4.0095



B1L002S, U7-ch67, adc2

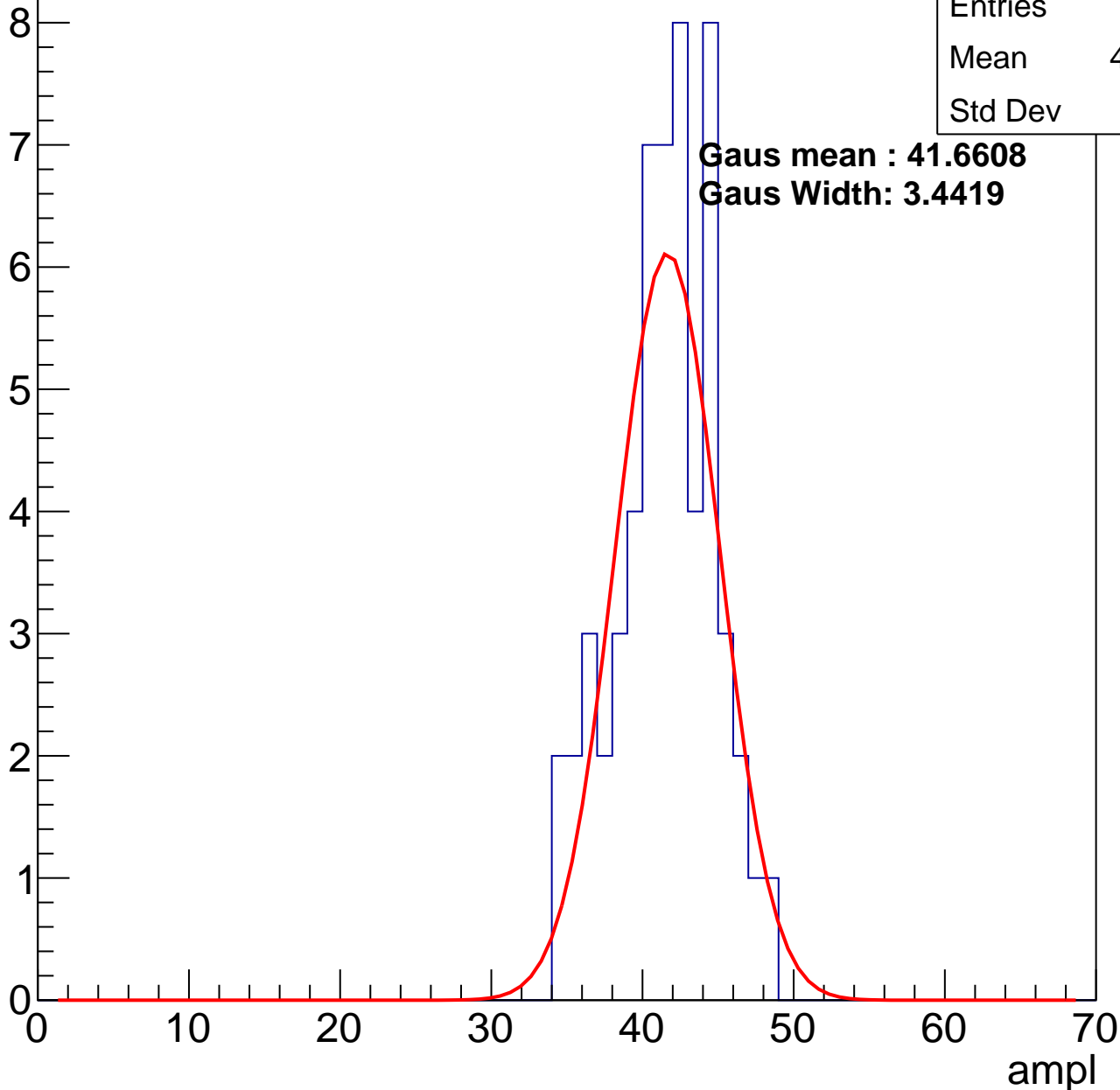
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.04
Std Dev	3.26

Gaus mean : 41.6608

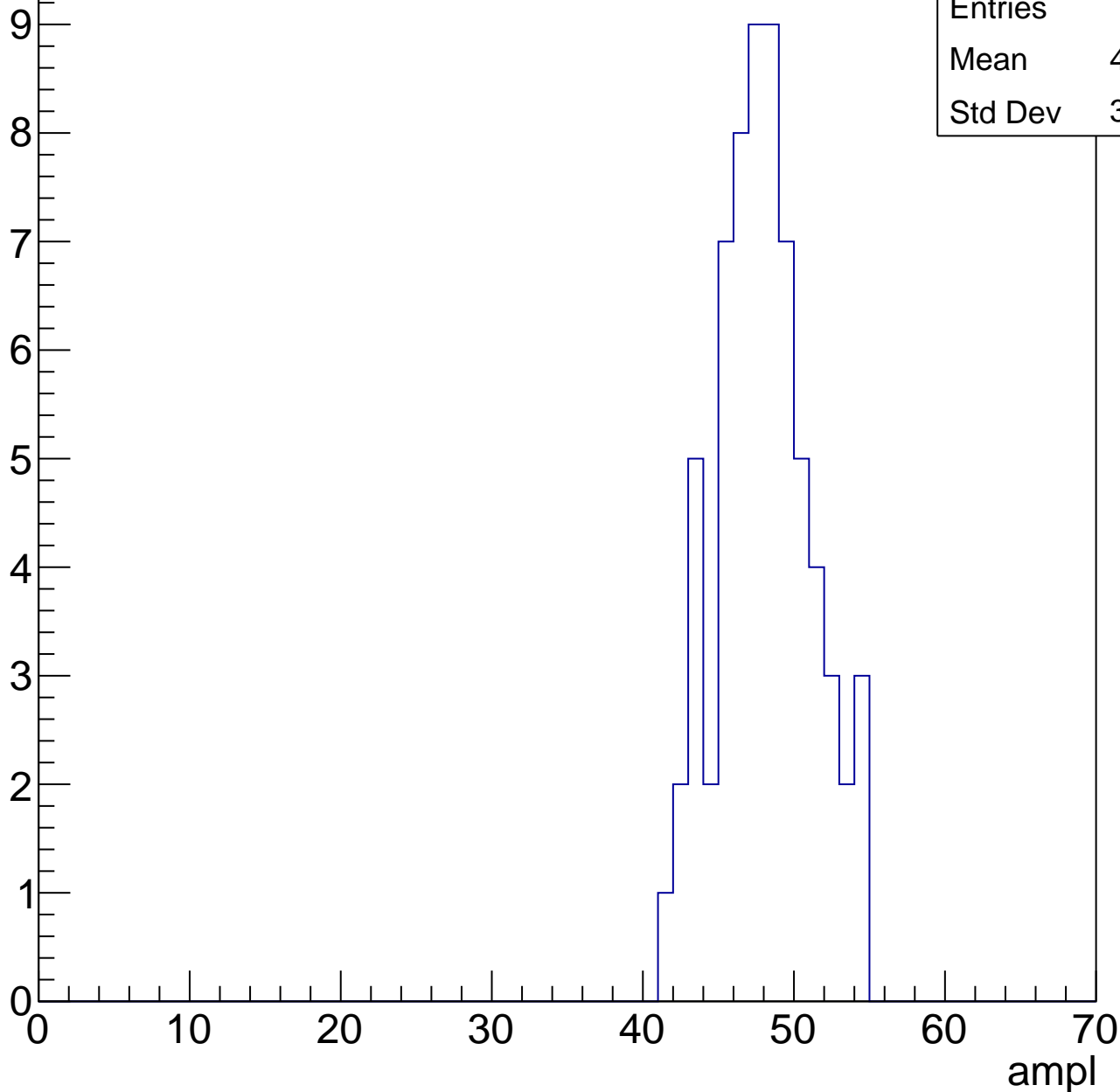
Gaus Width: 3.4419



B1L002S, U7-ch67, adc3

calib_packv5_042523_0143.root, FC#10, port B3

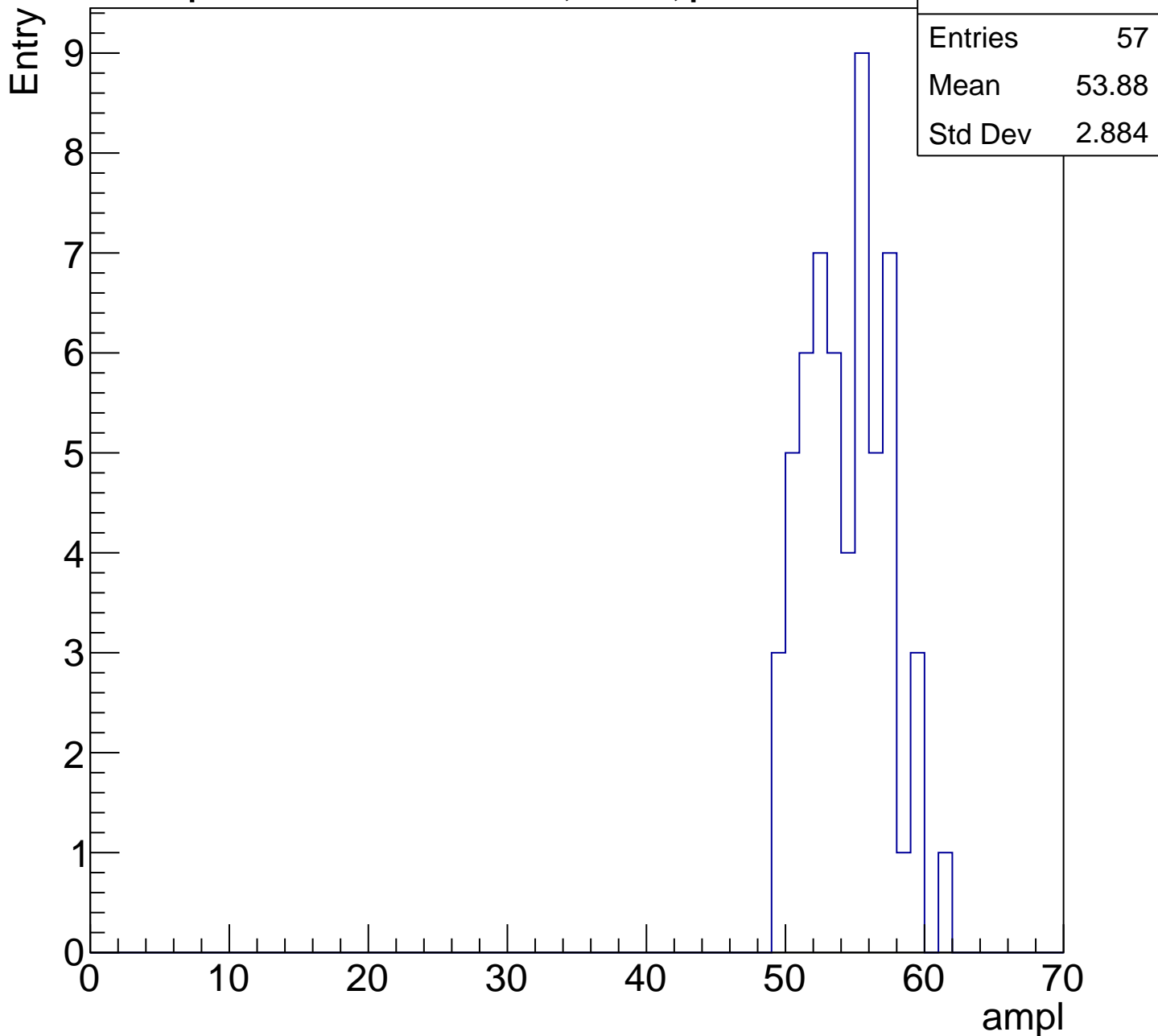
Entry



Entries	67
Mean	47.57
Std Dev	3.087

B1L002S, U7-ch67, adc4

calib_packv5_042523_0143.root, FC#10, port B3

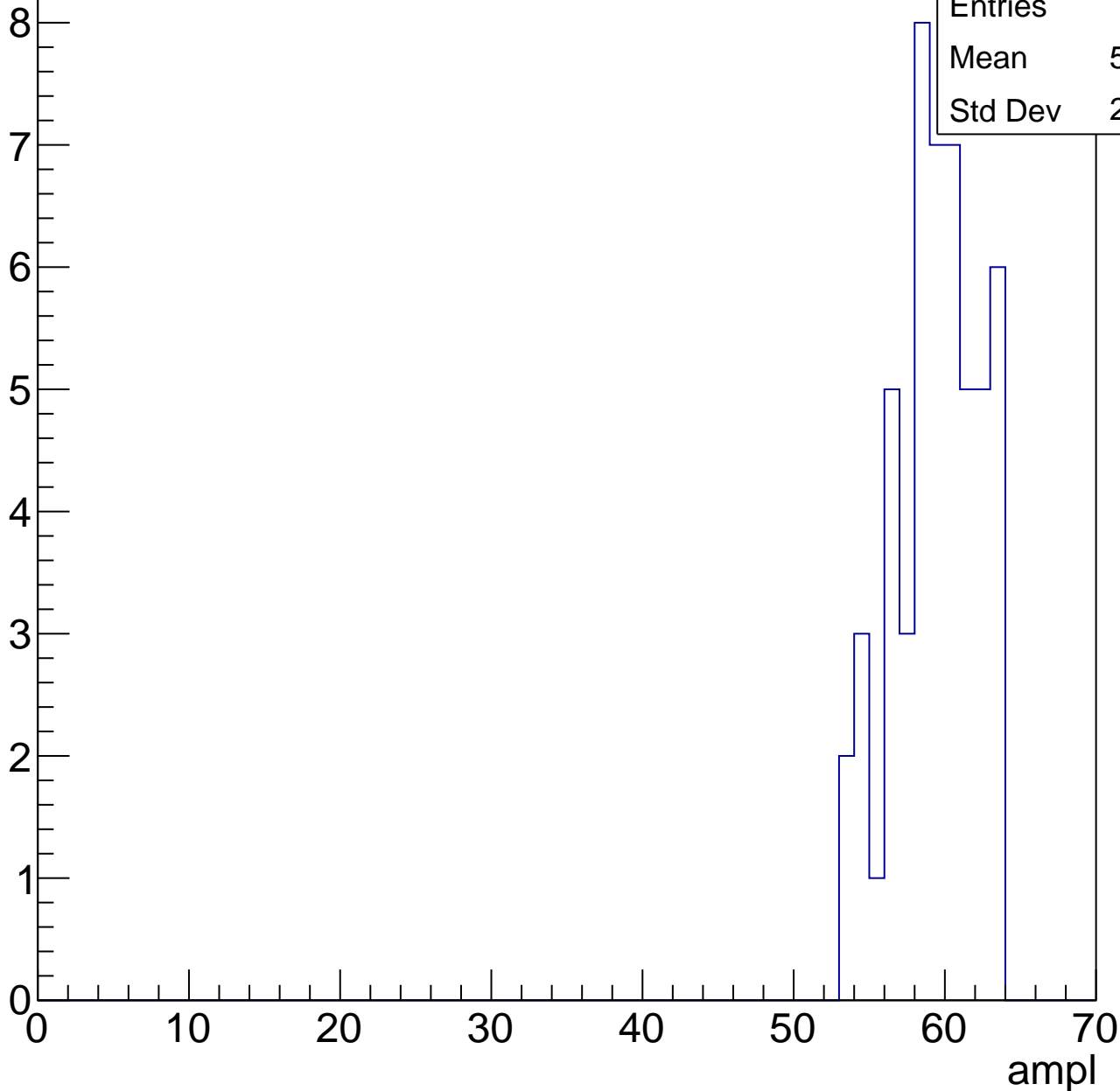


B1L002S, U7-ch67, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.92
Std Dev	2.759

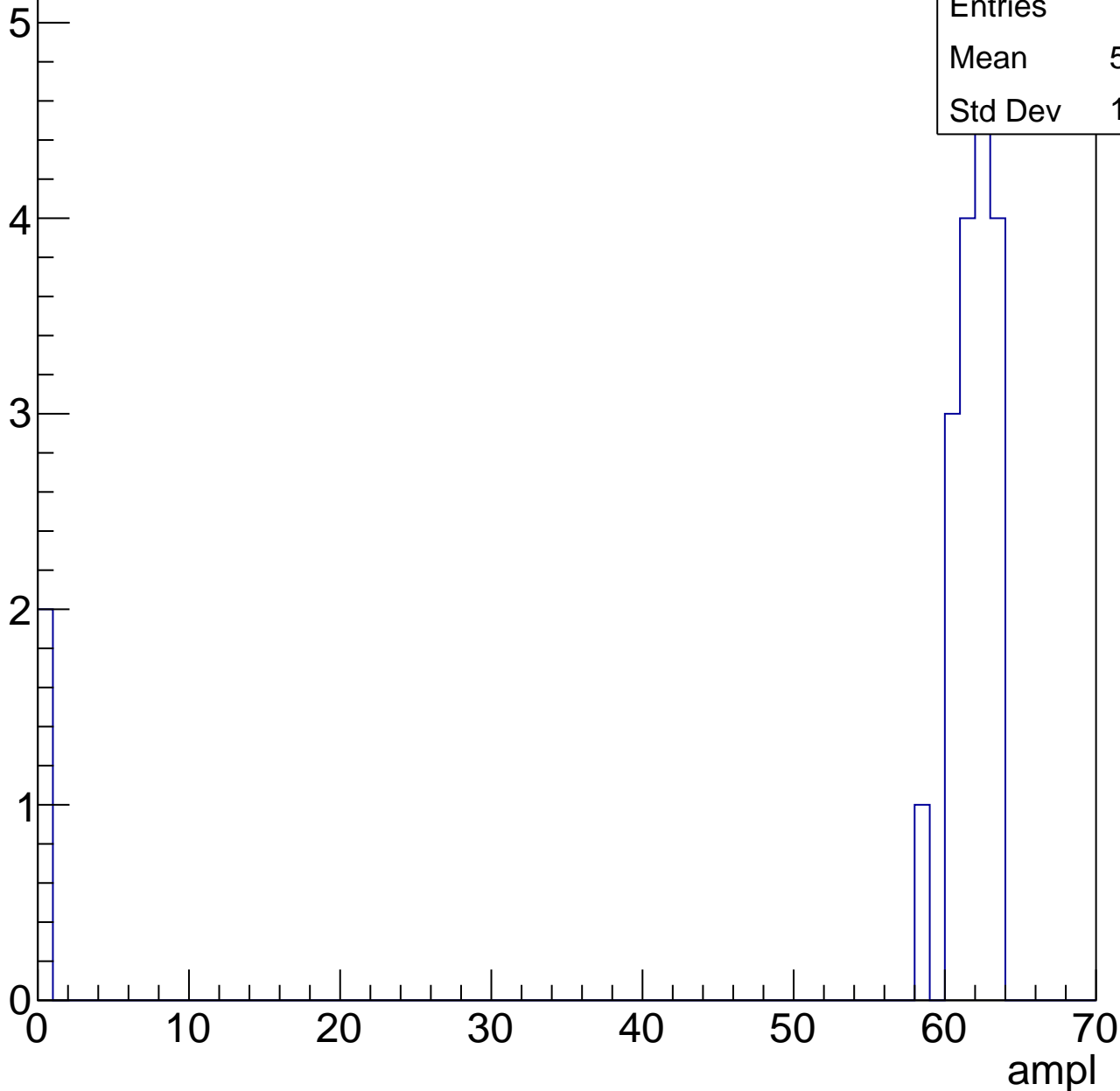


B1L002S, U7-ch67, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	54.95
Std Dev	18.89



B1L002S, U7-ch67, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch68, adc0

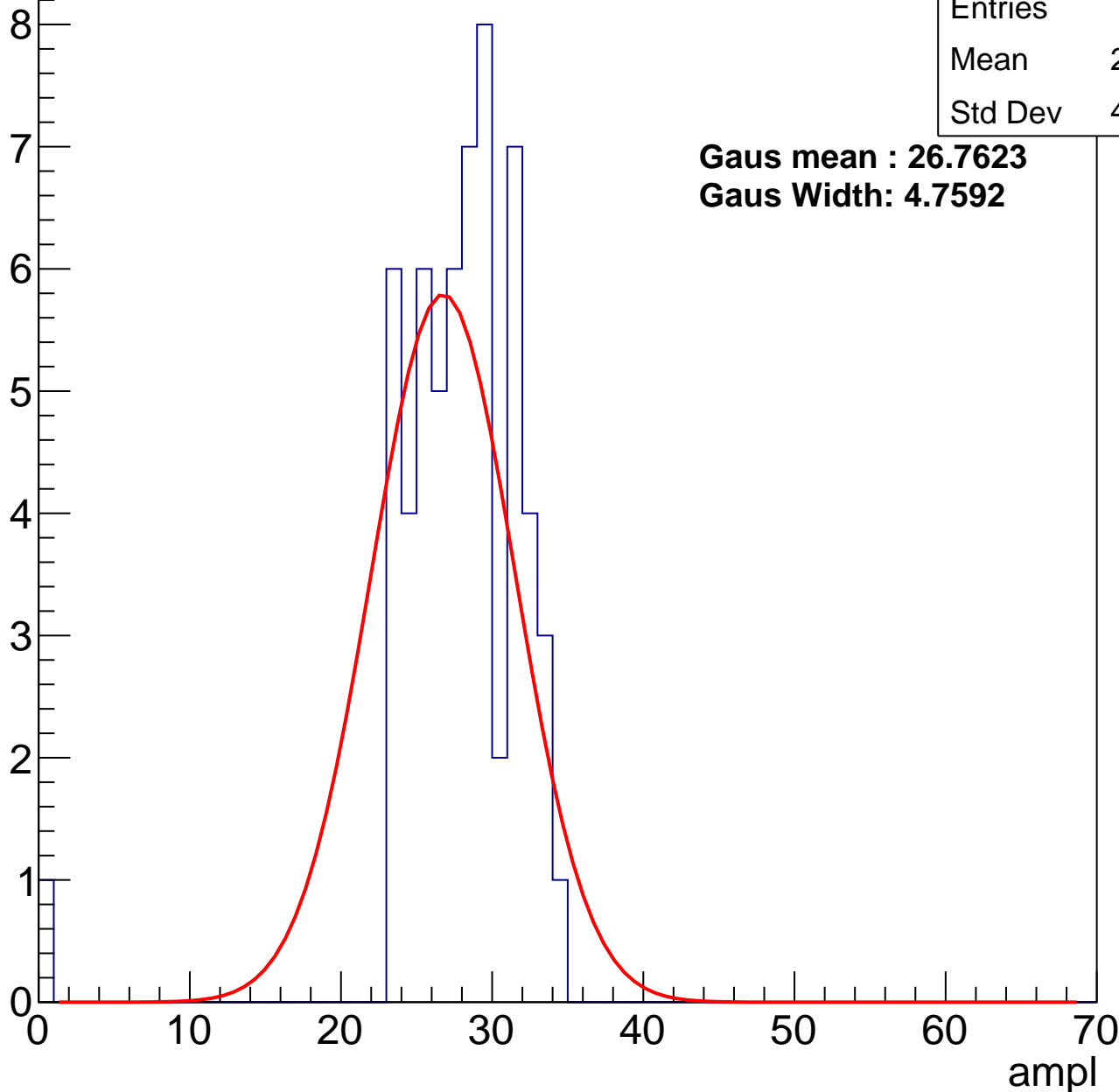
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.37
Std Dev	4.669

Gaus mean : 26.7623

Gaus Width: 4.7592



B1L002S, U7-ch68, adc1

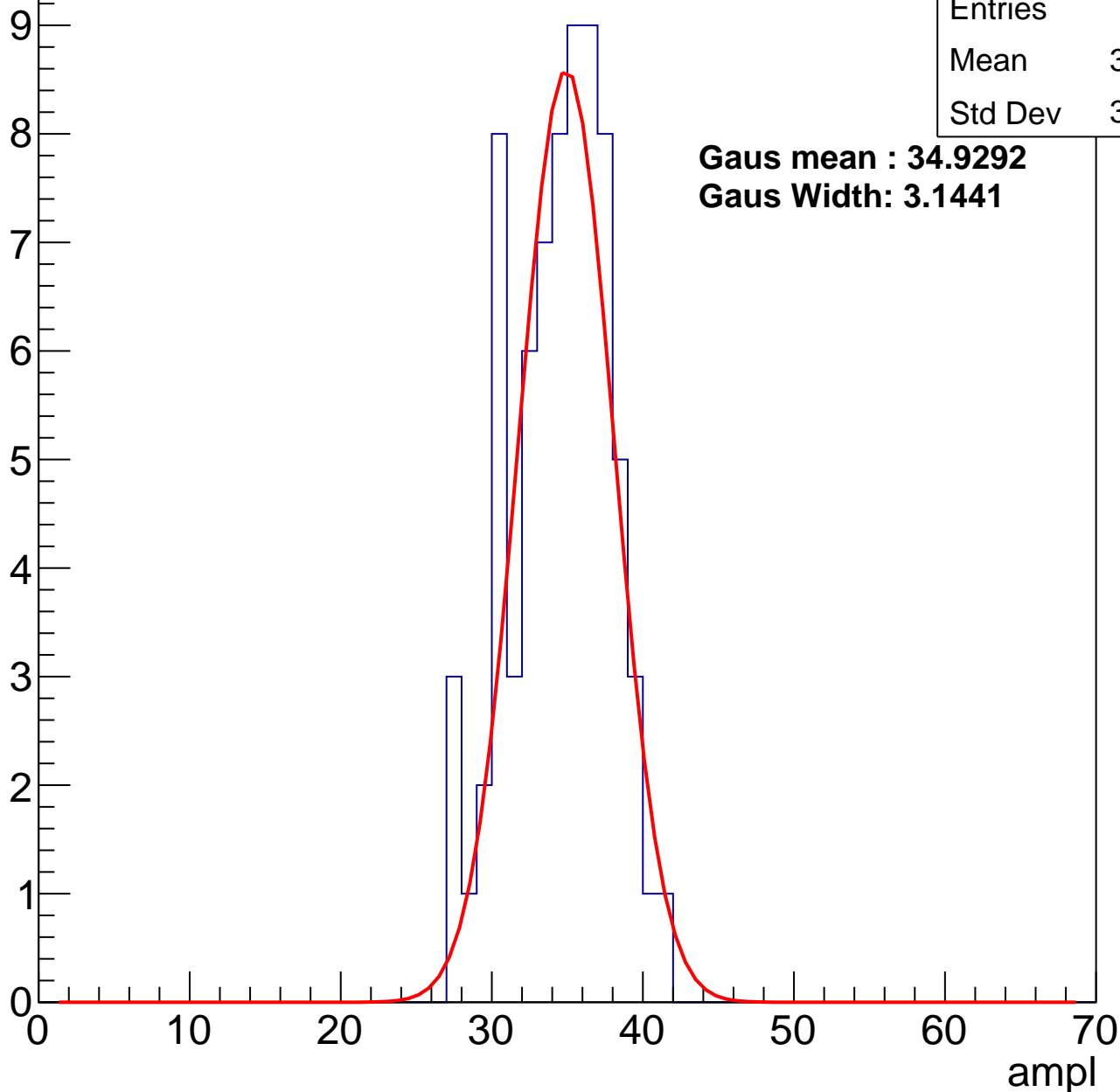
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	34.03
Std Dev	3.238

Gaus mean : 34.9292

Gaus Width: 3.1441



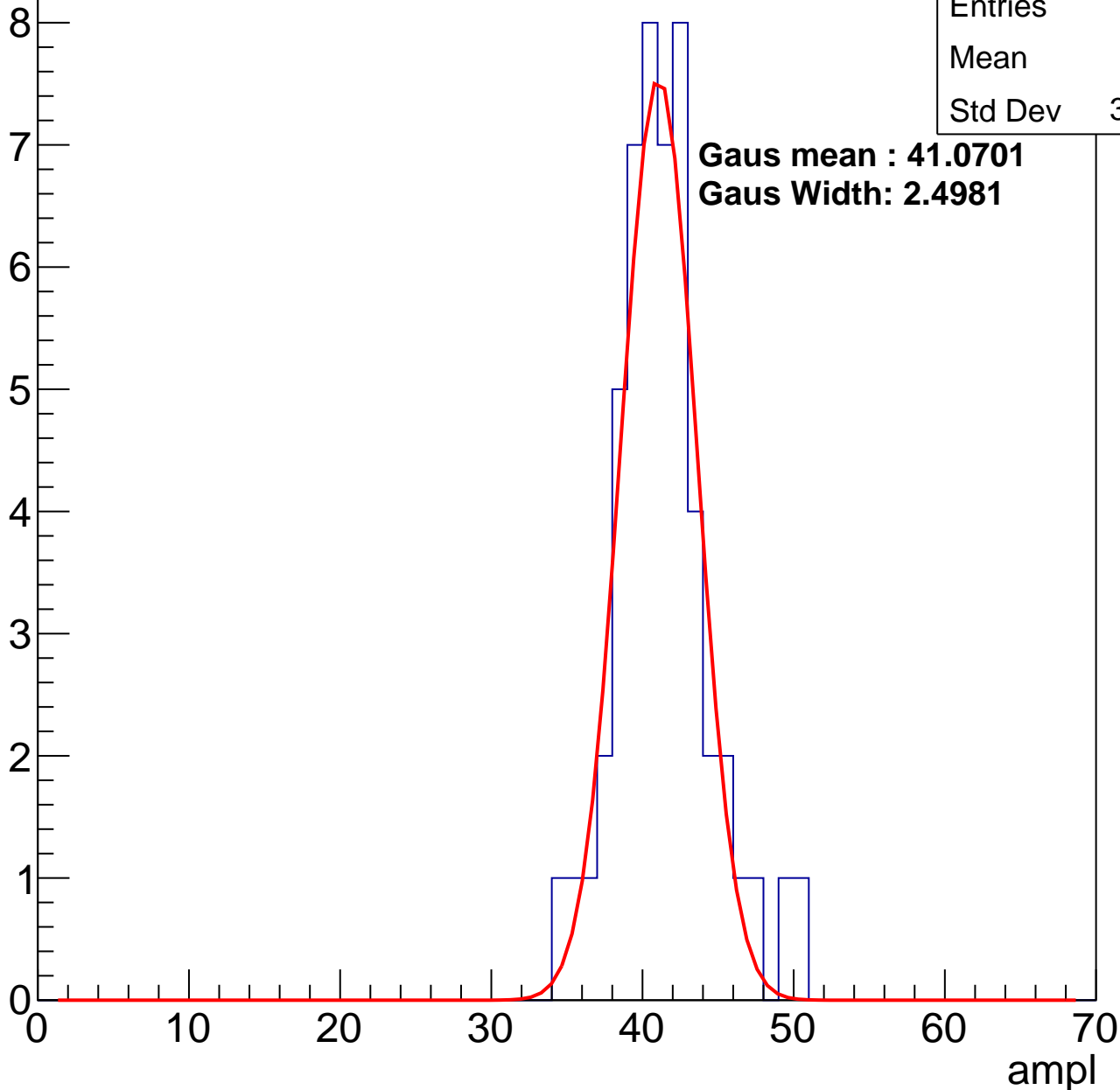
B1L002S, U7-ch68, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.9
Std Dev	3.109

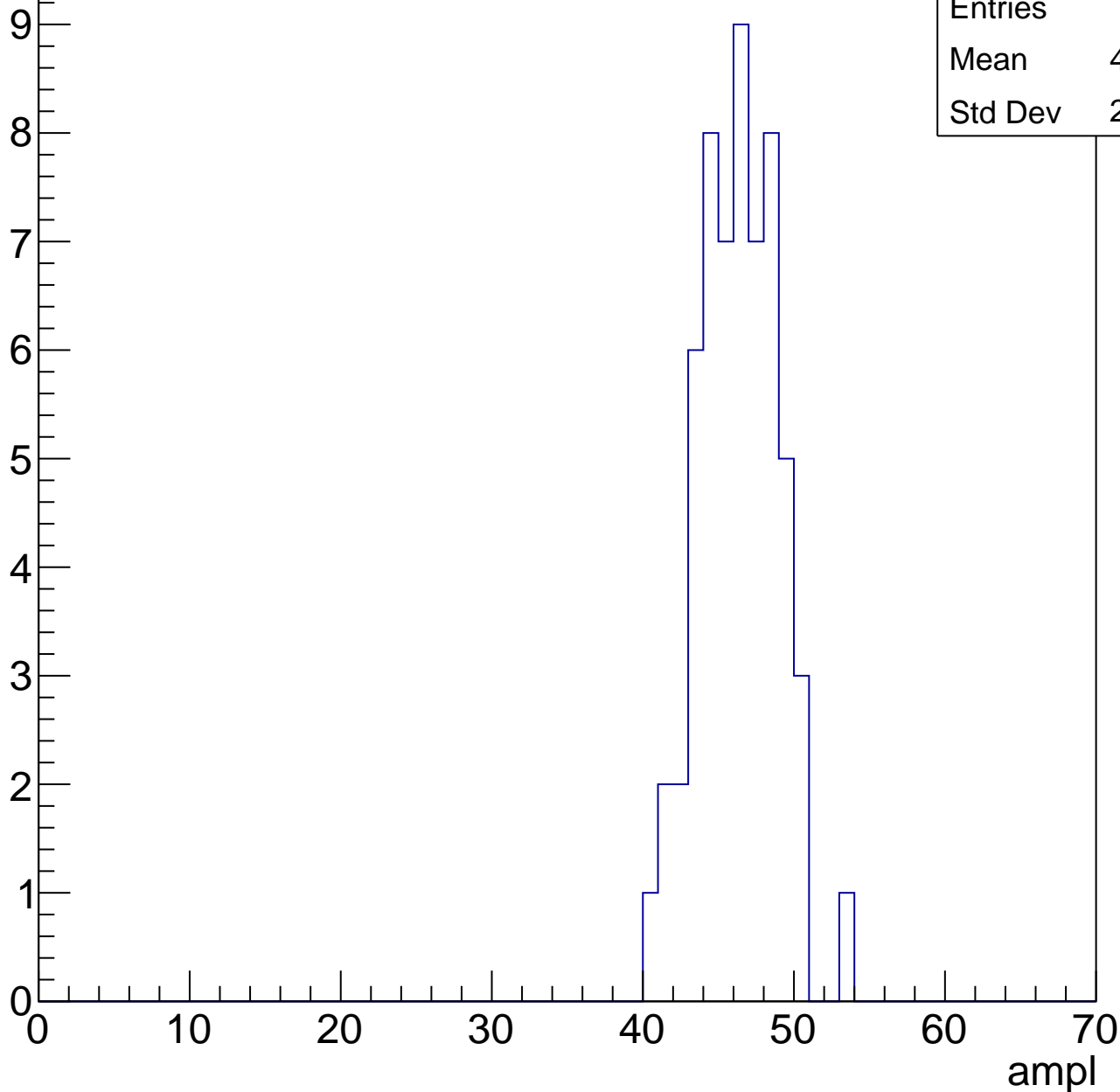
Gaus mean : 41.0701
Gaus Width: 2.4981



B1L002S, U7-ch68, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



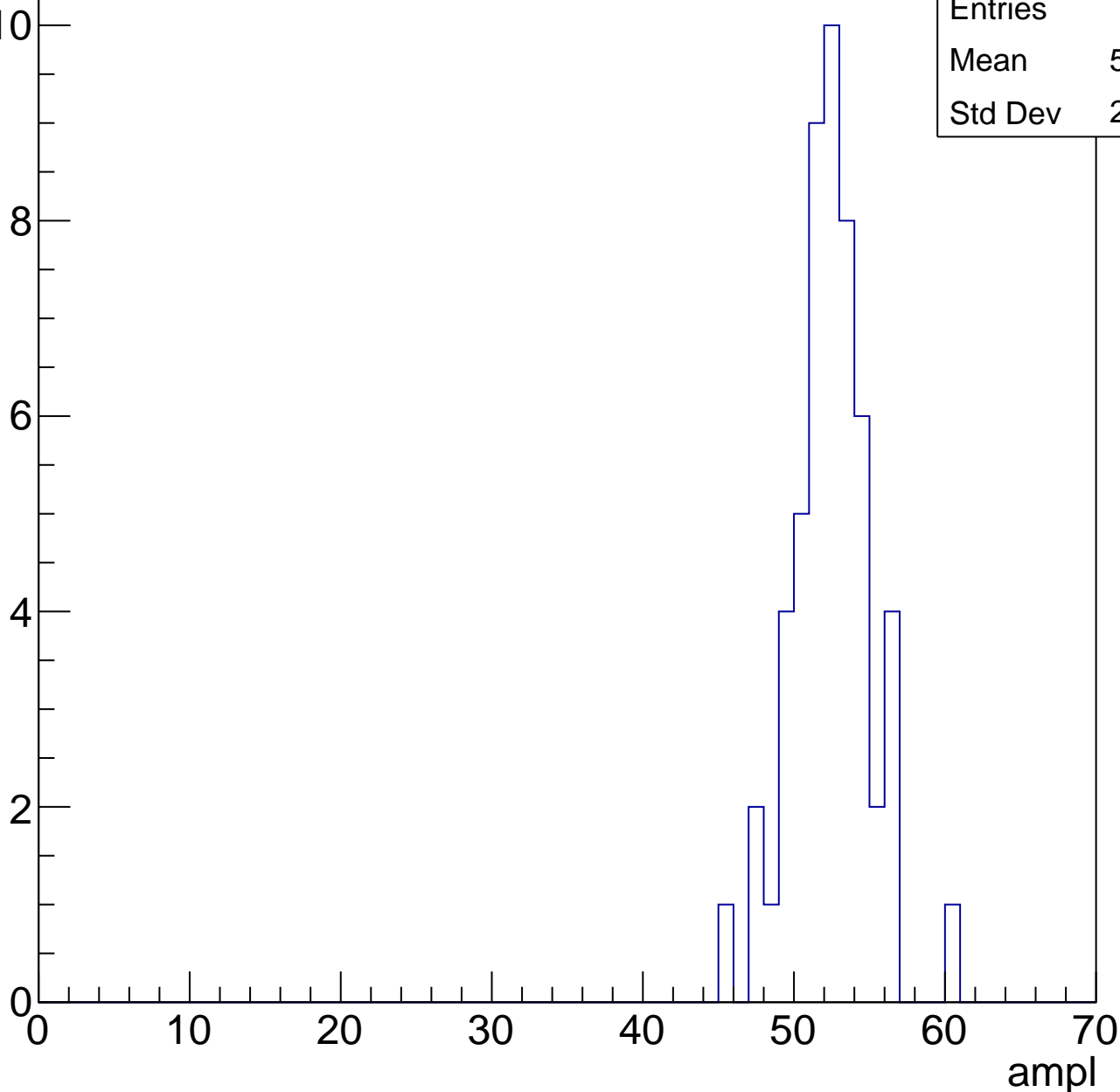
Entries	59
Mean	45.86
Std Dev	2.574

B1L002S, U7-ch68, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	51.96
Std Dev	2.599

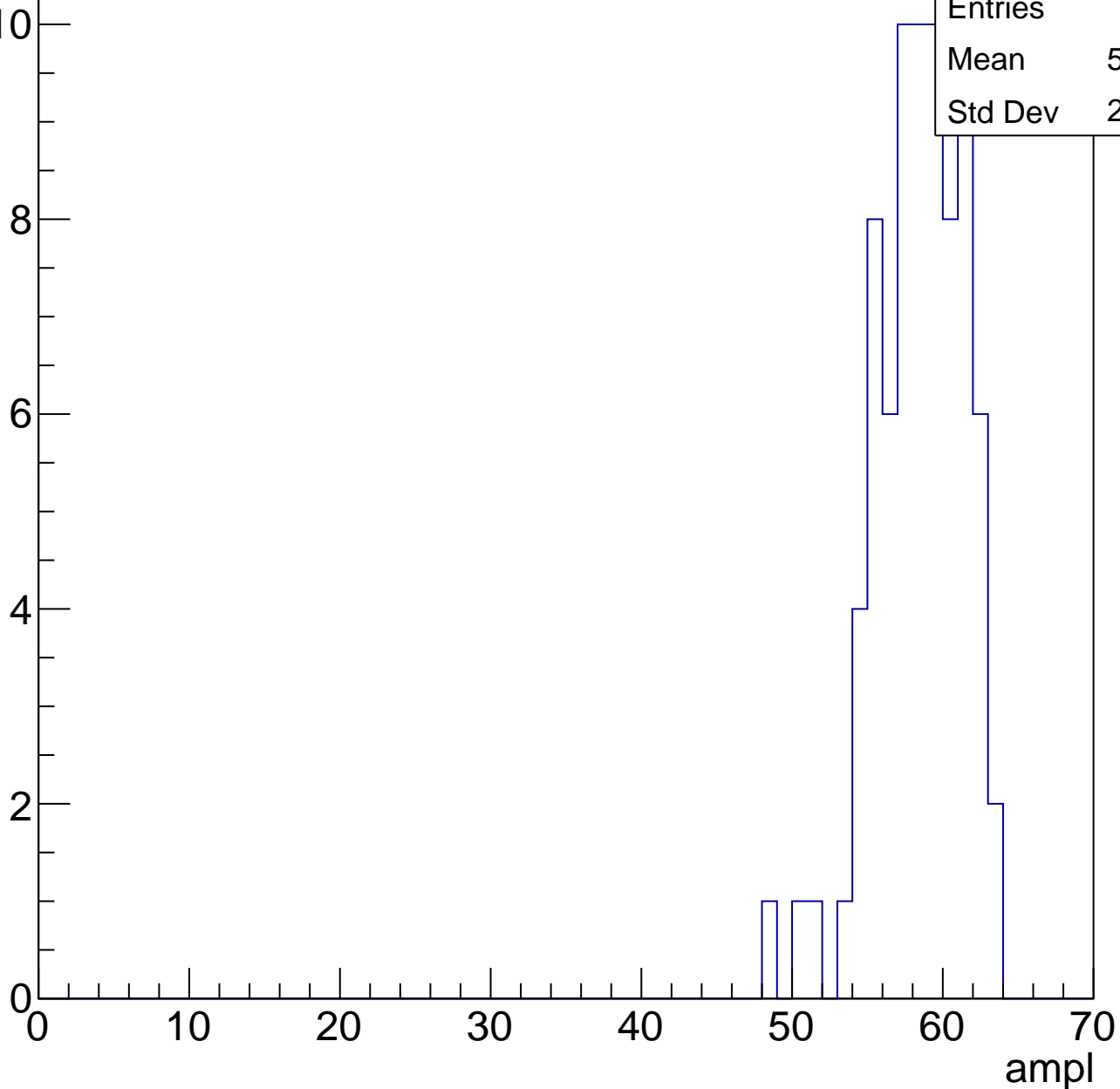


B1L002S, U7-ch68, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	57.94
Std Dev	2.964

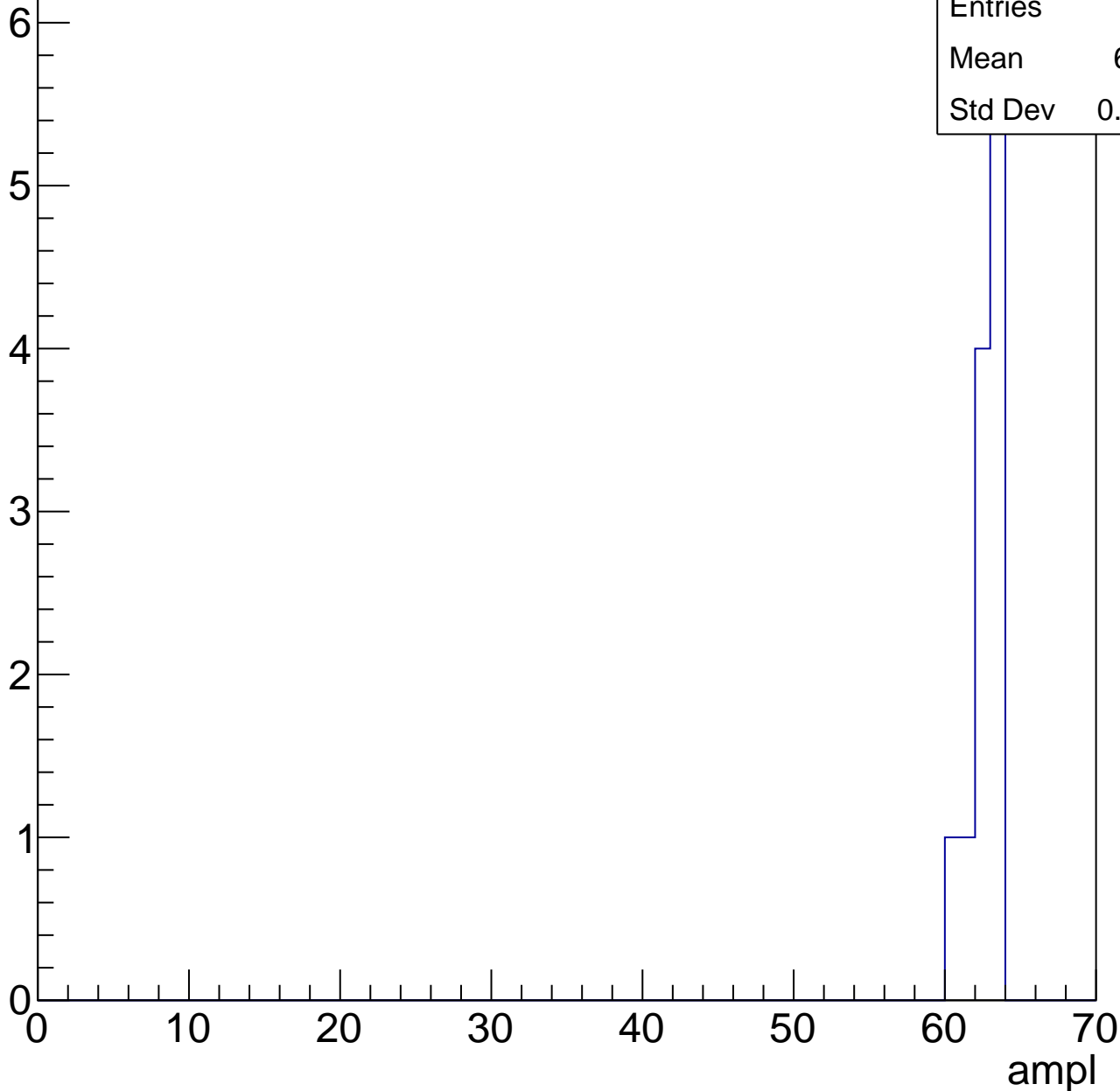


B1L002S, U7-ch68, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

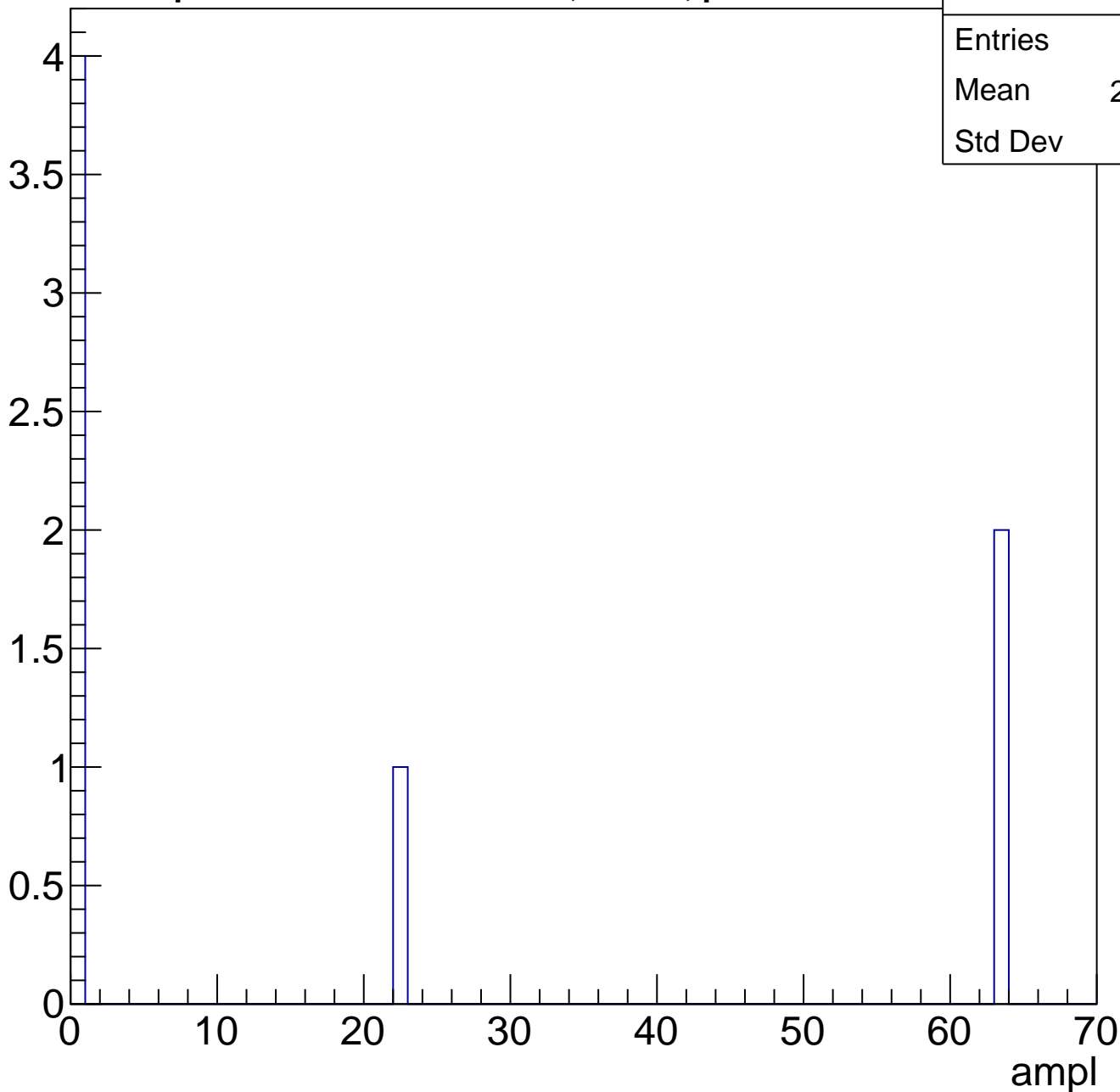
Entries	12
Mean	62.25
Std Dev	0.9242



B1L002S, U7-ch68, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	7
Mean	21.14
Std Dev	27.5

B1L002S, U7-ch69, adc0

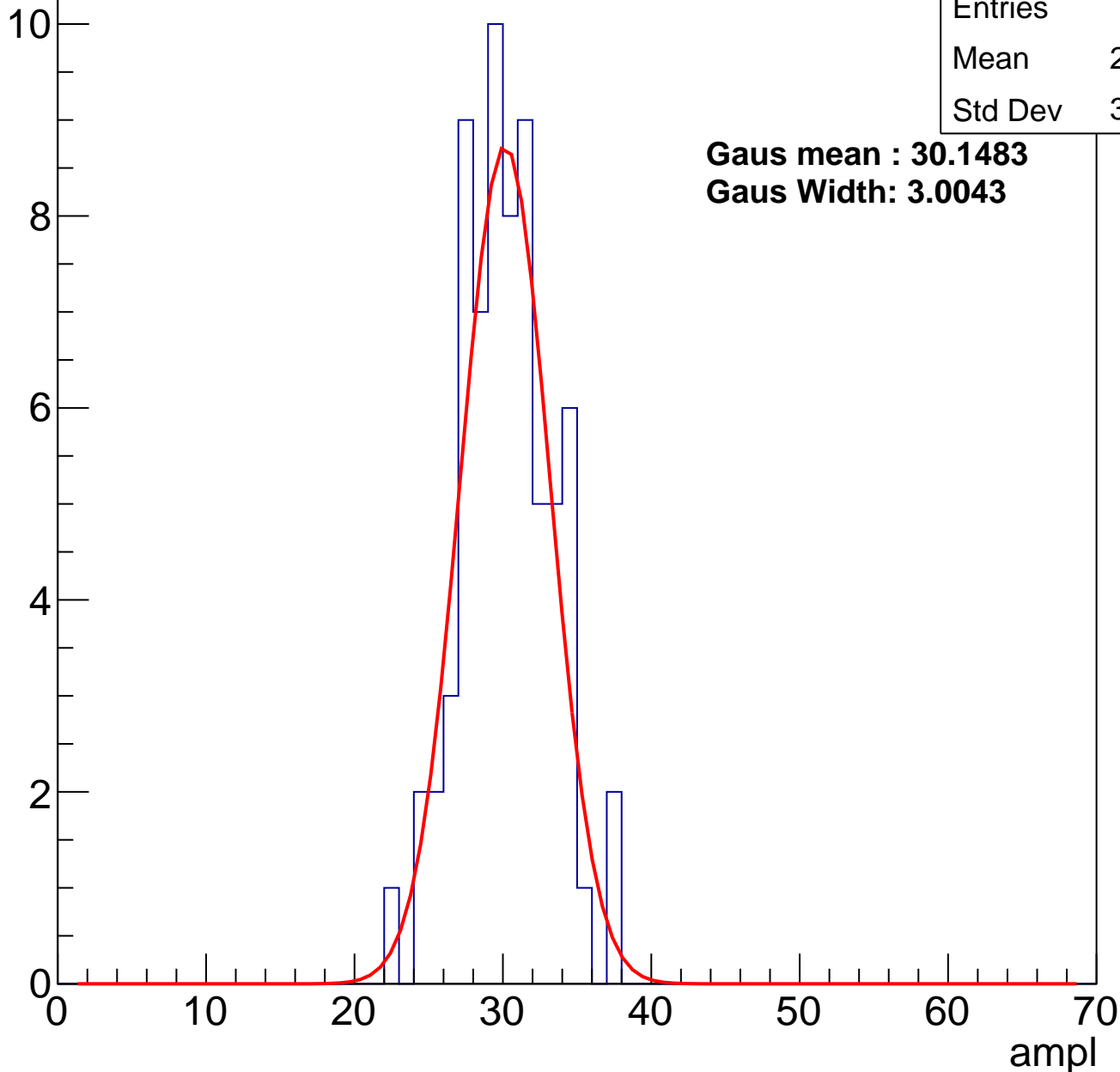
calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	29.77
Std Dev	3.029

Gaus mean : 30.1483

Gaus Width: 3.0043

Entry



B1L002S, U7-ch69, adc1

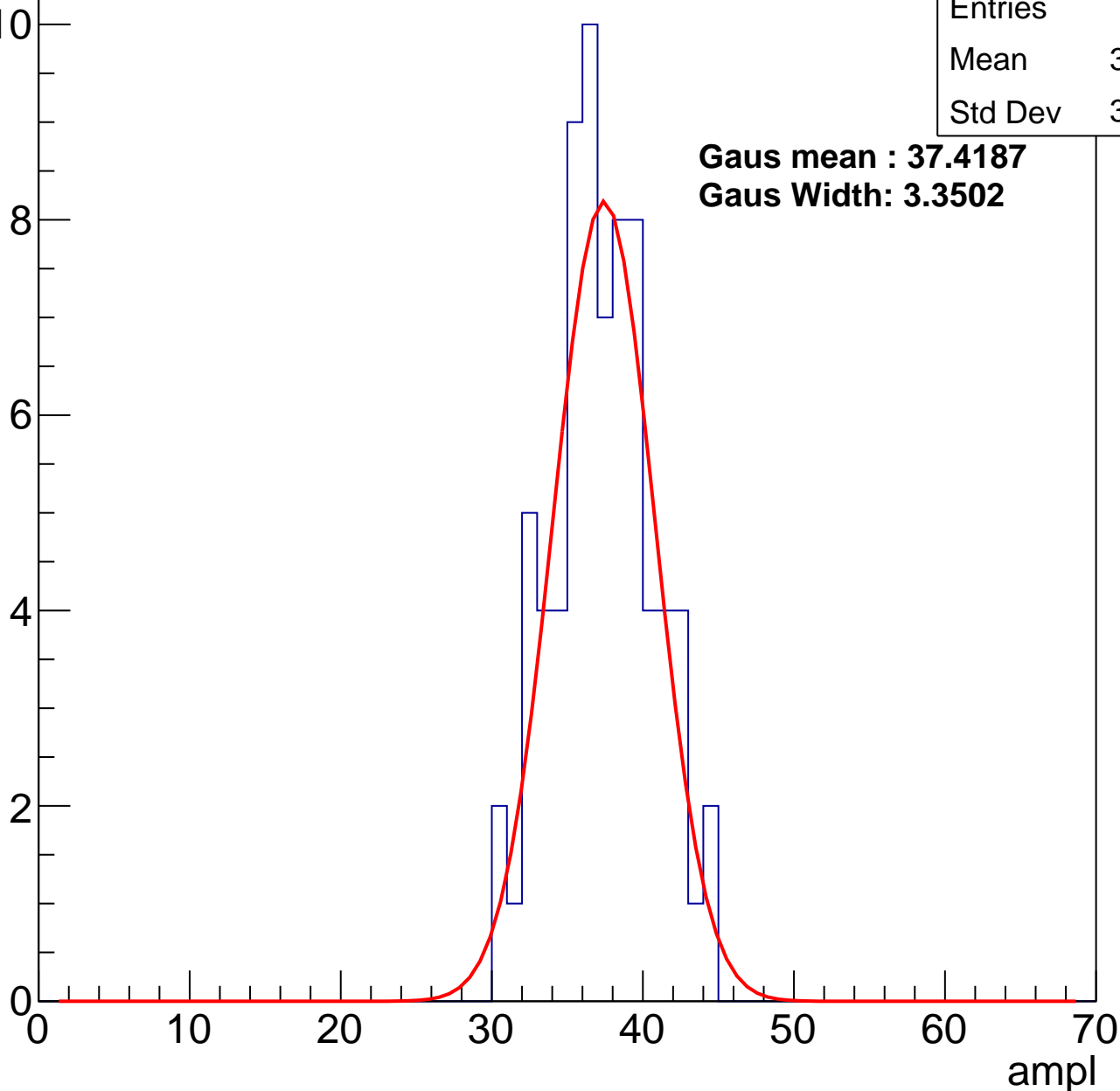
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.88
Std Dev	3.264

Gaus mean : 37.4187

Gaus Width: 3.3502



B1L002S, U7-ch69, adc2

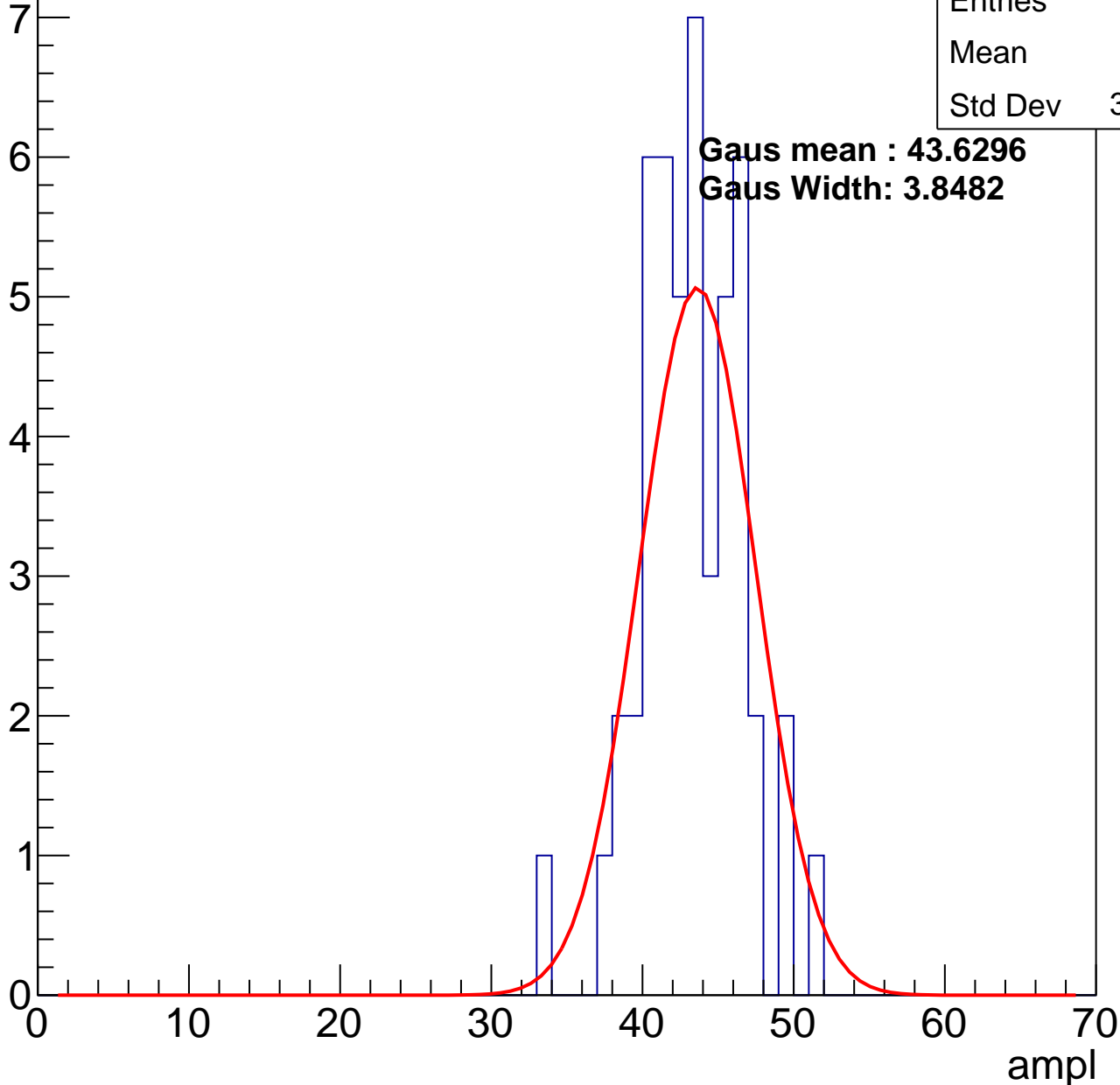
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	42.8
Std Dev	3.332

Gaus mean : 43.6296

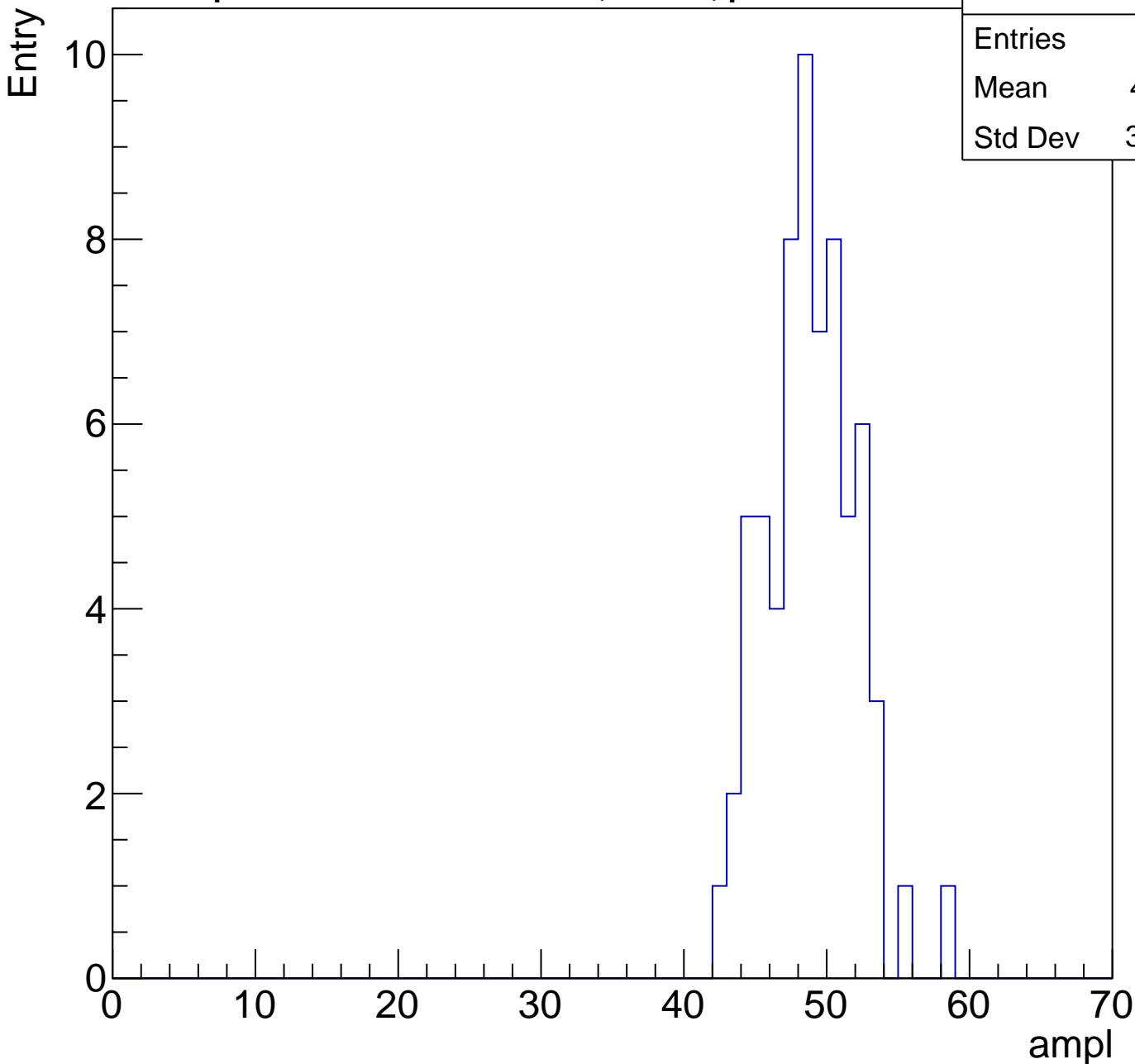
Gaus Width: 3.8482



B1L002S, U7-ch69, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	66
Mean	48.41
Std Dev	3.085



B1L002S, U7-ch69, adc4

calib_packv5_042523_0143.root, FC#10, port B3

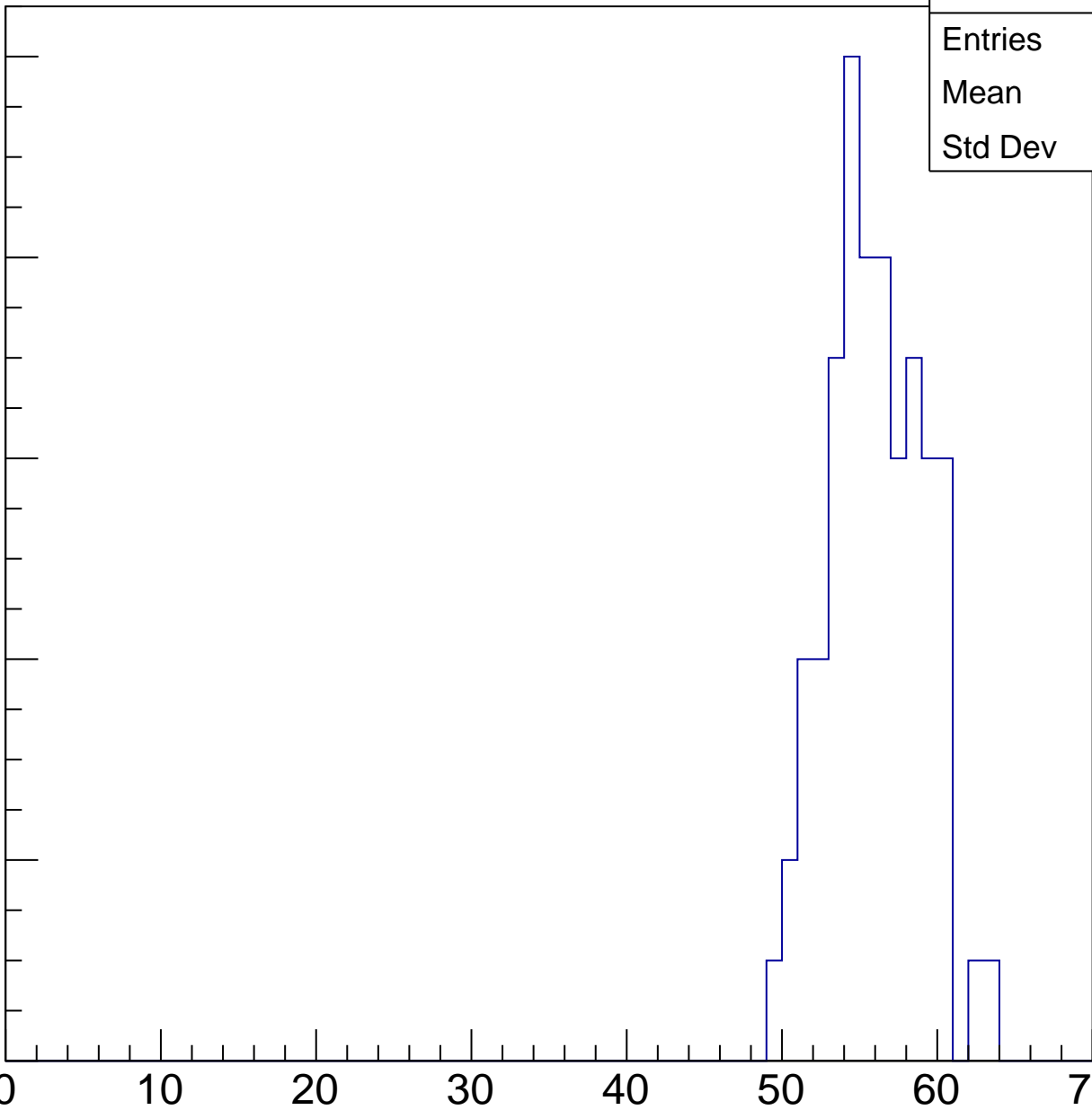
Entries	71
Mean	55.59
Std Dev	3.033

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch69, adc5

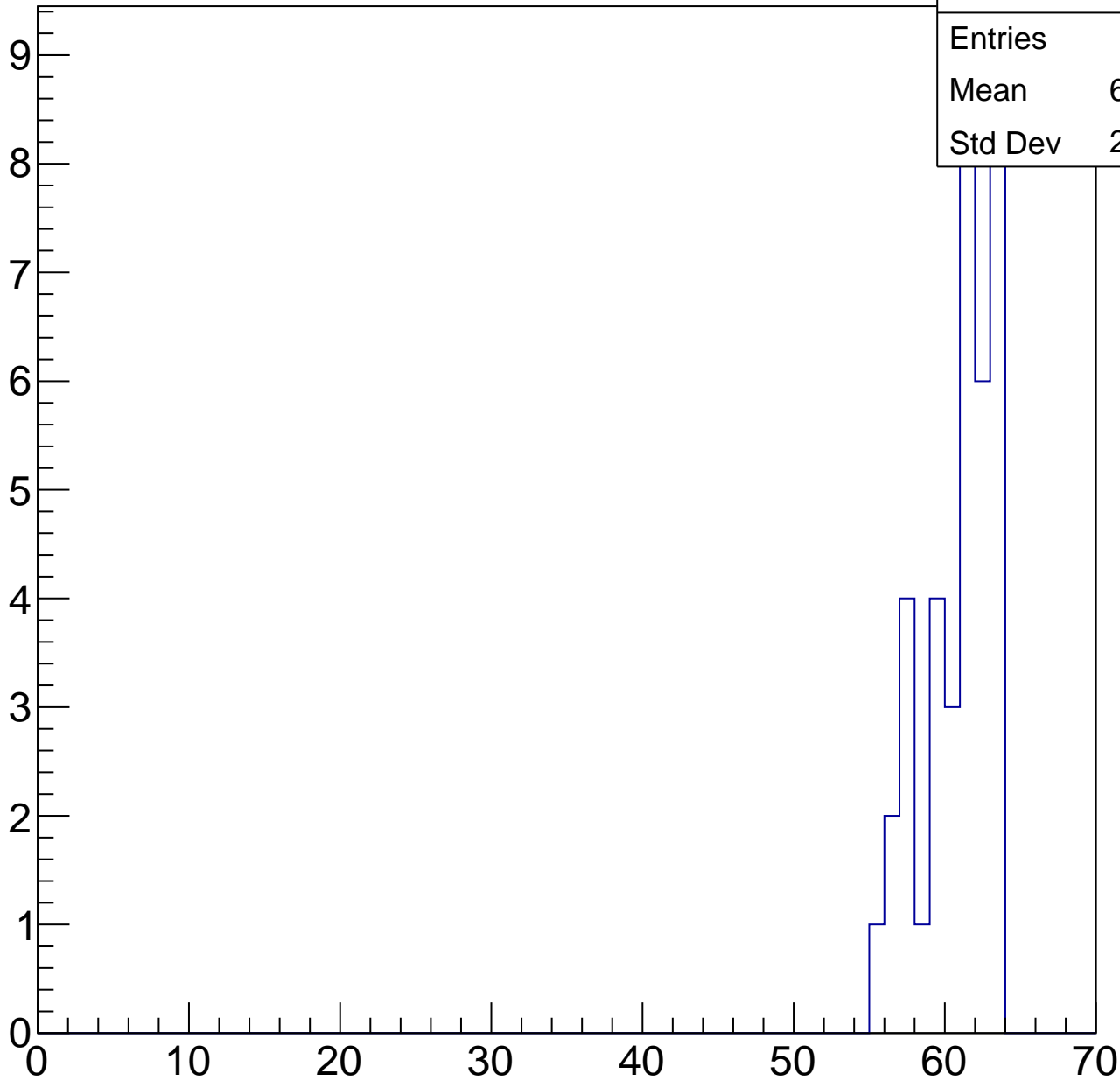
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.37
Std Dev	2.299

ampl



B1L002S, U7-ch69, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

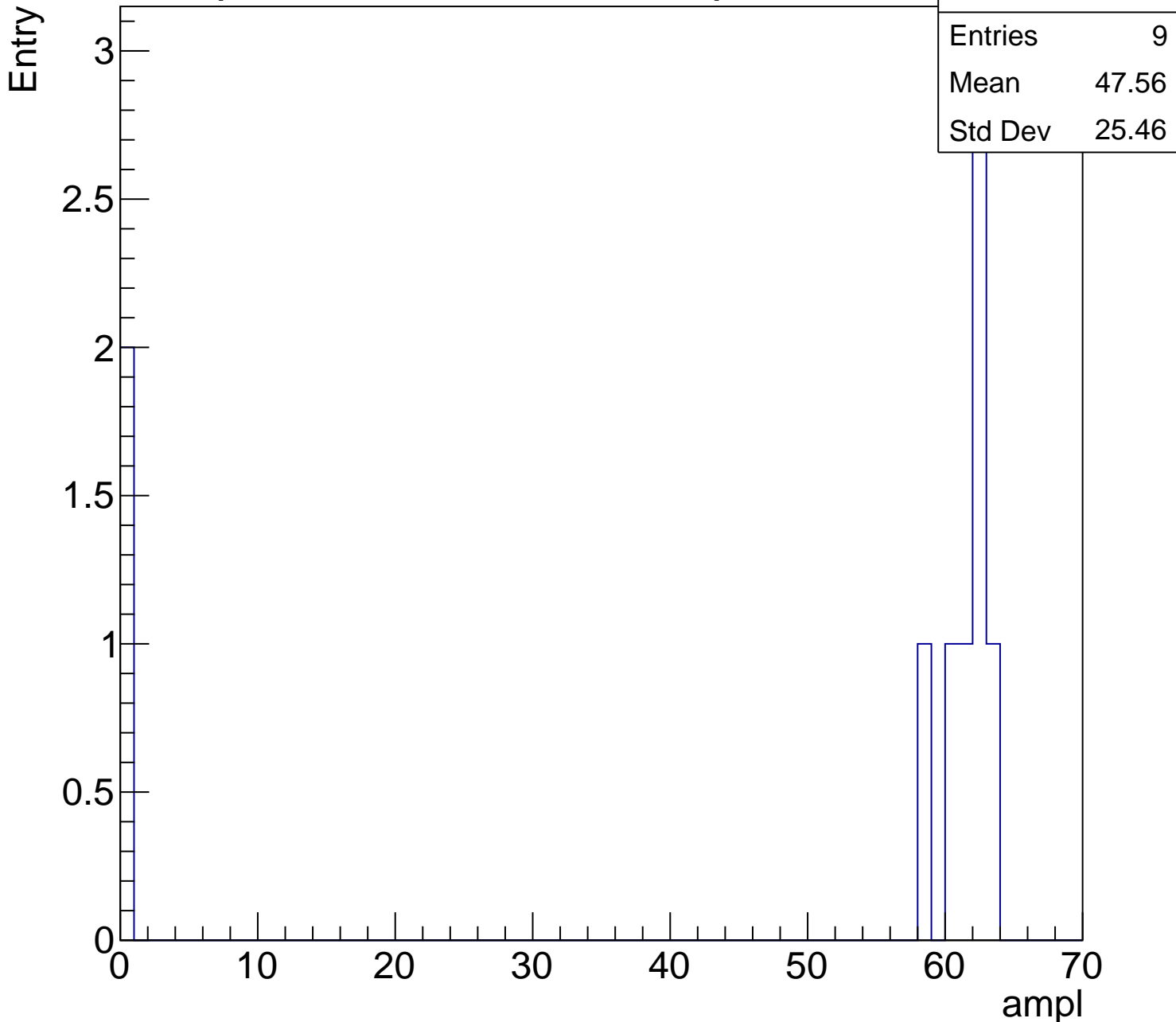
Mean

47.56

Std Dev

25.46

ampl



B1L002S, U7-ch69, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L002S, U7-ch70, adc0

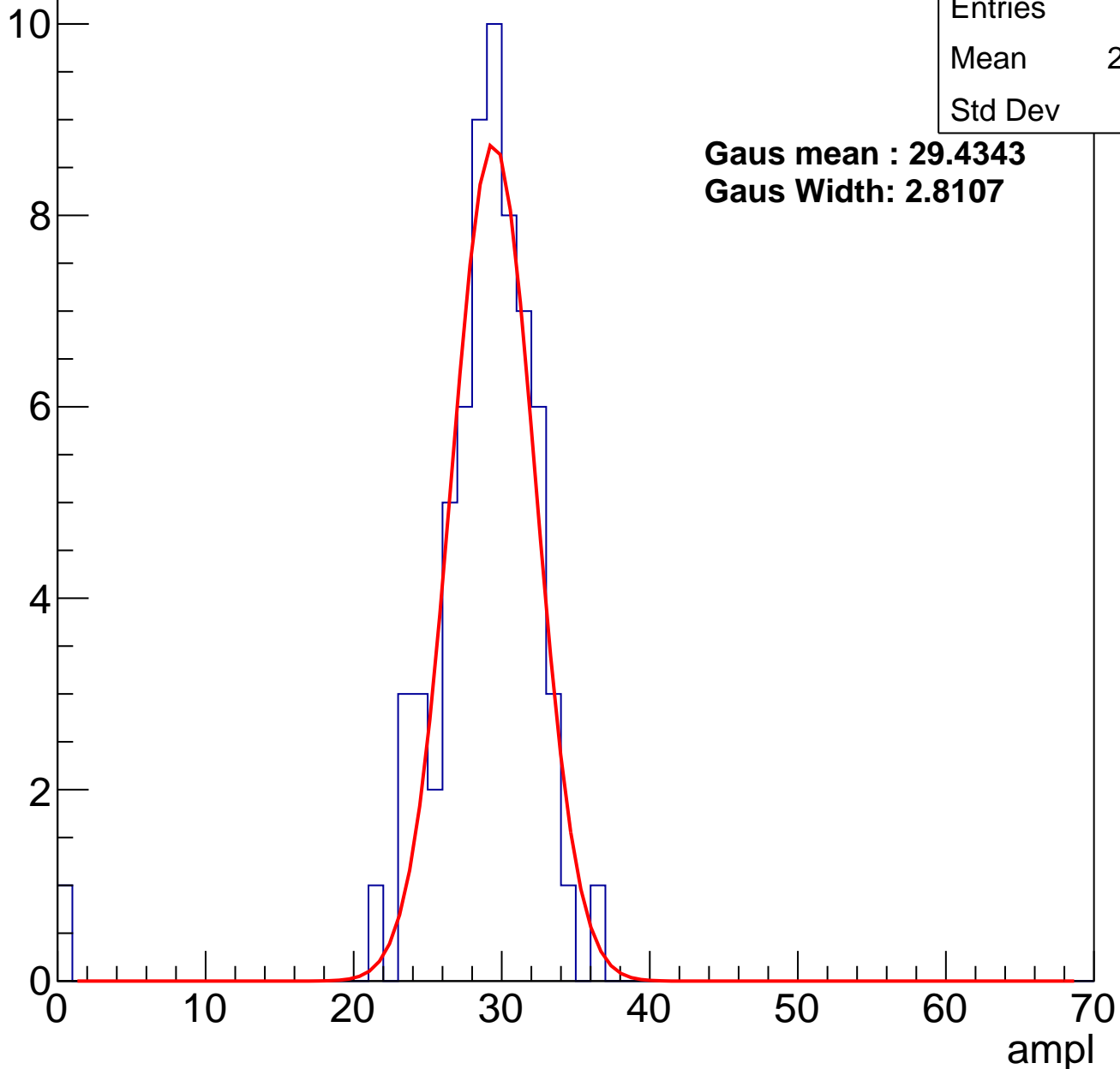
calib_packv5_042523_0143.root, FC#10, port B3

Entries	66
Mean	28.24
Std Dev	4.56

Gaus mean : 29.4343

Gaus Width: 2.8107

Entry



B1L002S, U7-ch70, adc1

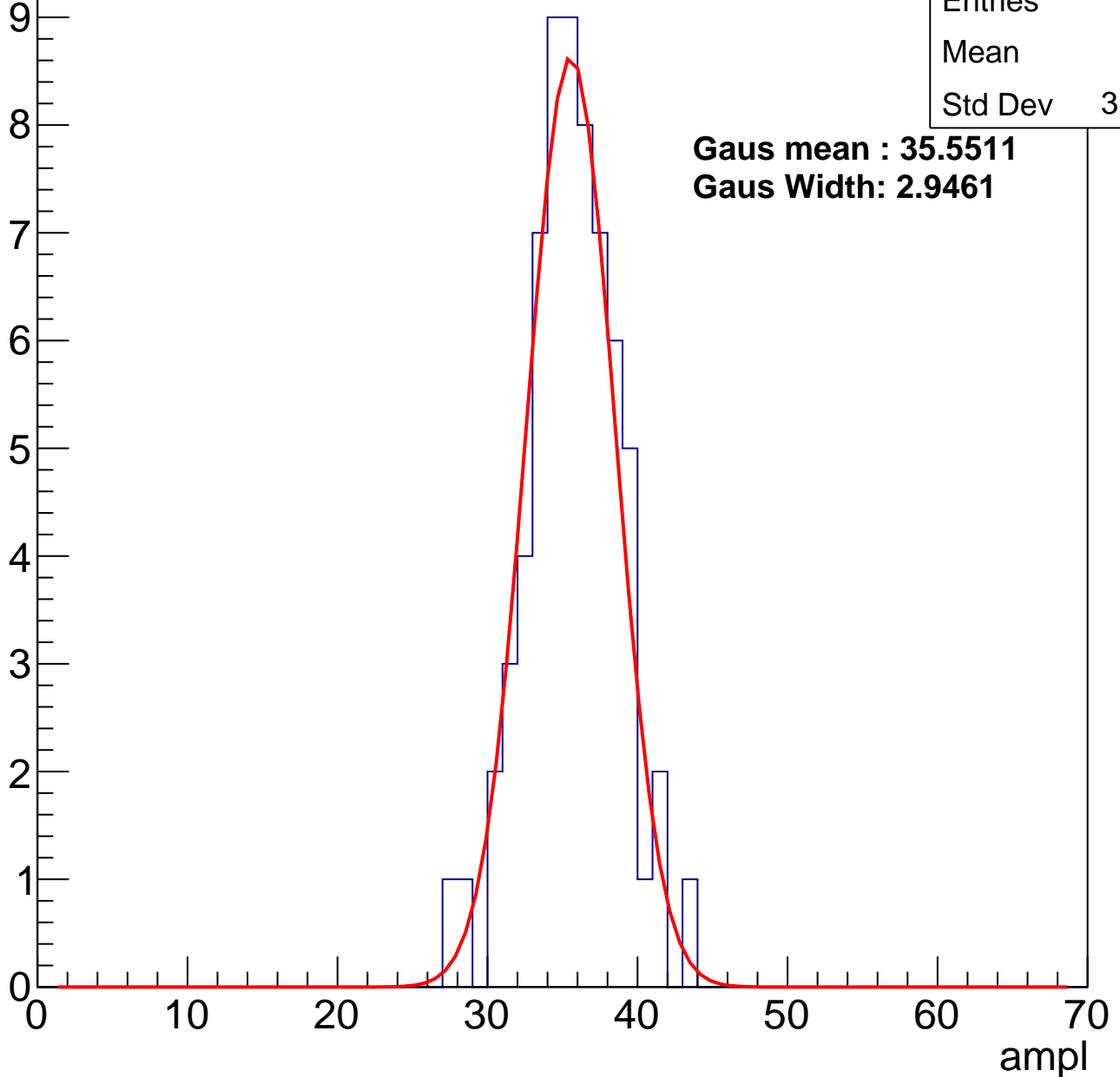
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	35.2
Std Dev	3.046

Gaus mean : 35.5511

Gaus Width: 2.9461



B1L002S, U7-ch70, adc2

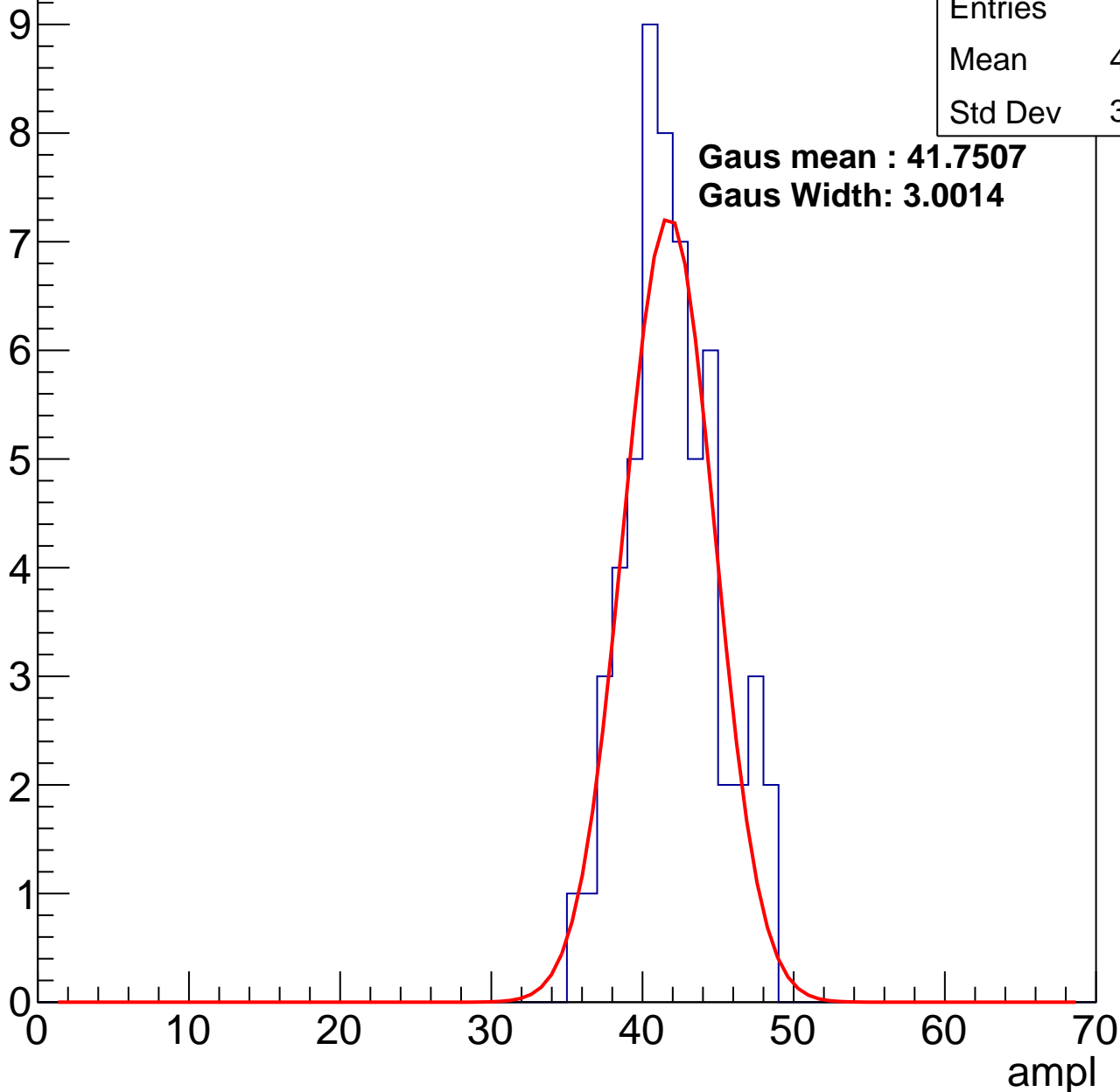
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.53
Std Dev	3.013

Gaus mean : 41.7507

Gaus Width: 3.0014

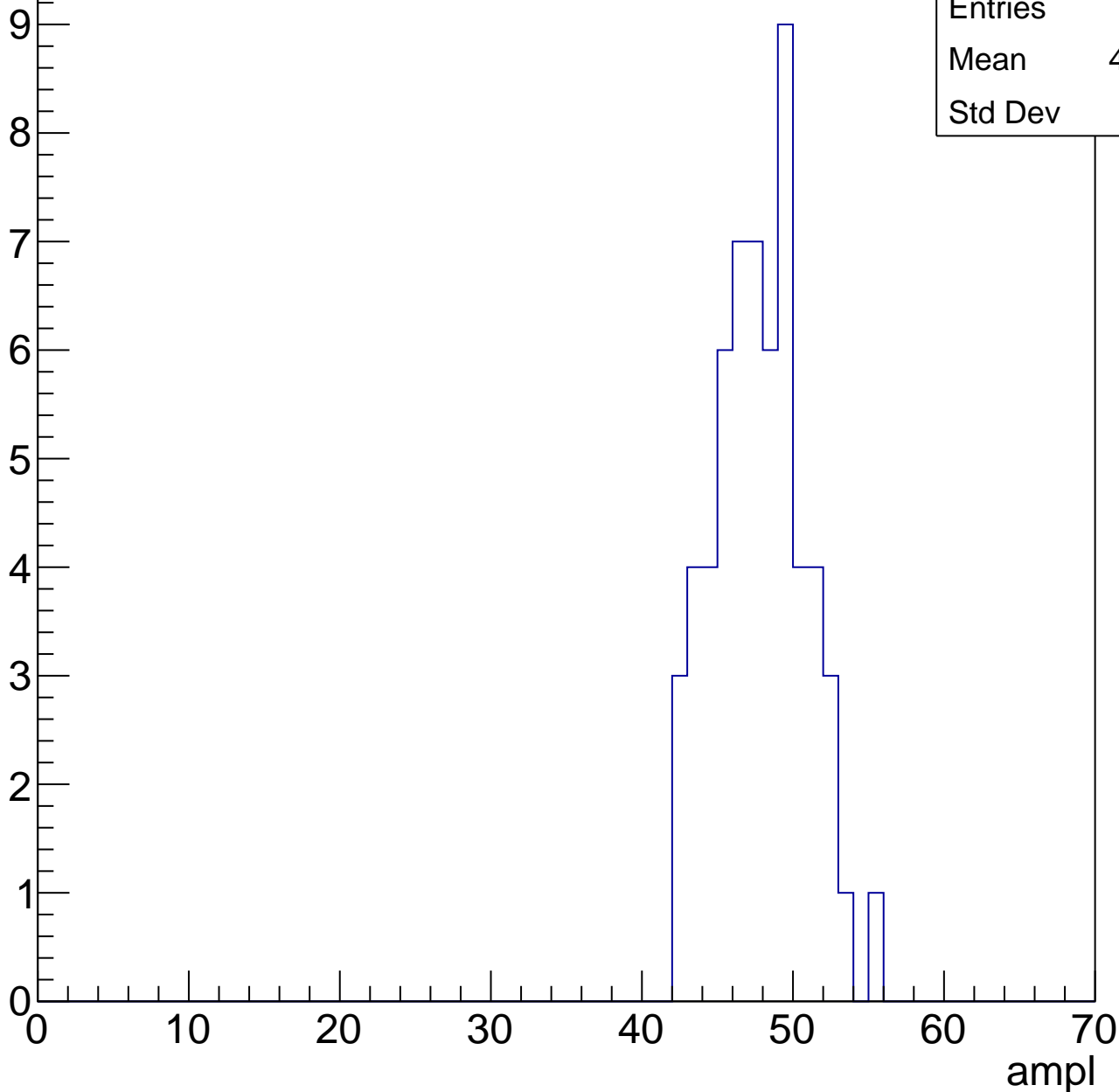


B1L002S, U7-ch70, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.32
Std Dev	2.96

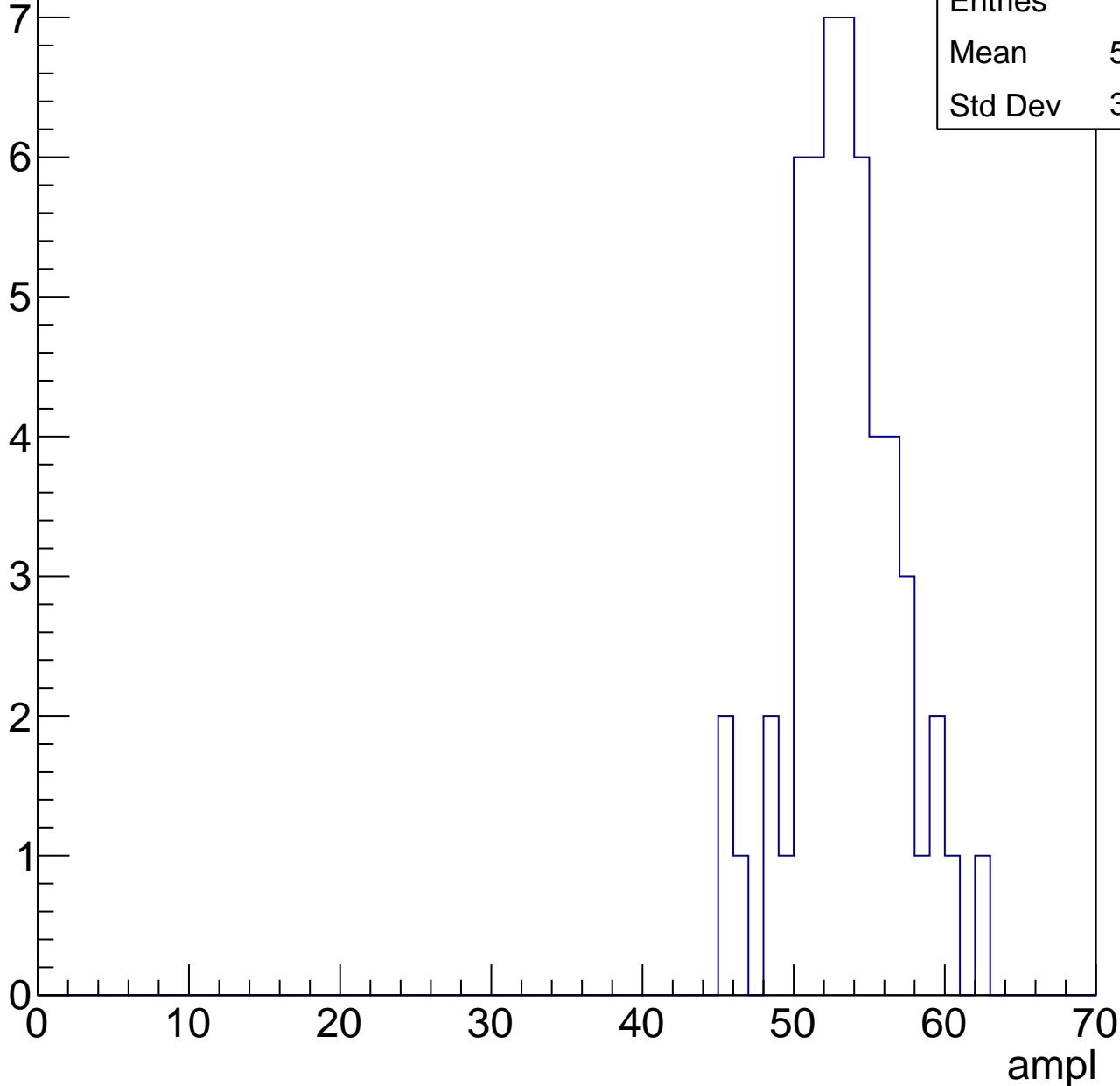


B1L002S, U7-ch70, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	52.94
Std Dev	3.498

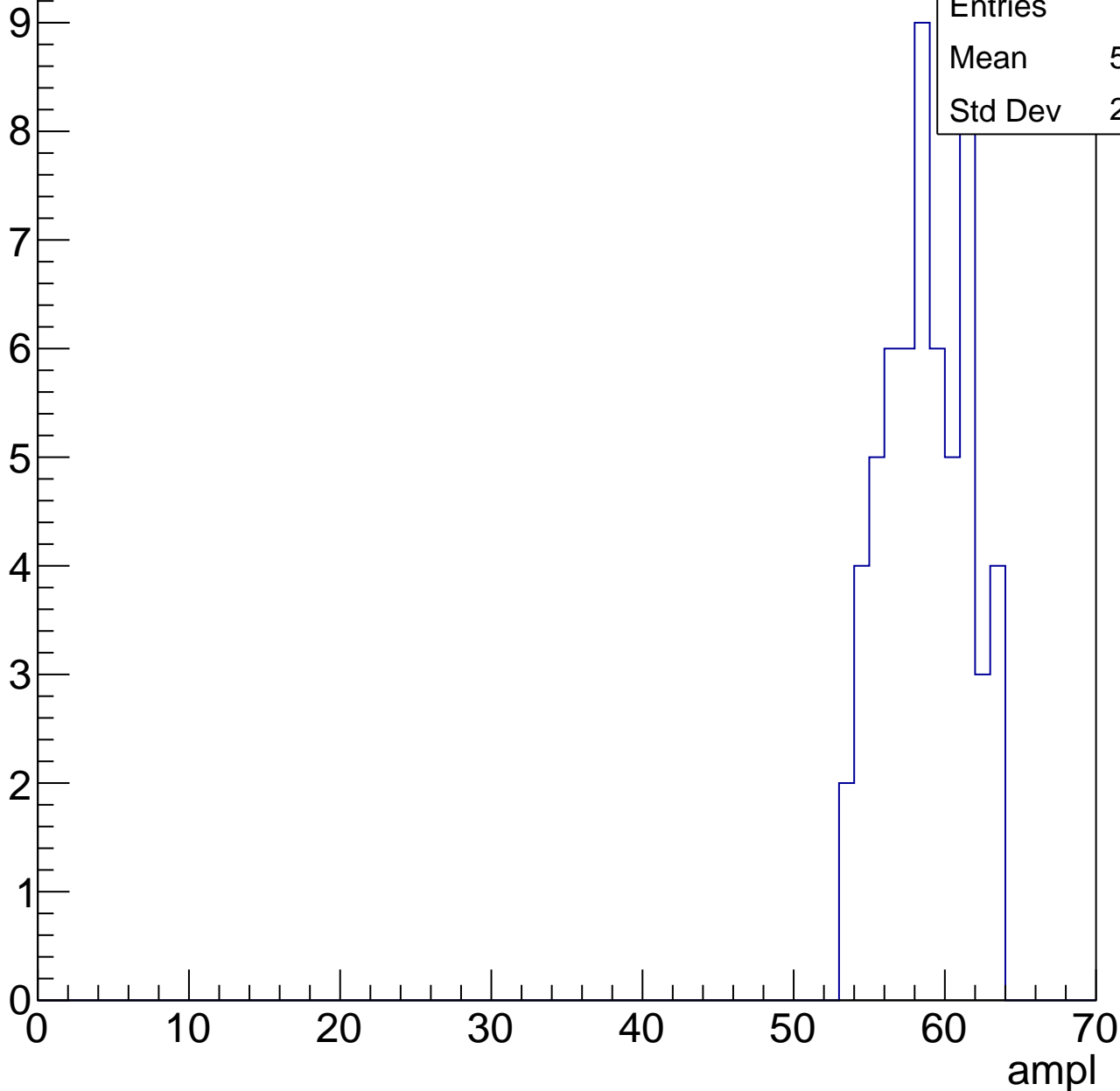


B1L002S, U7-ch70, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.22
Std Dev	2.729

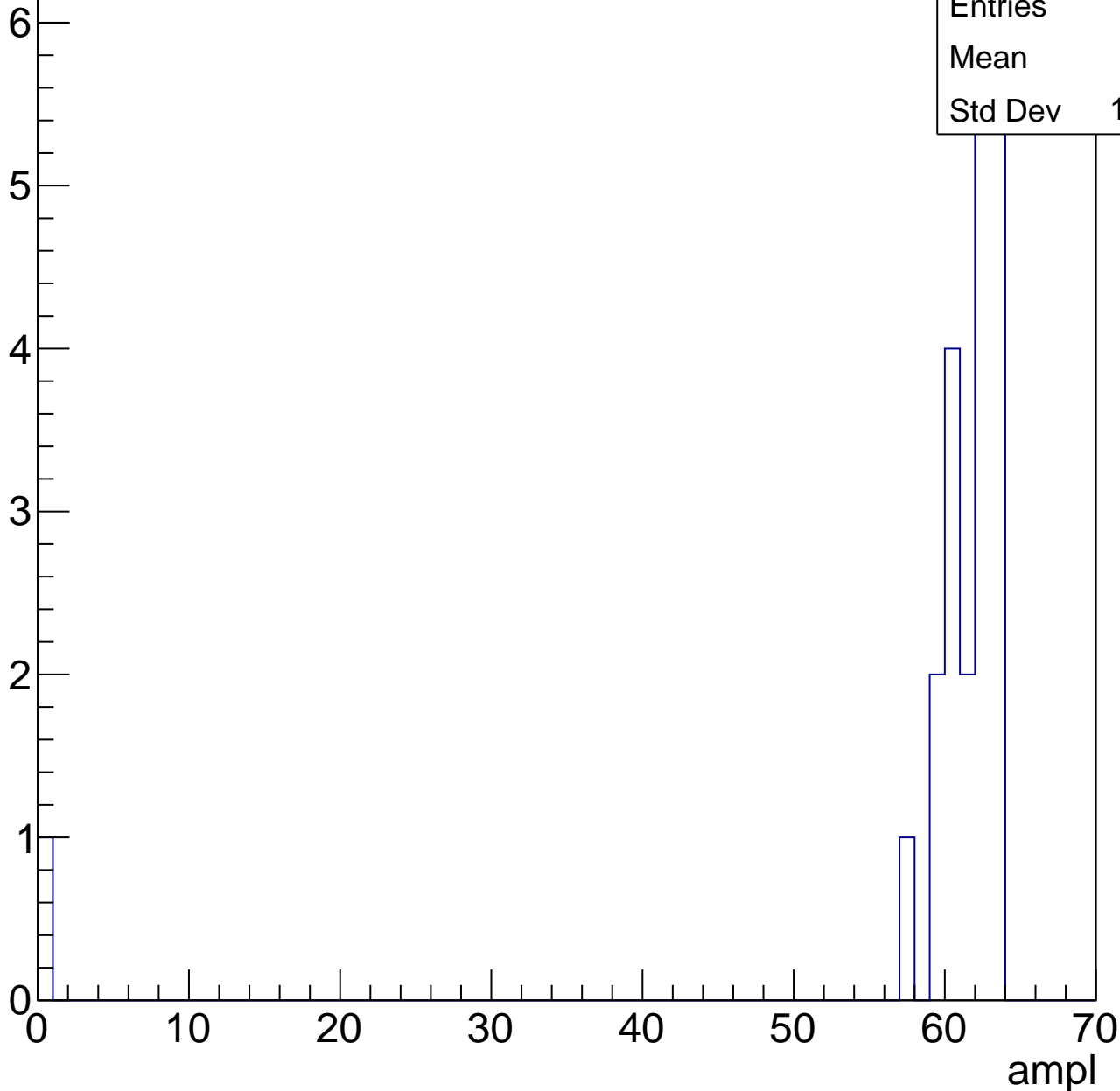


B1L002S, U7-ch70, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

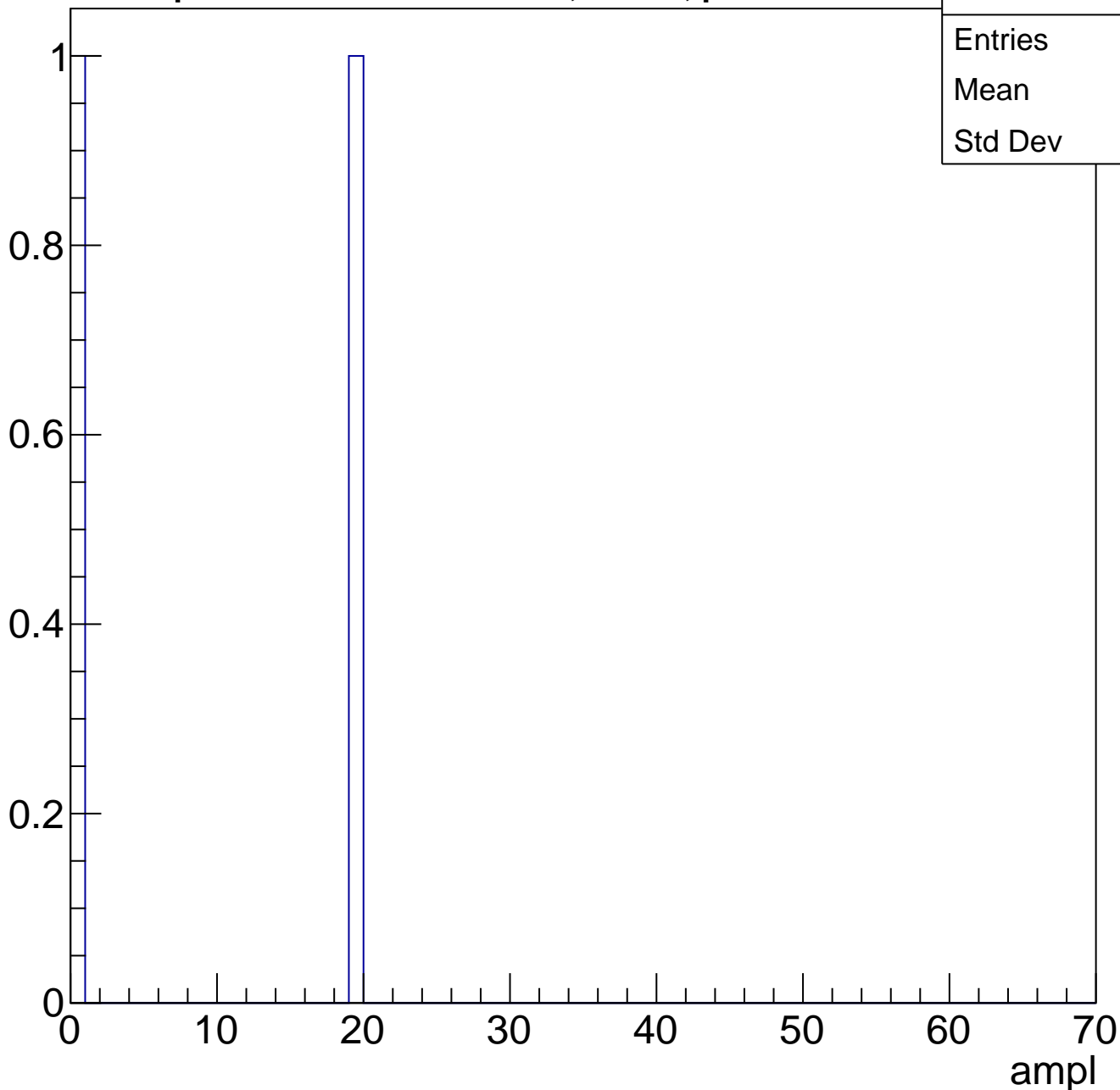
Entries	22
Mean	58.5
Std Dev	12.87



B1L002S, U7-ch70, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch71, adc0

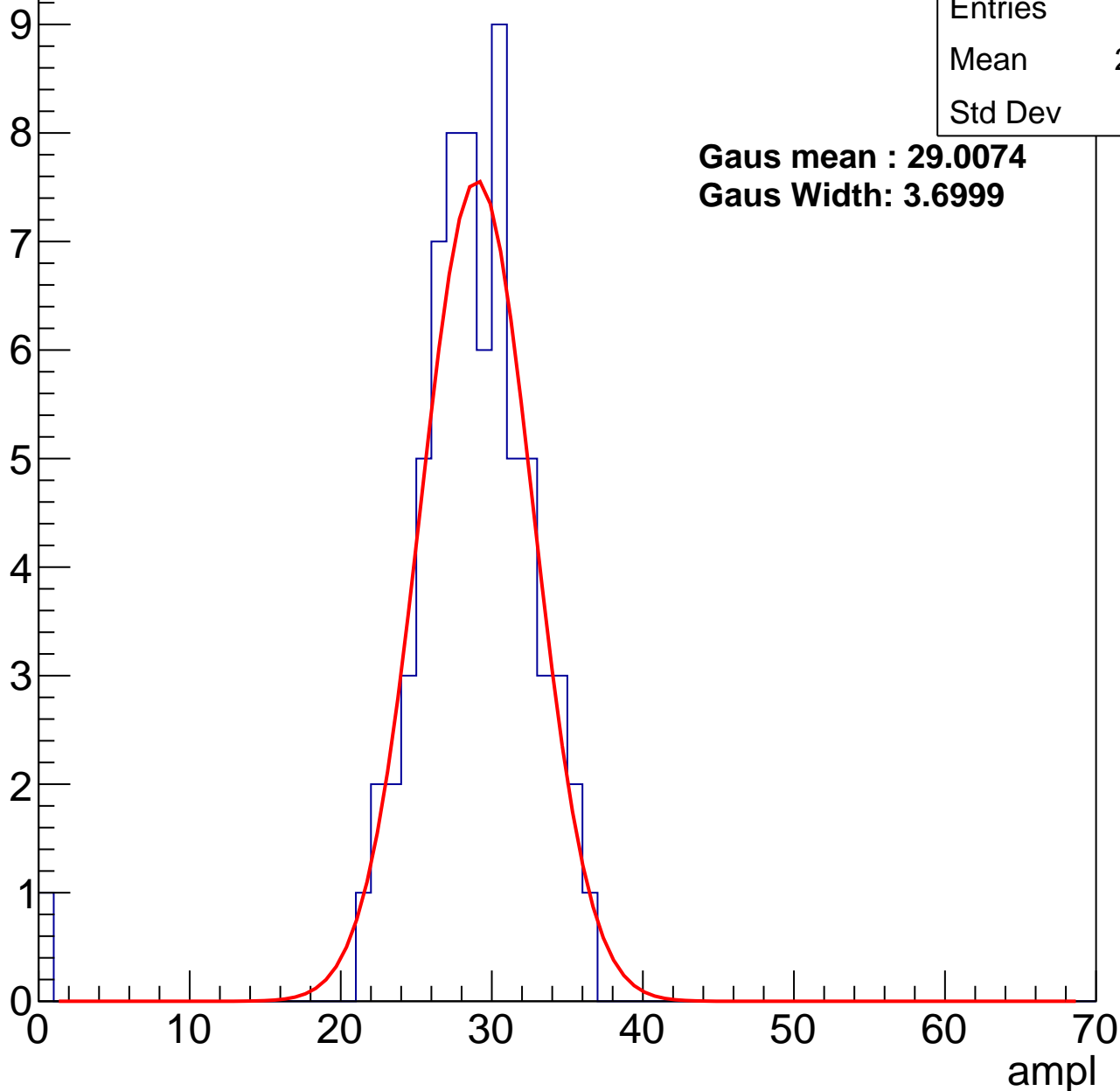
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.11
Std Dev	4.74

Gaus mean : 29.0074

Gaus Width: 3.6999



B1L002S, U7-ch71, adc1

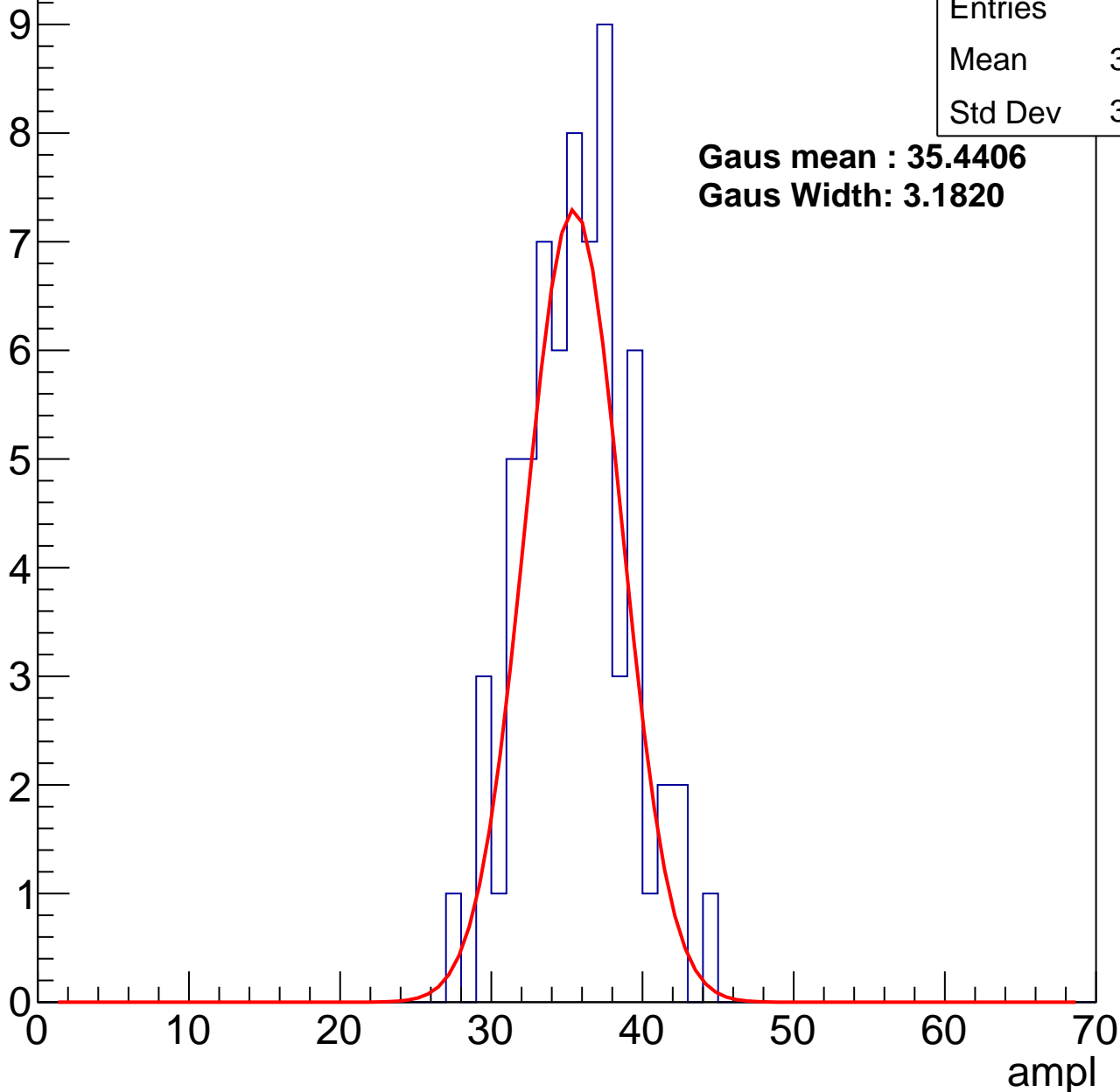
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	35.18
Std Dev	3.447

Gaus mean : 35.4406

Gaus Width: 3.1820



B1L002S, U7-ch71, adc2

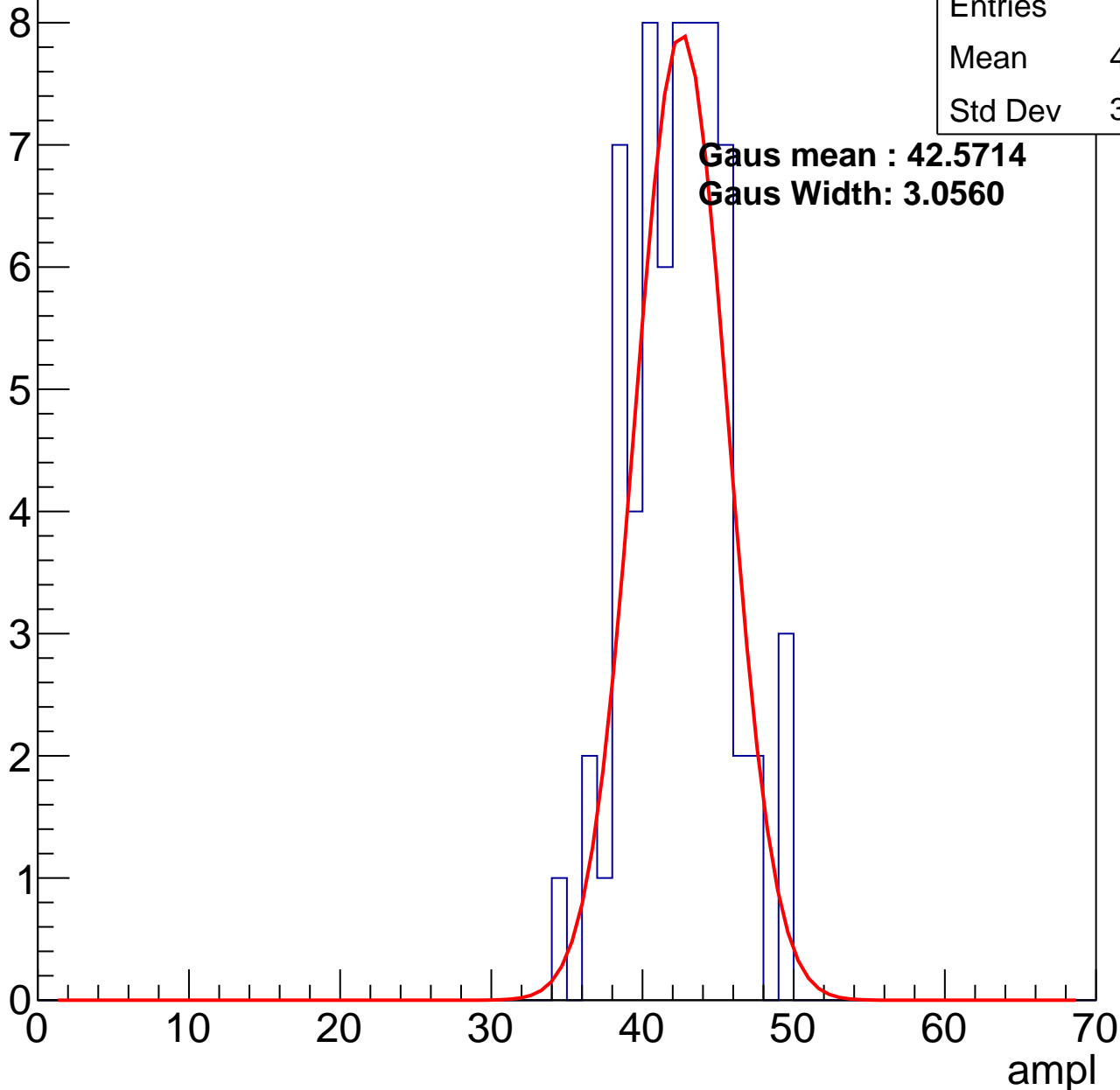
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	41.96
Std Dev	3.183

Gaus mean : 42.5714

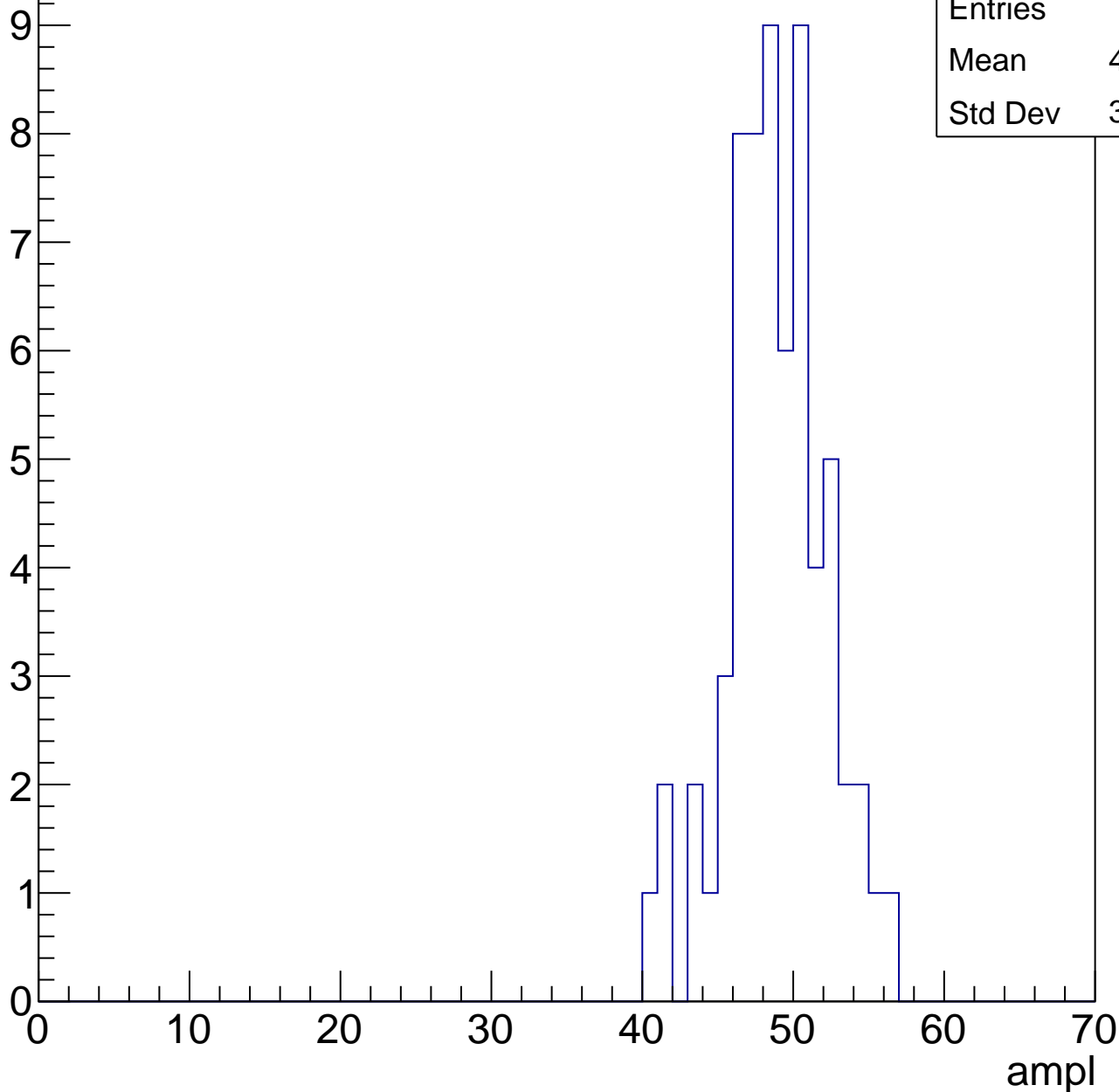
Gaus Width: 3.0560



B1L002S, U7-ch71, adc3

calib_packv5_042523_0143.root, FC#10, port B3

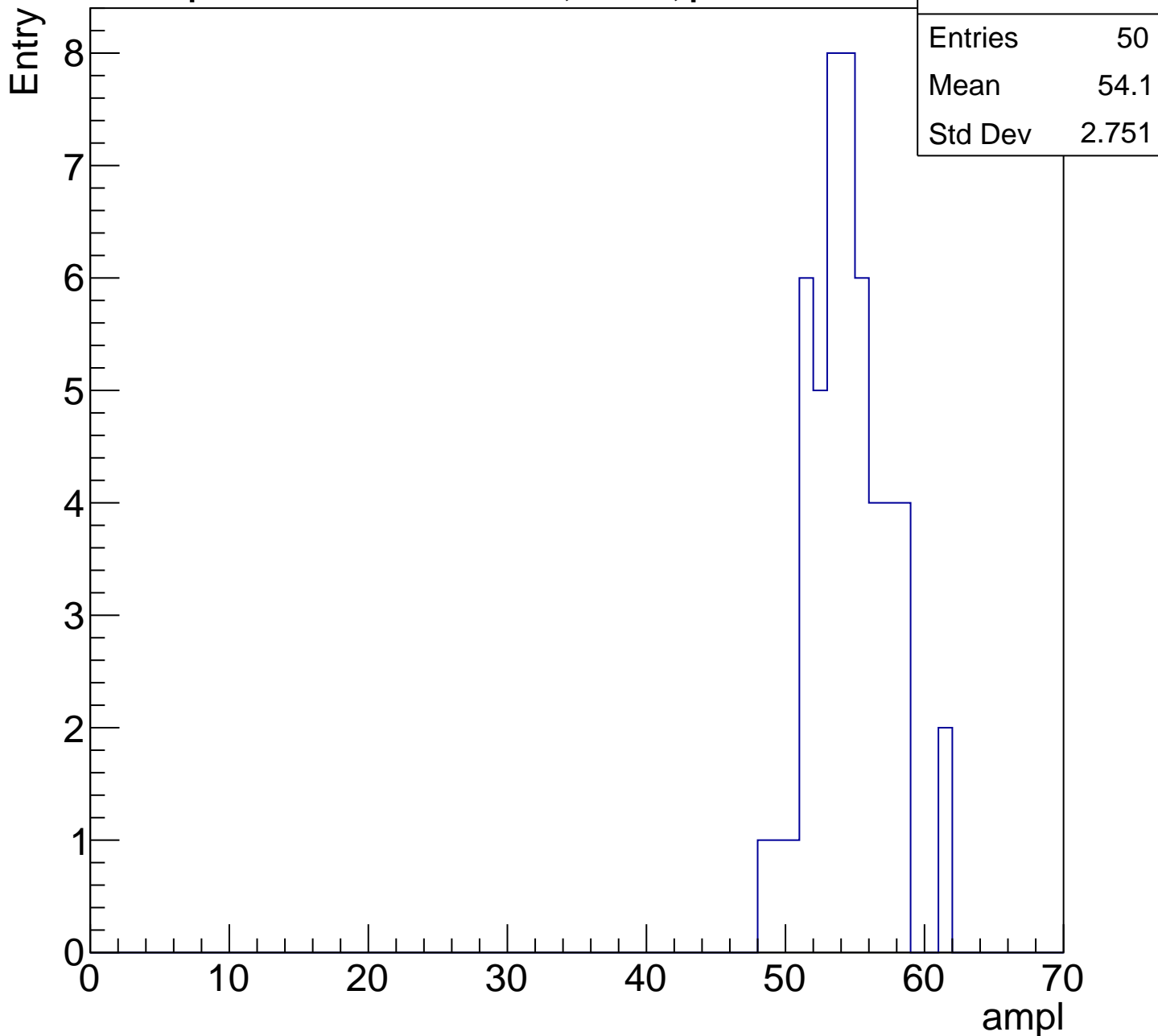
Entry



Entries	64
Mean	48.38
Std Dev	3.257

B1L002S, U7-ch71, adc4

calib_packv5_042523_0143.root, FC#10, port B3

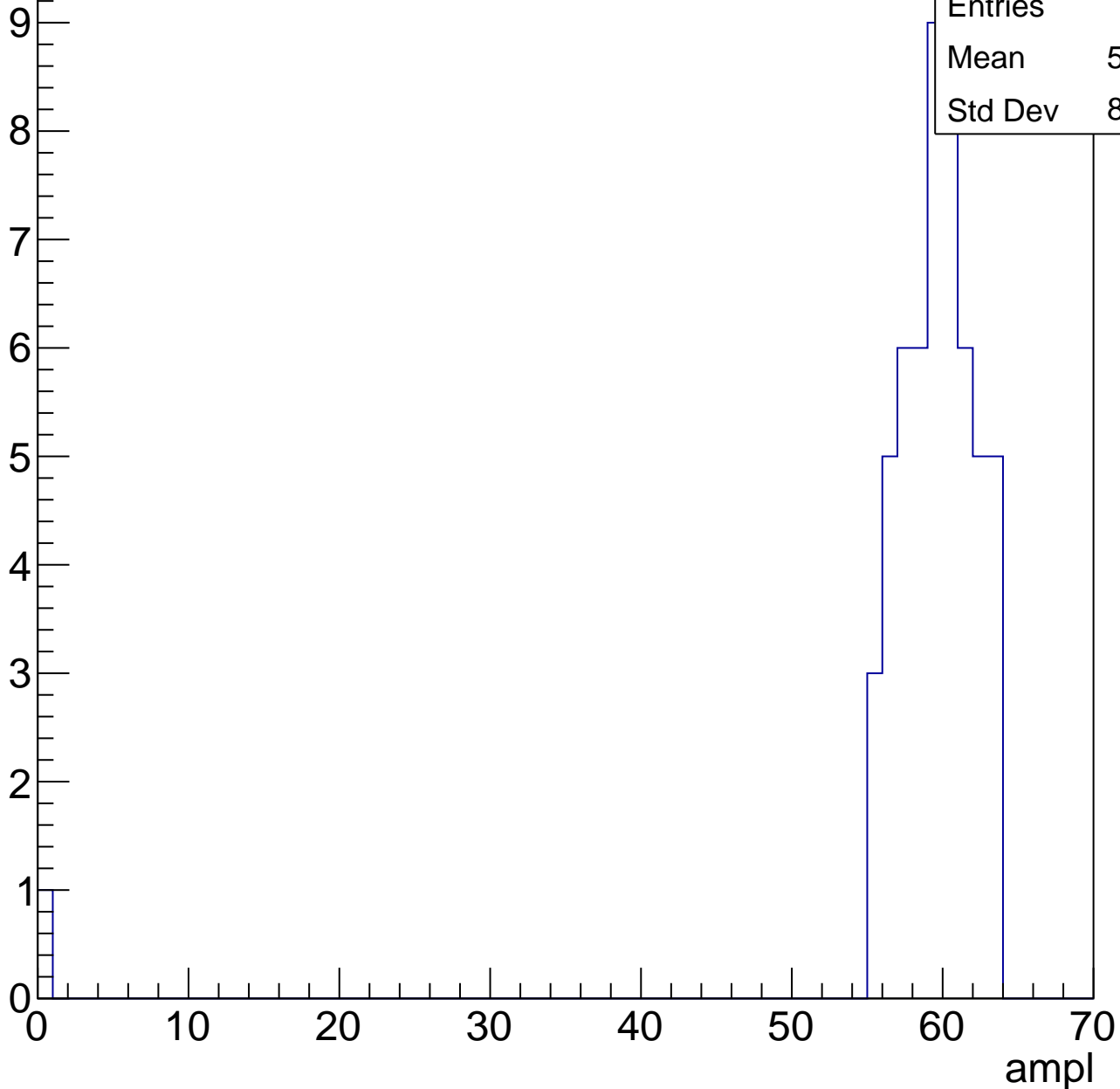


B1L002S, U7-ch71, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

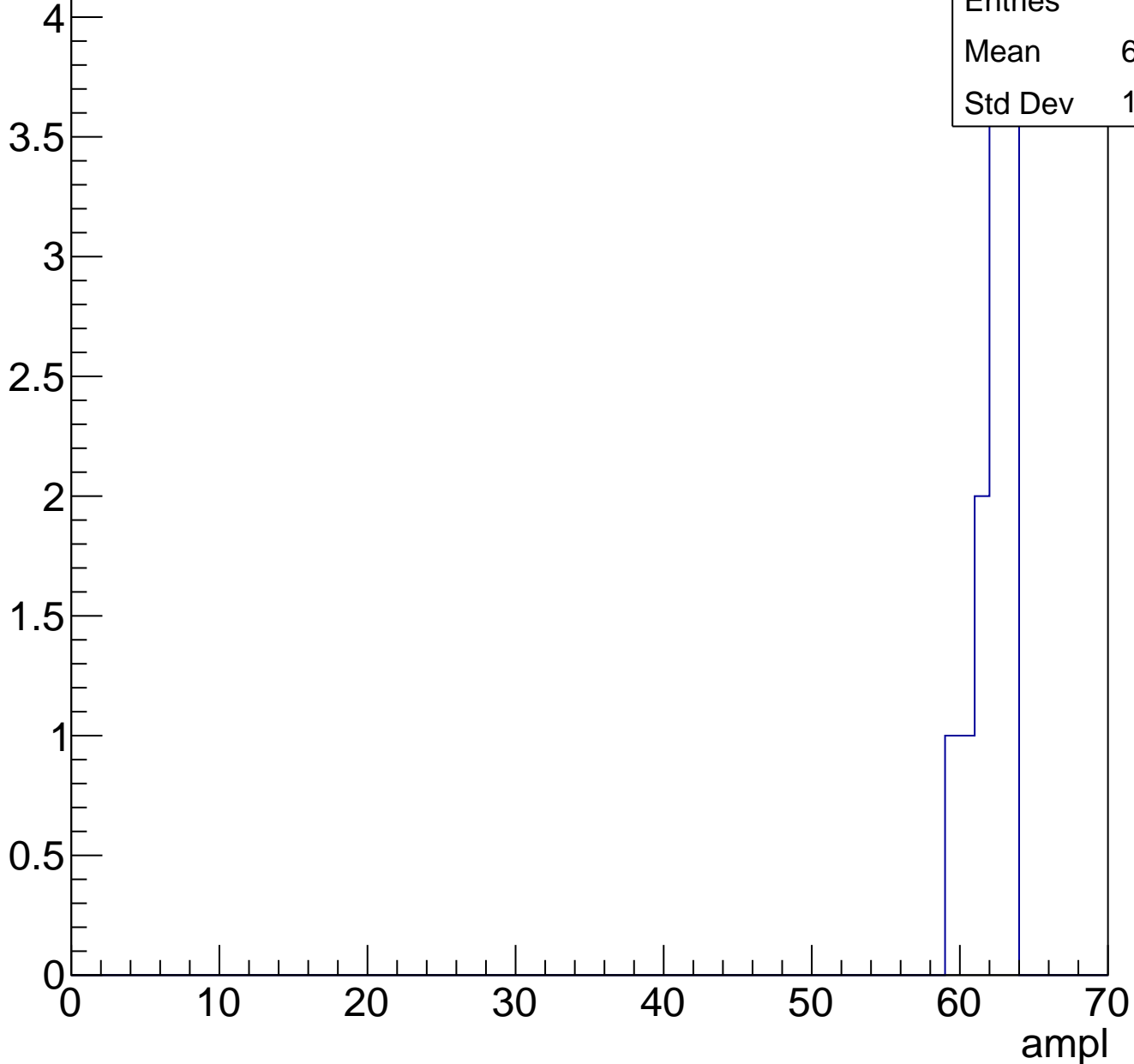
Entries	55
Mean	58.13
Std Dev	8.224



B1L002S, U7-ch71, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch71, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch72, adc0

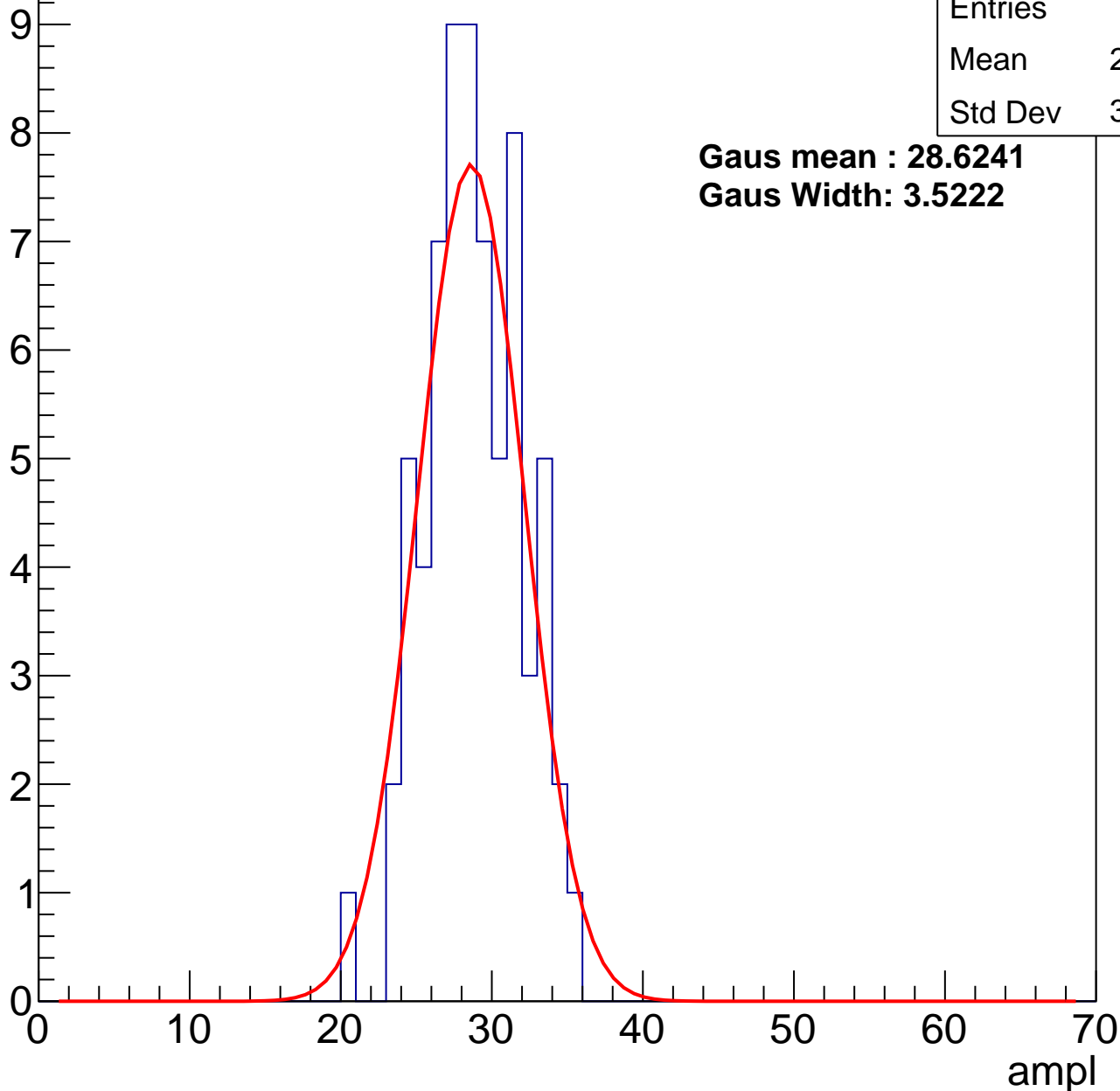
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	28.35
Std Dev	3.095

Gaus mean : 28.6241

Gaus Width: 3.5222



B1L002S, U7-ch72, adc1

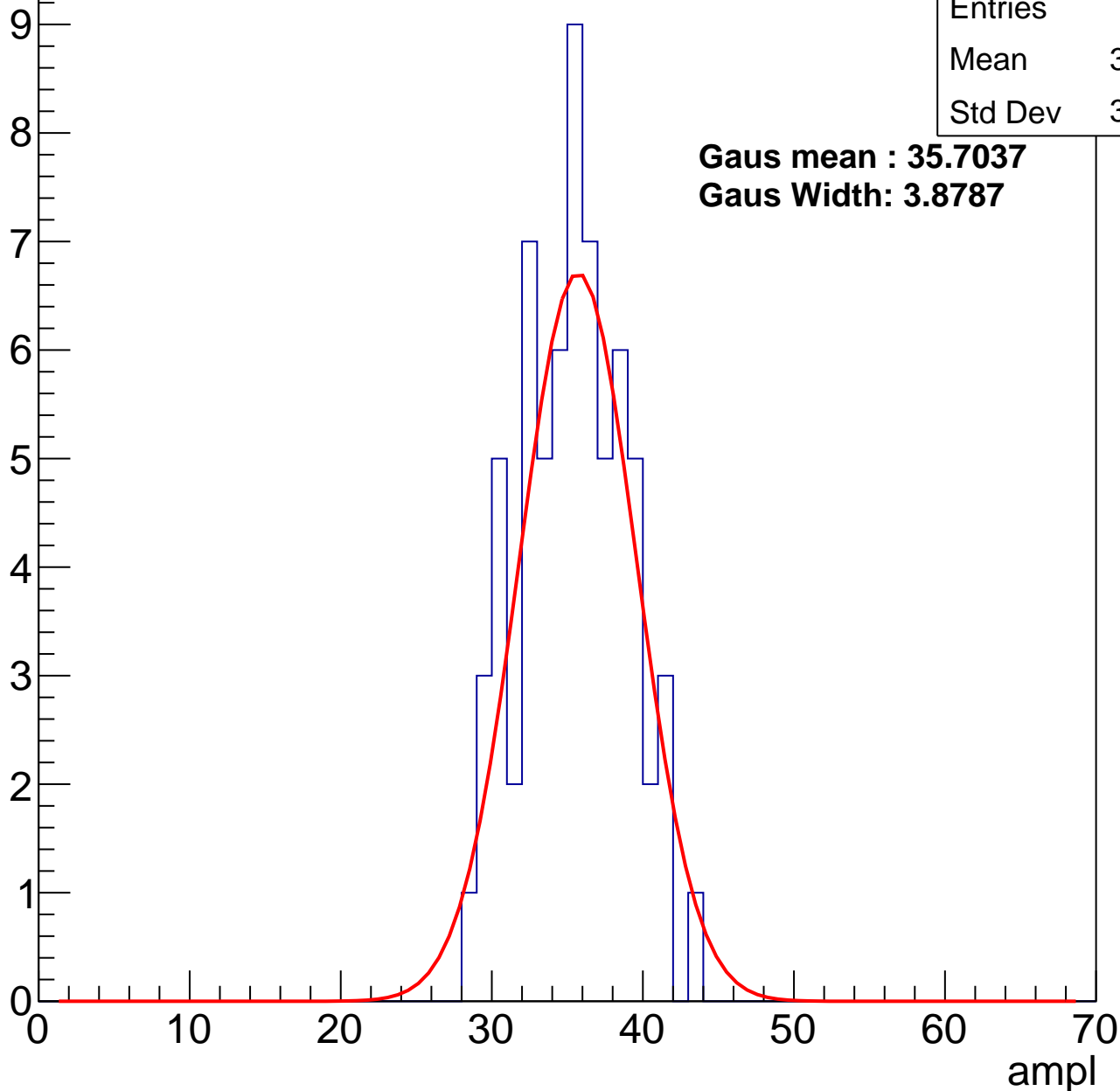
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.94
Std Dev	3.425

Gaus mean : 35.7037

Gaus Width: 3.8787



B1L002S, U7-ch72, adc2

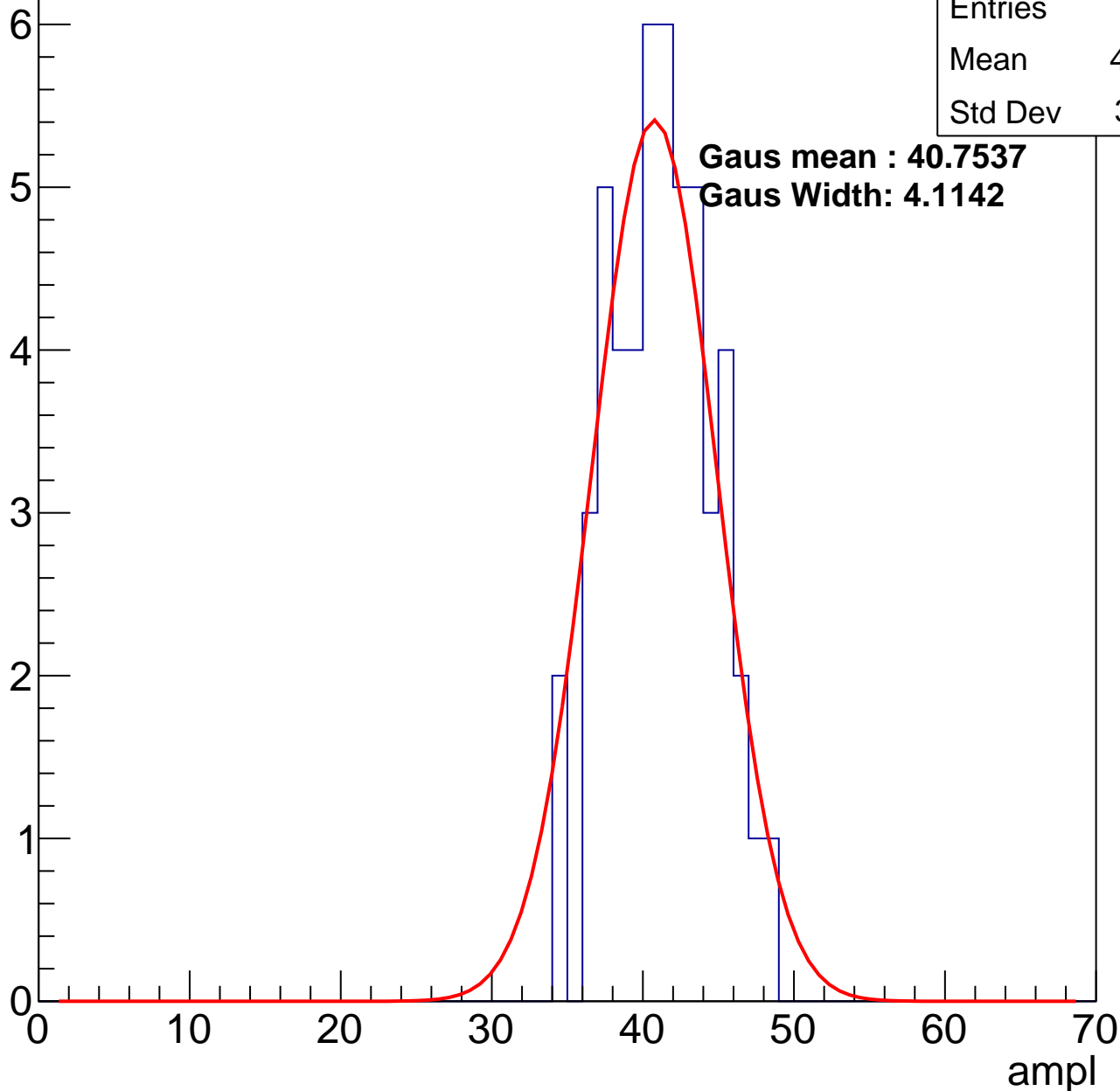
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.76
Std Dev	3.311

Gaus mean : 40.7537

Gaus Width: 4.1142

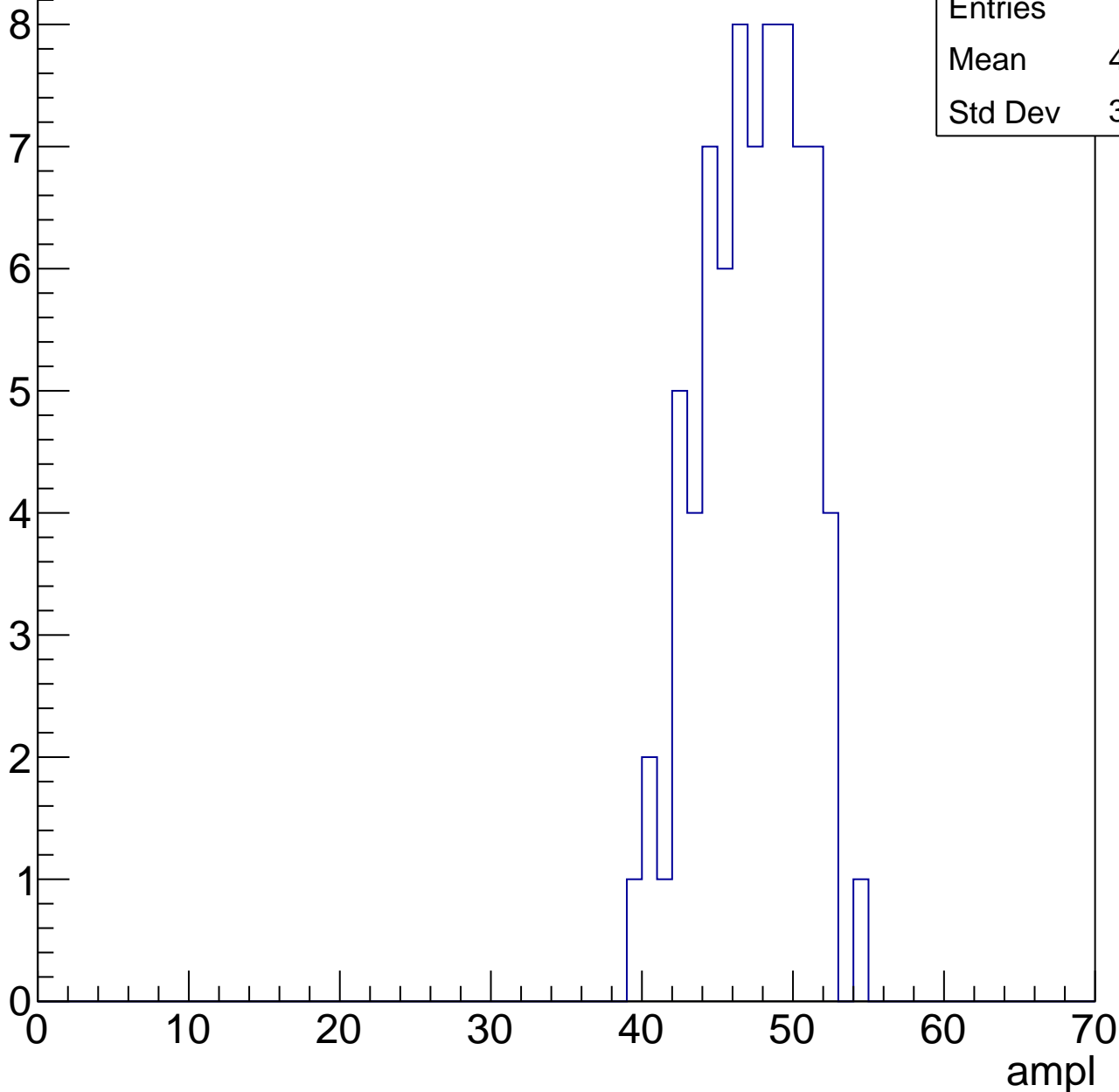


B1L002S, U7-ch72, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	76
Mean	46.87
Std Dev	3.334

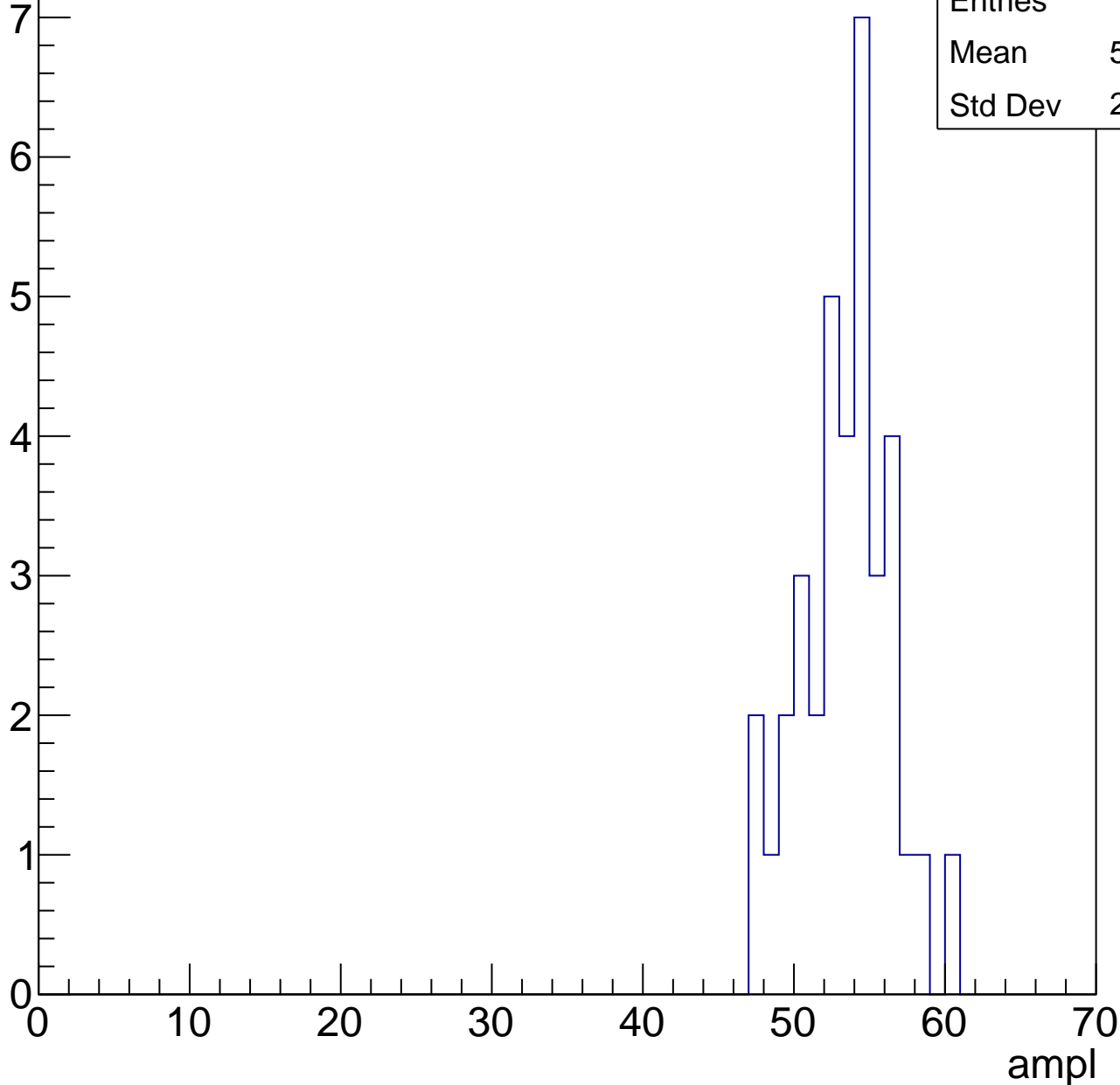


B1L002S, U7-ch72, adc4

calib_packv5_042523_0143.root, FC#10, port B3

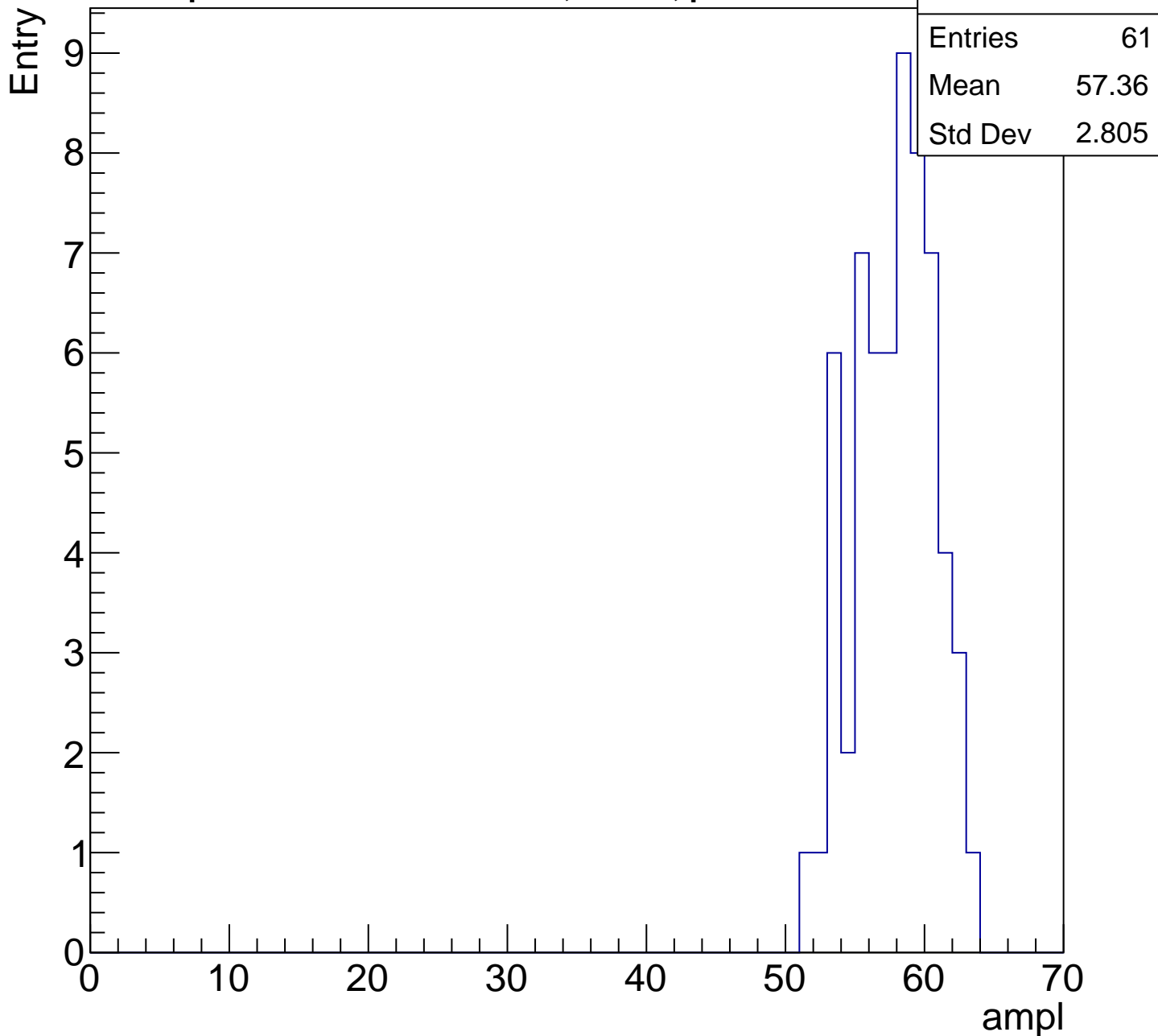
Entry

Entries	36
Mean	52.94
Std Dev	2.953



B1L002S, U7-ch72, adc5

calib_packv5_042523_0143.root, FC#10, port B3

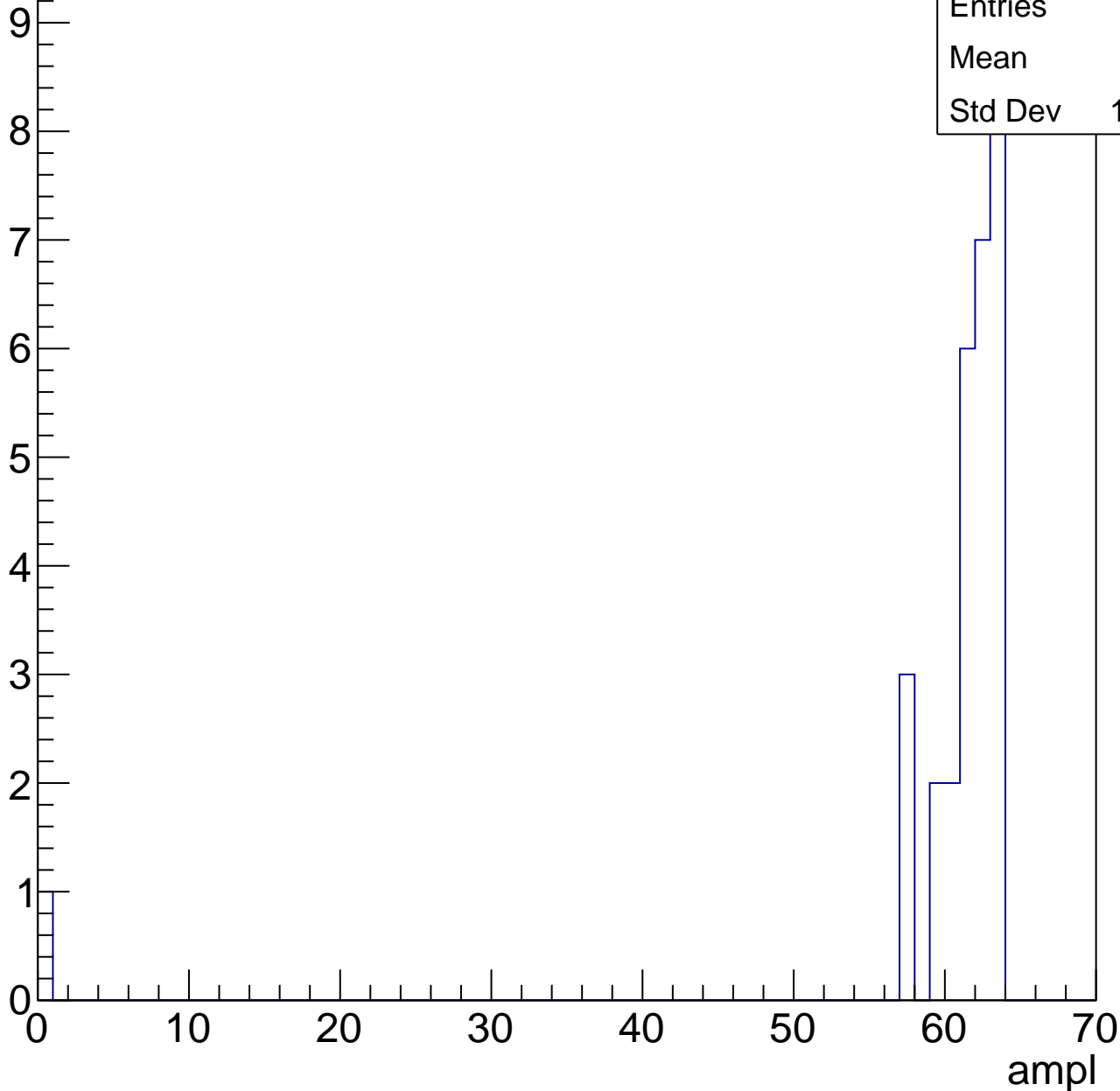


B1L002S, U7-ch72, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59.2
Std Dev	11.14



B1L002S, U7-ch72, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch73, adc0

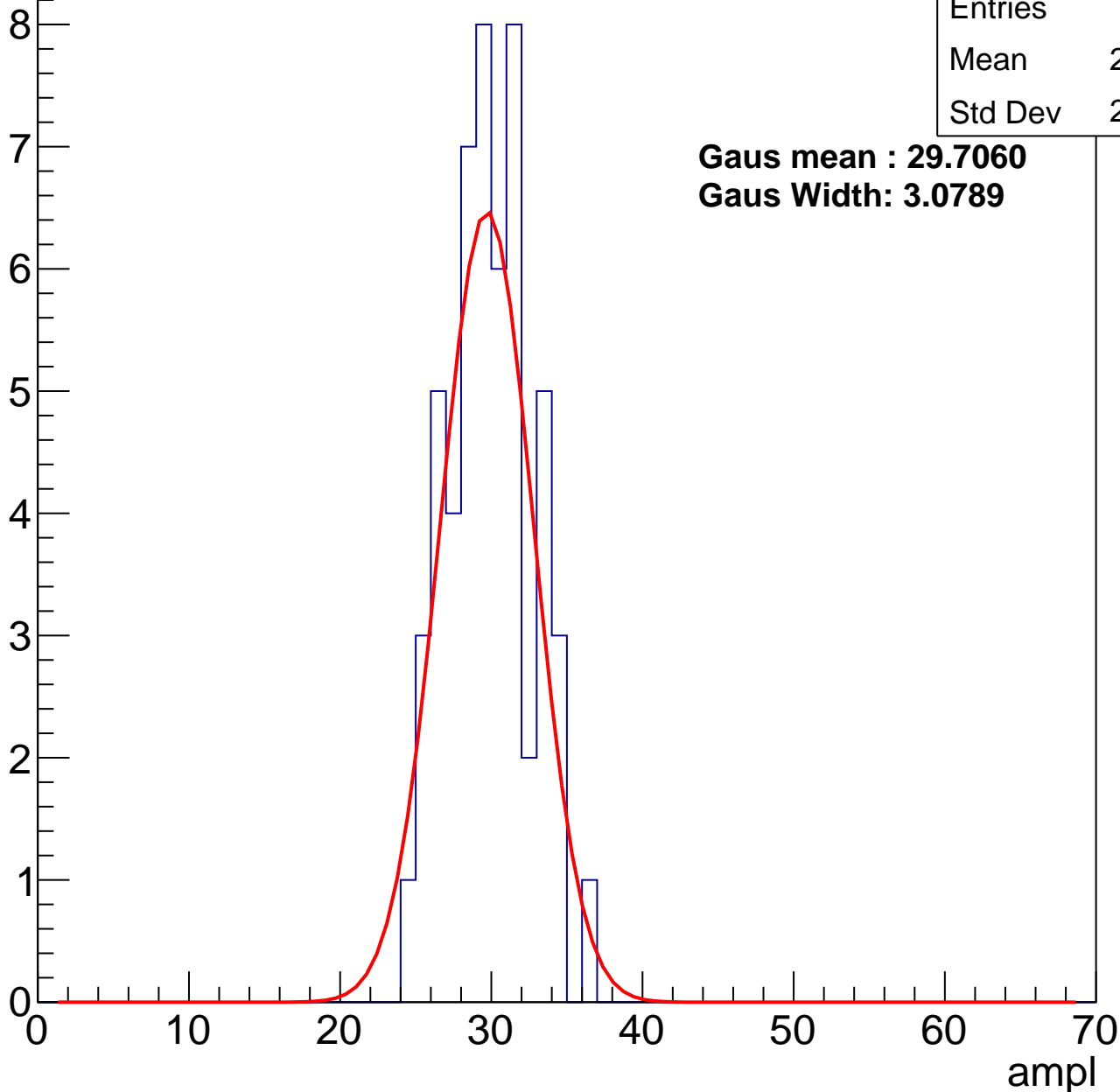
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	29.43
Std Dev	2.716

Gaus mean : 29.7060

Gaus Width: 3.0789



B1L002S, U7-ch73, adc1

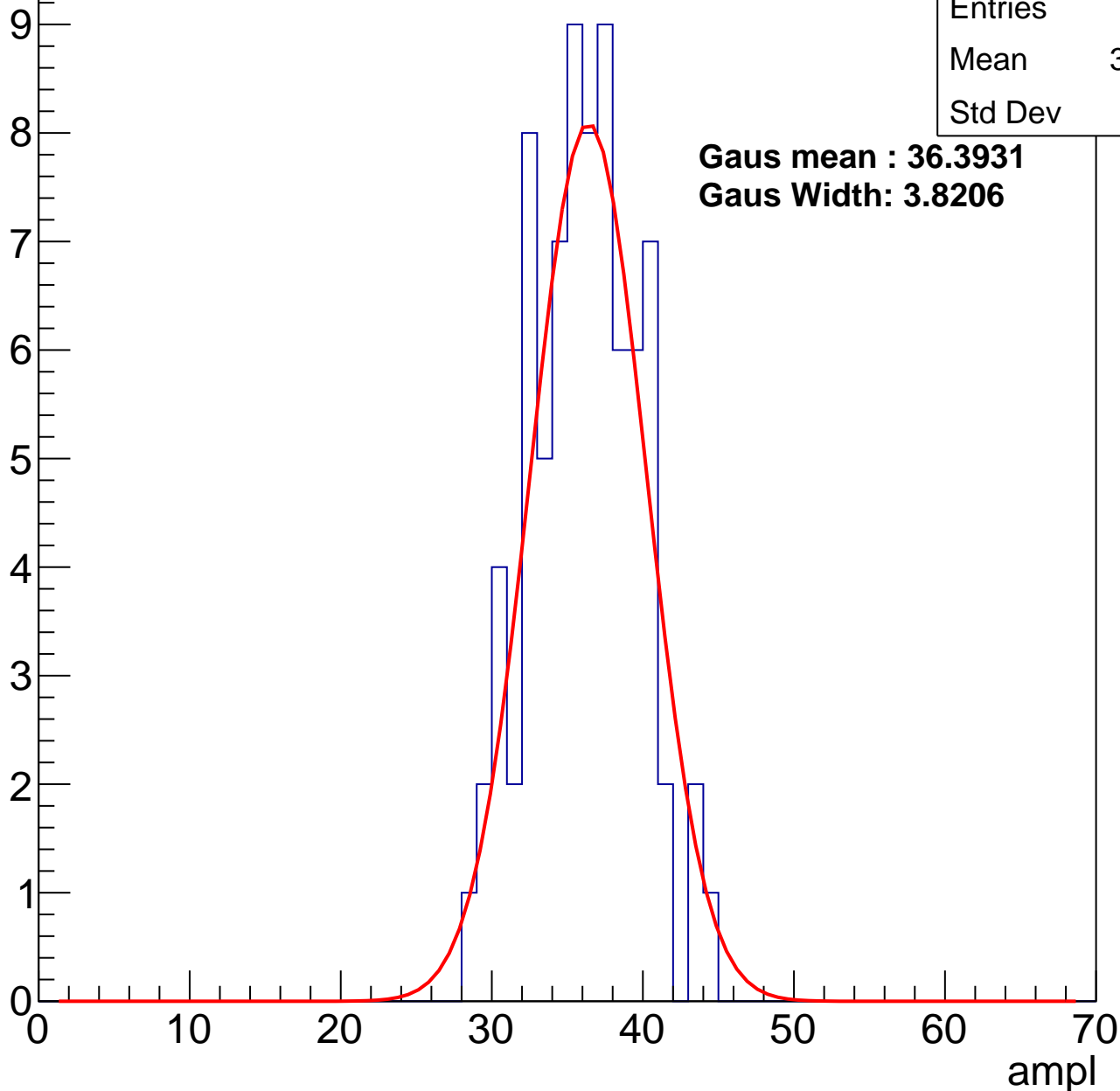
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	79
Mean	35.66
Std Dev	3.5

Gaus mean : 36.3931

Gaus Width: 3.8206



B1L002S, U7-ch73, adc2

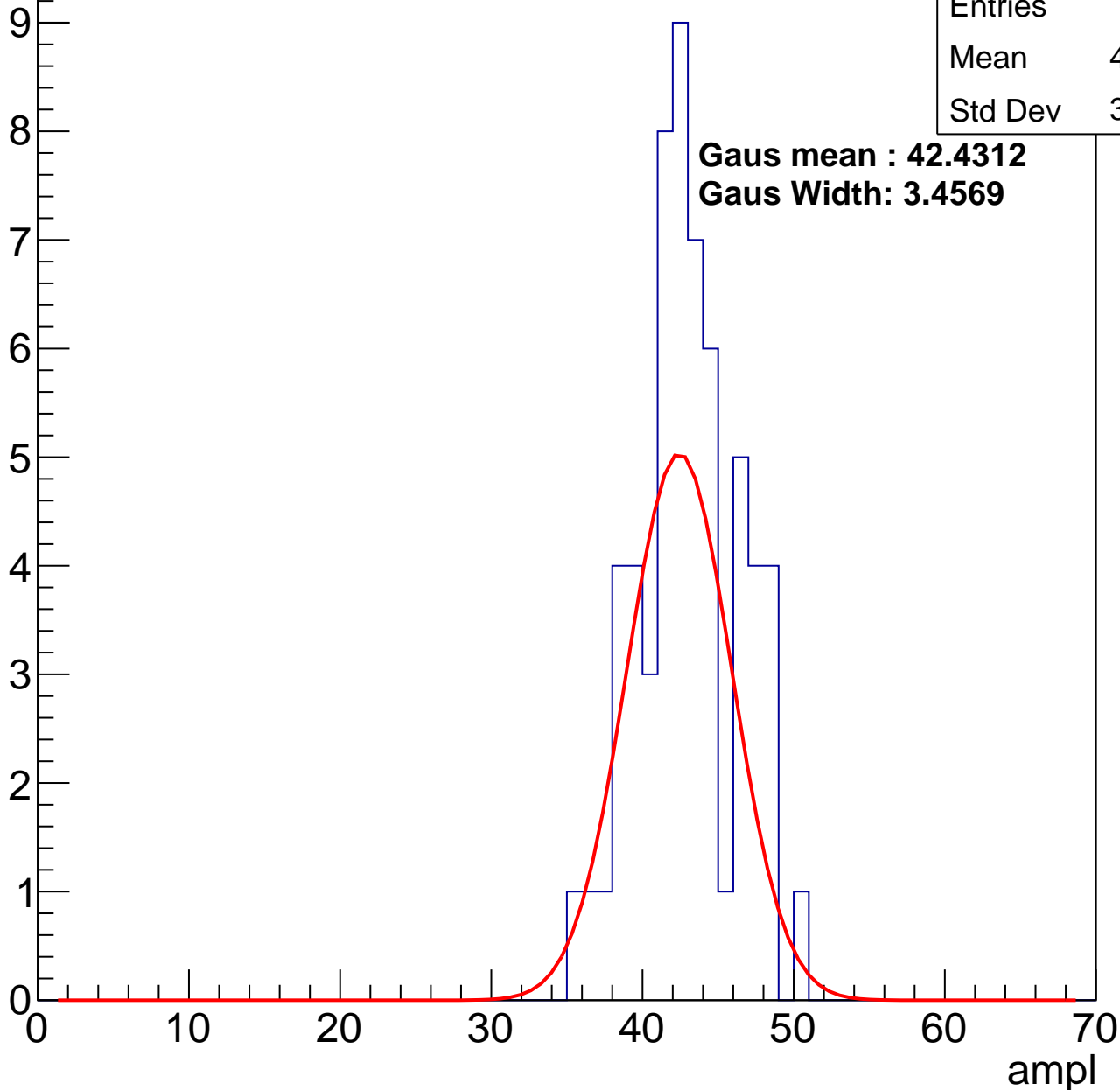
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.58
Std Dev	3.289

Gaus mean : 42.4312

Gaus Width: 3.4569



B1L002S, U7-ch73, adc3

calib_packv5_042523_0143.root, FC#10, port B3

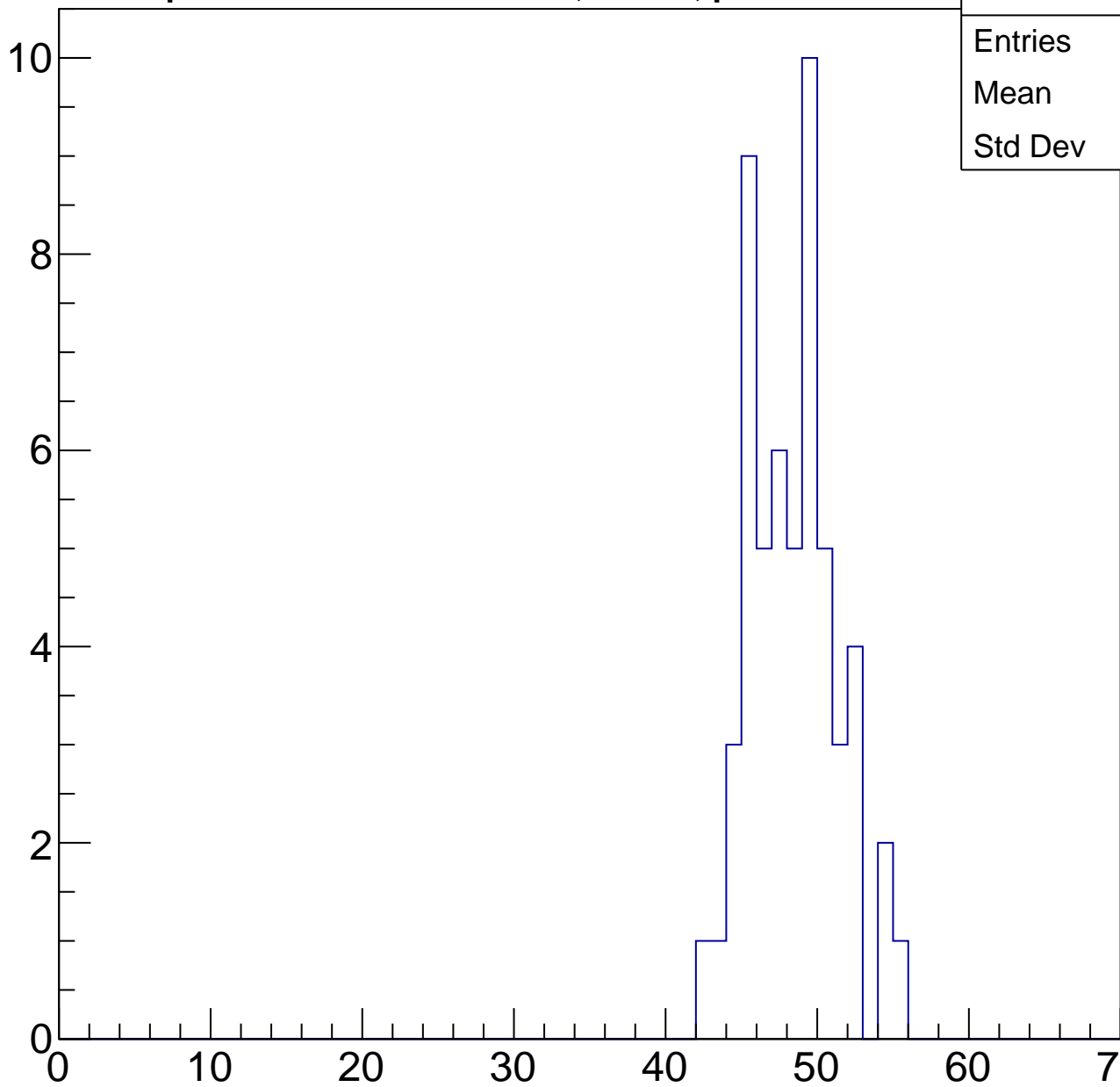
Entries	55
Mean	47.96
Std Dev	2.885

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

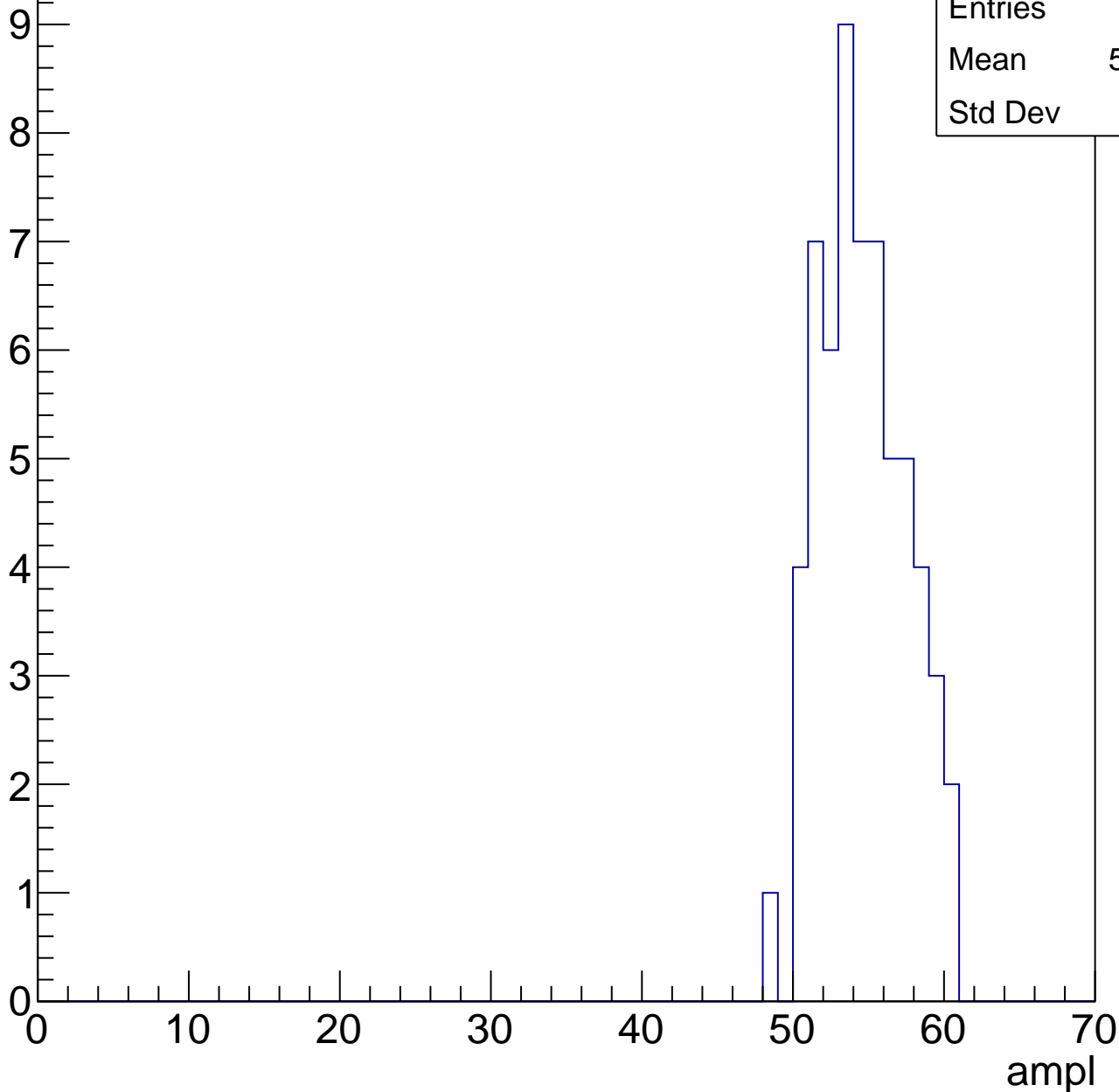


B1L002S, U7-ch73, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	54.18
Std Dev	2.82



B1L002S, U7-ch73, adc5

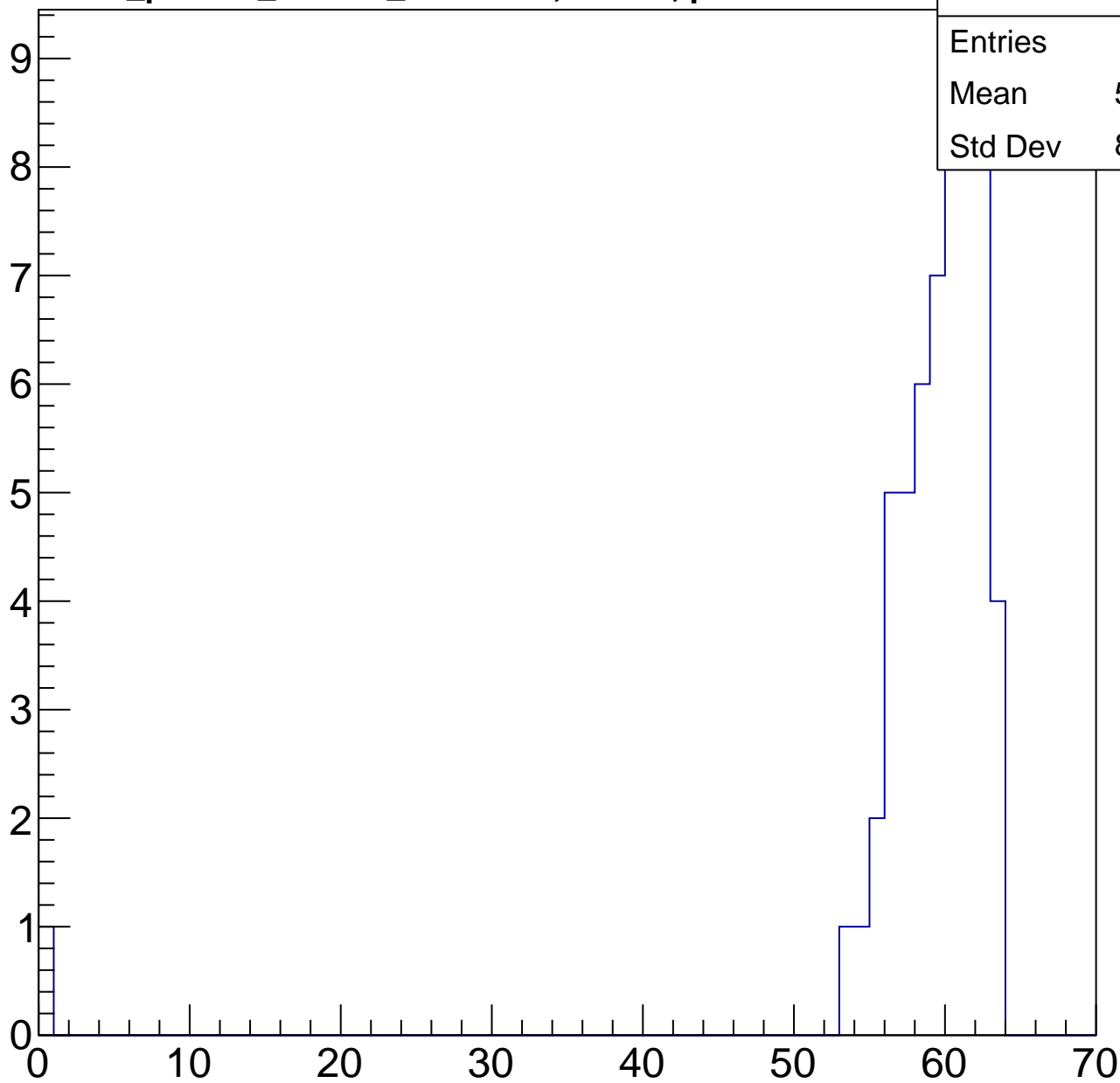
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.31
Std Dev	8.101

ampl

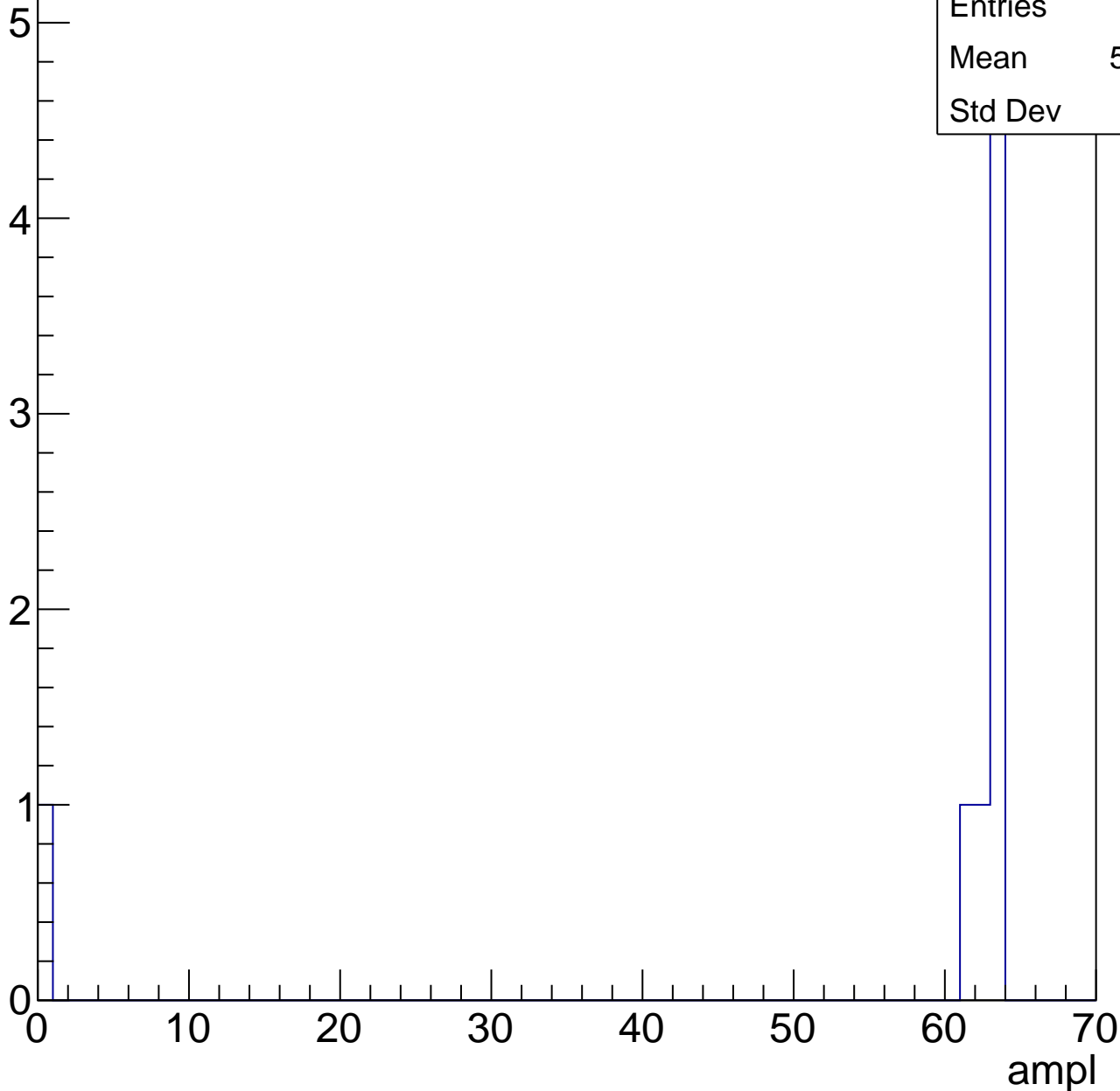


B1L002S, U7-ch73, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

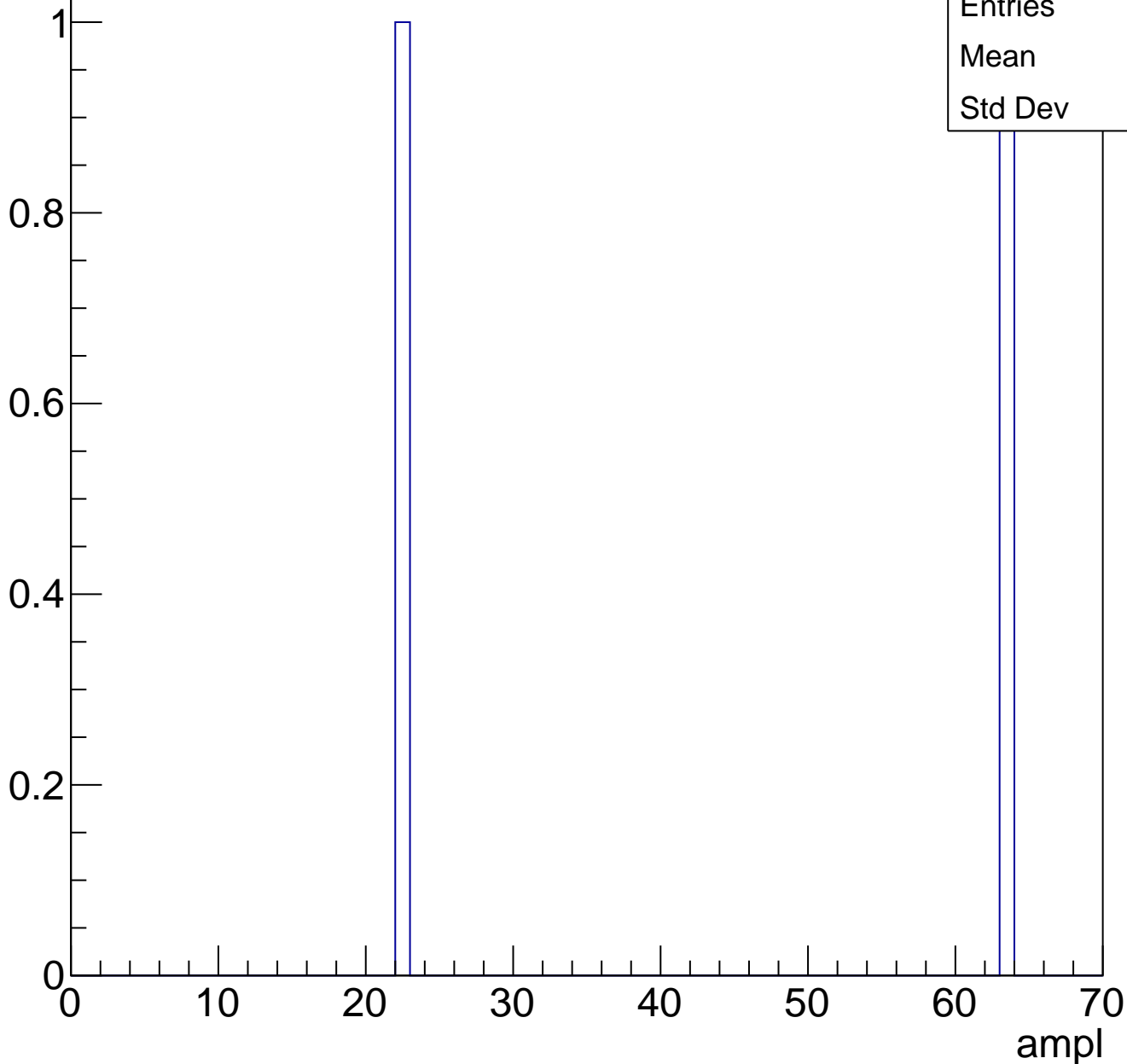
Entries	8
Mean	54.75
Std Dev	20.7



B1L002S, U7-ch73, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch74, adc0

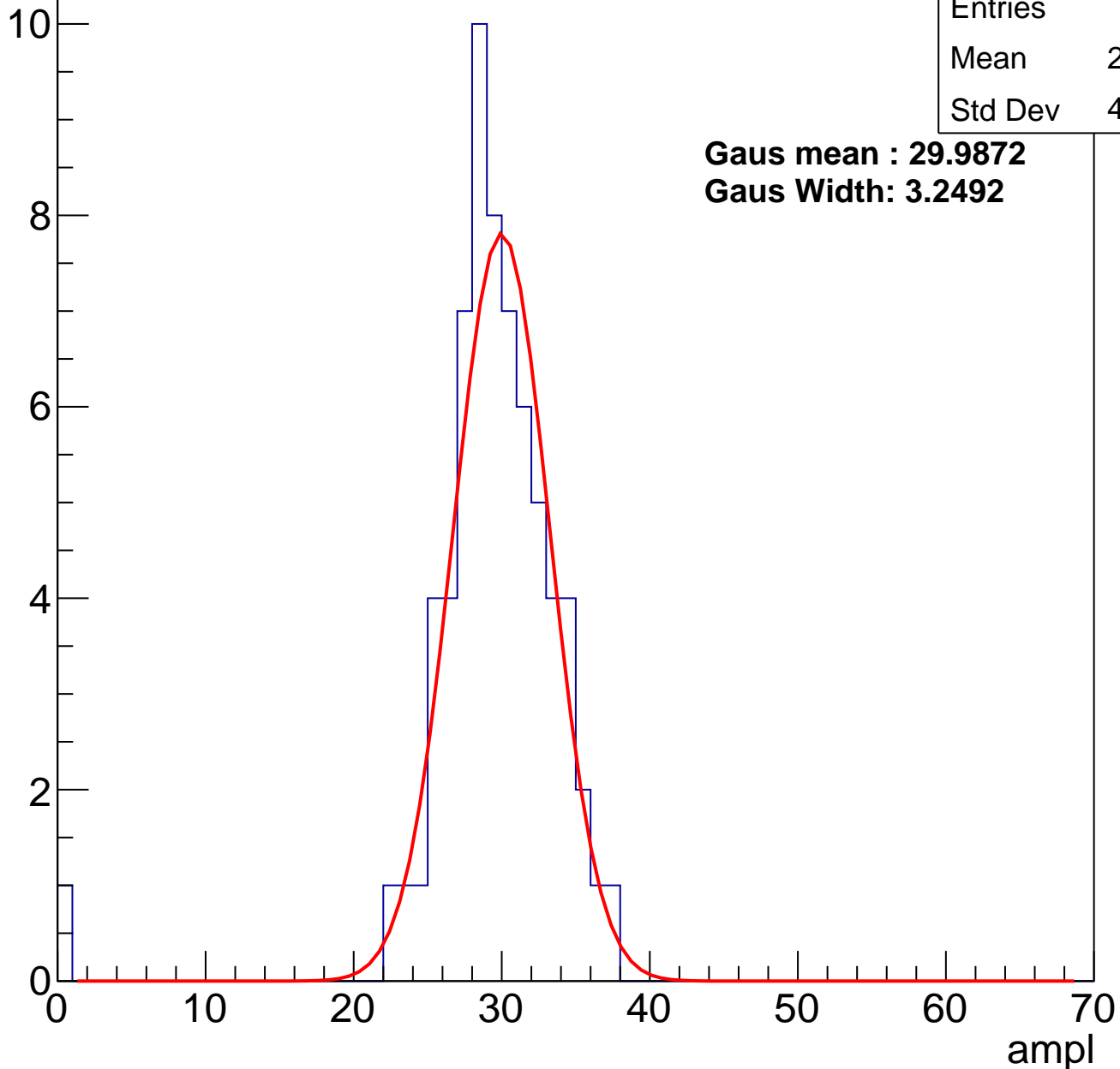
calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	28.97
Std Dev	4.754

Gaus mean : 29.9872

Gaus Width: 3.2492

Entry



B1L002S, U7-ch74, adc1

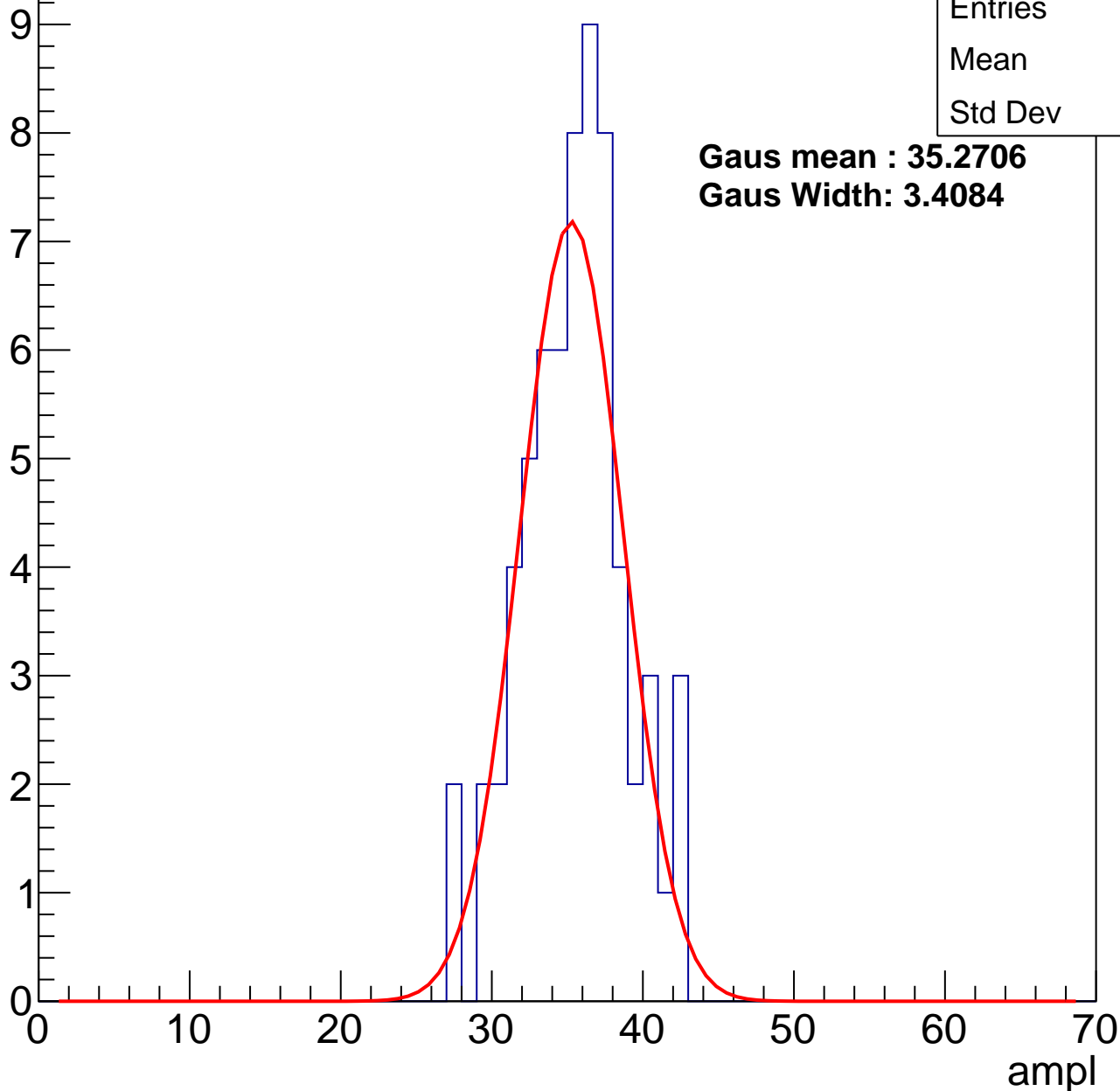
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35
Std Dev	3.41

Gaus mean : 35.2706

Gaus Width: 3.4084



B1L002S, U7-ch74, adc2

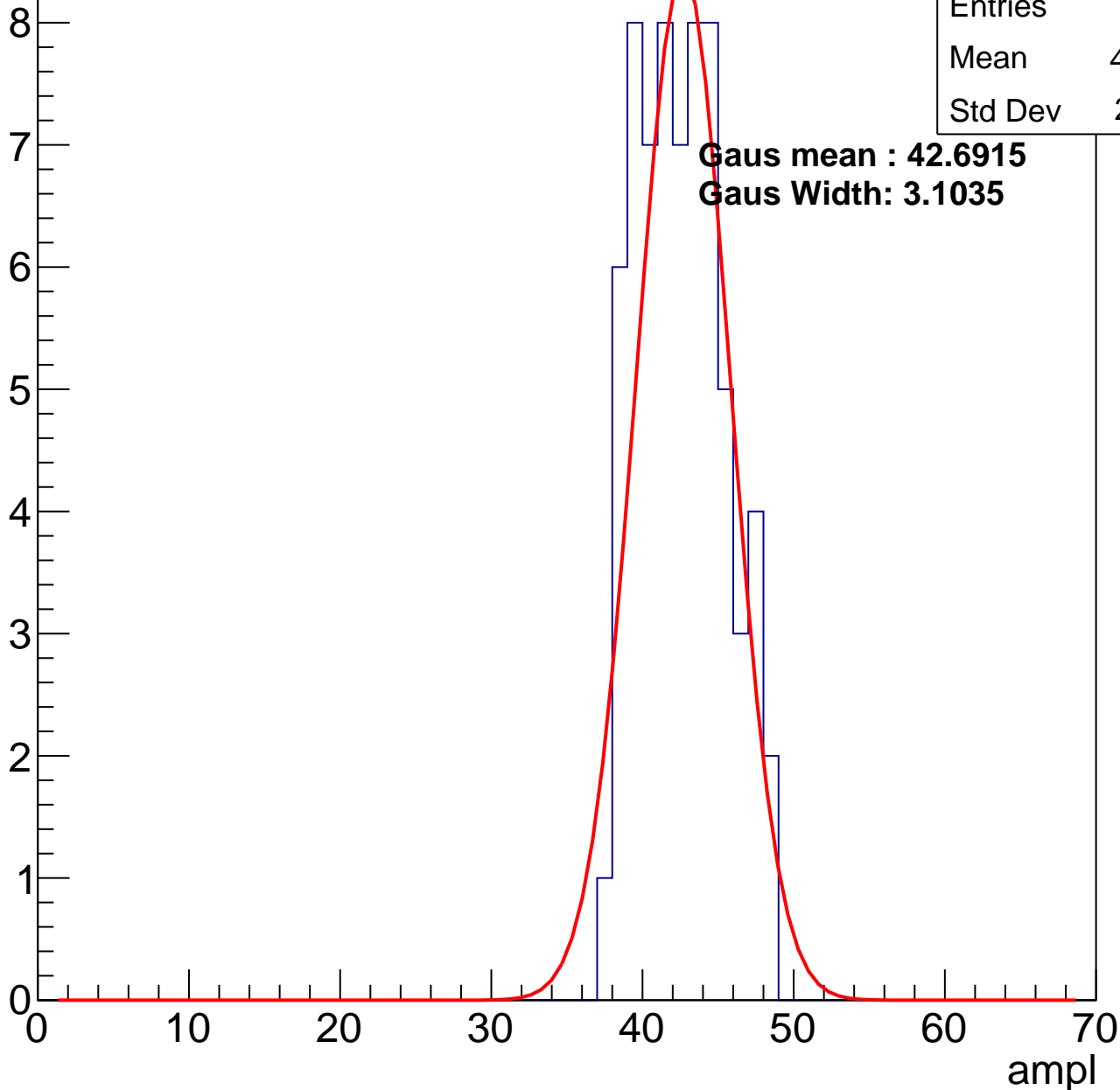
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	42.12
Std Dev	2.821

Gaus mean : 42.6915

Gaus Width: 3.1035

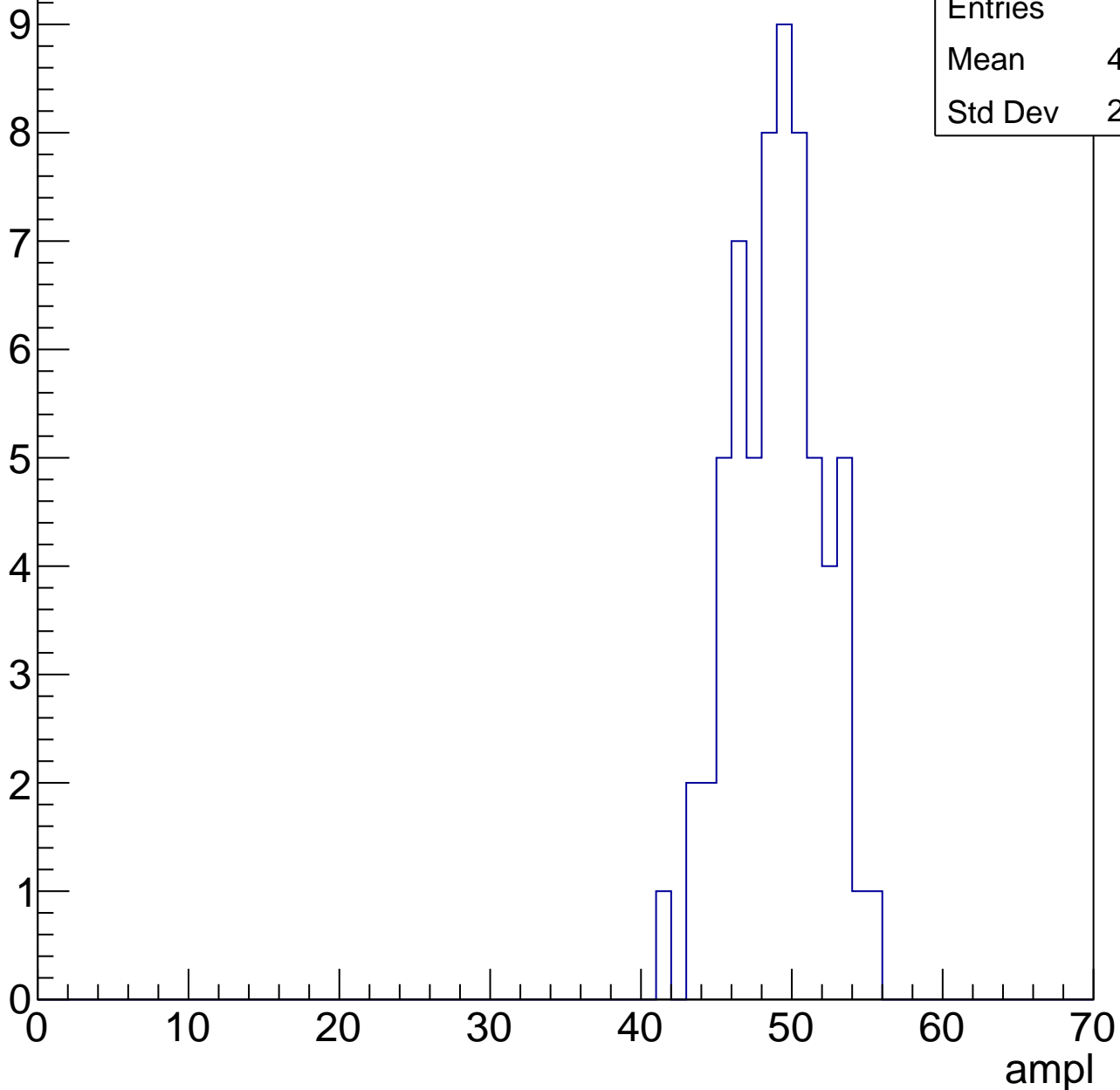


B1L002S, U7-ch74, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	48.56
Std Dev	2.953

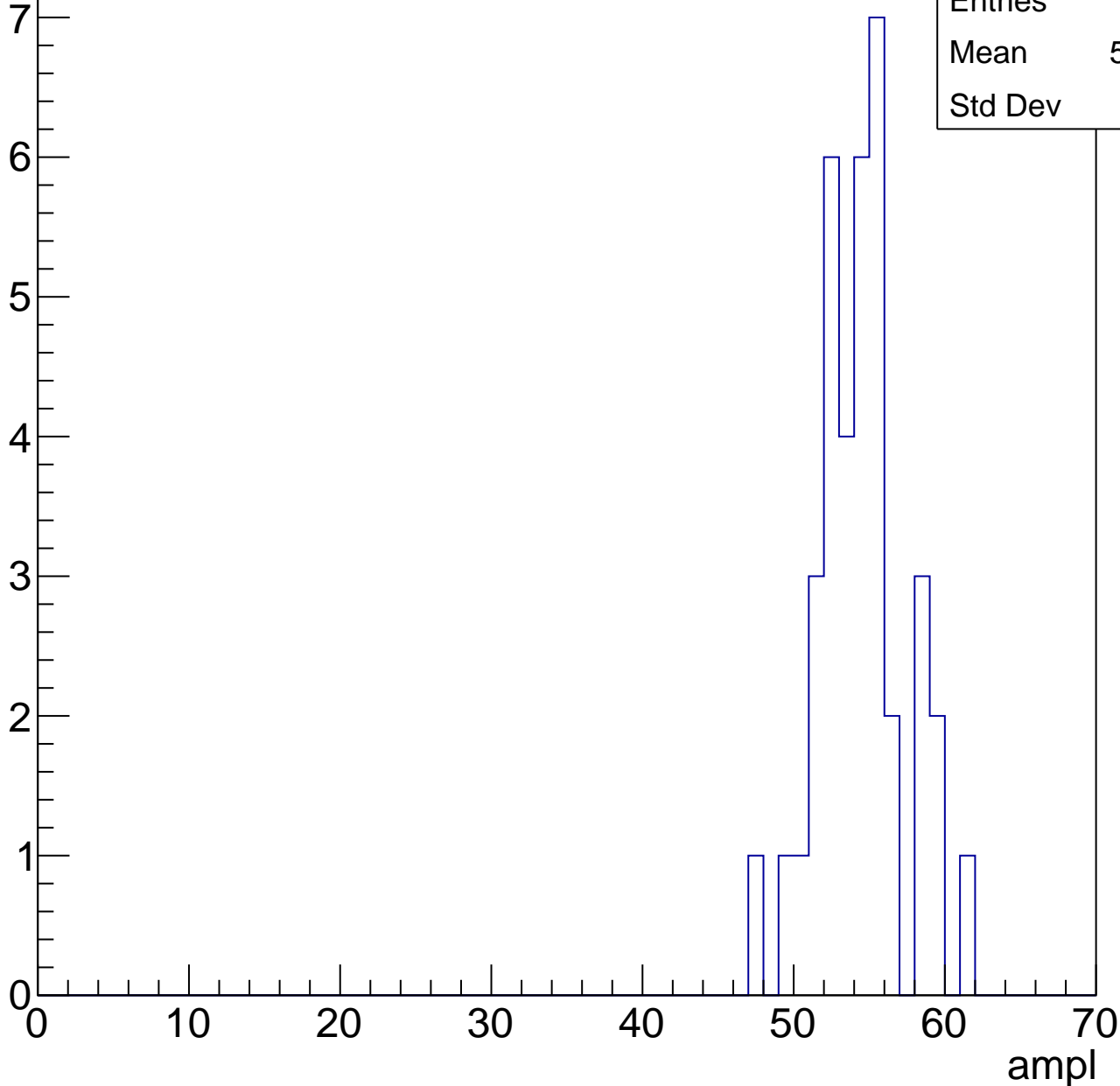


B1L002S, U7-ch74, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	37
Mean	53.97
Std Dev	2.88



B1L002S, U7-ch74, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	57.7
Std Dev	7.571

Entry

10

8

6

4

2

0

0

10

20

30

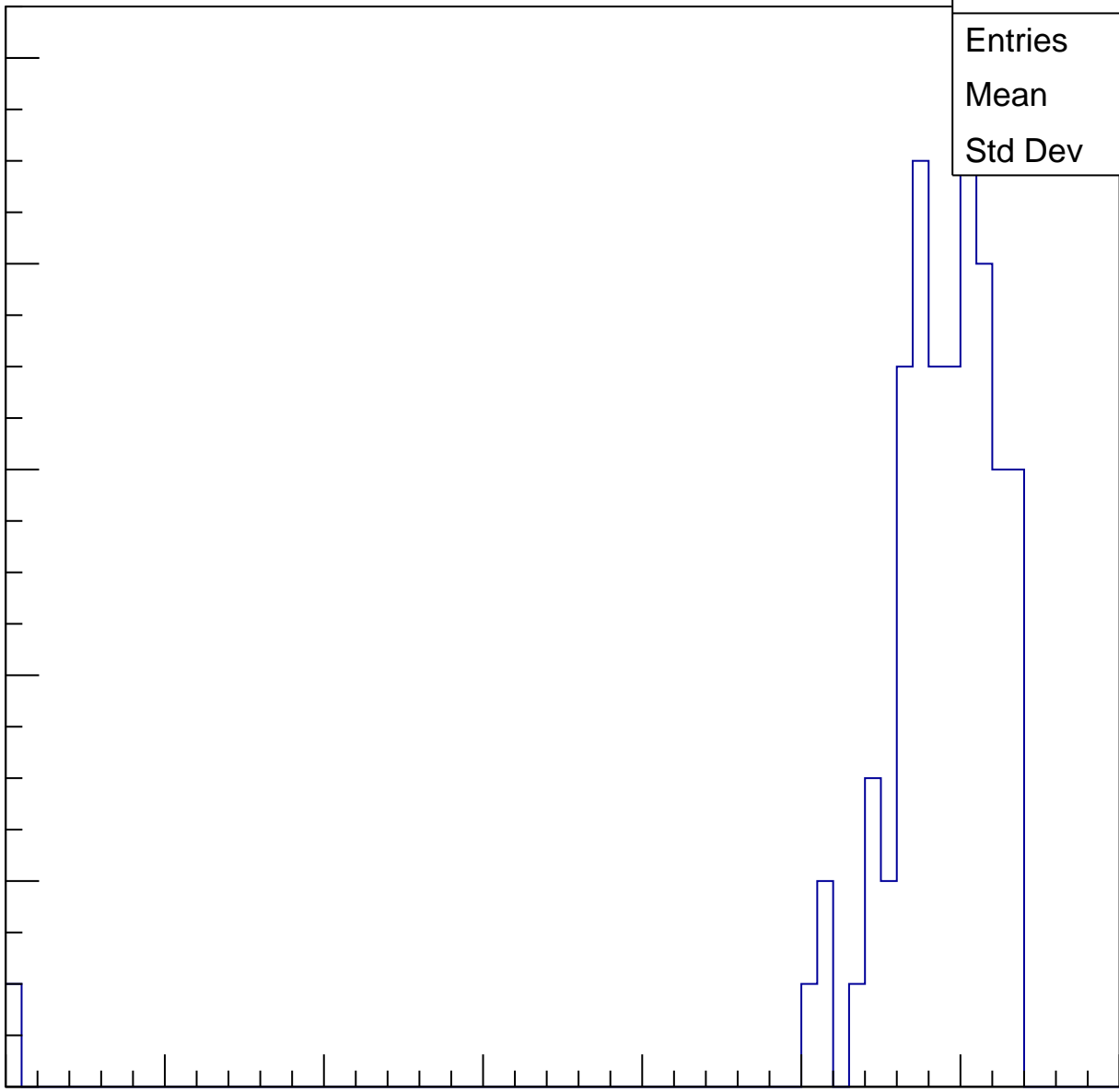
40

50

60

ampl

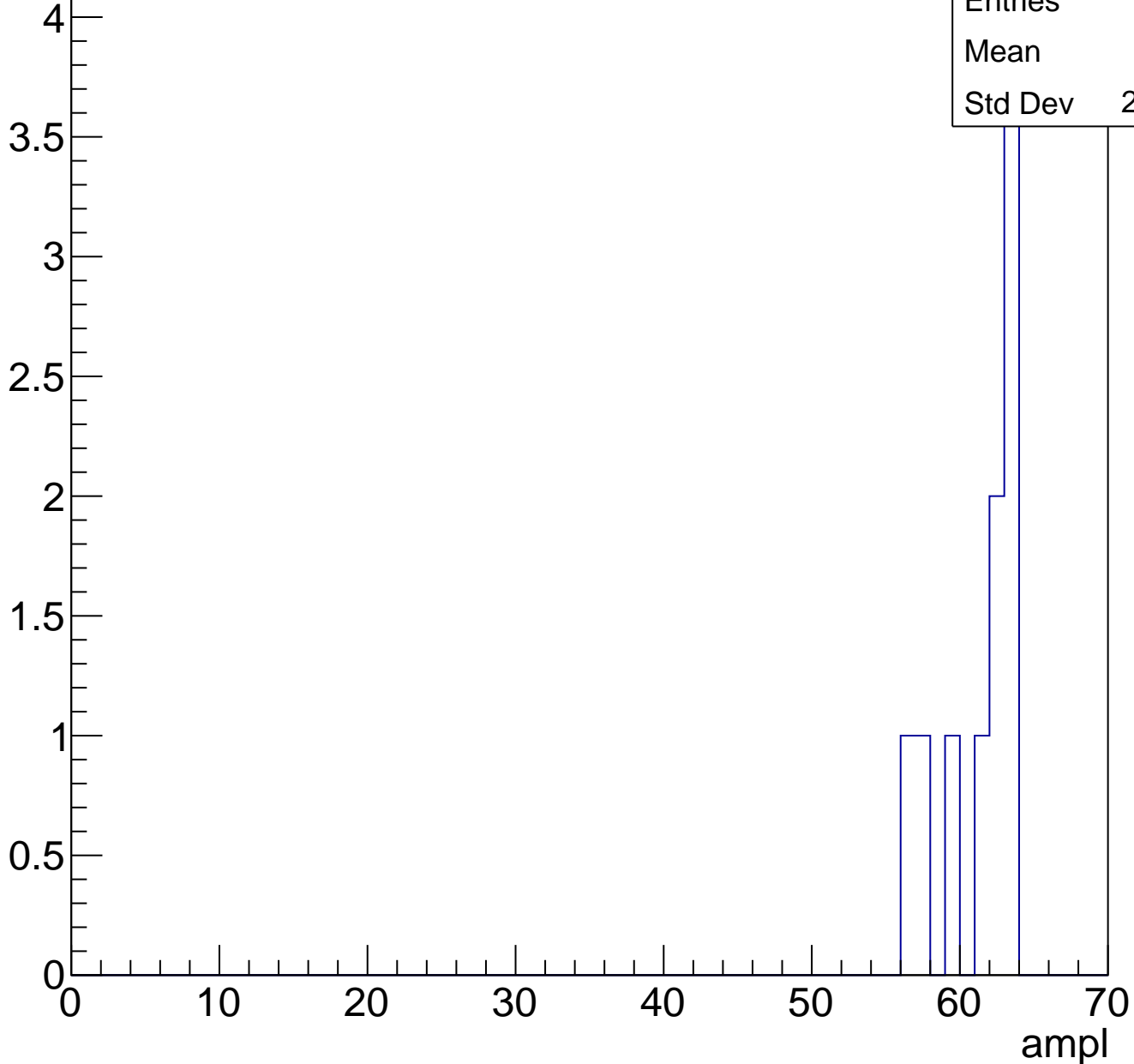
70



B1L002S, U7-ch74, adc6

calib_packv5_042523_0143.root, FC#10, port B3

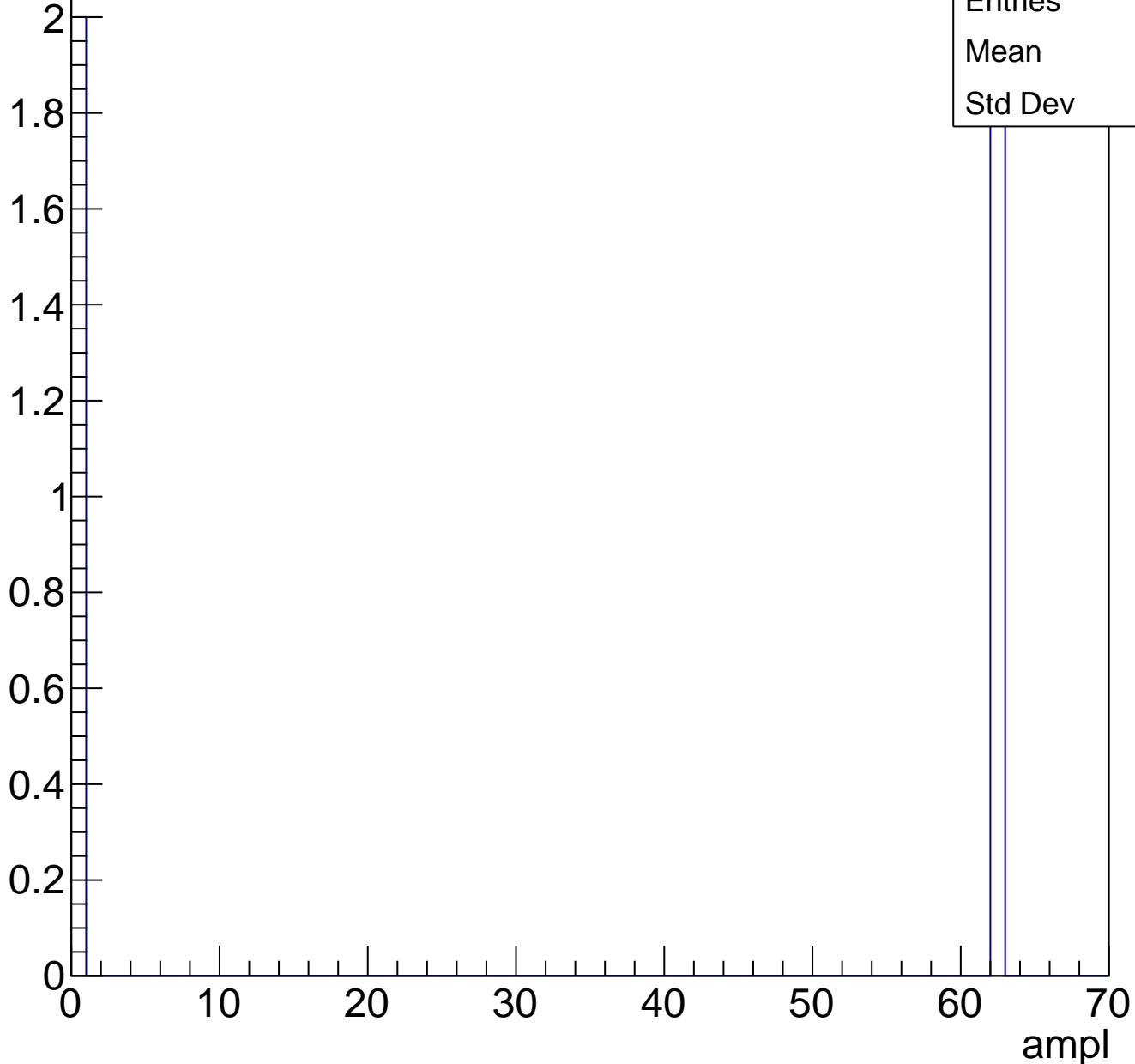
Entry



B1L002S, U7-ch74, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch75, adc0

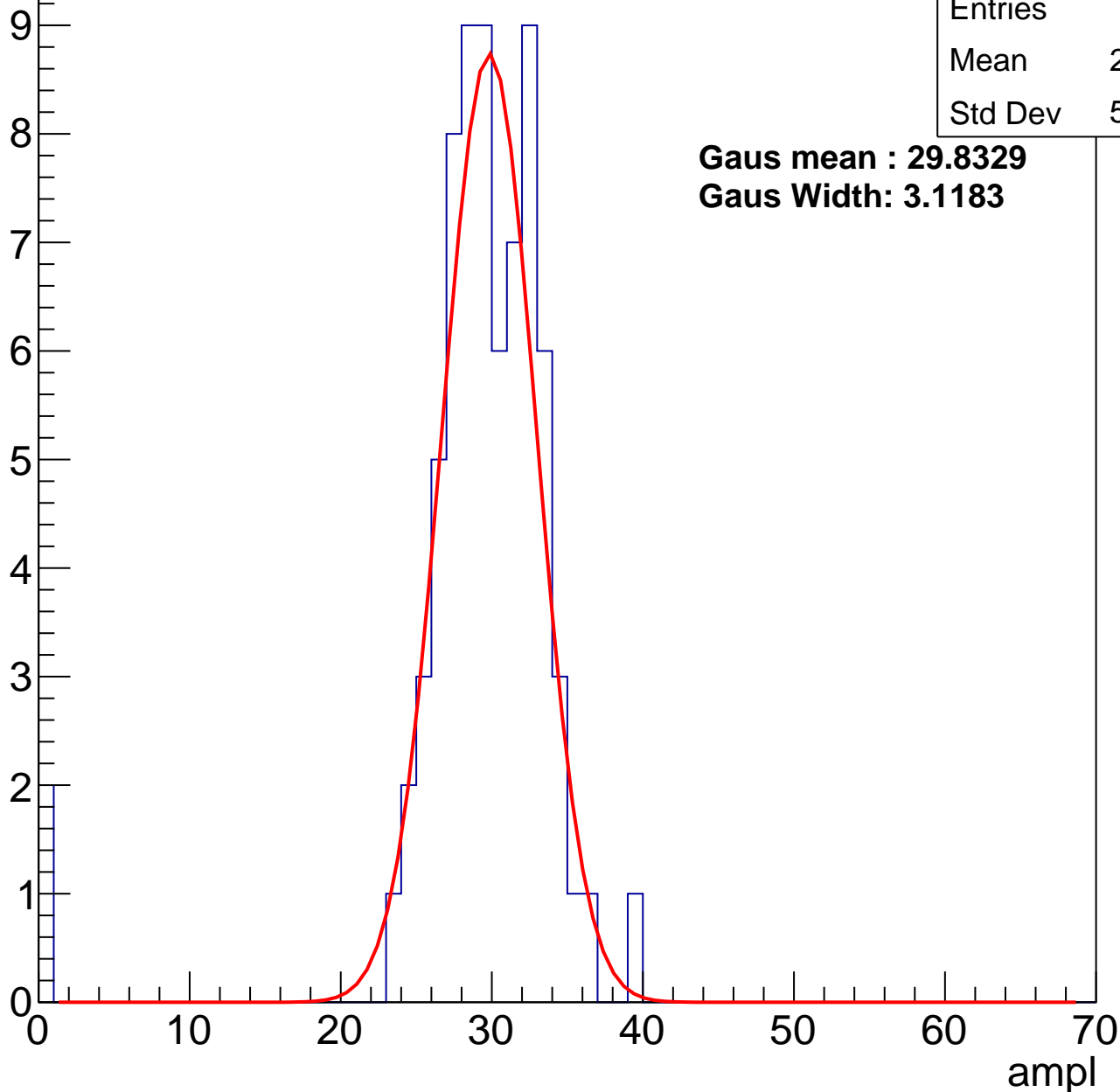
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	28.77
Std Dev	5.697

Gaus mean : 29.8329

Gaus Width: 3.1183



B1L002S, U7-ch75, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	61
Mean	36.18
Std Dev	2.773

Gaus mean : 36.9259

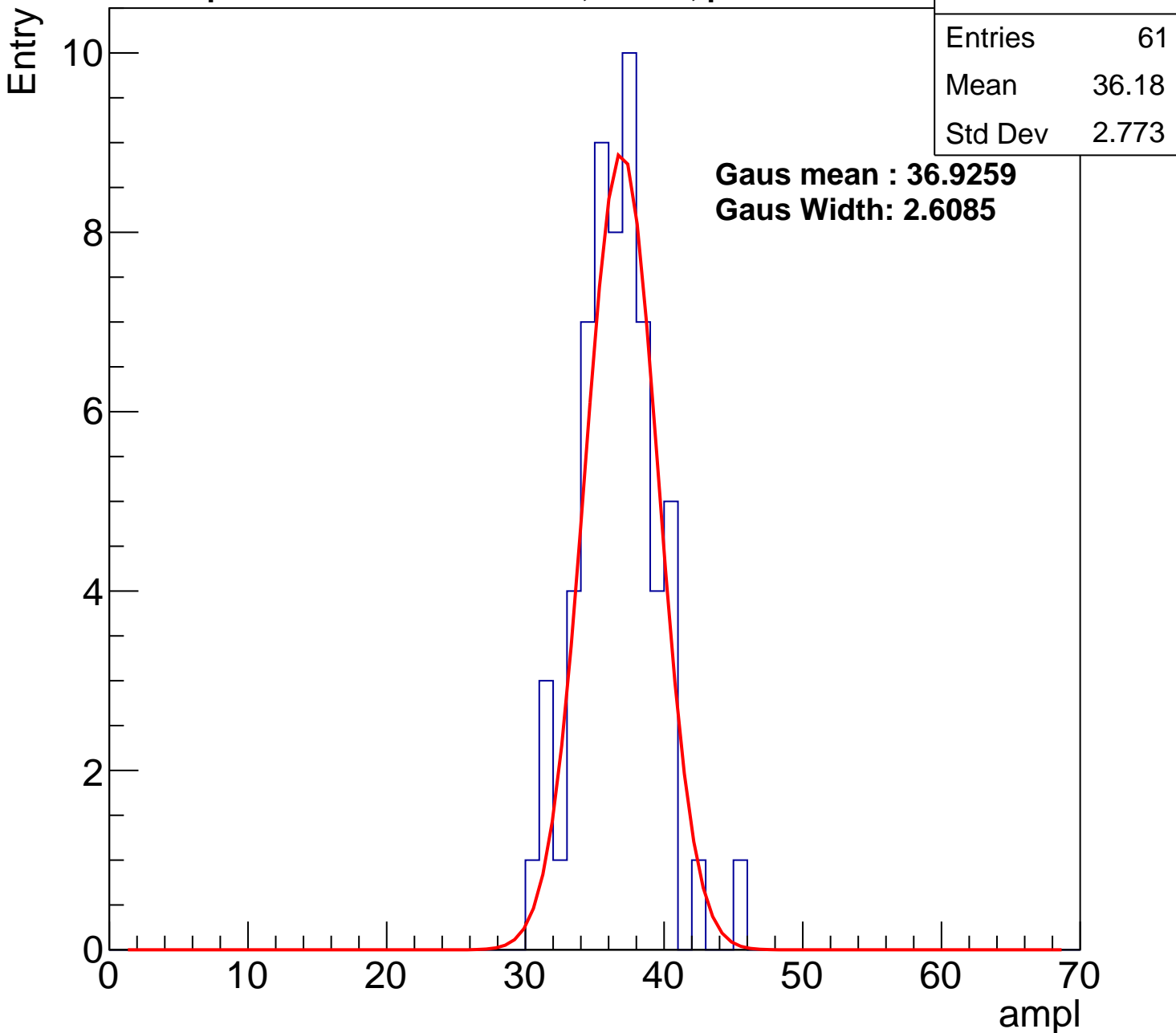
Gaus Width: 2.6085

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch75, adc2

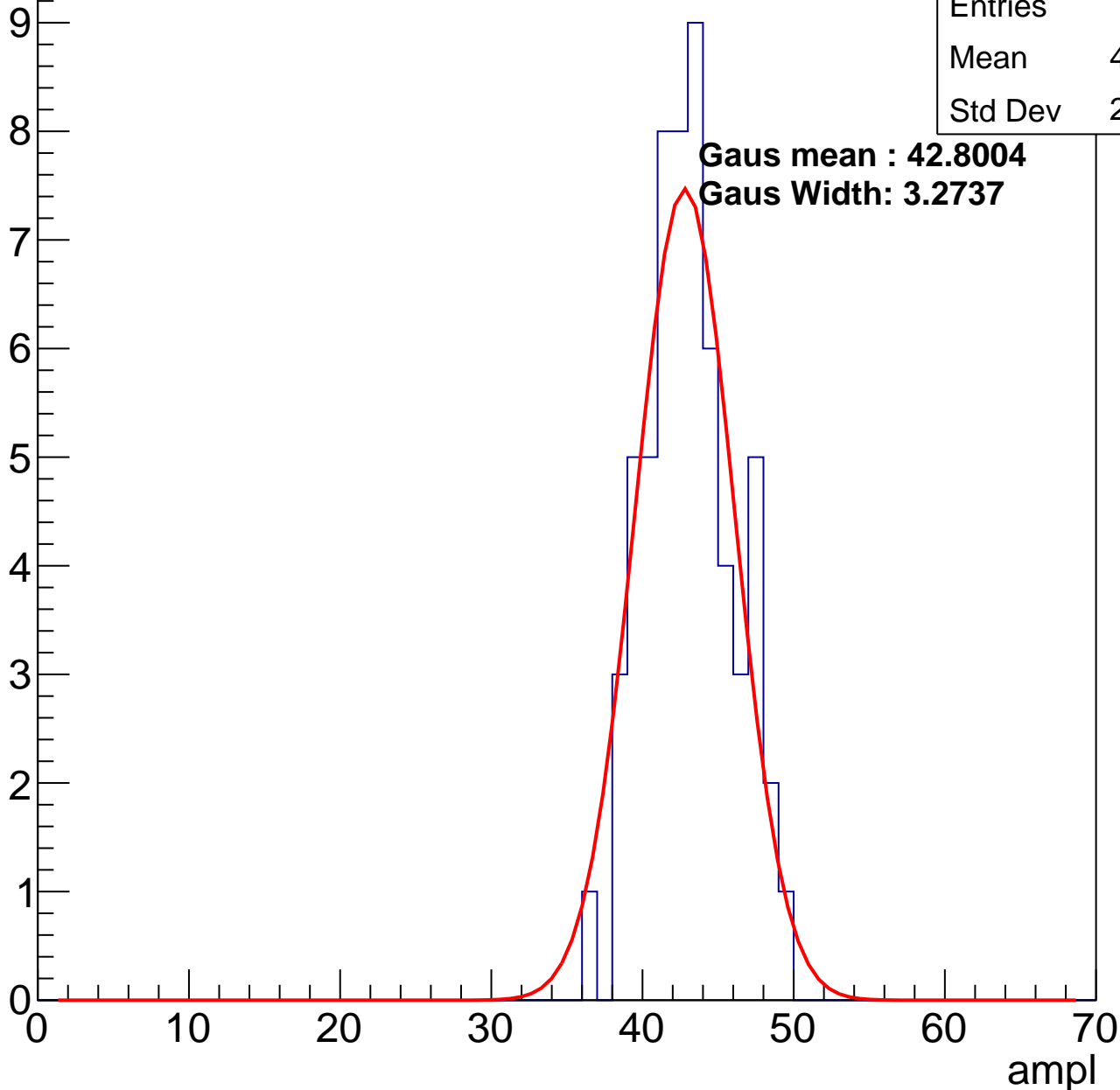
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	42.63
Std Dev	2.875

Gaus mean : 42.8004

Gaus Width: 3.2737

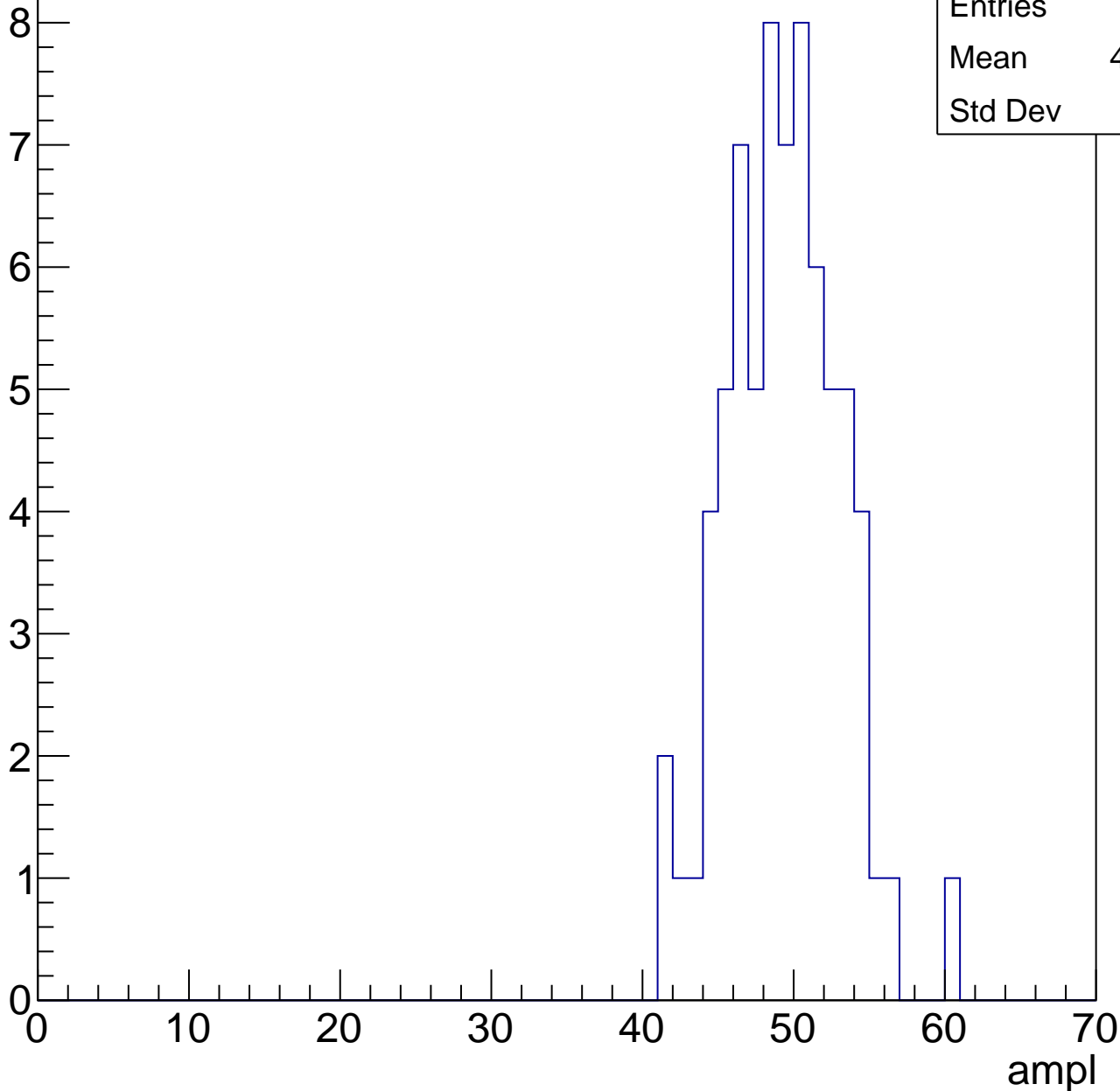


B1L002S, U7-ch75, adc3

calib_packv5_042523_0143.root, FC#10, port B3

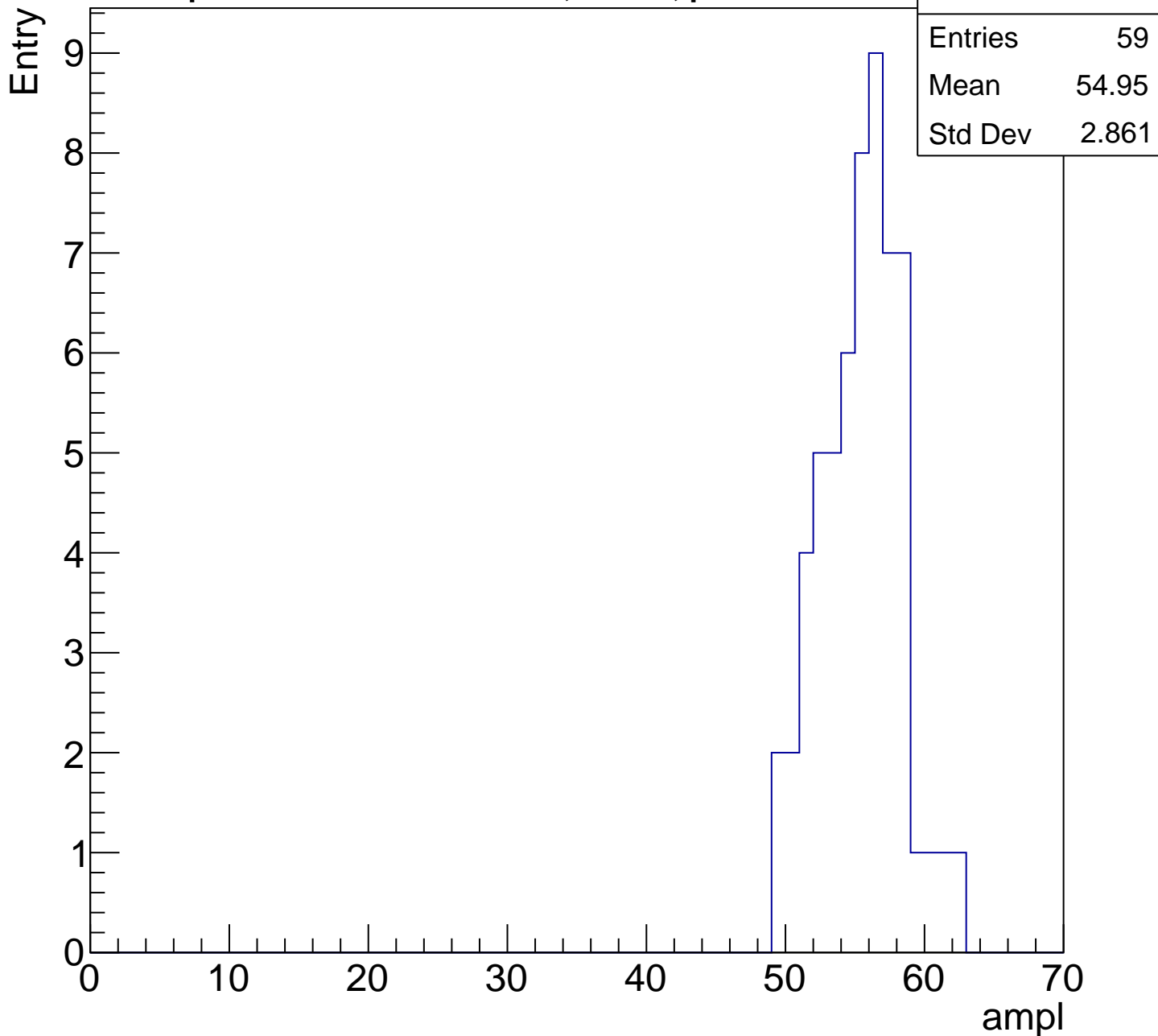
Entry

Entries	71
Mean	48.87
Std Dev	3.65



B1L002S, U7-ch75, adc4

calib_packv5_042523_0143.root, FC#10, port B3

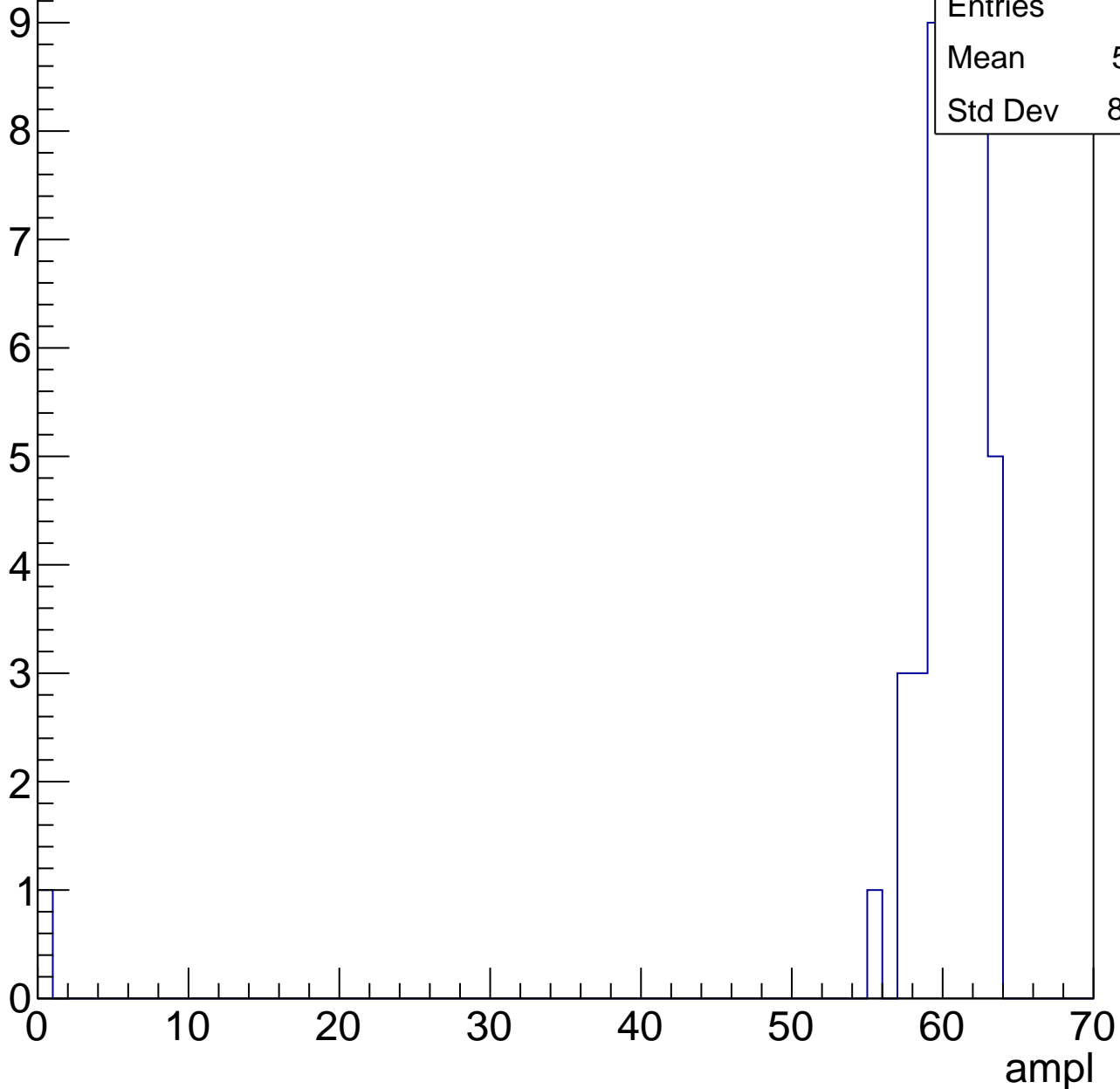


B1L002S, U7-ch75, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	58.91
Std Dev	8.973

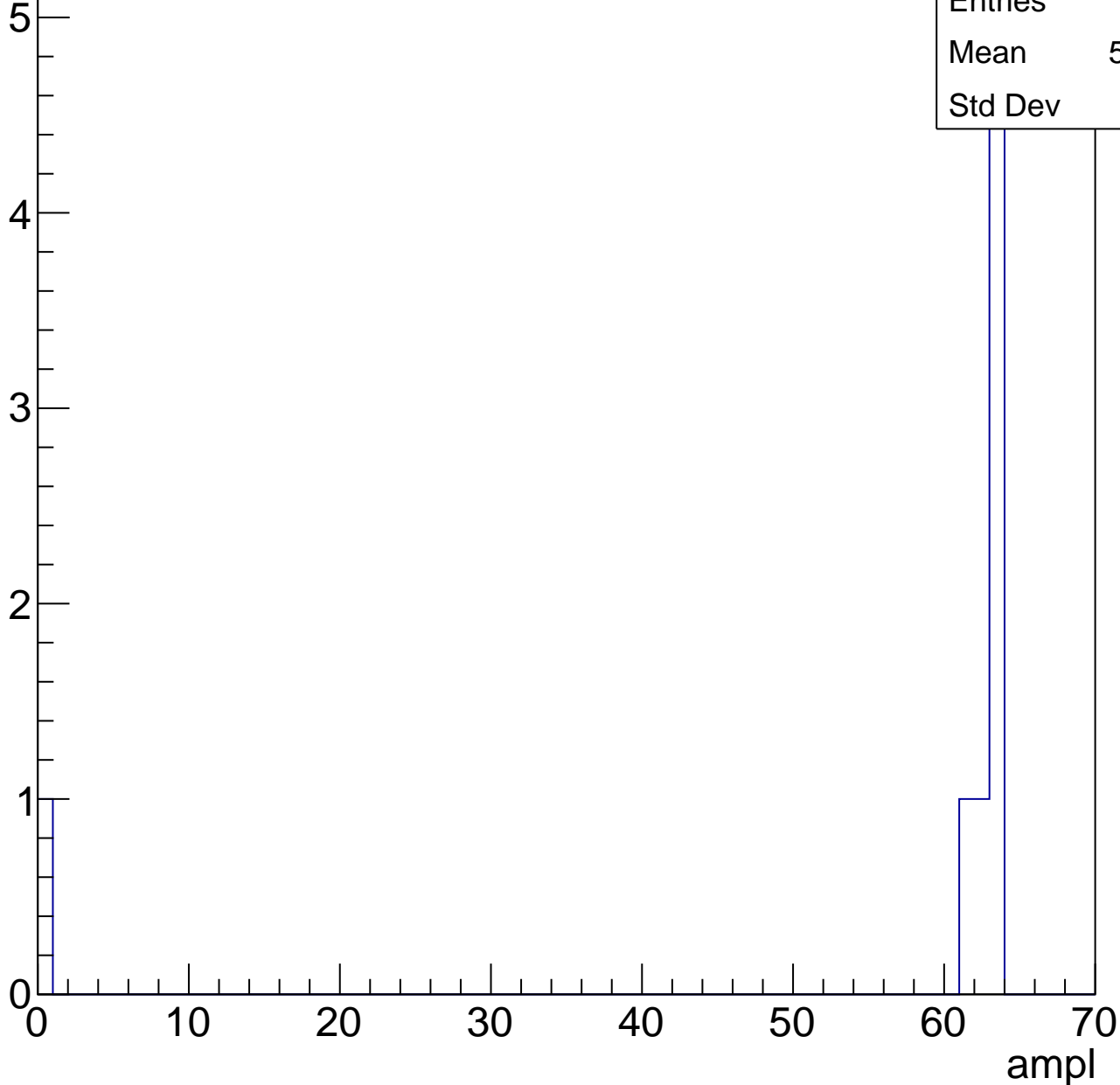


B1L002S, U7-ch75, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

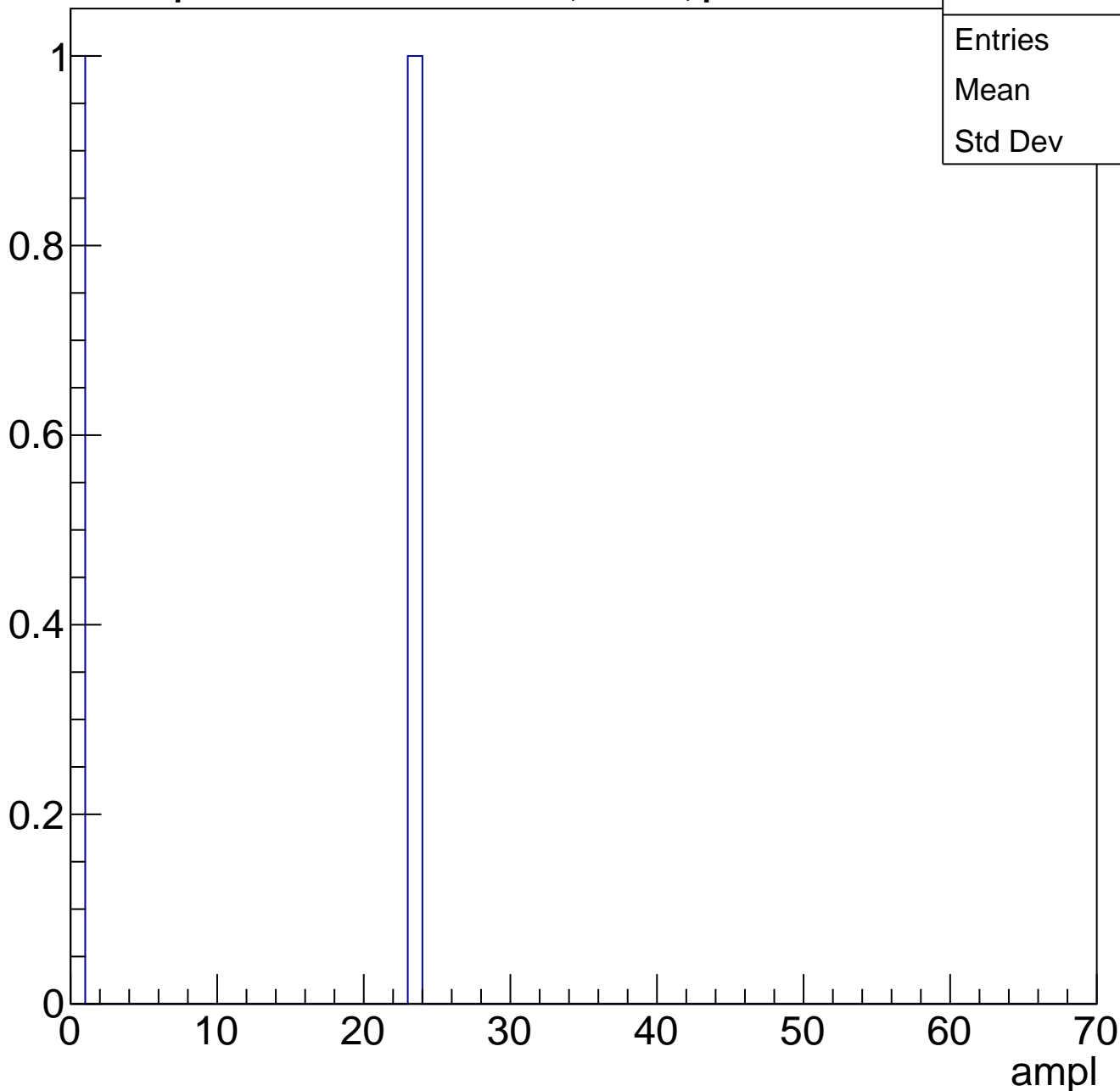
Entries	8
Mean	54.75
Std Dev	20.7



B1L002S, U7-ch75, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L002S, U7-ch76, adc0

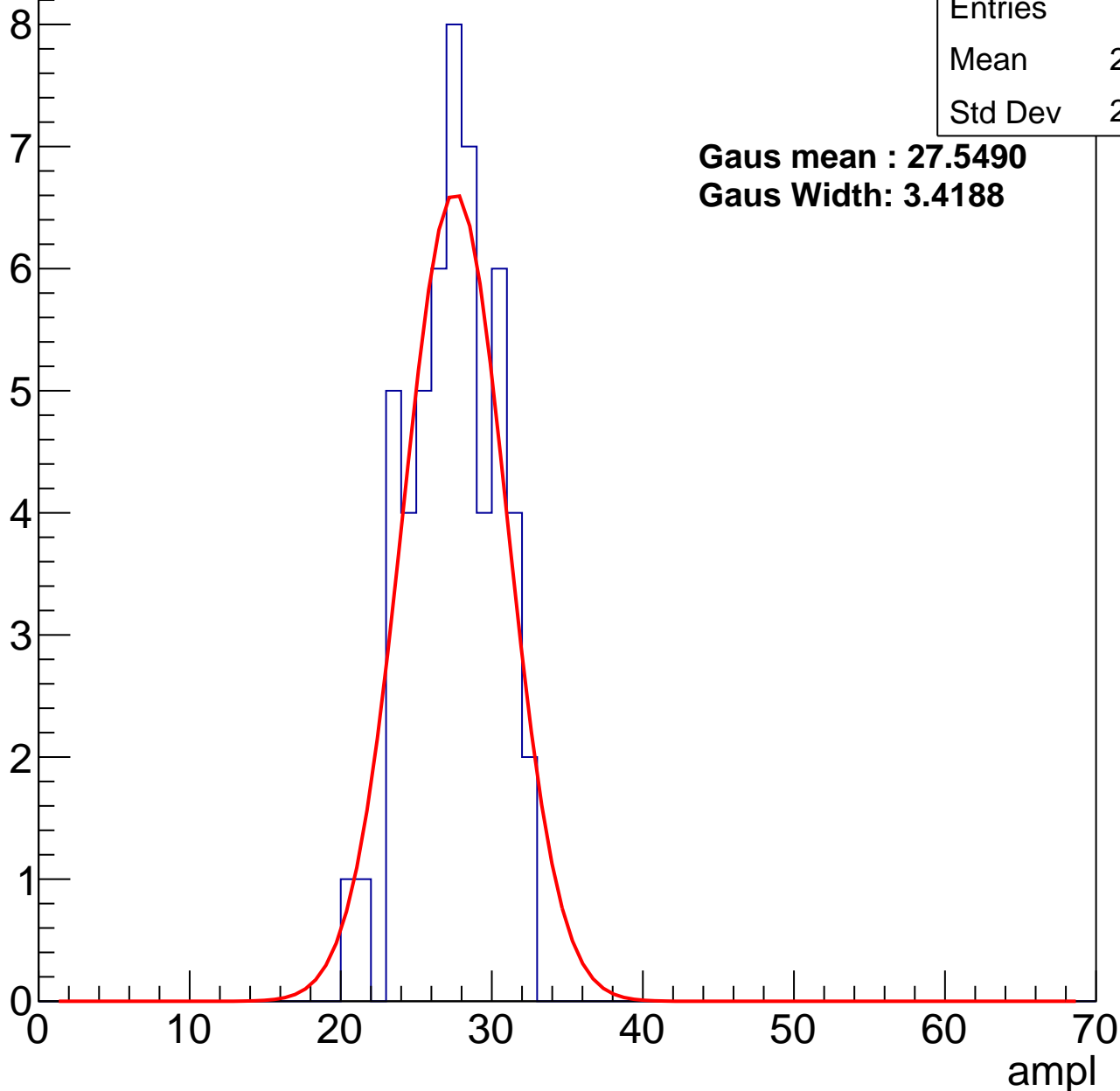
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	26.96
Std Dev	2.808

Gaus mean : 27.5490

Gaus Width: 3.4188



B1L002S, U7-ch76, adc1

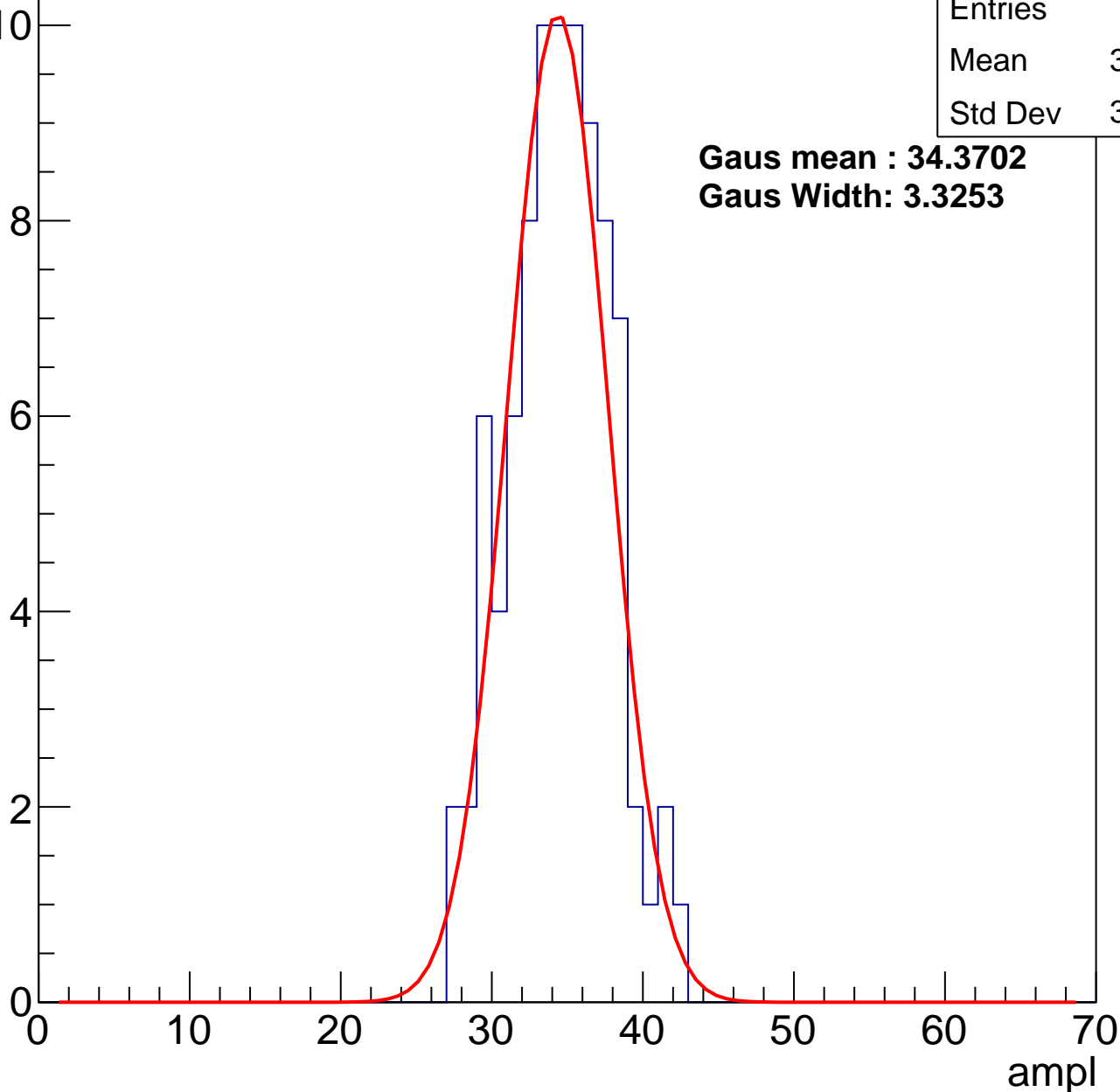
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	88
Mean	34.02
Std Dev	3.299

Gaus mean : 34.3702

Gaus Width: 3.3253



B1L002S, U7-ch76, adc2

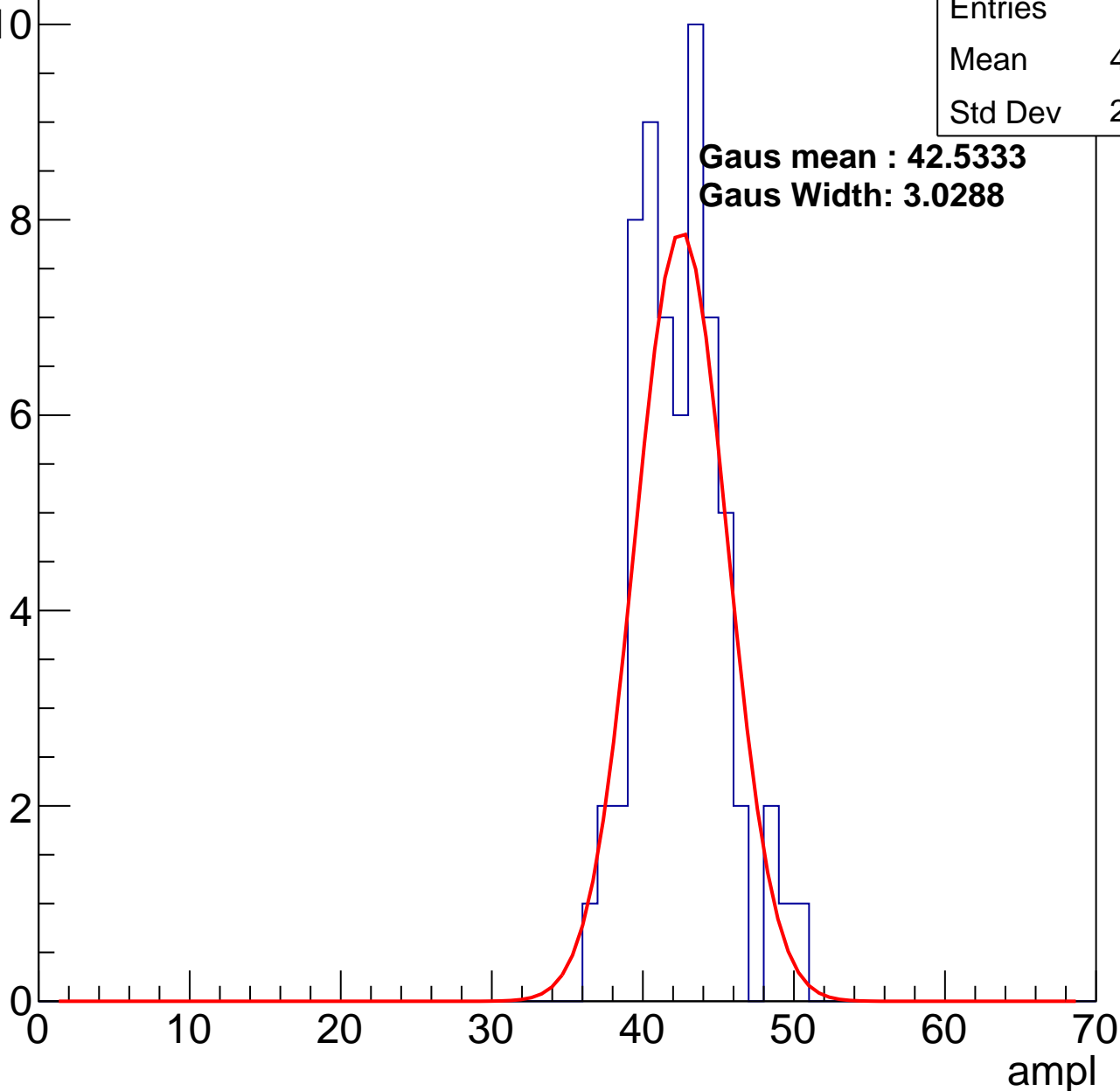
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	42.02
Std Dev	2.909

Gaus mean : 42.5333

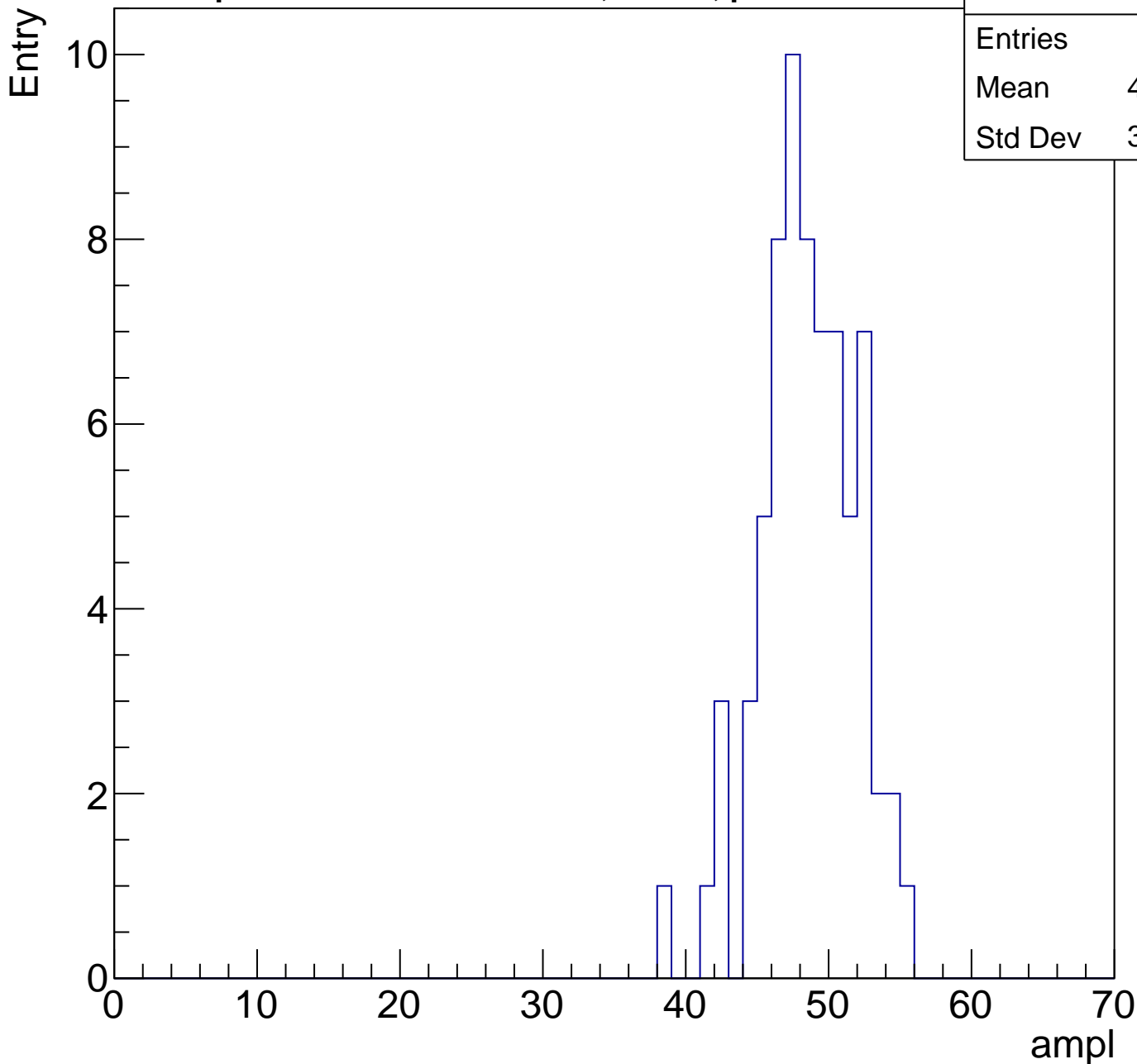
Gaus Width: 3.0288



B1L002S, U7-ch76, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	48.07
Std Dev	3.283

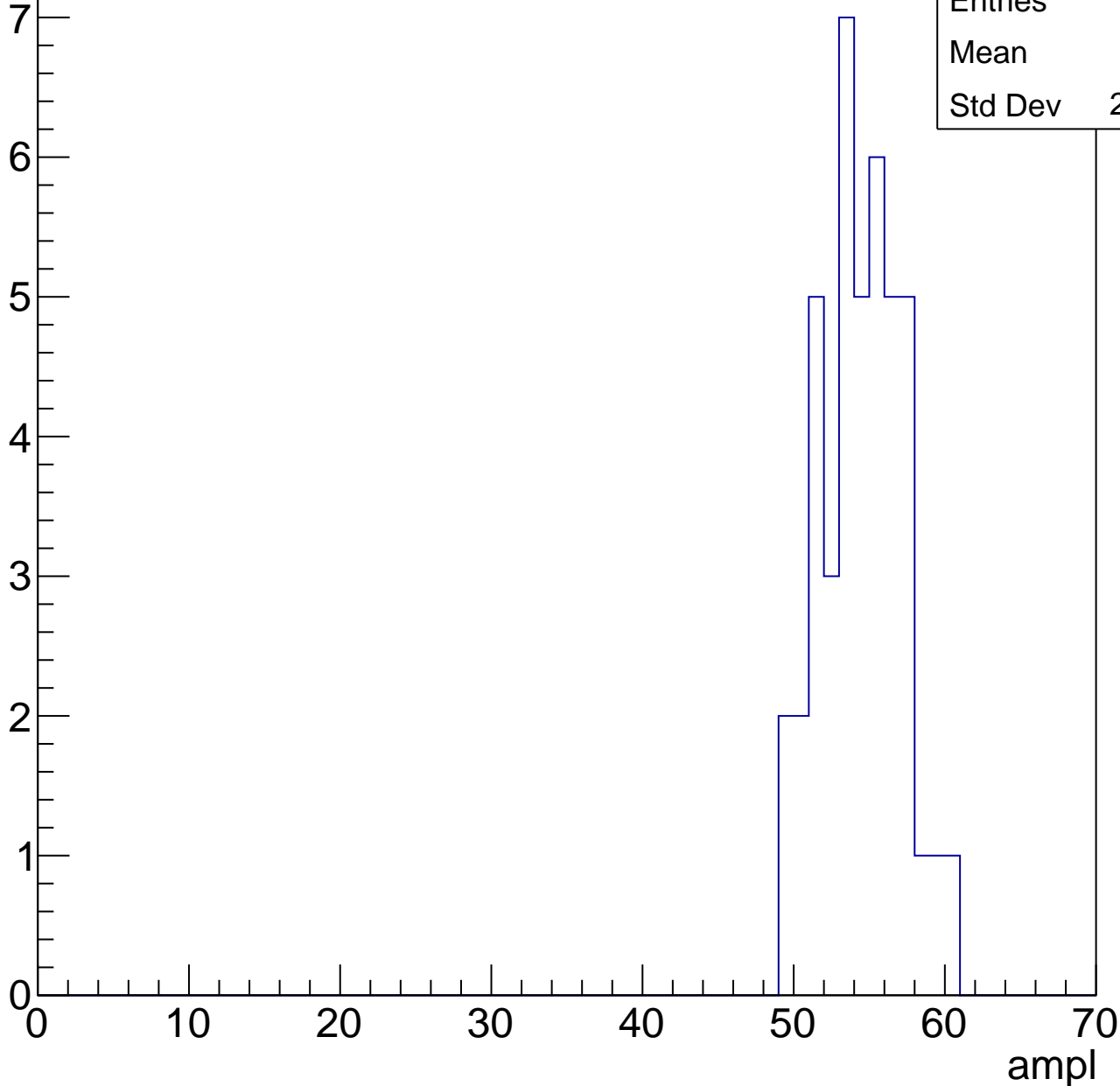


B1L002S, U7-ch76, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

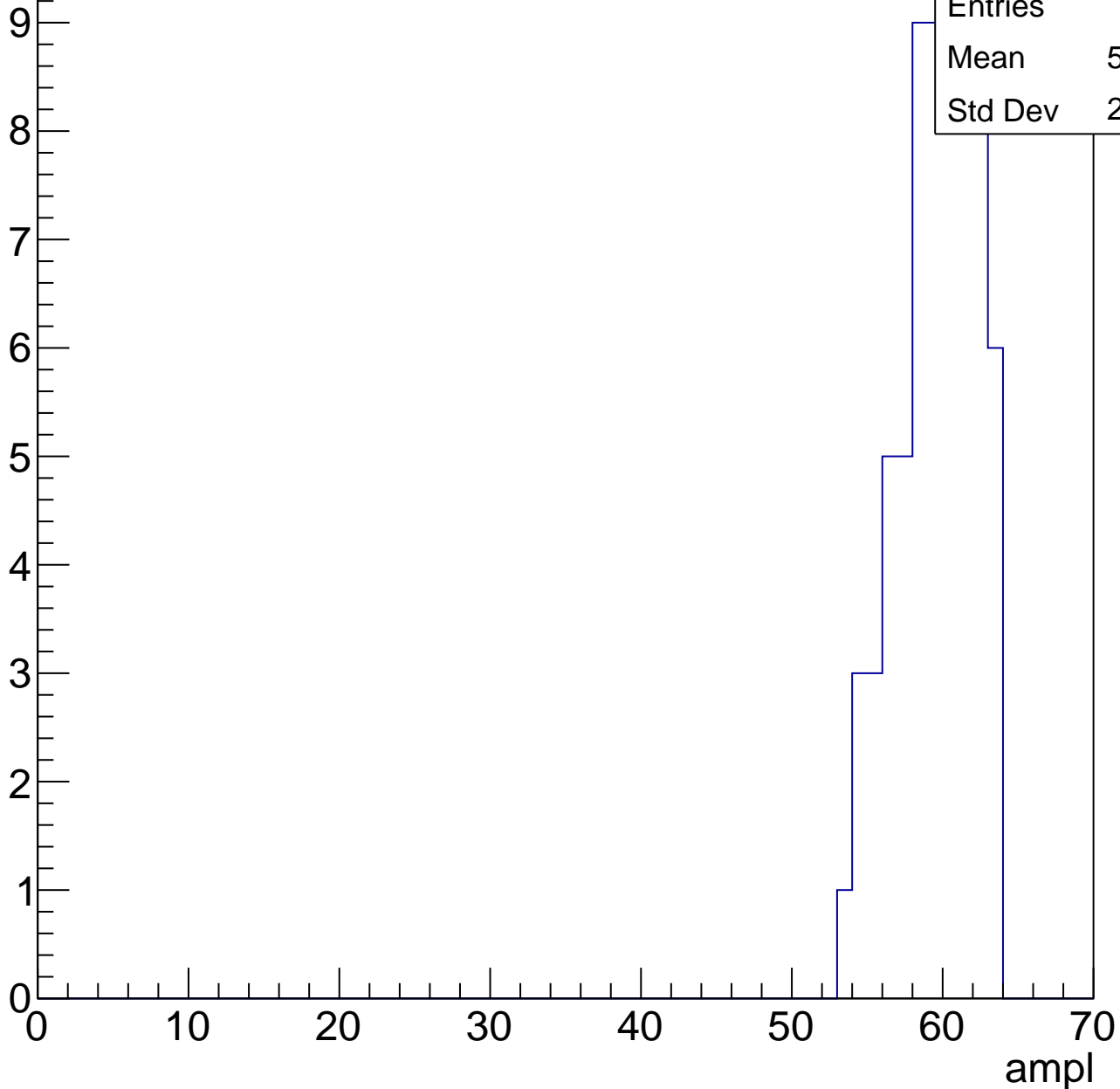
Entries	43
Mean	54
Std Dev	2.615



B1L002S, U7-ch76, adc5

calib_packv5_042523_0143.root, FC#10, port B3

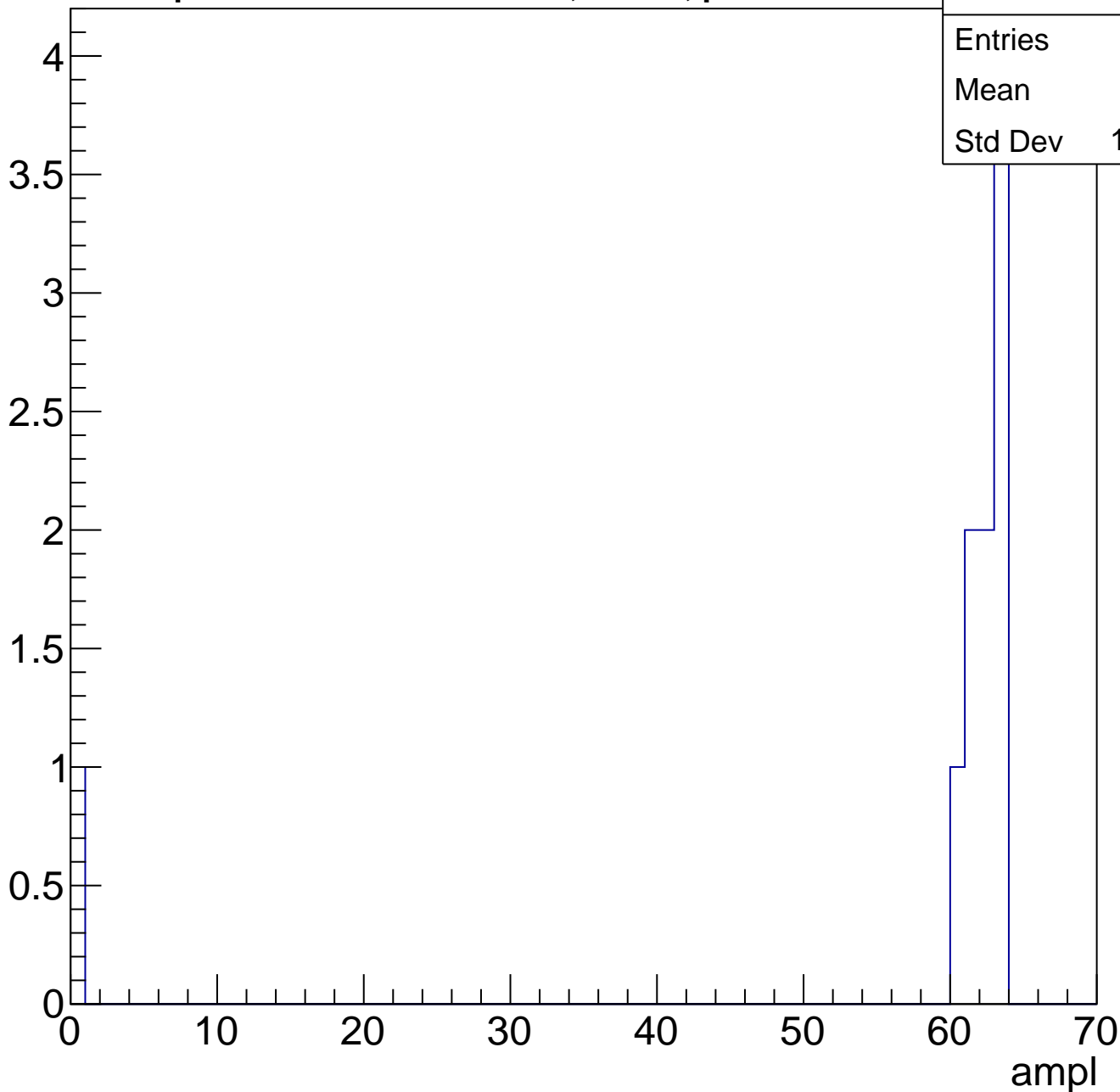
Entry



B1L002S, U7-ch76, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch76, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch77, adc0

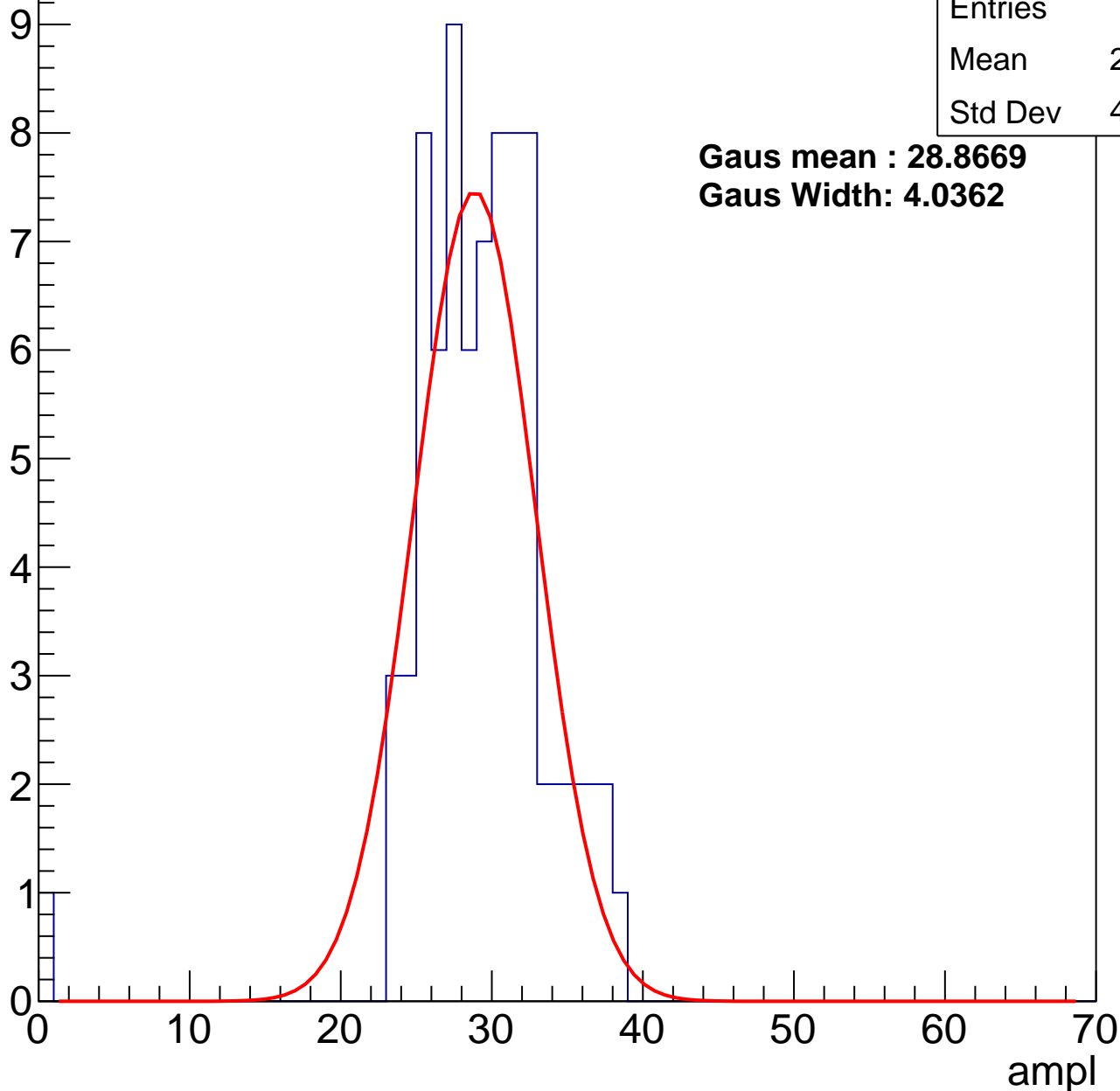
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	78
Mean	28.76
Std Dev	4.826

Gaus mean : 28.8669

Gaus Width: 4.0362



B1L002S, U7-ch77, adc1

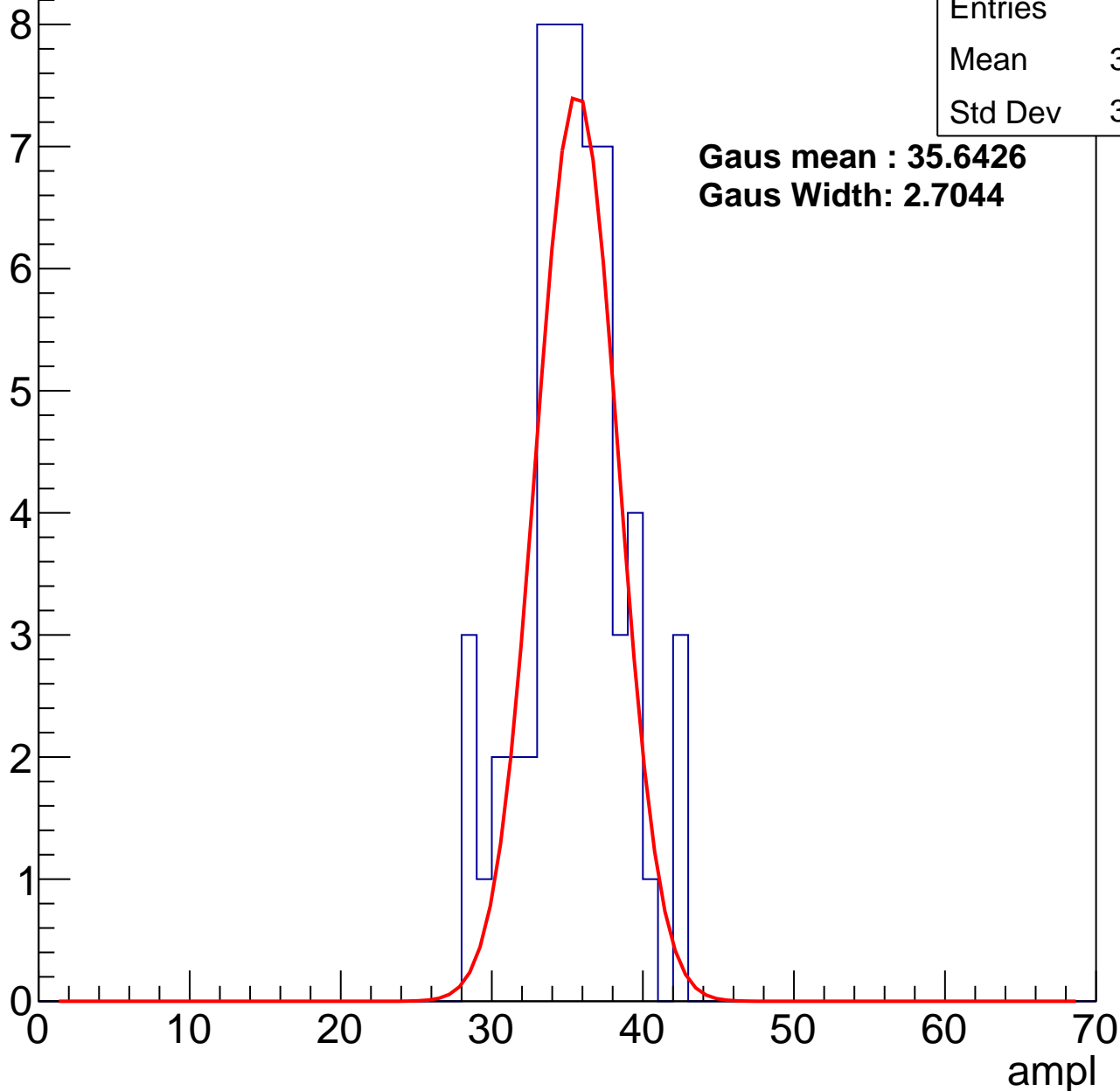
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.95
Std Dev	3.244

Gaus mean : 35.6426

Gaus Width: 2.7044



B1L002S, U7-ch77, adc2

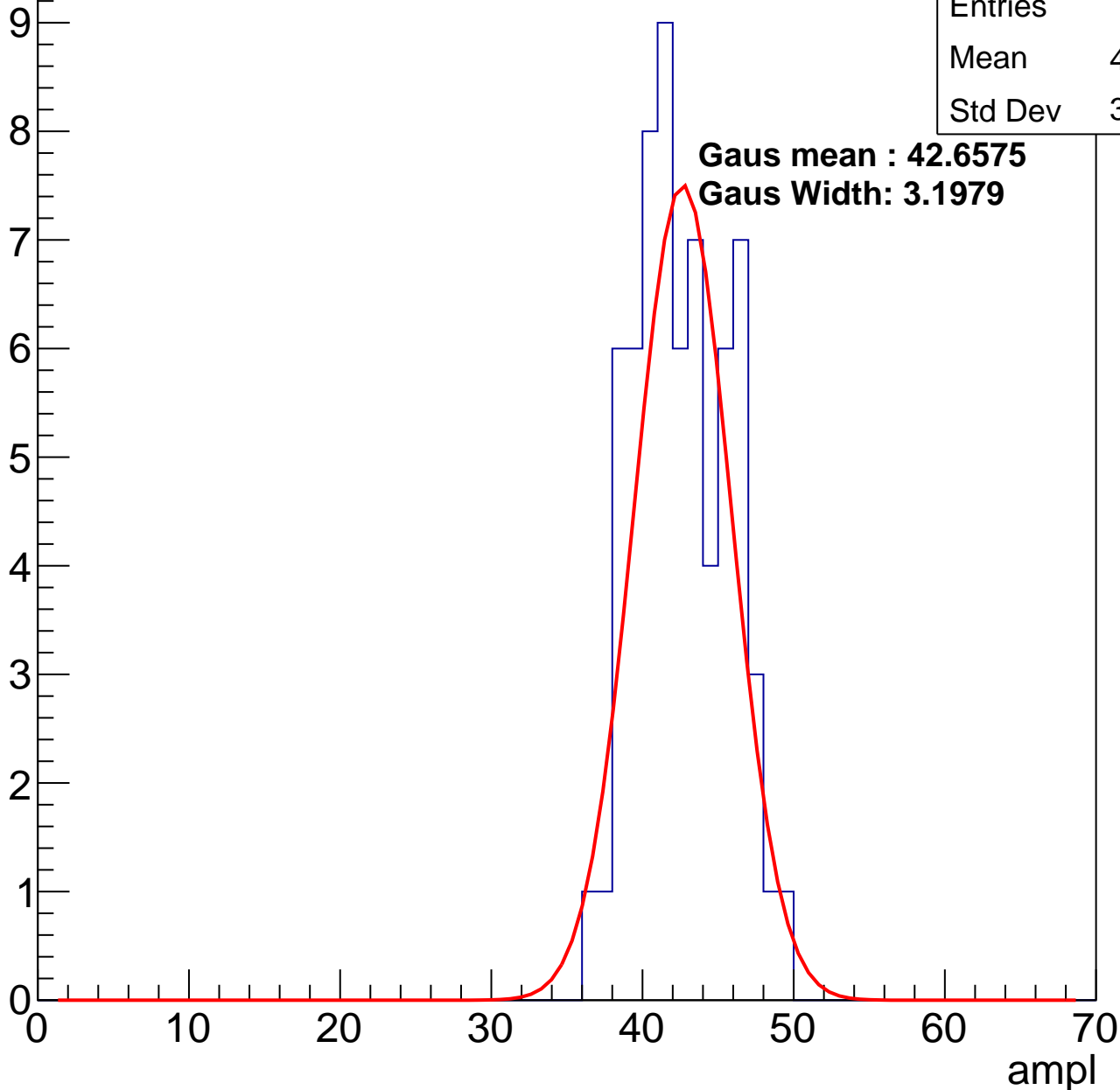
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	42.17
Std Dev	3.013

Gaus mean : 42.6575

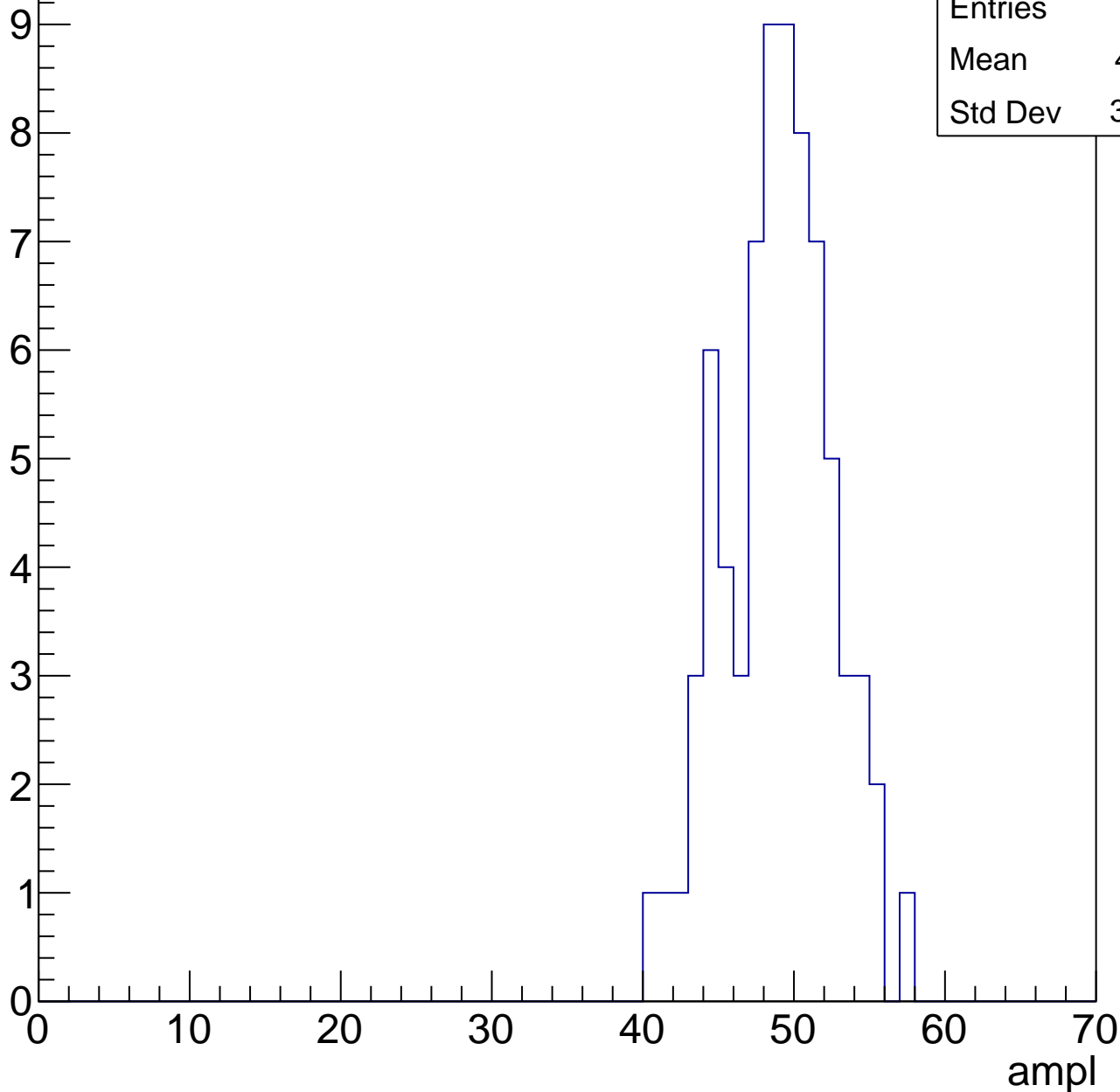
Gaus Width: 3.1979



B1L002S, U7-ch77, adc3

calib_packv5_042523_0143.root, FC#10, port B3

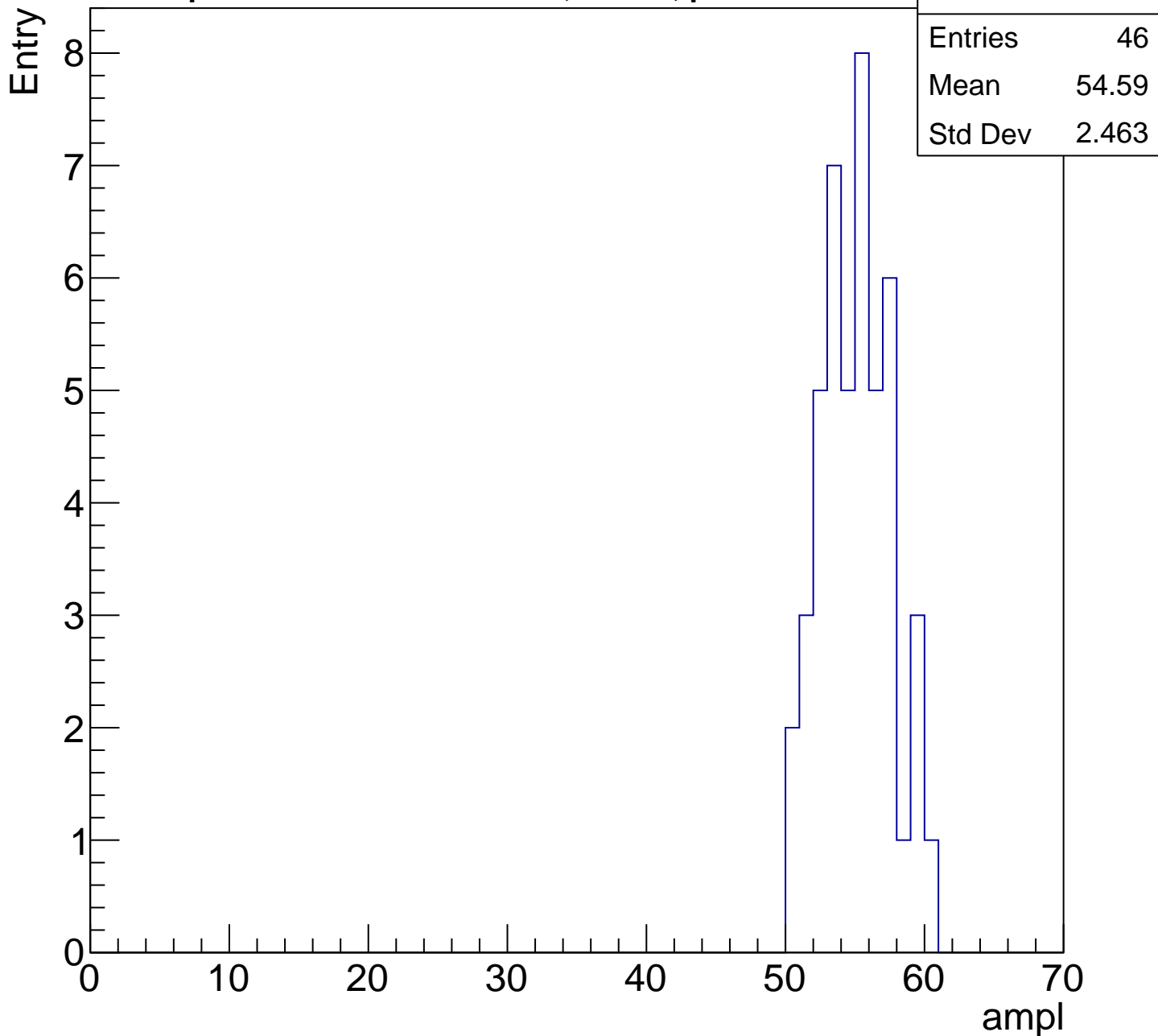
Entry



Entries	73
Mean	48.51
Std Dev	3.516

B1L002S, U7-ch77, adc4

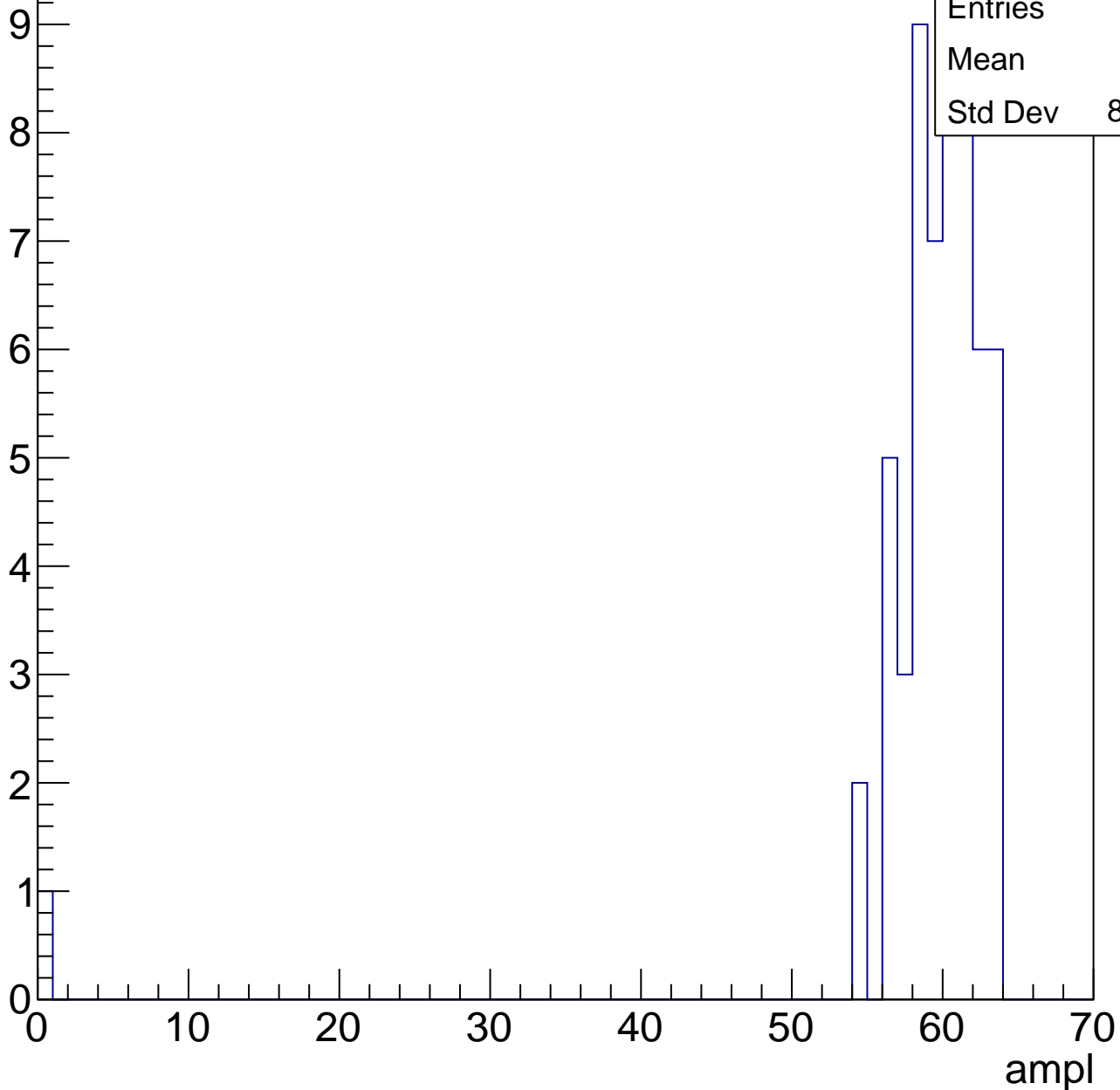
calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch77, adc5

calib_packv5_042523_0143.root, FC#10, port B3

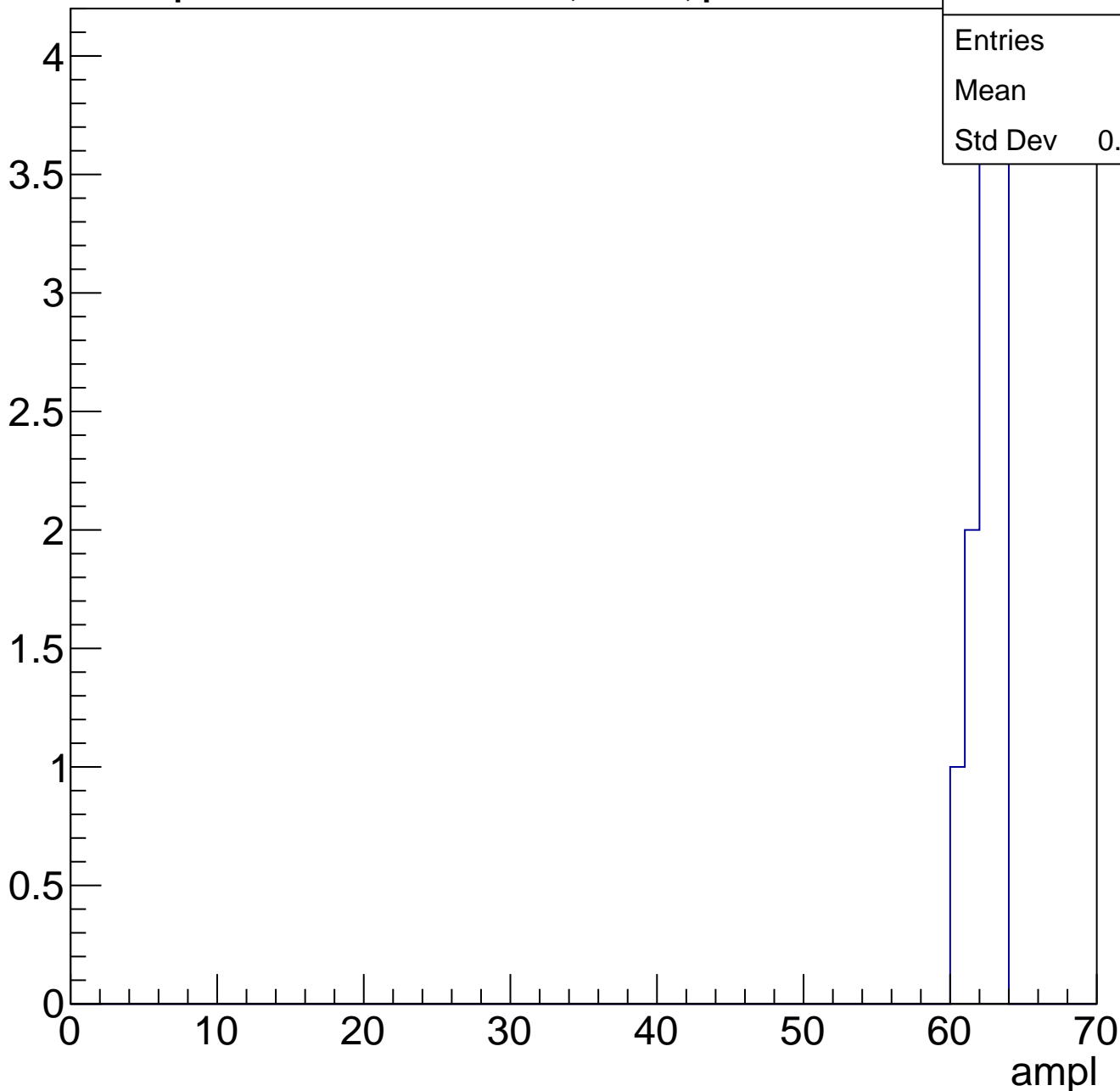
Entry



B1L002S, U7-ch77, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch77, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch78, adc0

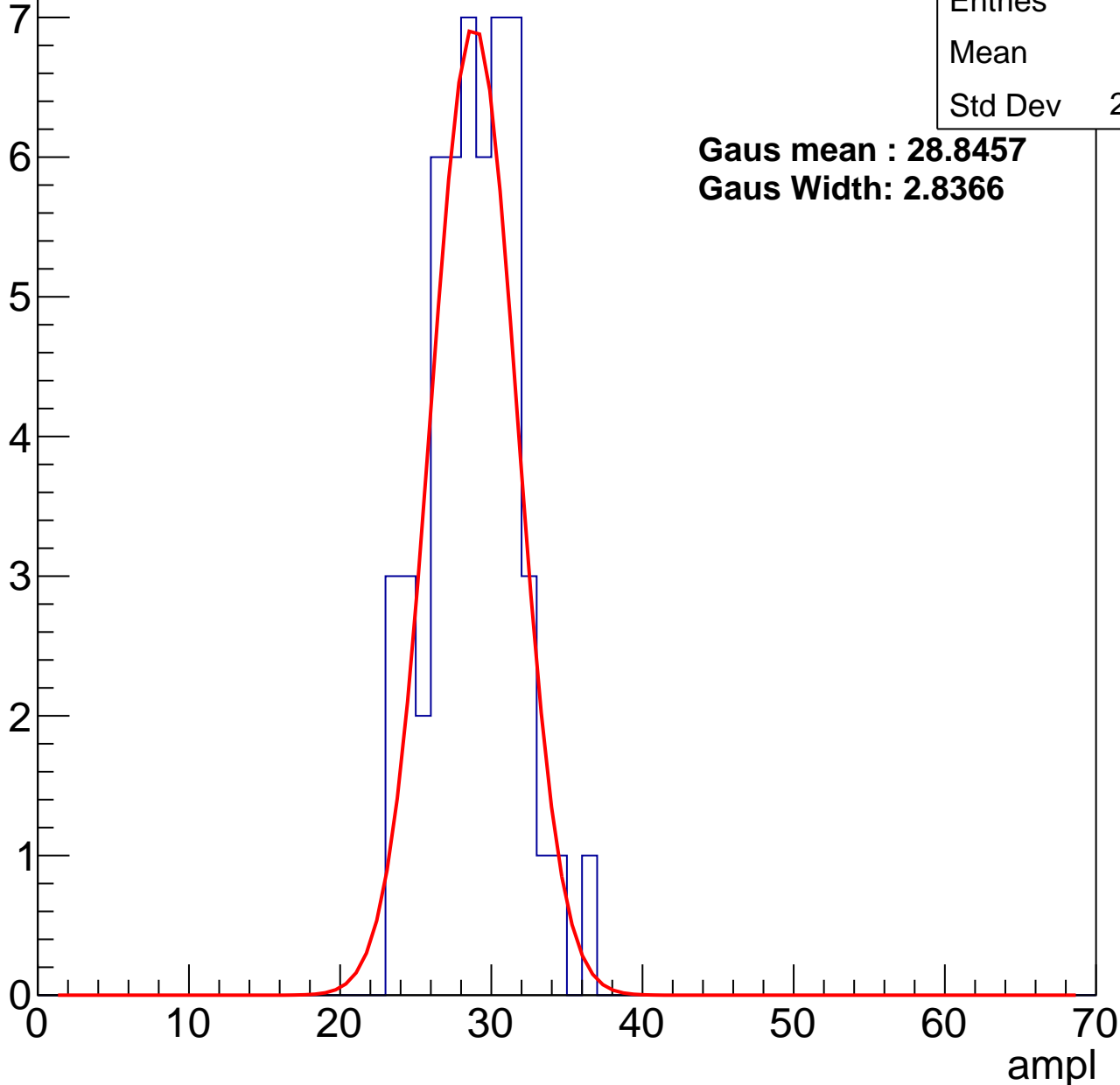
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.4
Std Dev	2.857

Gaus mean : 28.8457

Gaus Width: 2.8366



B1L002S, U7-ch78, adc1

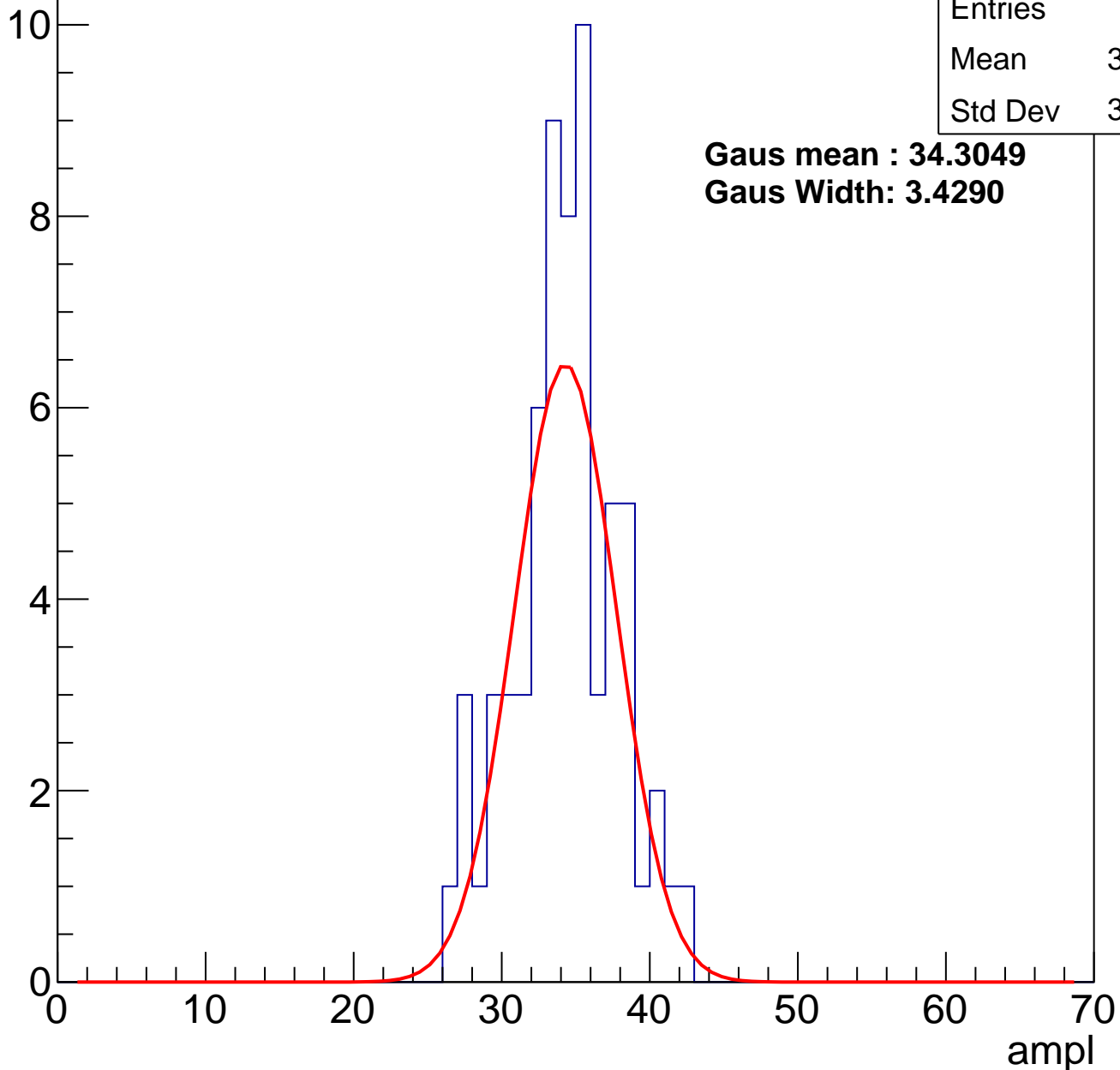
calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	33.86
Std Dev	3.477

Gaus mean : 34.3049

Gaus Width: 3.4290

Entry



B1L002S, U7-ch78, adc2

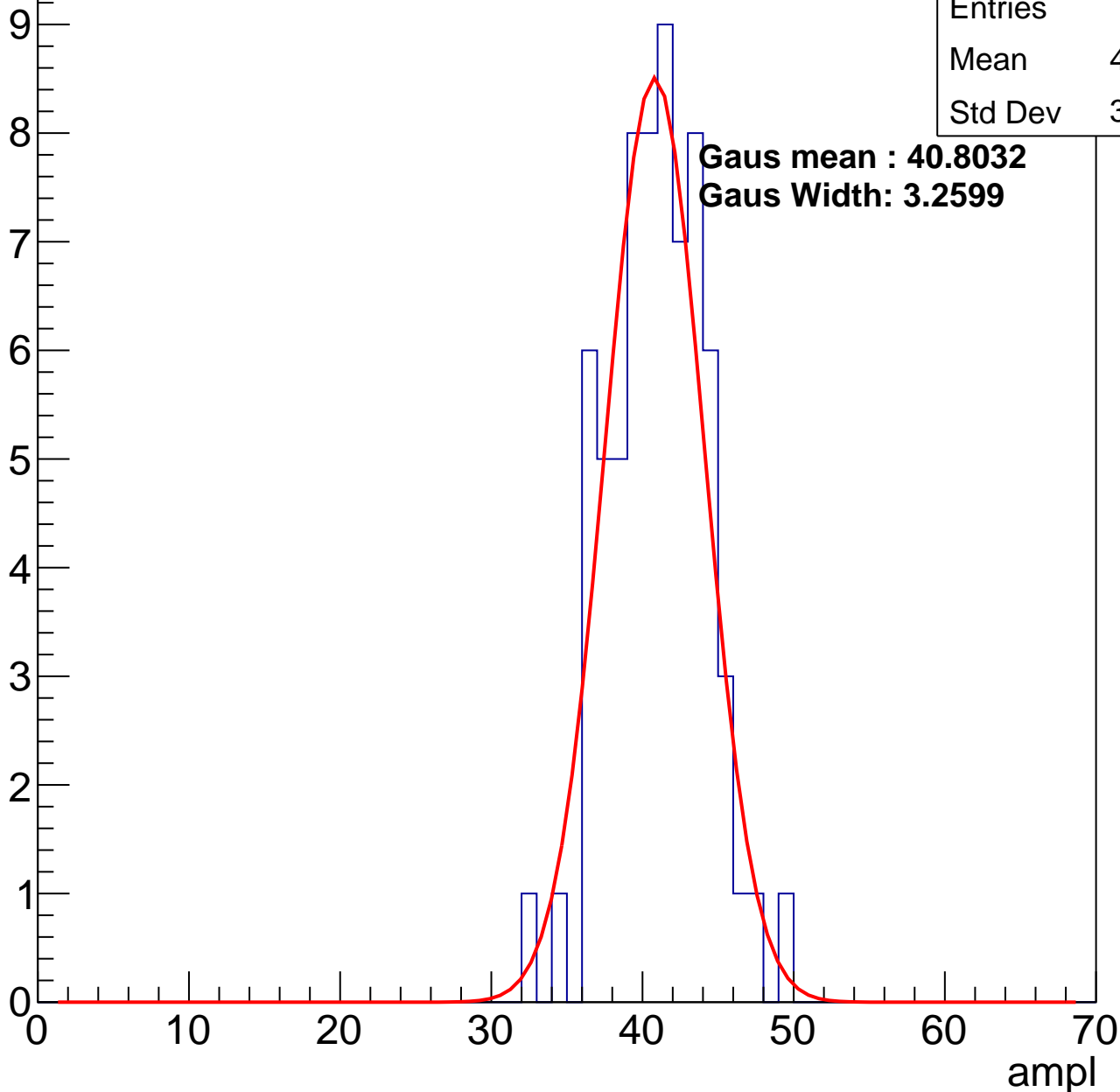
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.53
Std Dev	3.152

Gaus mean : 40.8032

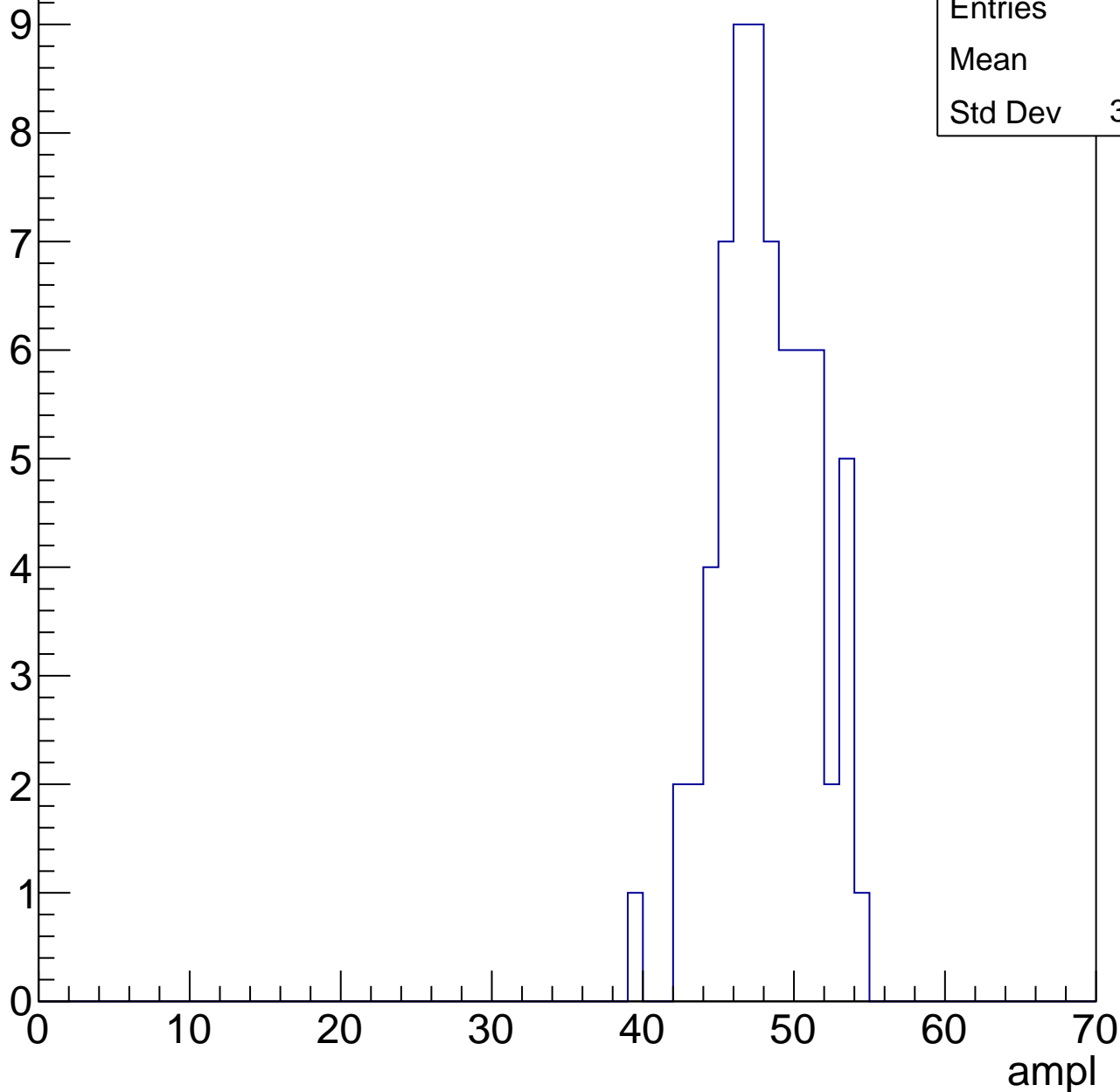
Gaus Width: 3.2599



B1L002S, U7-ch78, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

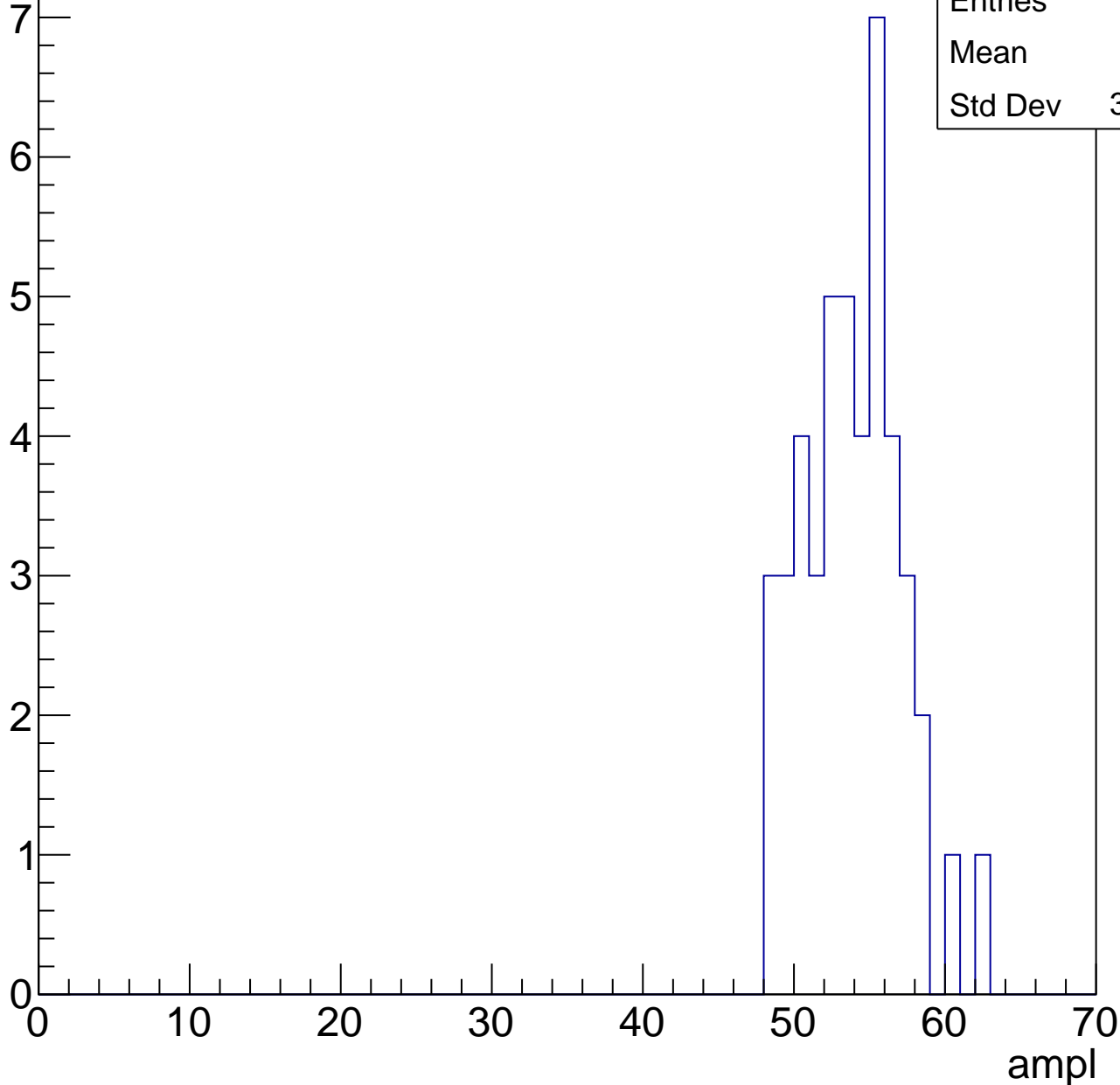


Entries	67
Mean	47.7
Std Dev	3.105

B1L002S, U7-ch78, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



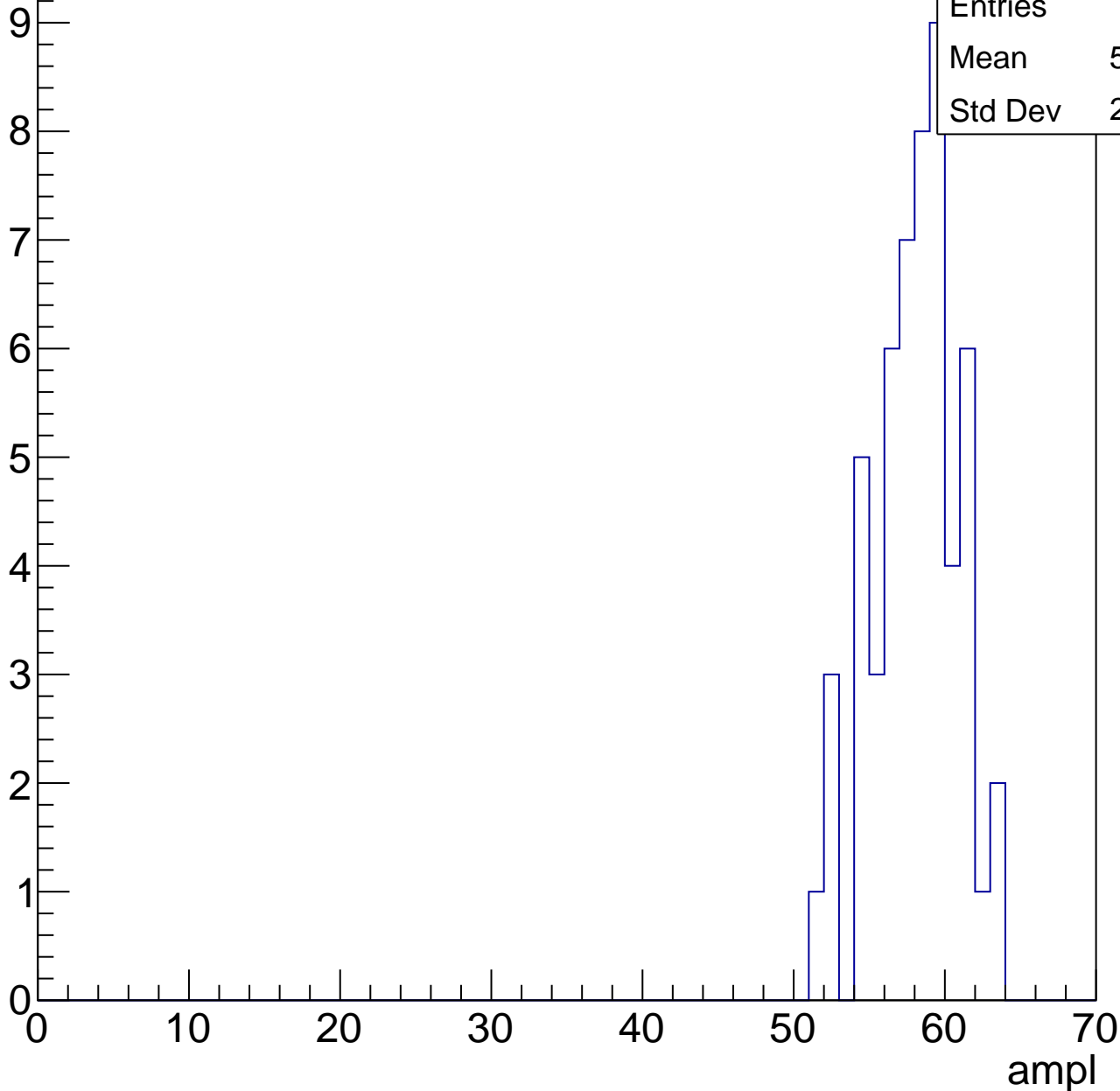
Entries	45
Mean	53.4
Std Dev	3.214

B1L002S, U7-ch78, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.56
Std Dev	2.795

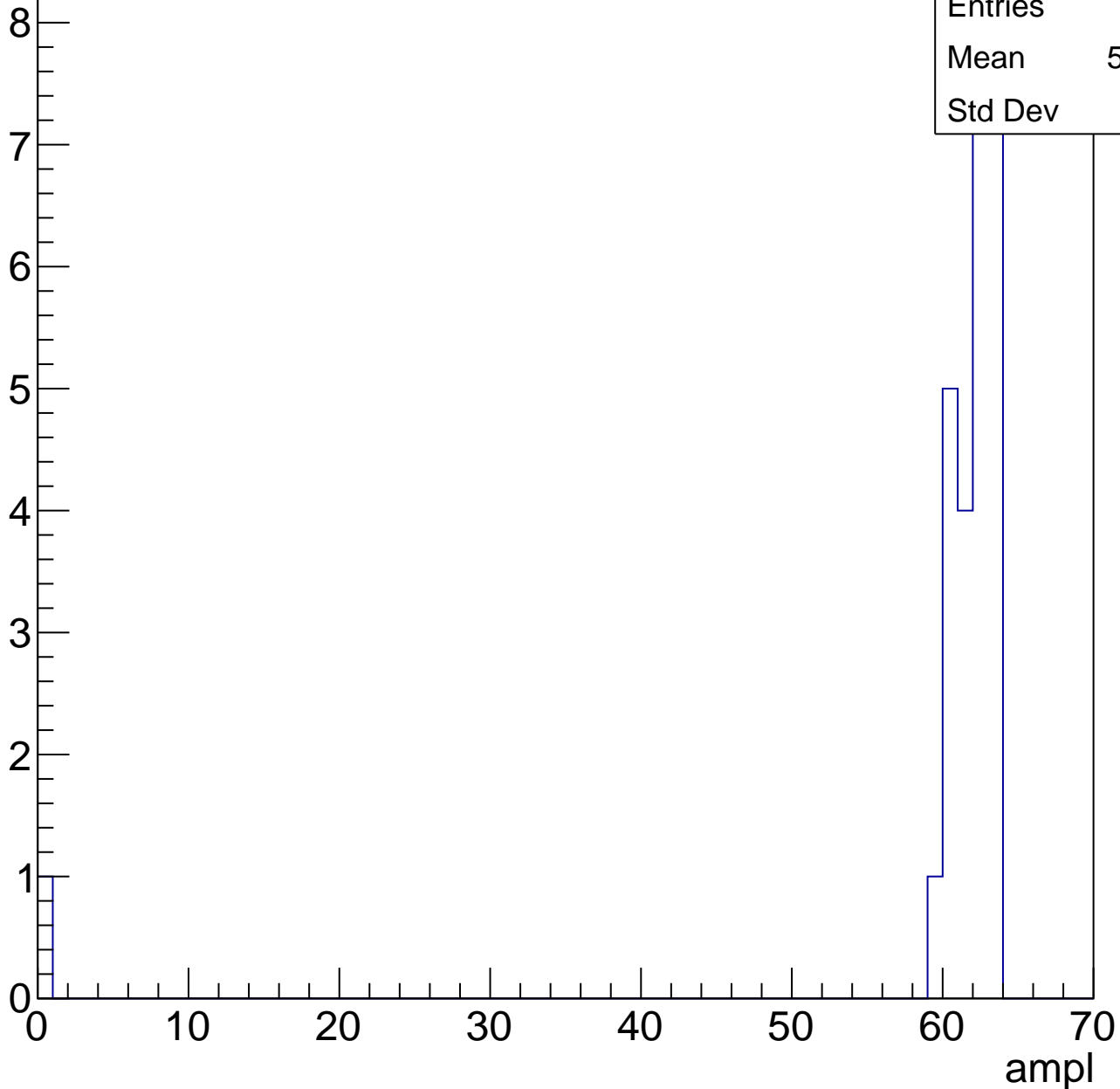


B1L002S, U7-ch78, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	27
Mean	59.37
Std Dev	11.7



B1L002S, U7-ch78, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch79, adc0

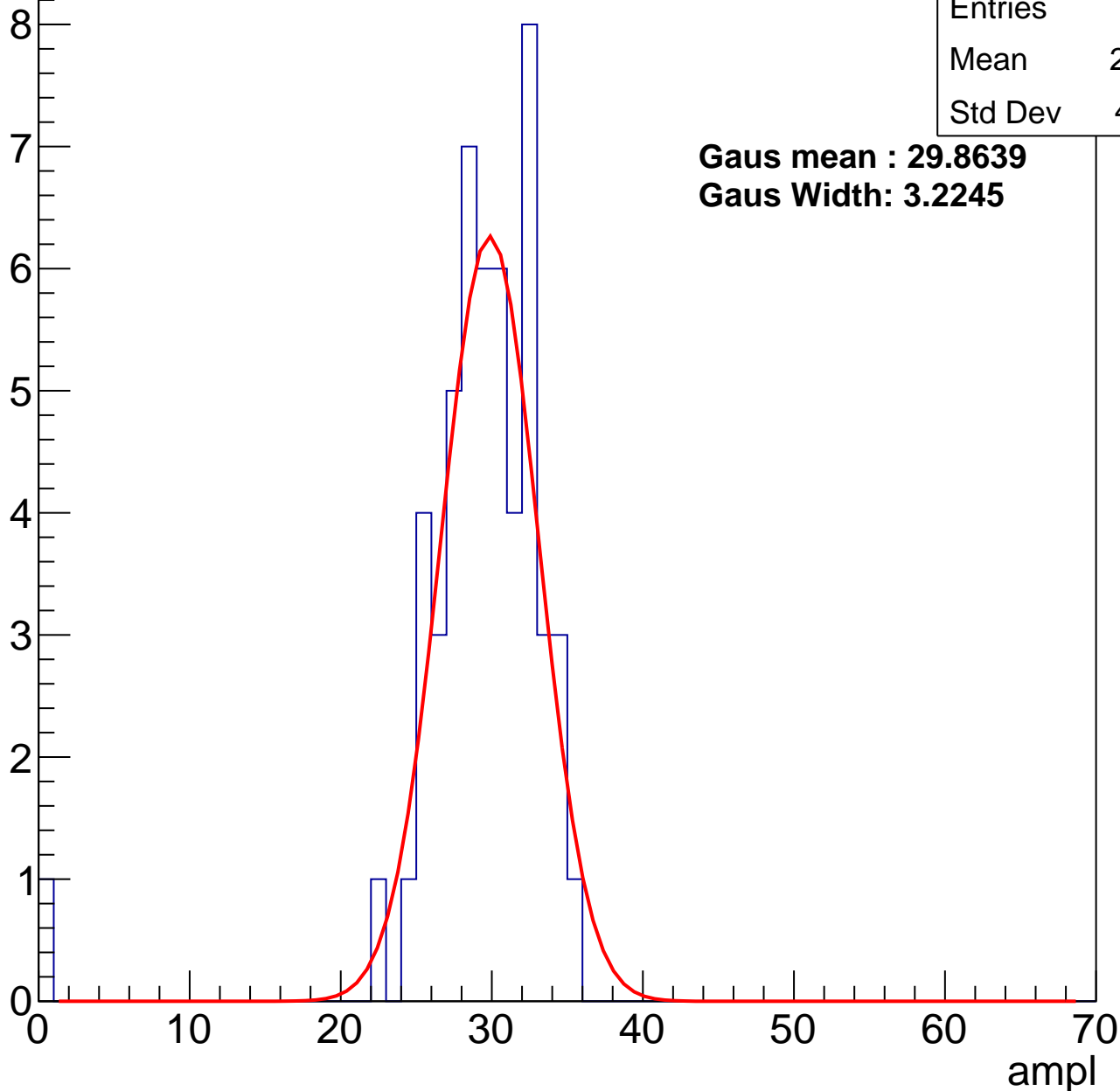
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.77
Std Dev	4.921

Gaus mean : 29.8639

Gaus Width: 3.2245



B1L002S, U7-ch79, adc1

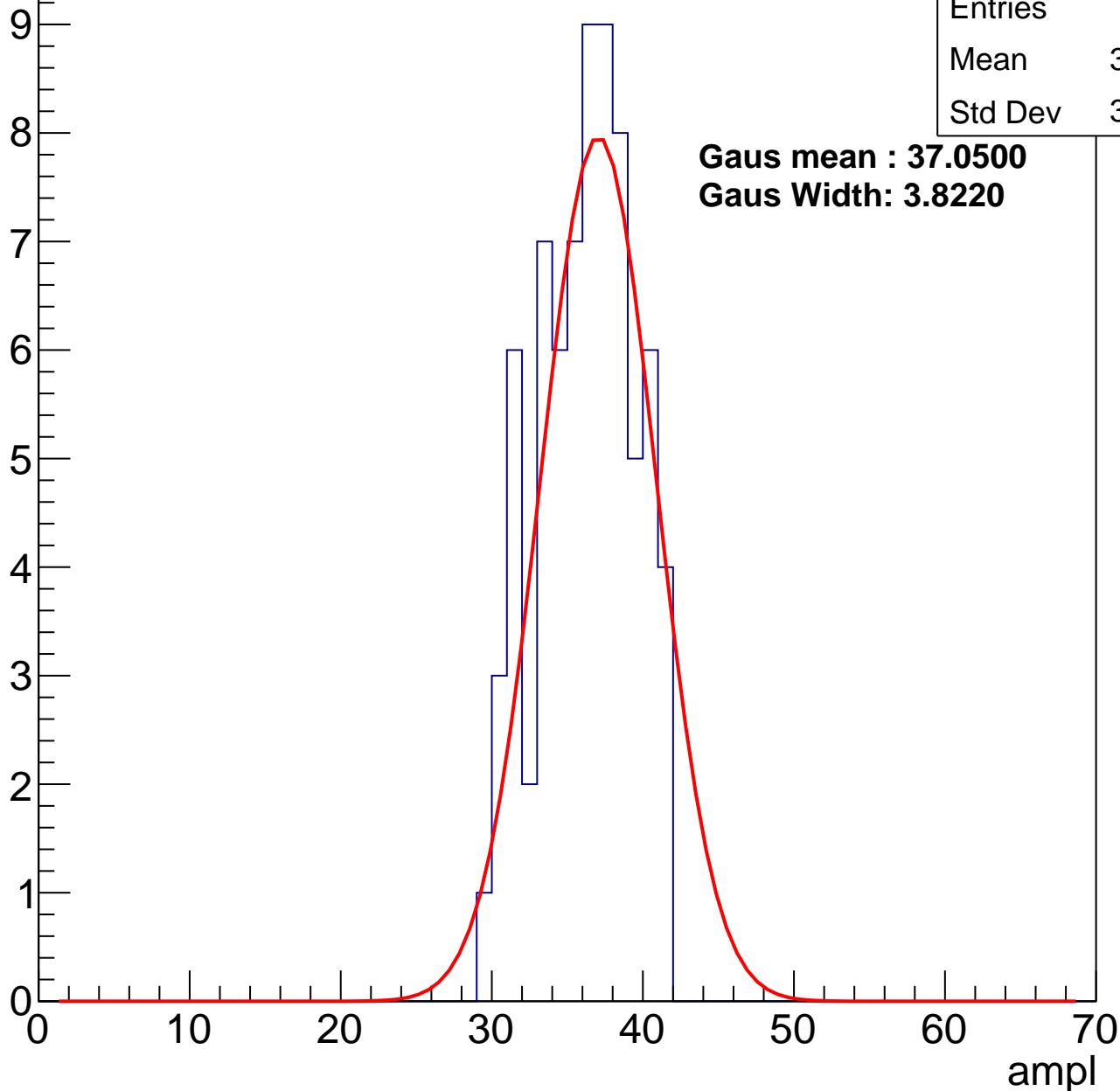
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	35.74
Std Dev	3.119

Gaus mean : 37.0500

Gaus Width: 3.8220



B1L002S, U7-ch79, adc2

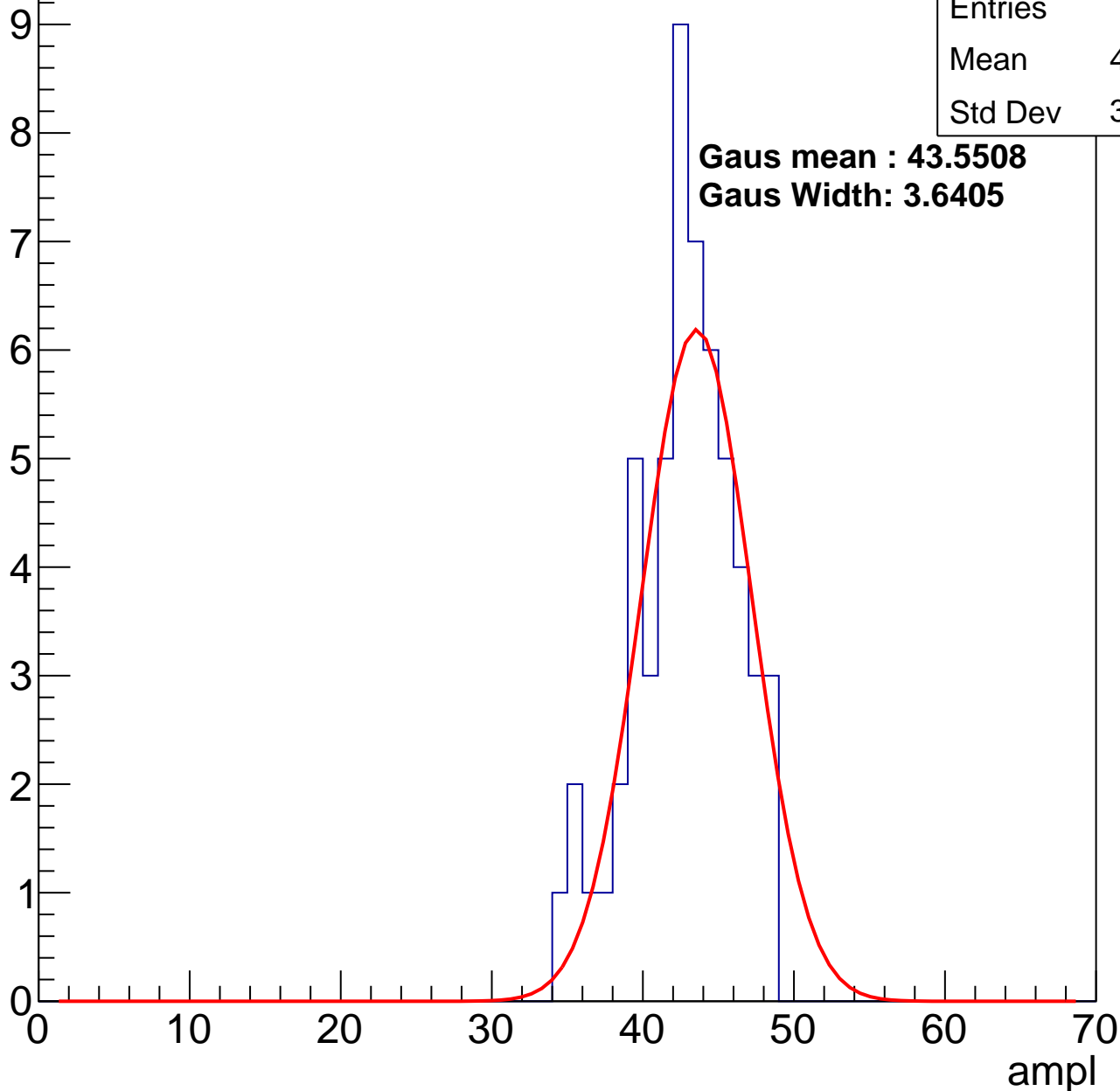
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	42.28
Std Dev	3.339

Gaus mean : 43.5508

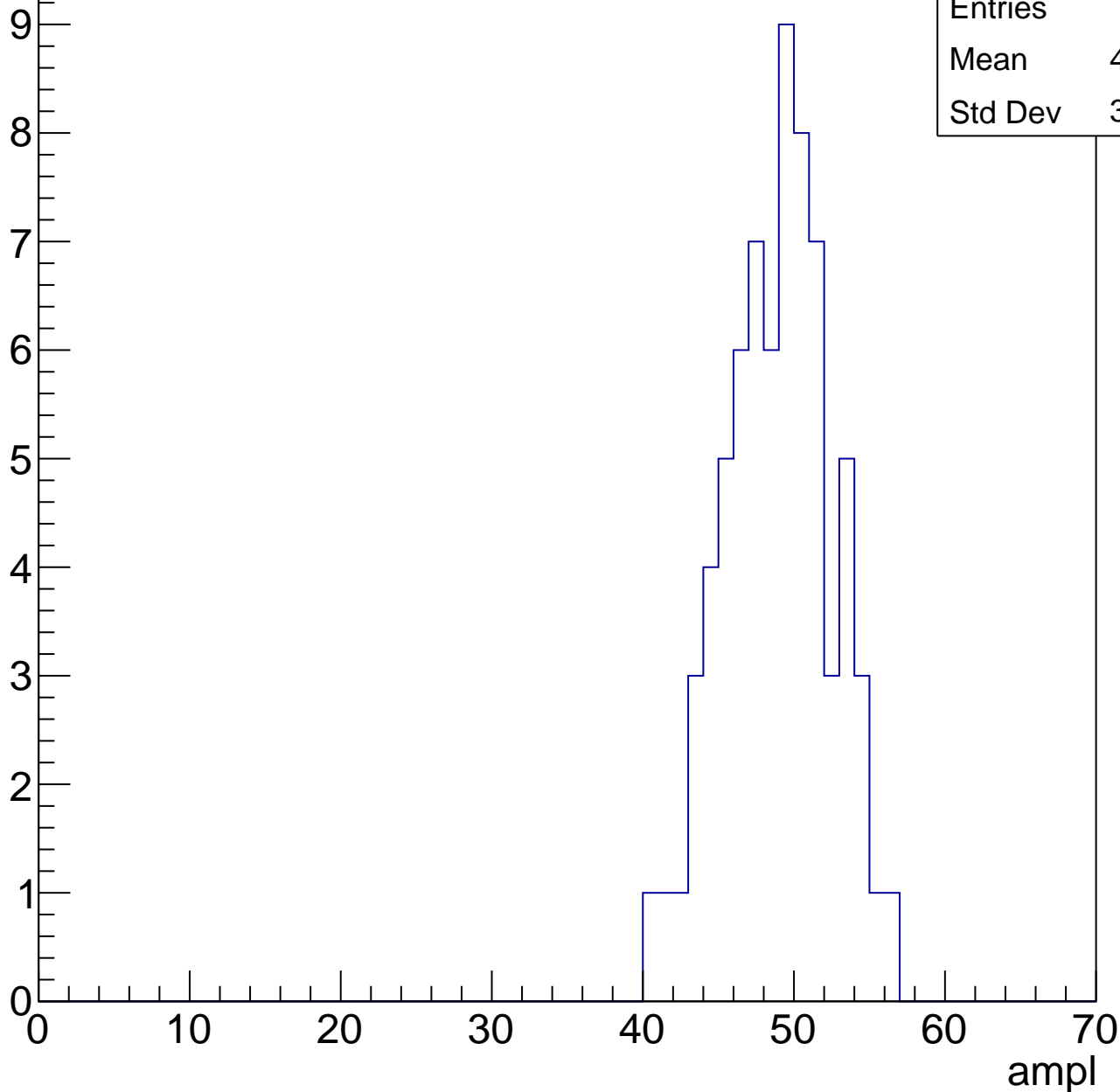
Gaus Width: 3.6405



B1L002S, U7-ch79, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



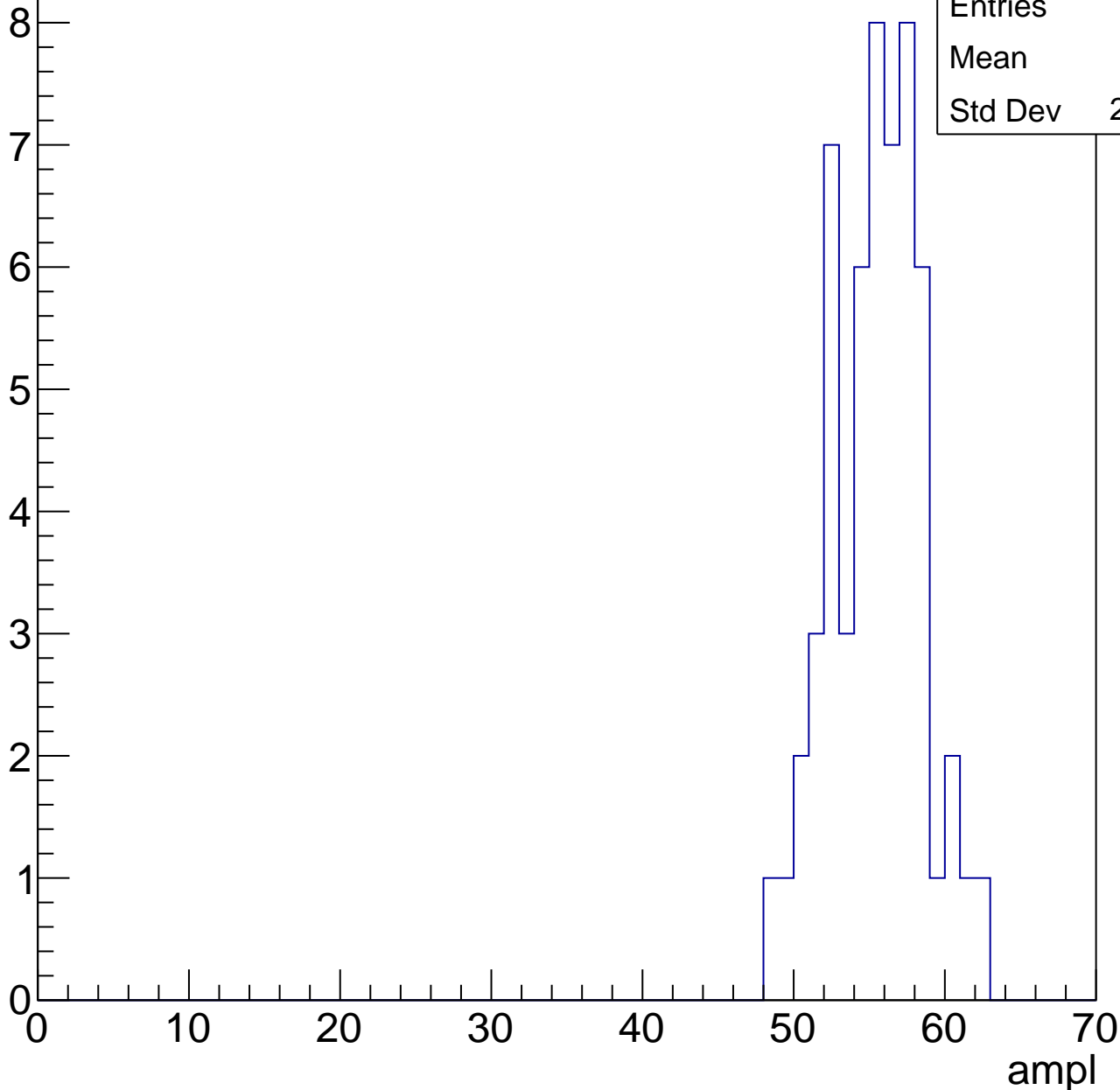
Entries	71
Mean	48.42
Std Dev	3.459

B1L002S, U7-ch79, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	55
Std Dev	2.985

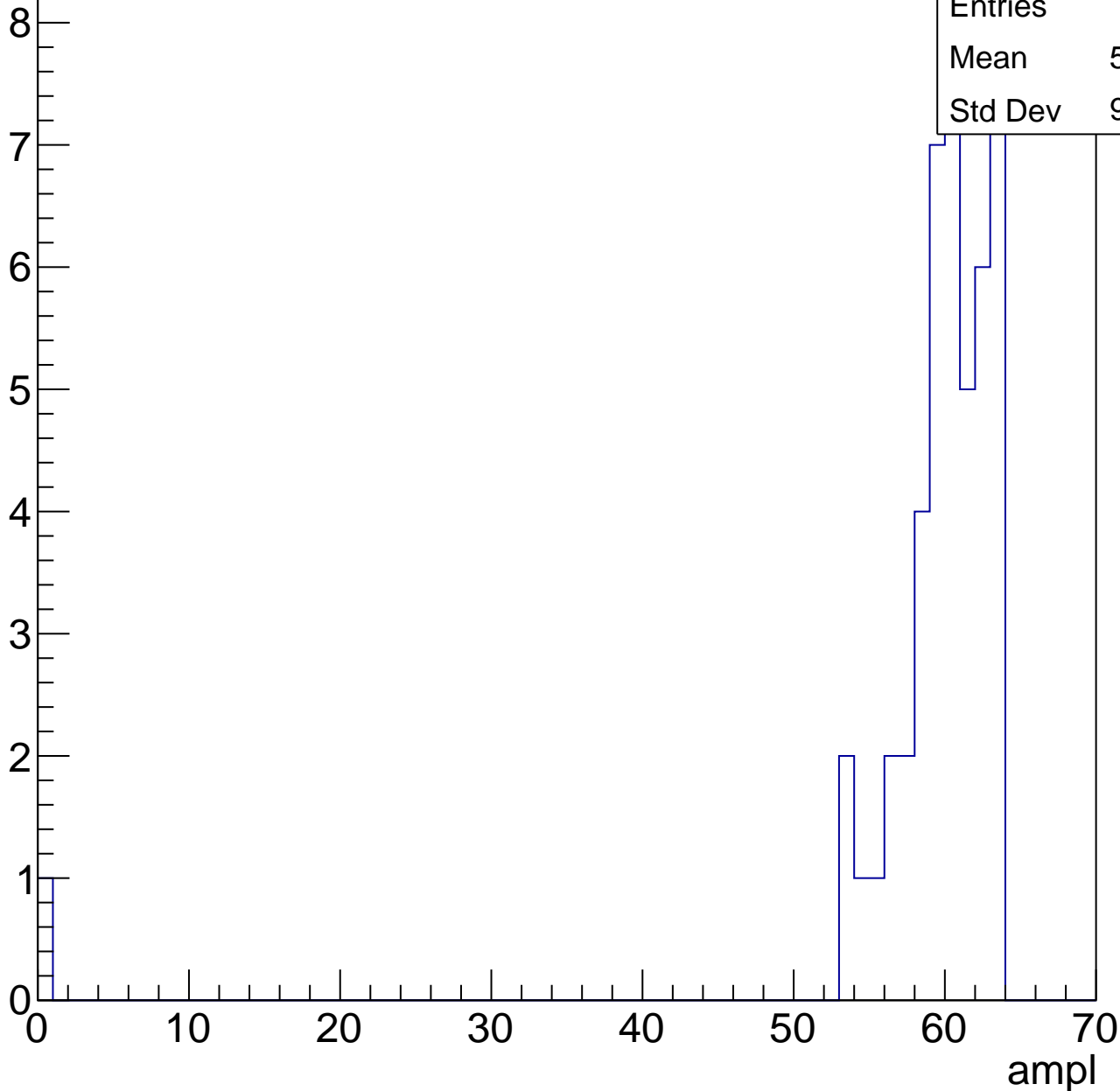


B1L002S, U7-ch79, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

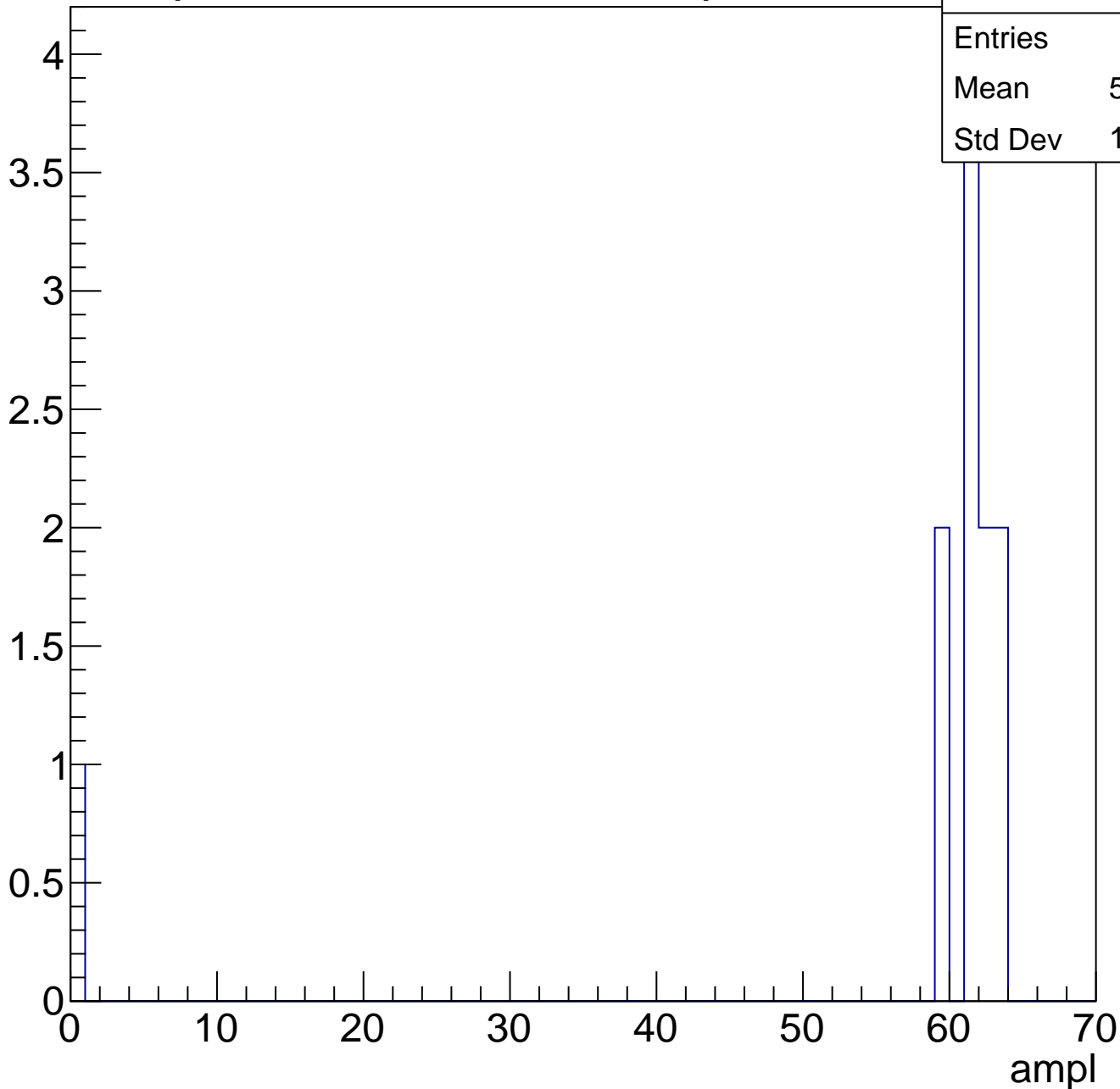
Entries	47
Mean	58.45
Std Dev	9.015



B1L002S, U7-ch79, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch79, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch80, adc0

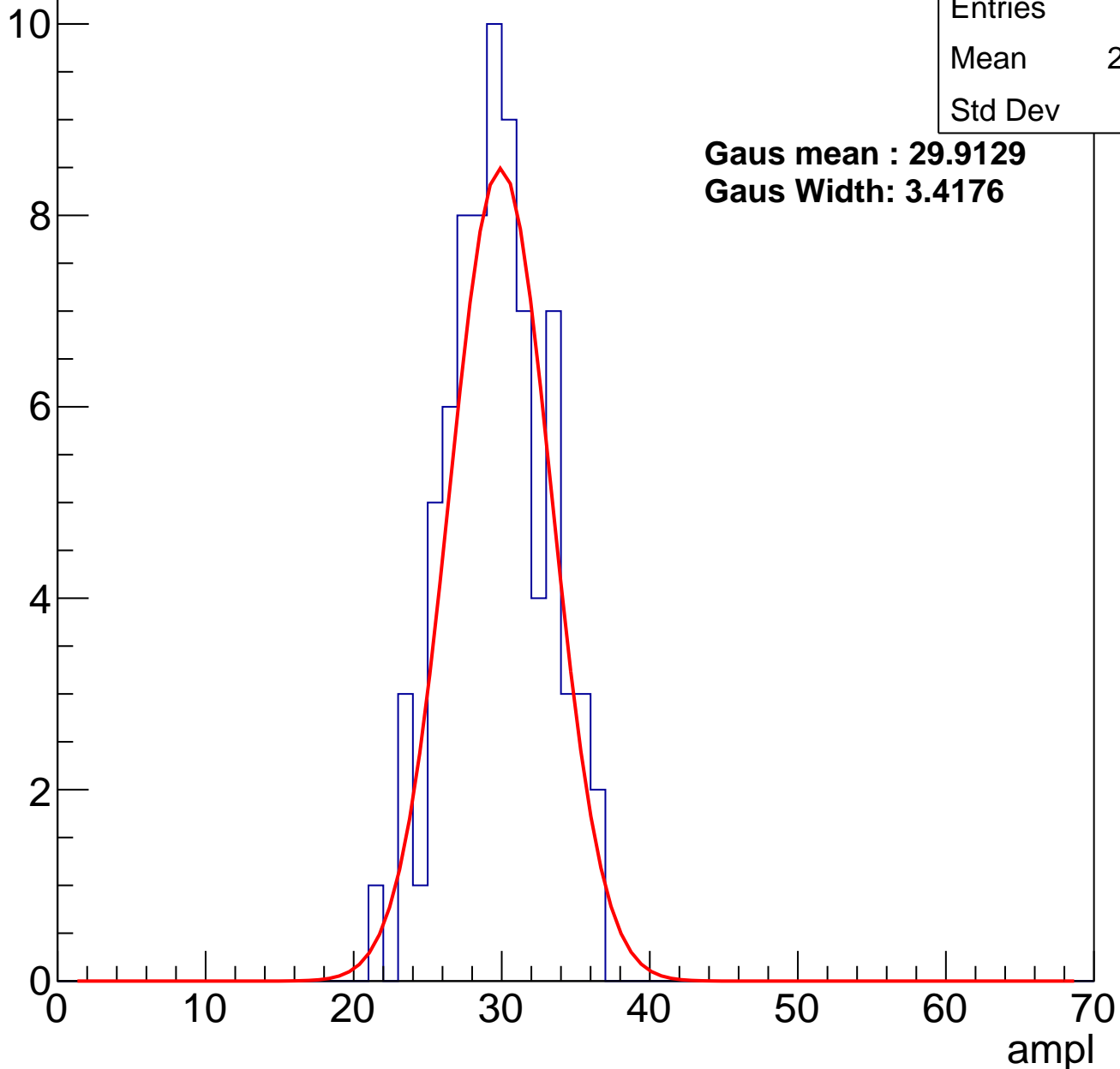
calib_packv5_042523_0143.root, FC#10, port B3

Entries	77
Mean	29.22
Std Dev	3.29

Gaus mean : 29.9129

Gaus Width: 3.4176

Entry



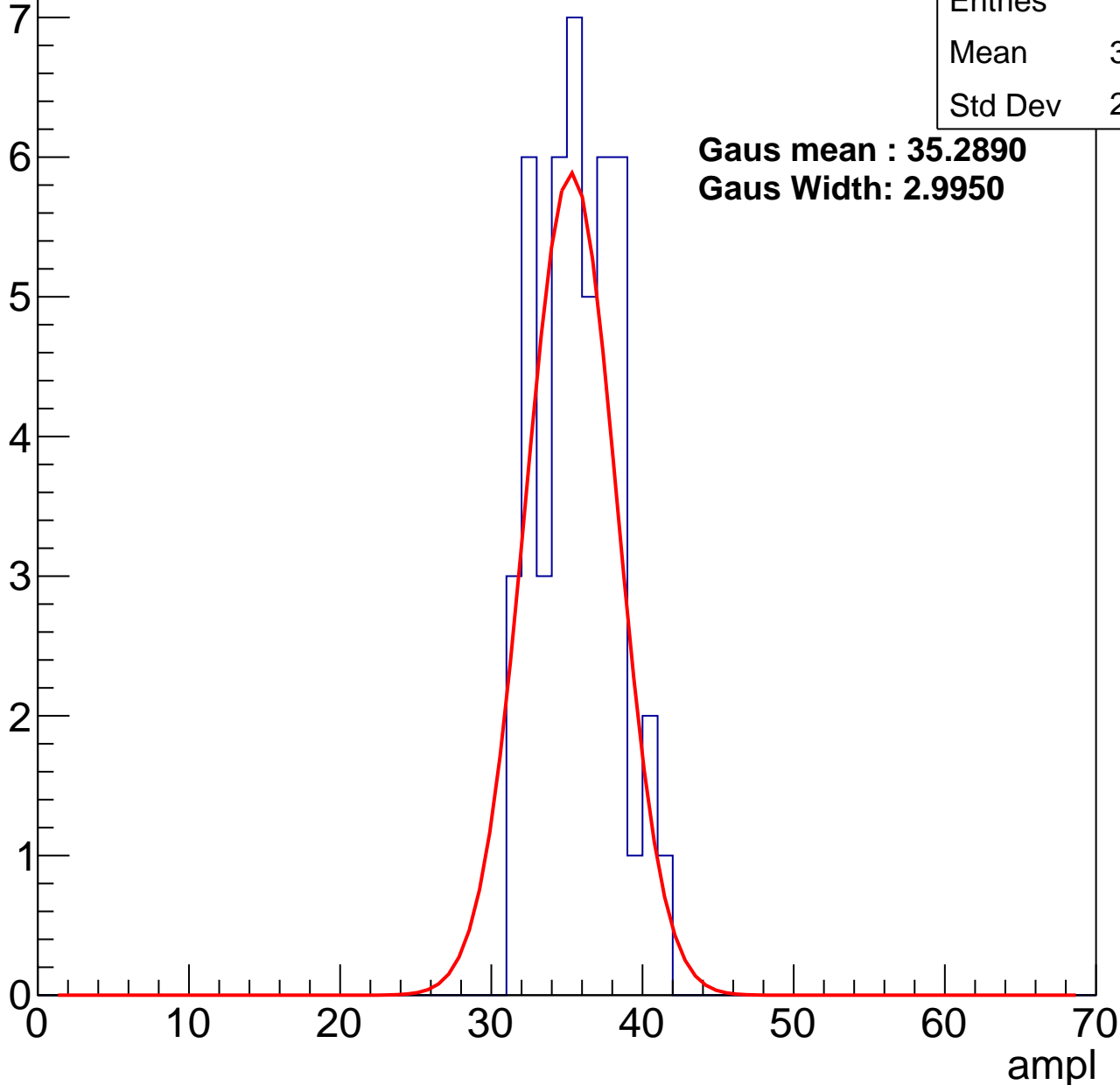
B1L002S, U7-ch80, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	35.28
Std Dev	2.559

Gaus mean : 35.2890
Gaus Width: 2.9950



B1L002S, U7-ch80, adc2

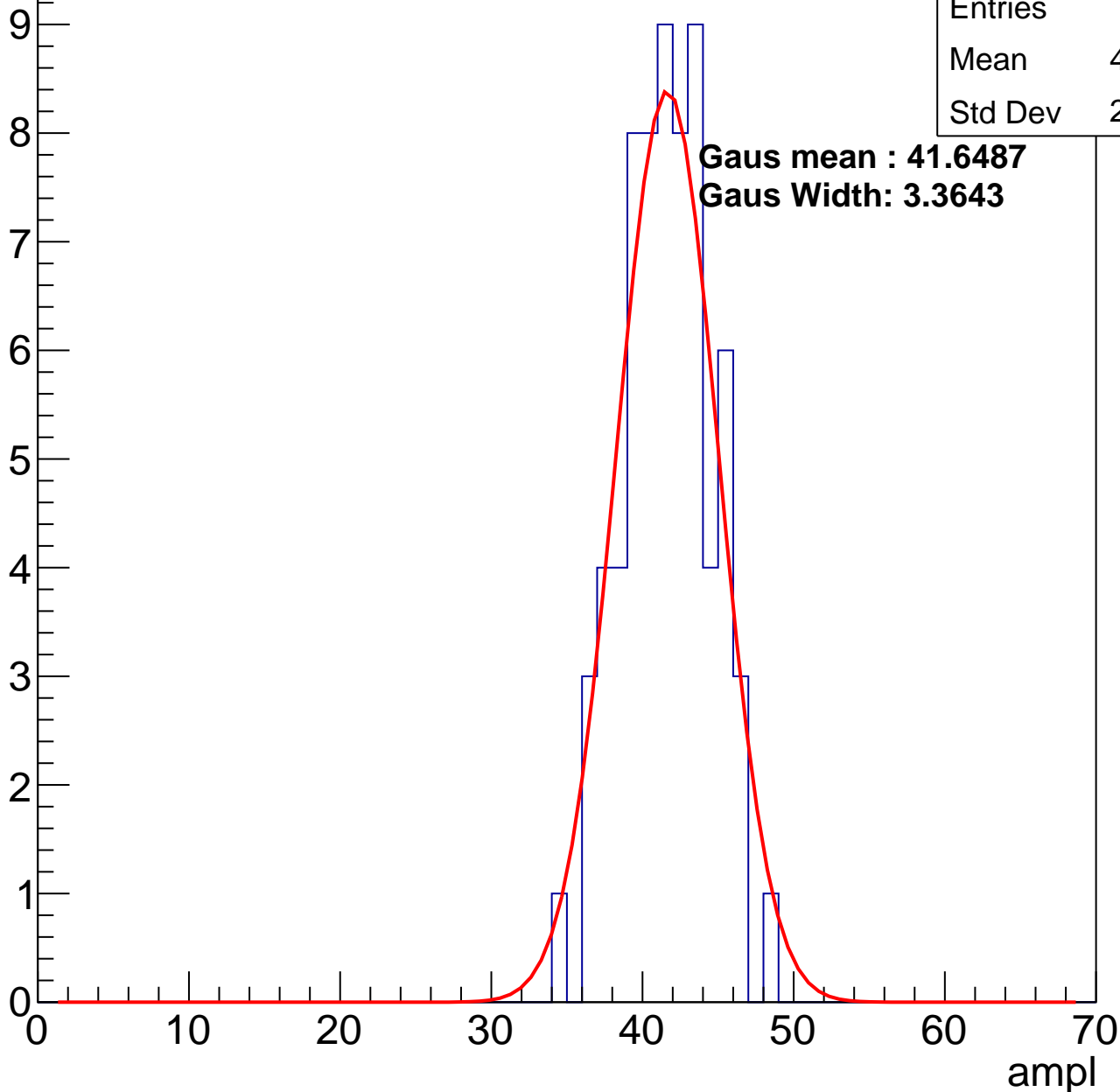
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.15
Std Dev	2.876

Gaus mean : 41.6487

Gaus Width: 3.3643

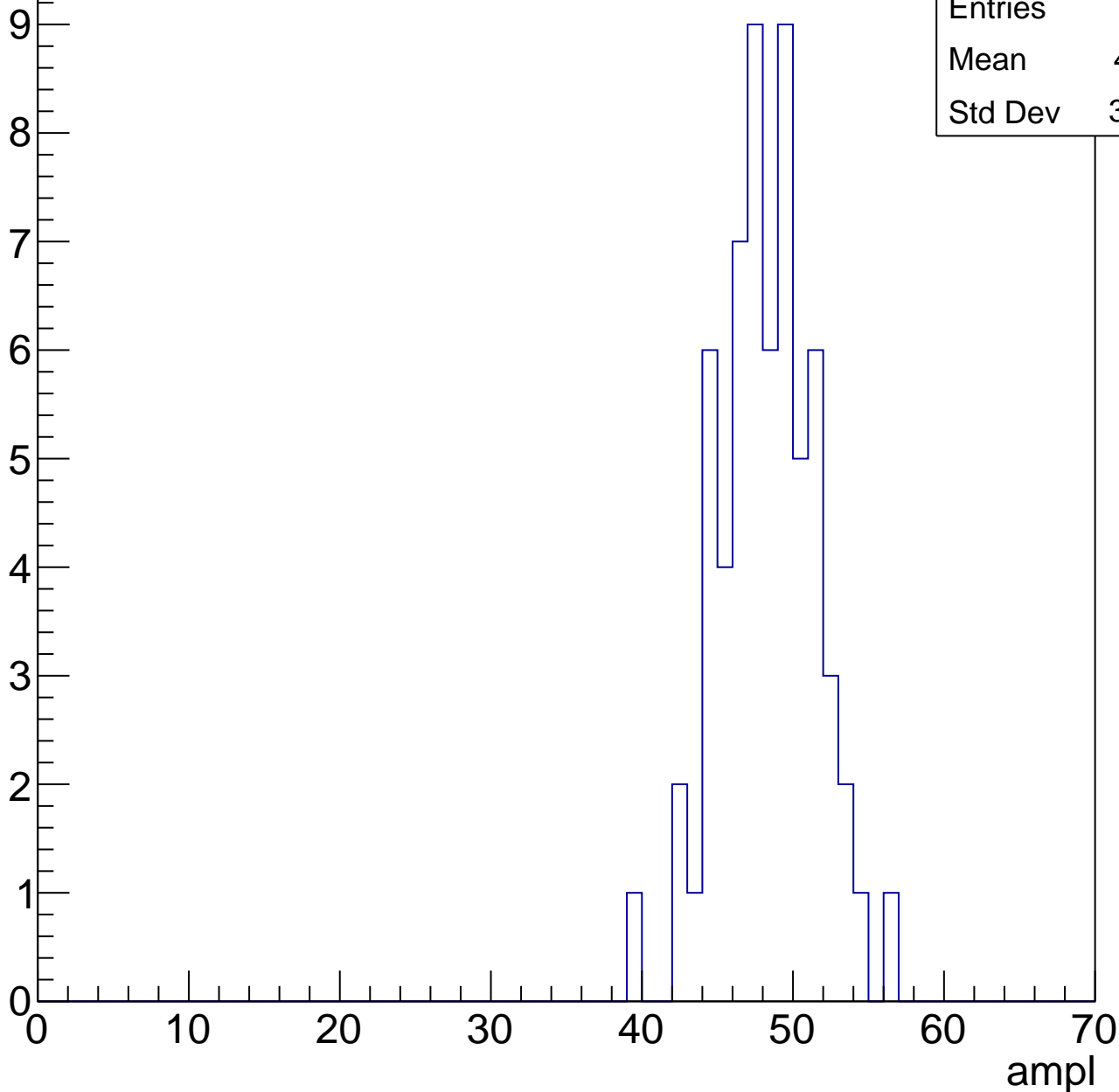


B1L002S, U7-ch80, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	47.81
Std Dev	3.152

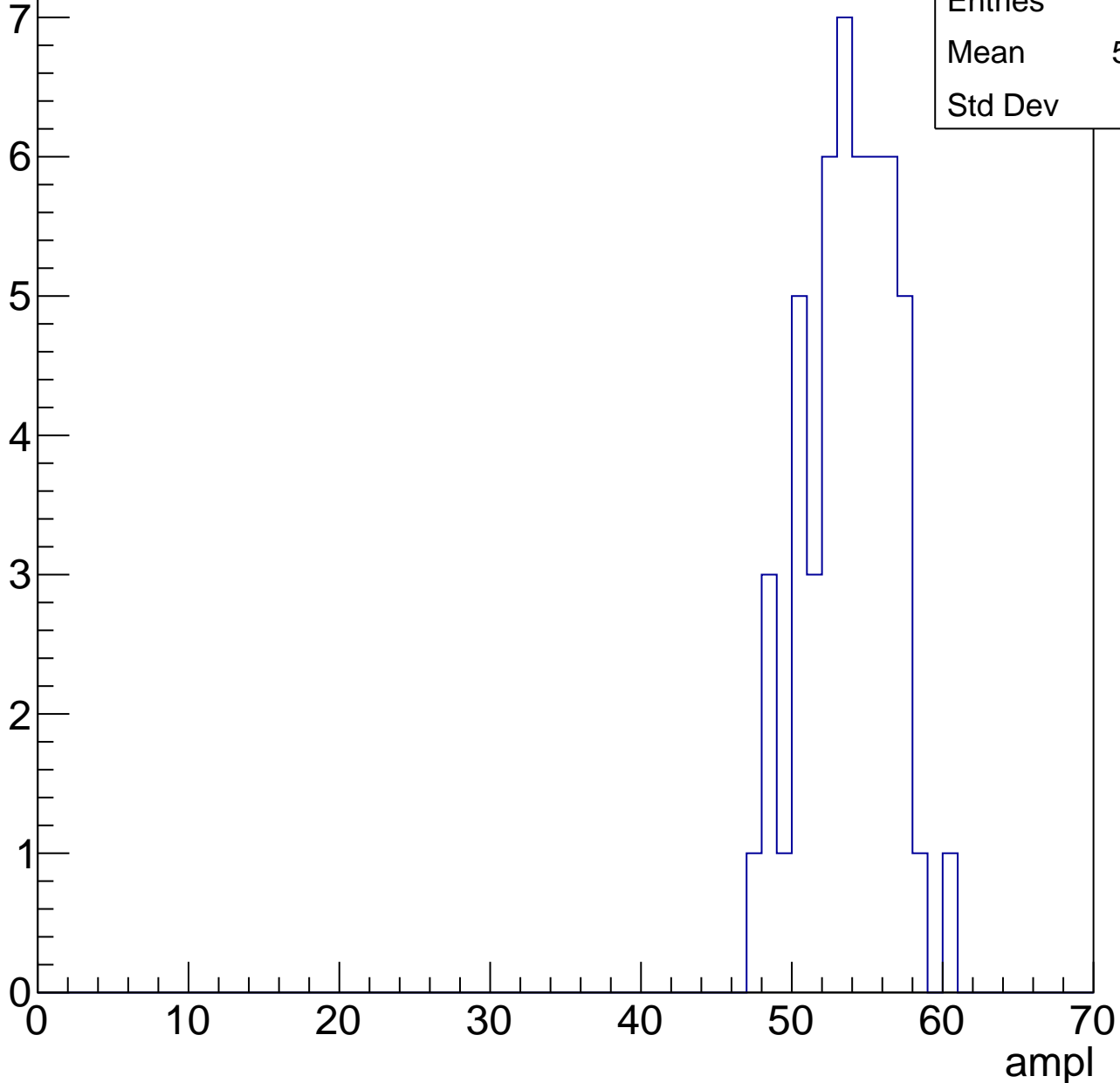


B1L002S, U7-ch80, adc4

calib_packv5_042523_0143.root, FC#10, port B3

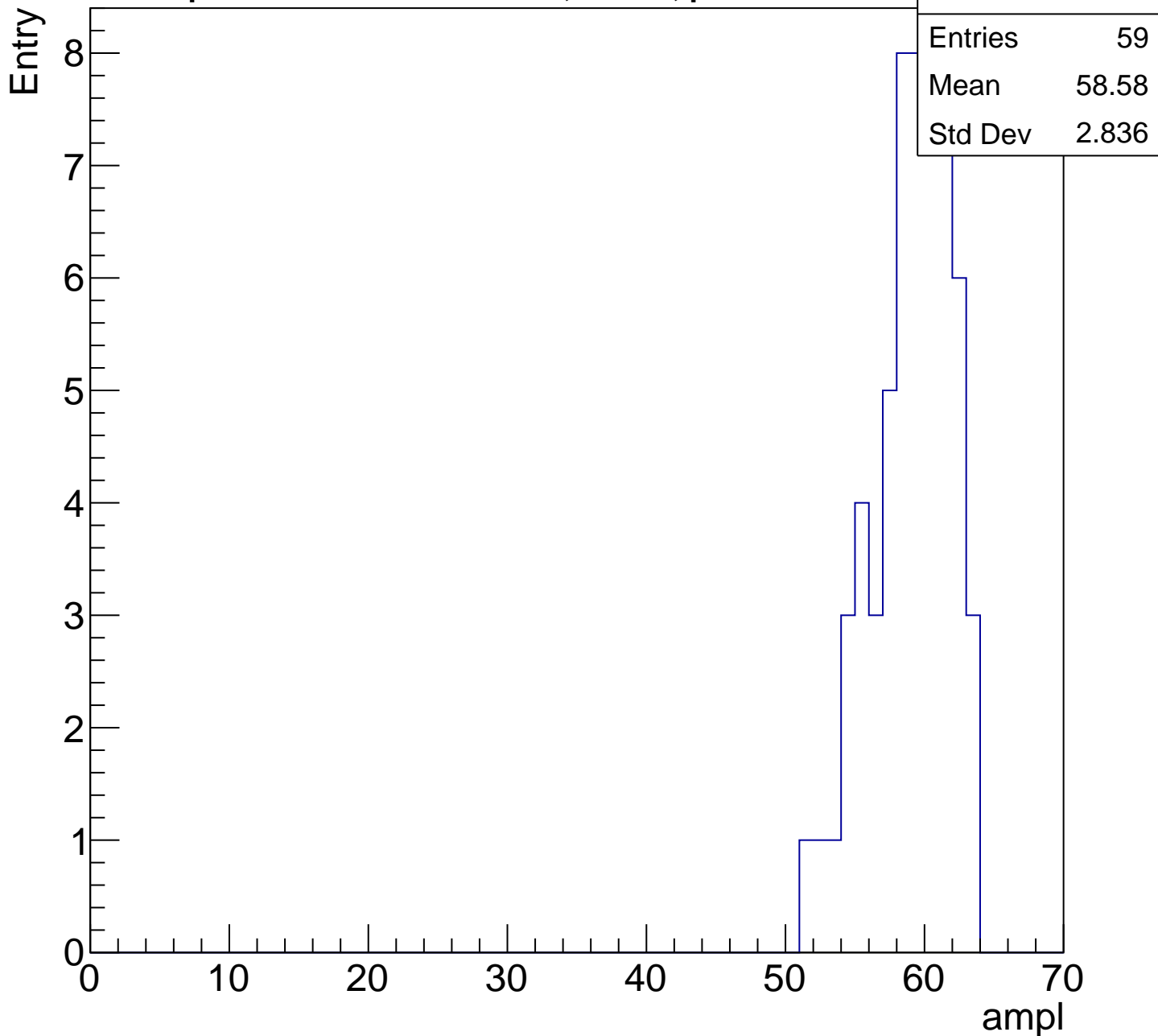
Entry

Entries	51
Mean	53.31
Std Dev	2.88



B1L002S, U7-ch80, adc5

calib_packv5_042523_0143.root, FC#10, port B3

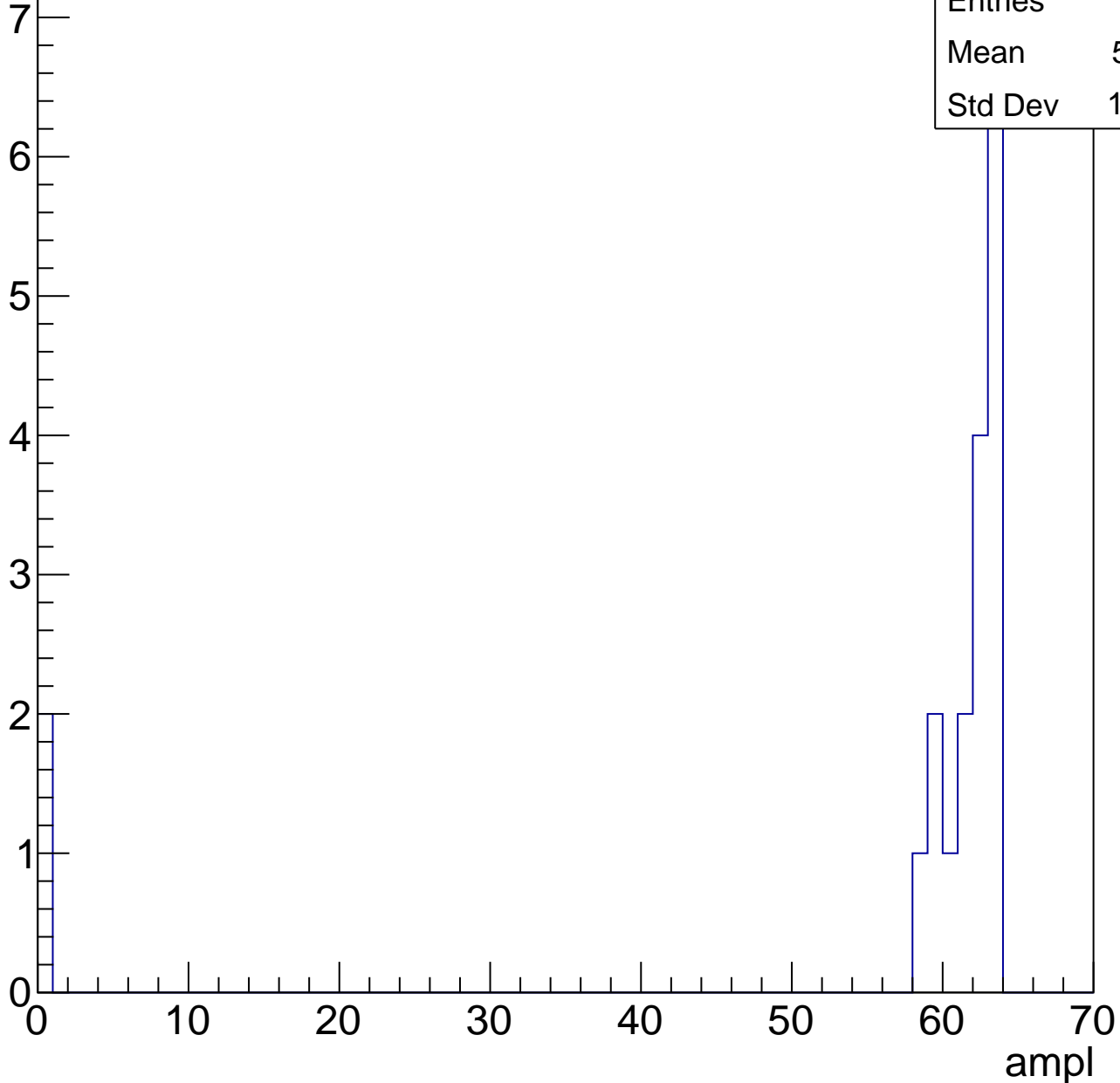


B1L002S, U7-ch80, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	55.11
Std Dev	18.96



B1L002S, U7-ch80, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch81, adc0

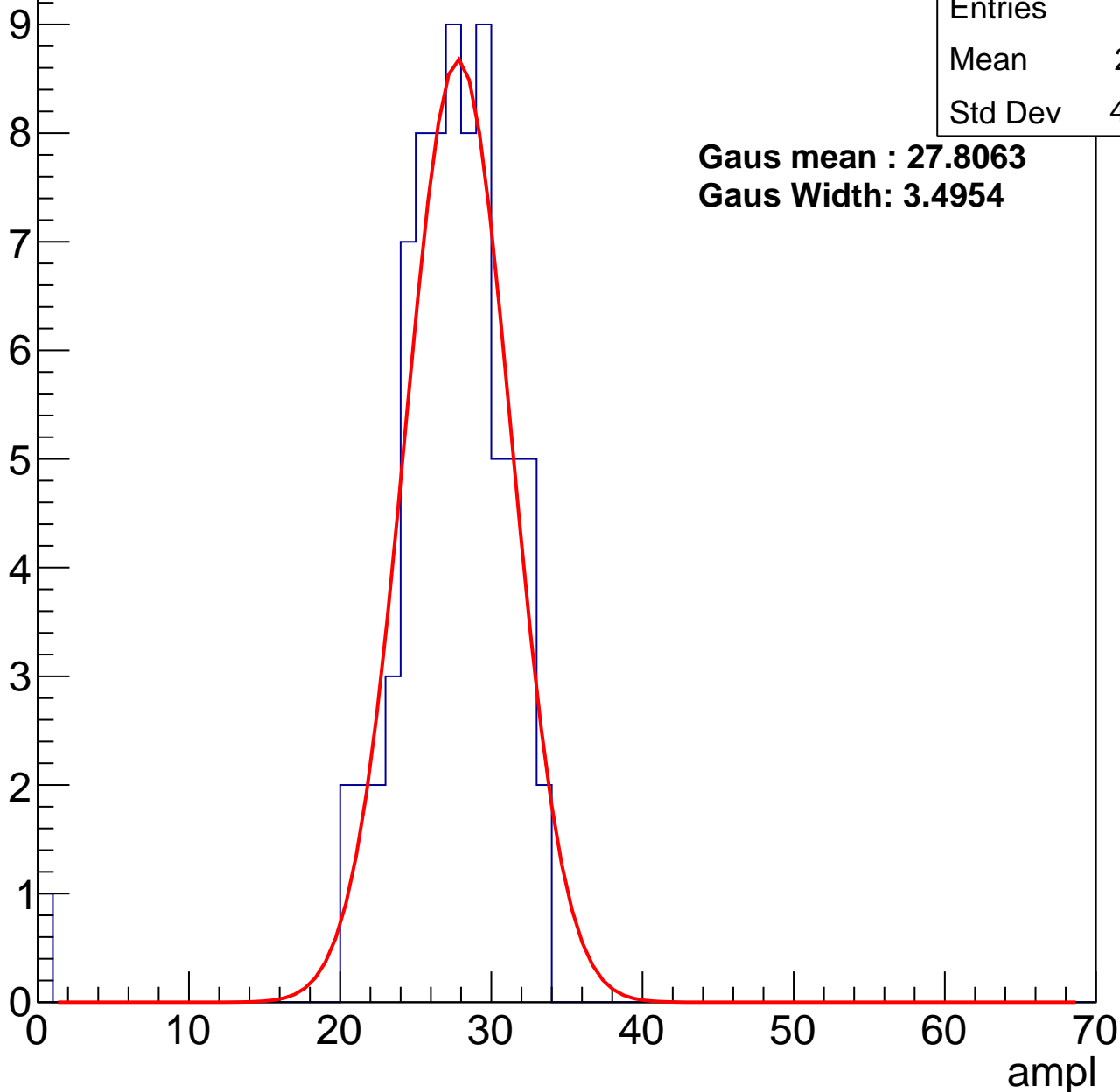
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	76
Mean	26.71
Std Dev	4.382

Gaus mean : 27.8063

Gaus Width: 3.4954



B1L002S, U7-ch81, adc1

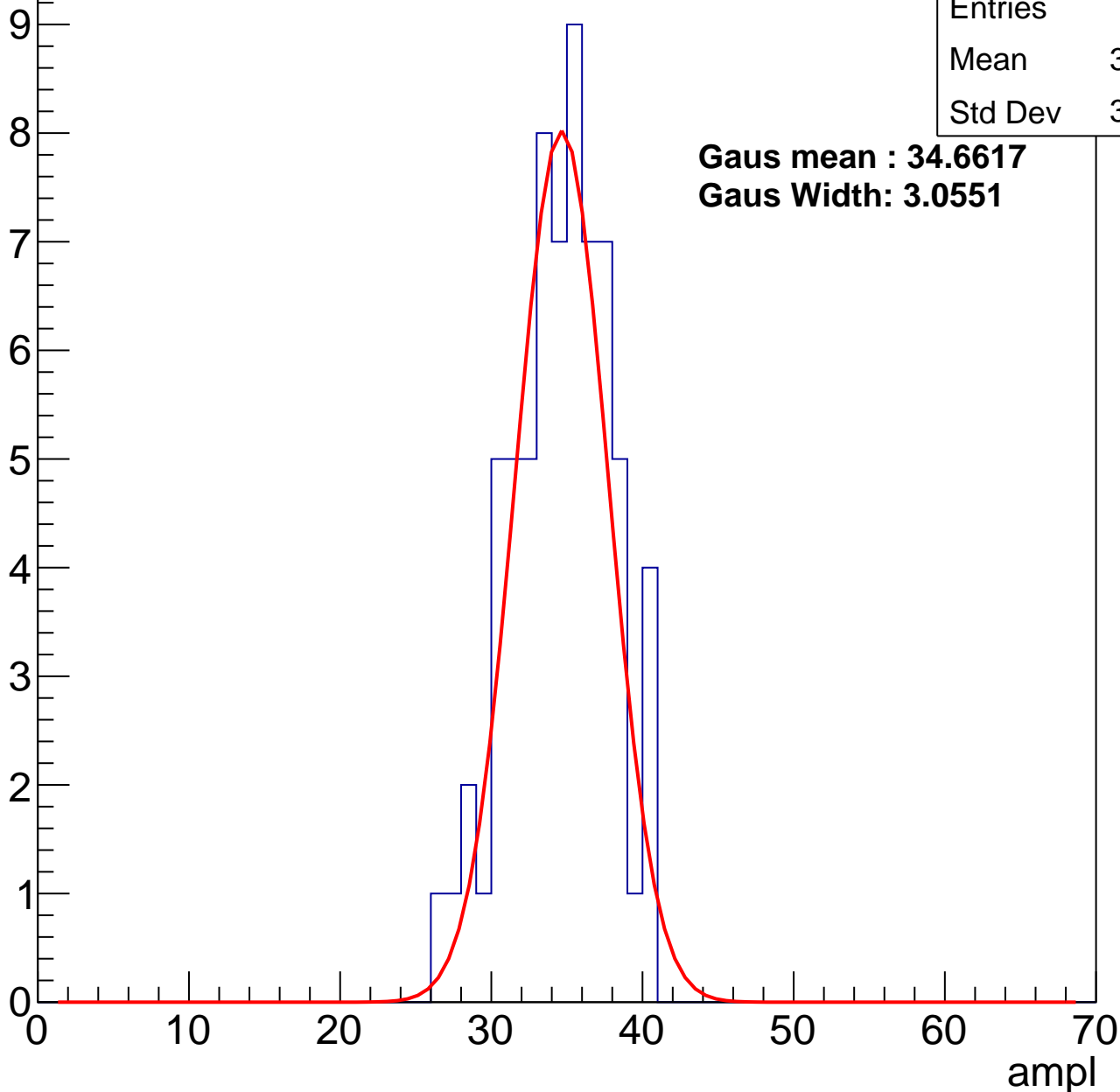
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.12
Std Dev	3.234

Gaus mean : 34.6617

Gaus Width: 3.0551



B1L002S, U7-ch81, adc2

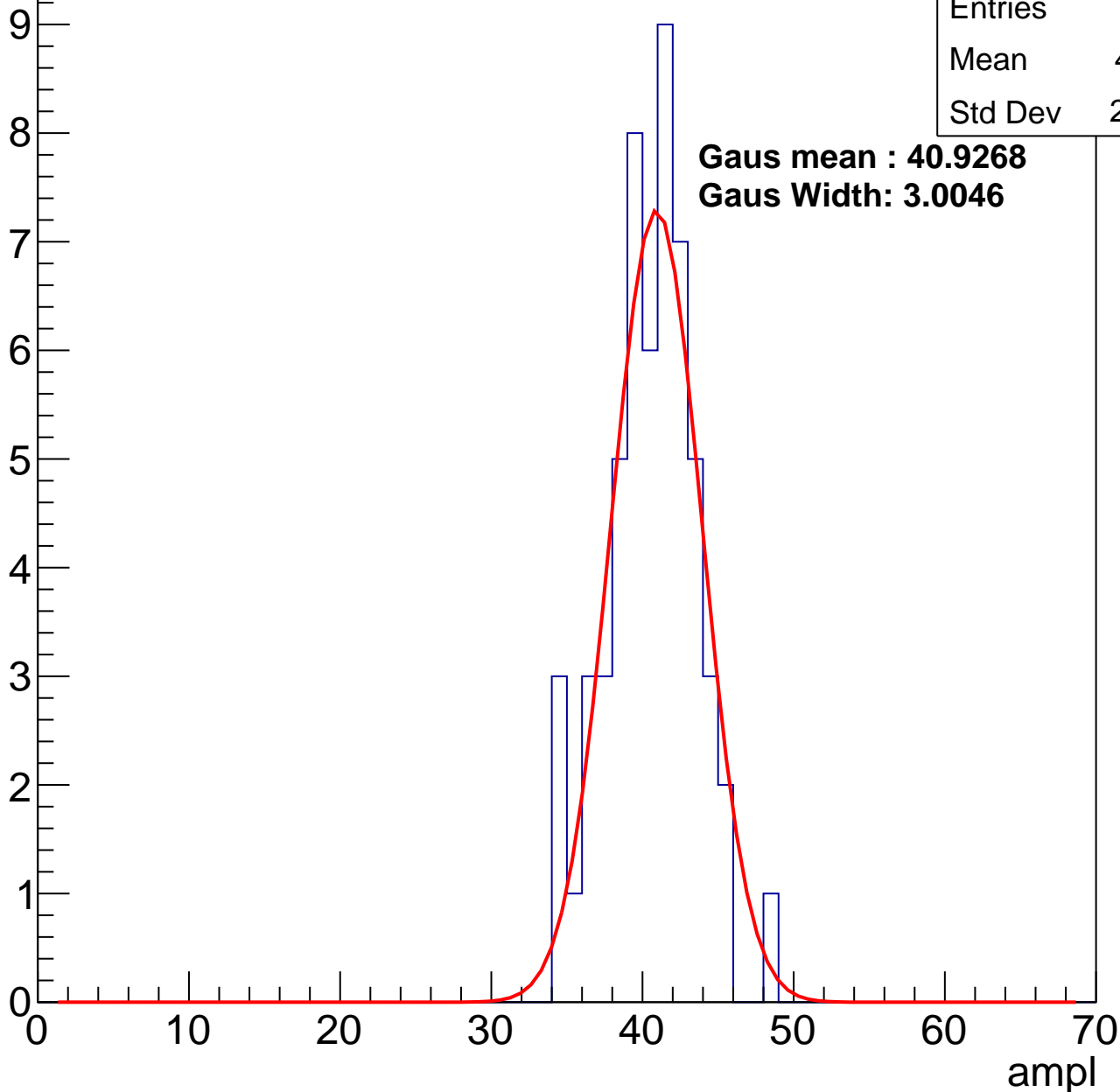
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.11
Std Dev	2.926

Gaus mean : 40.9268

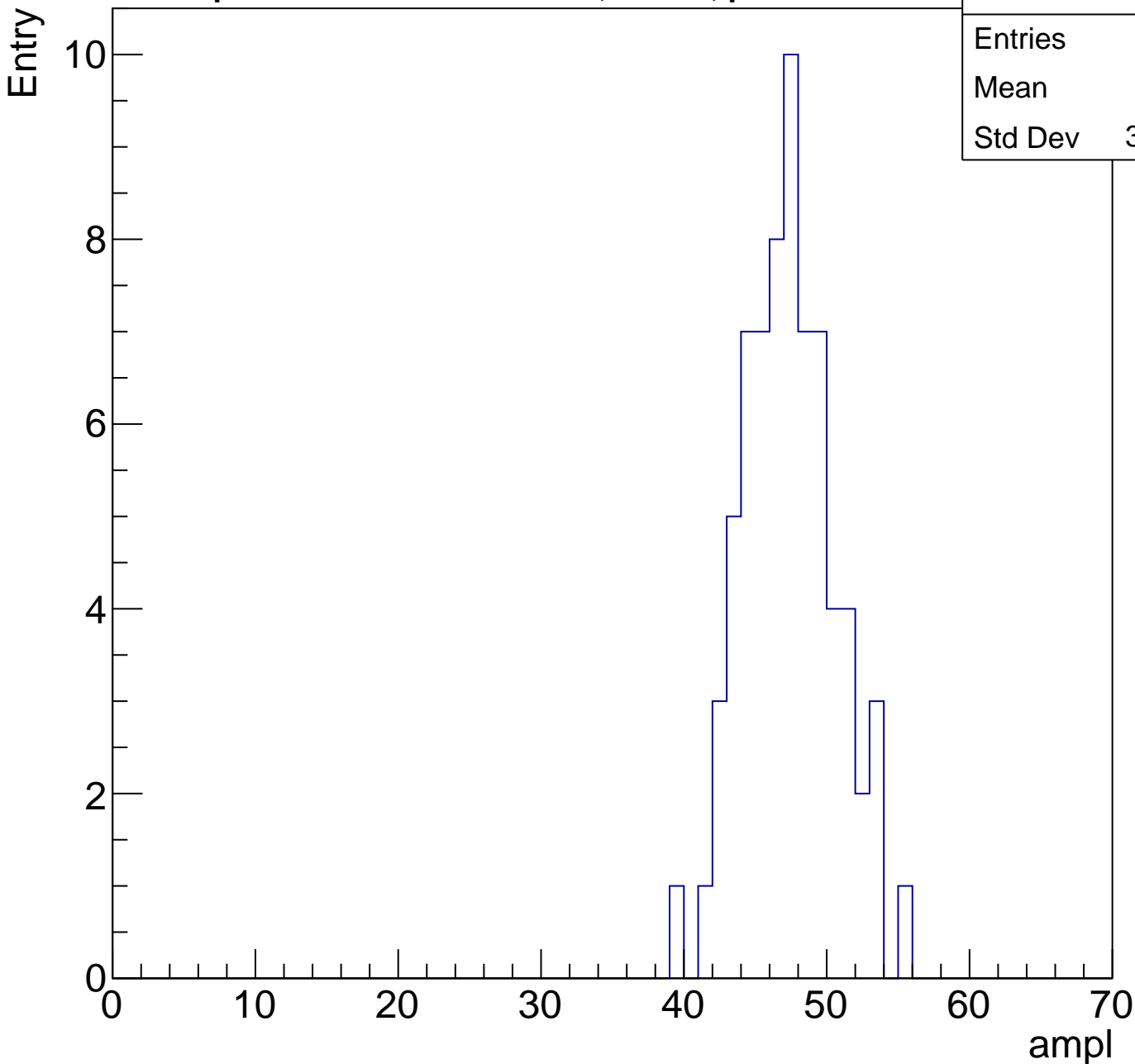
Gaus Width: 3.0046



B1L002S, U7-ch81, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	70
Mean	46.9
Std Dev	3.185

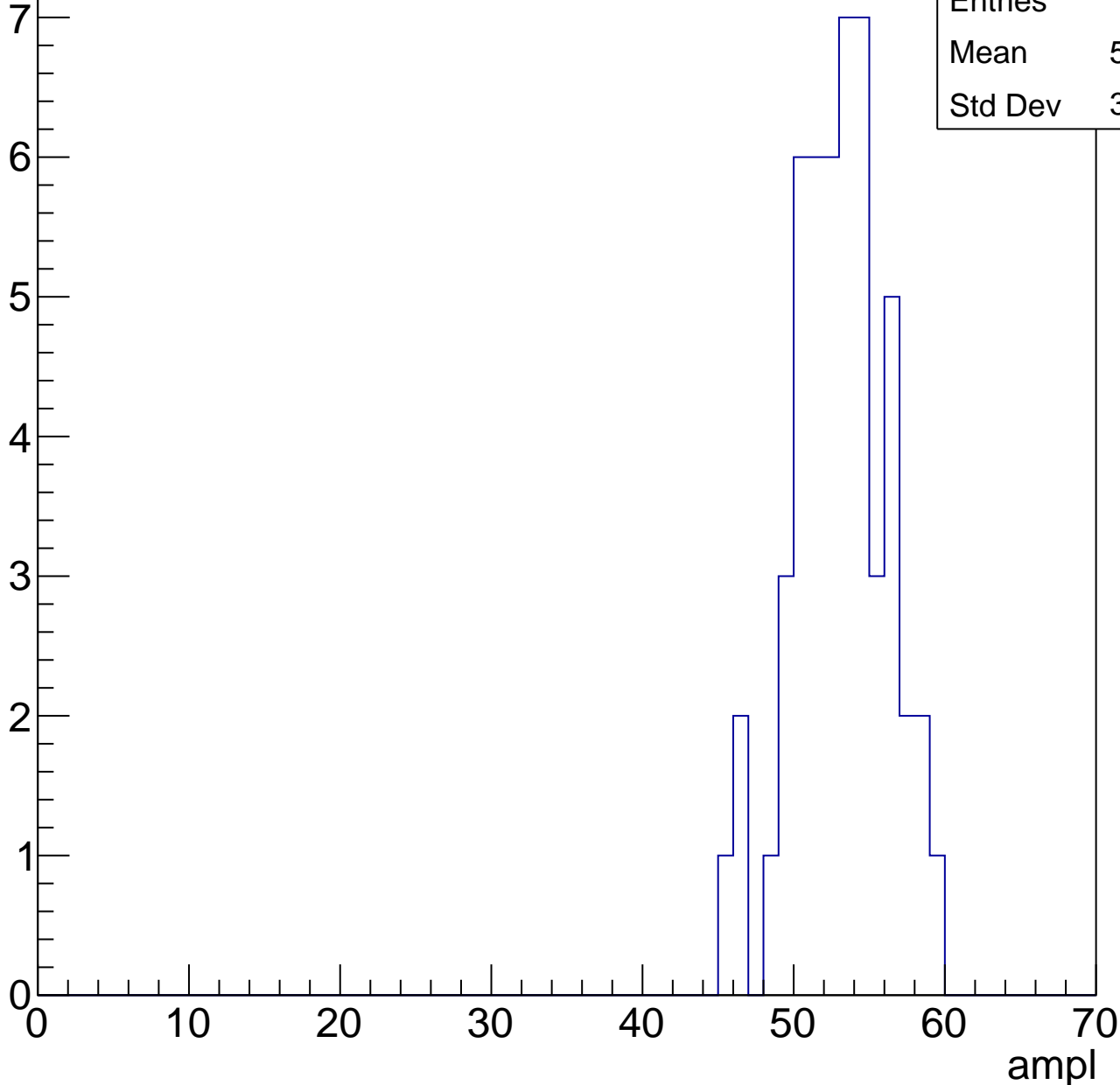


B1L002S, U7-ch81, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.56
Std Dev	3.072

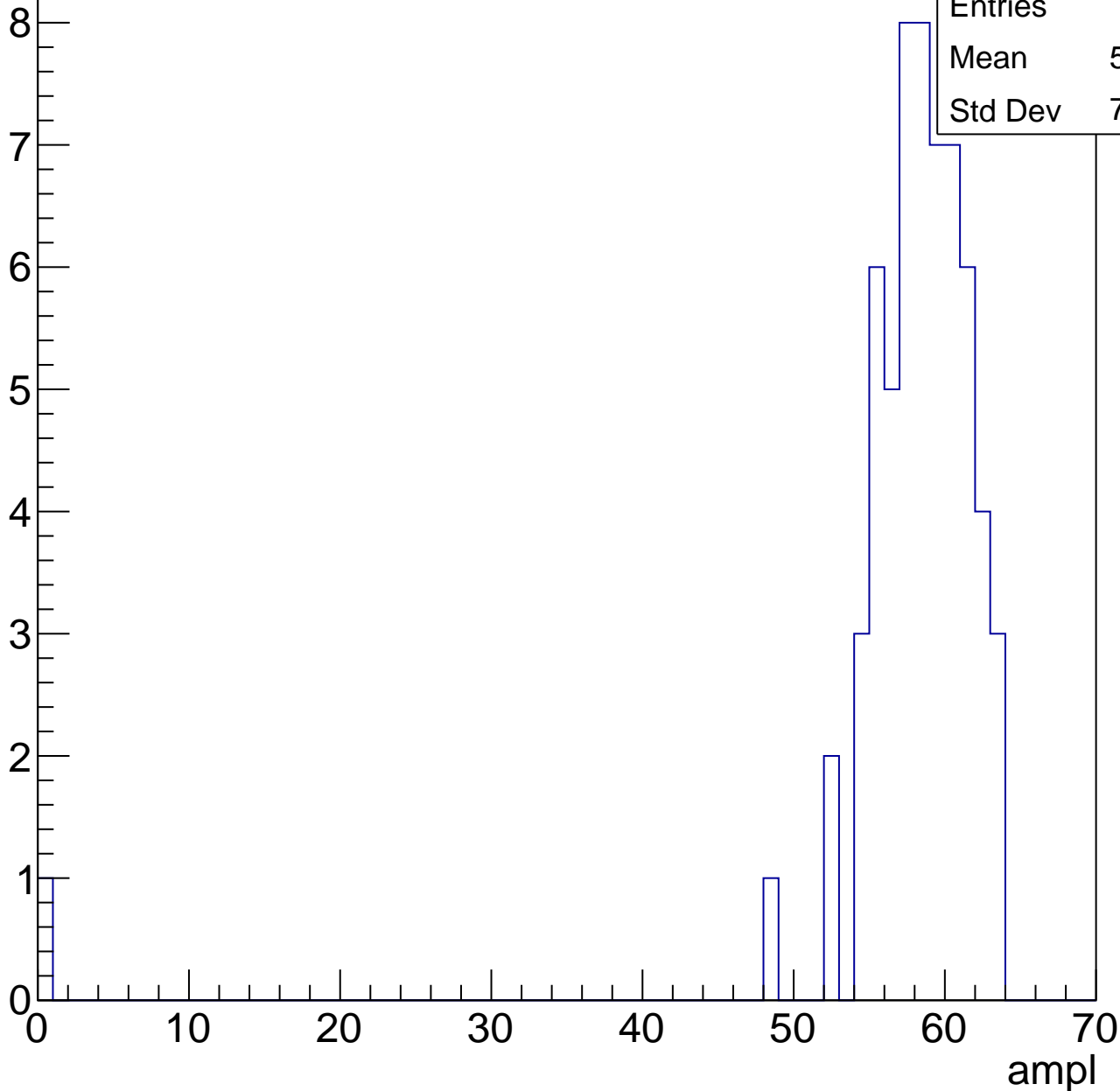


B1L002S, U7-ch81, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.05
Std Dev	7.933

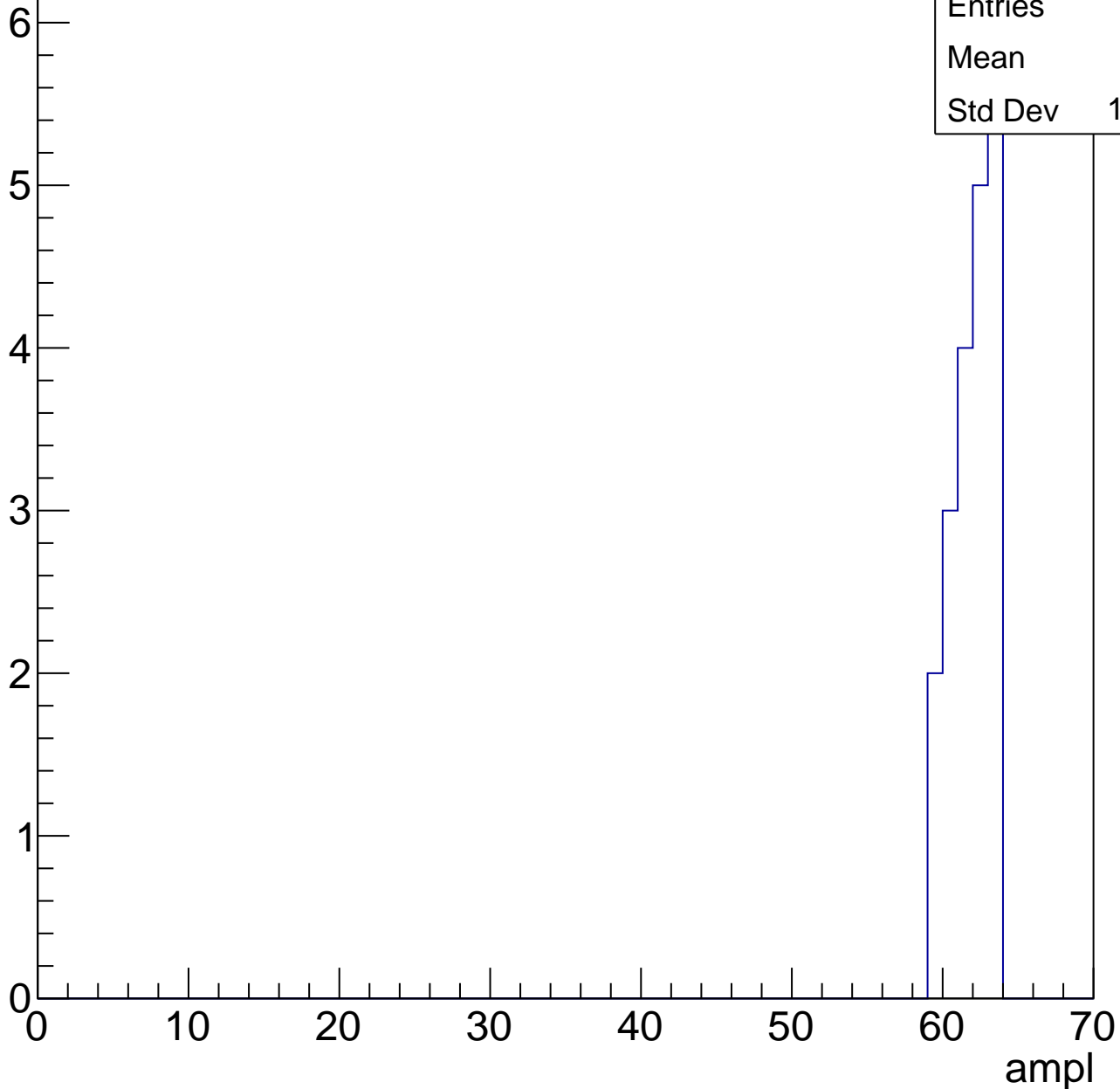


B1L002S, U7-ch81, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

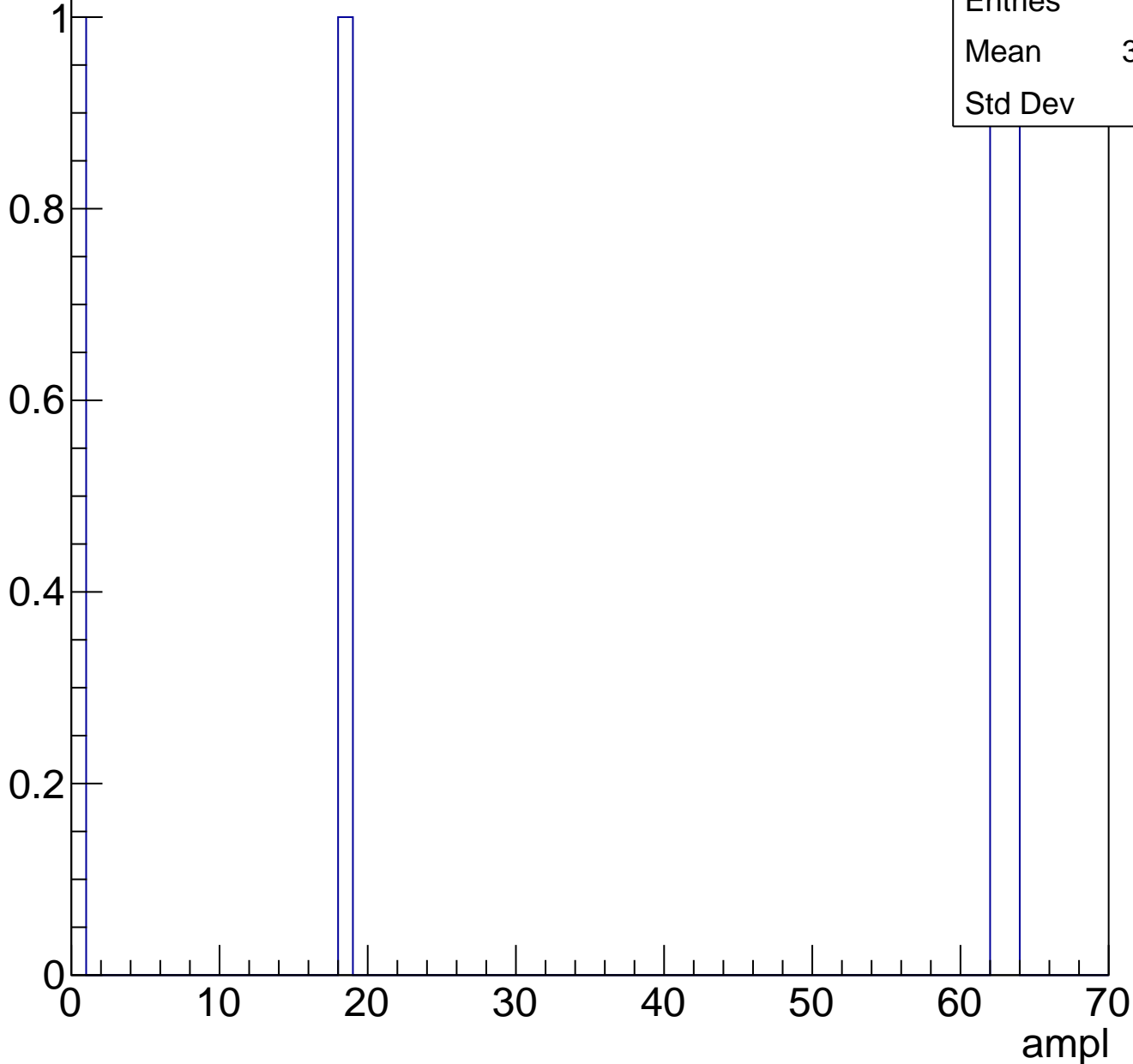
Entries	20
Mean	61.5
Std Dev	1.323



B1L002S, U7-ch81, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



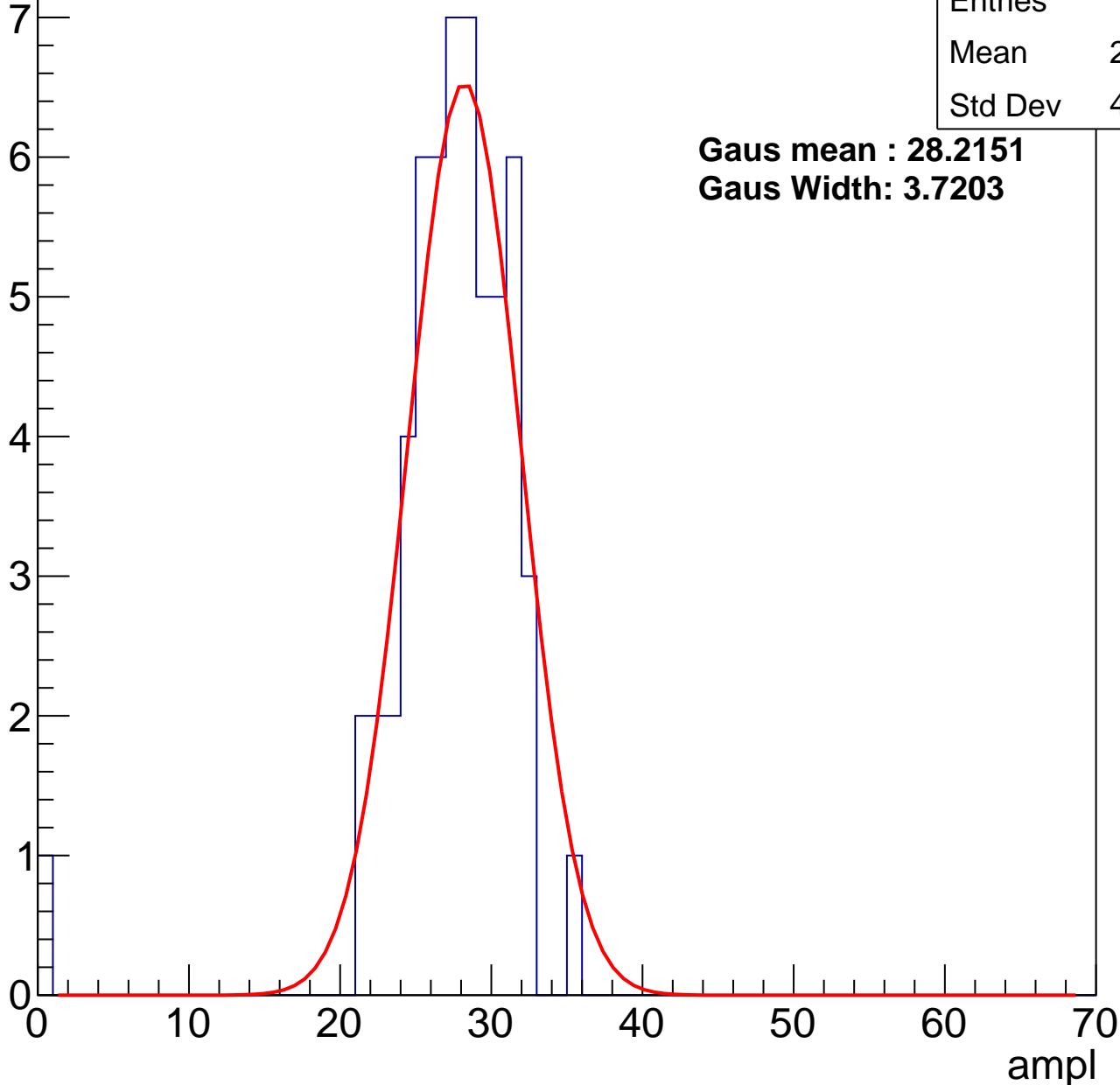
B1L002S, U7-ch82, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	26.86
Std Dev	4.692

Gaus mean : 28.2151
Gaus Width: 3.7203



B1L002S, U7-ch82, adc1

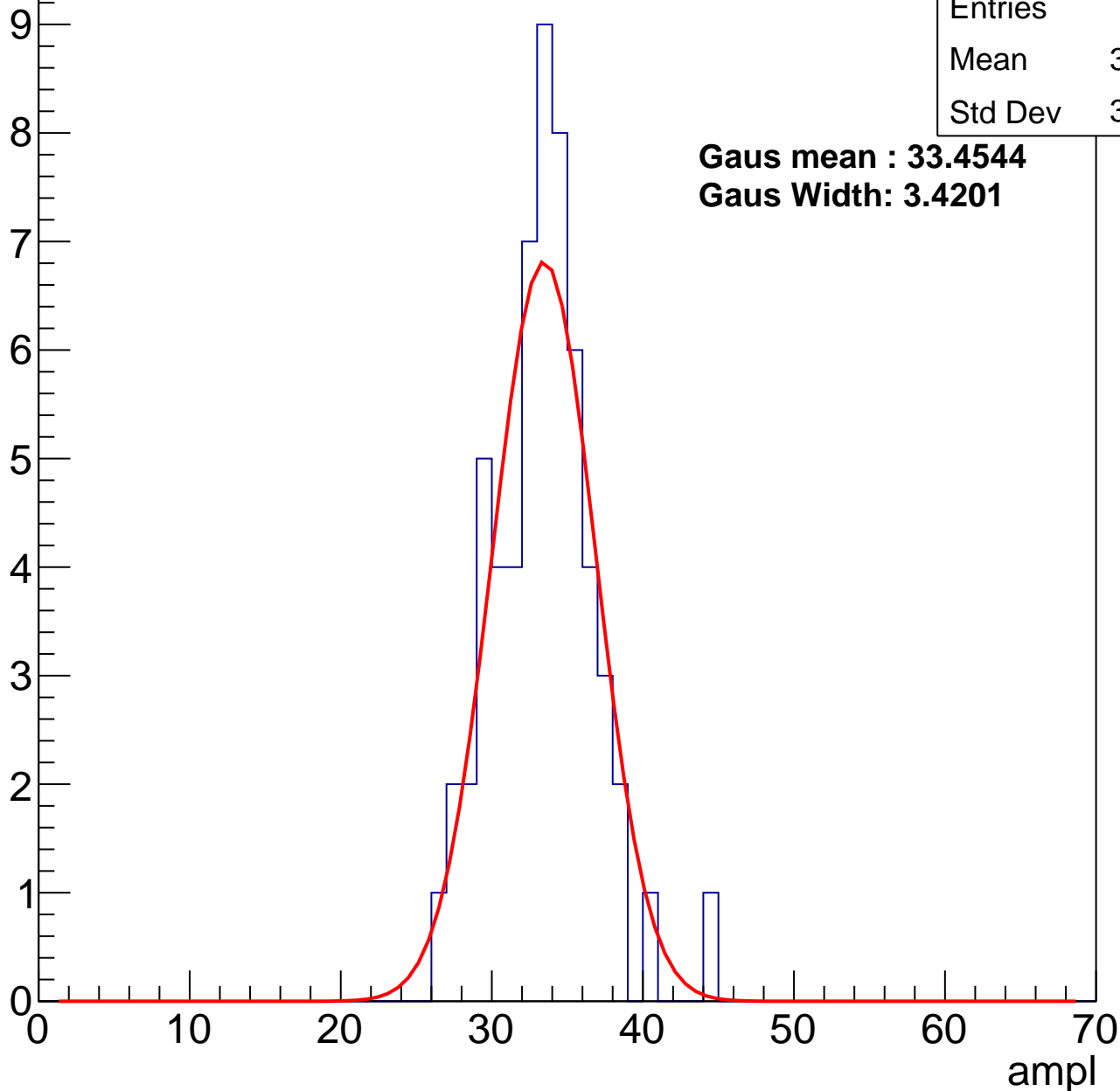
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	32.93
Std Dev	3.308

Gaus mean : 33.4544

Gaus Width: 3.4201



B1L002S, U7-ch82, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entry

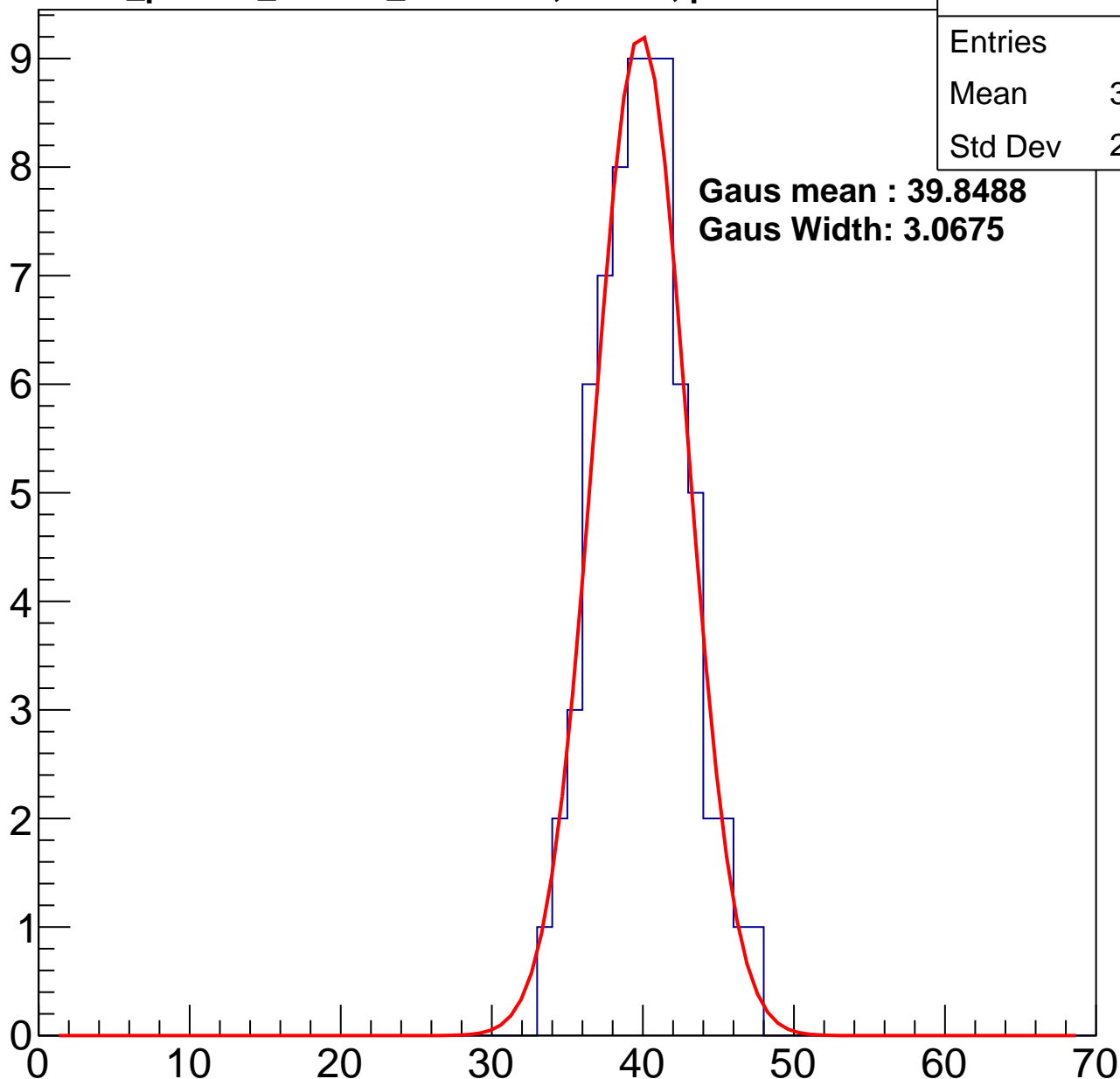
9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	39.48
Std Dev	2.959

Gaus mean : 39.8488

Gaus Width: 3.0675

ampl

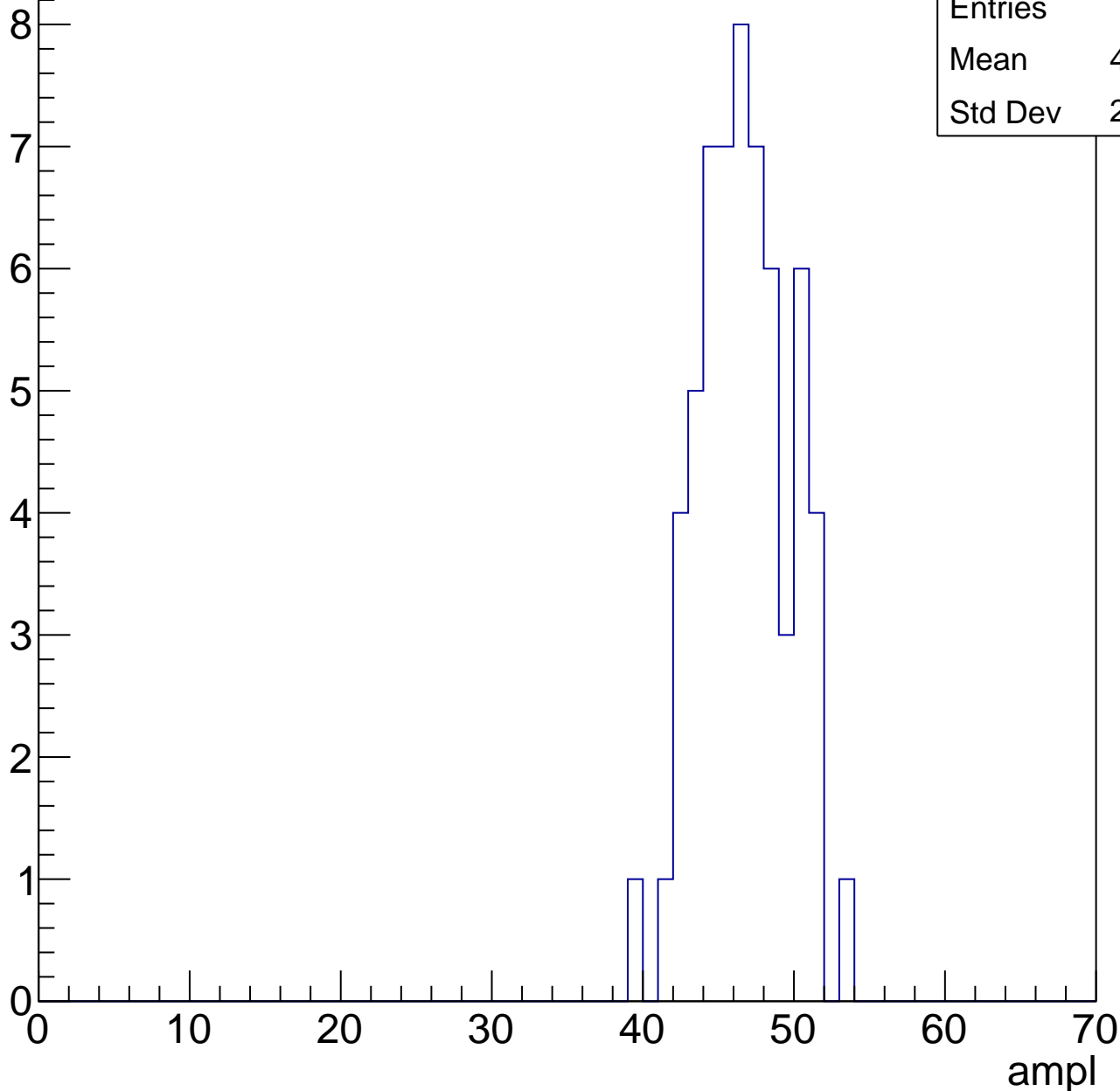


B1L002S, U7-ch82, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.25
Std Dev	2.936

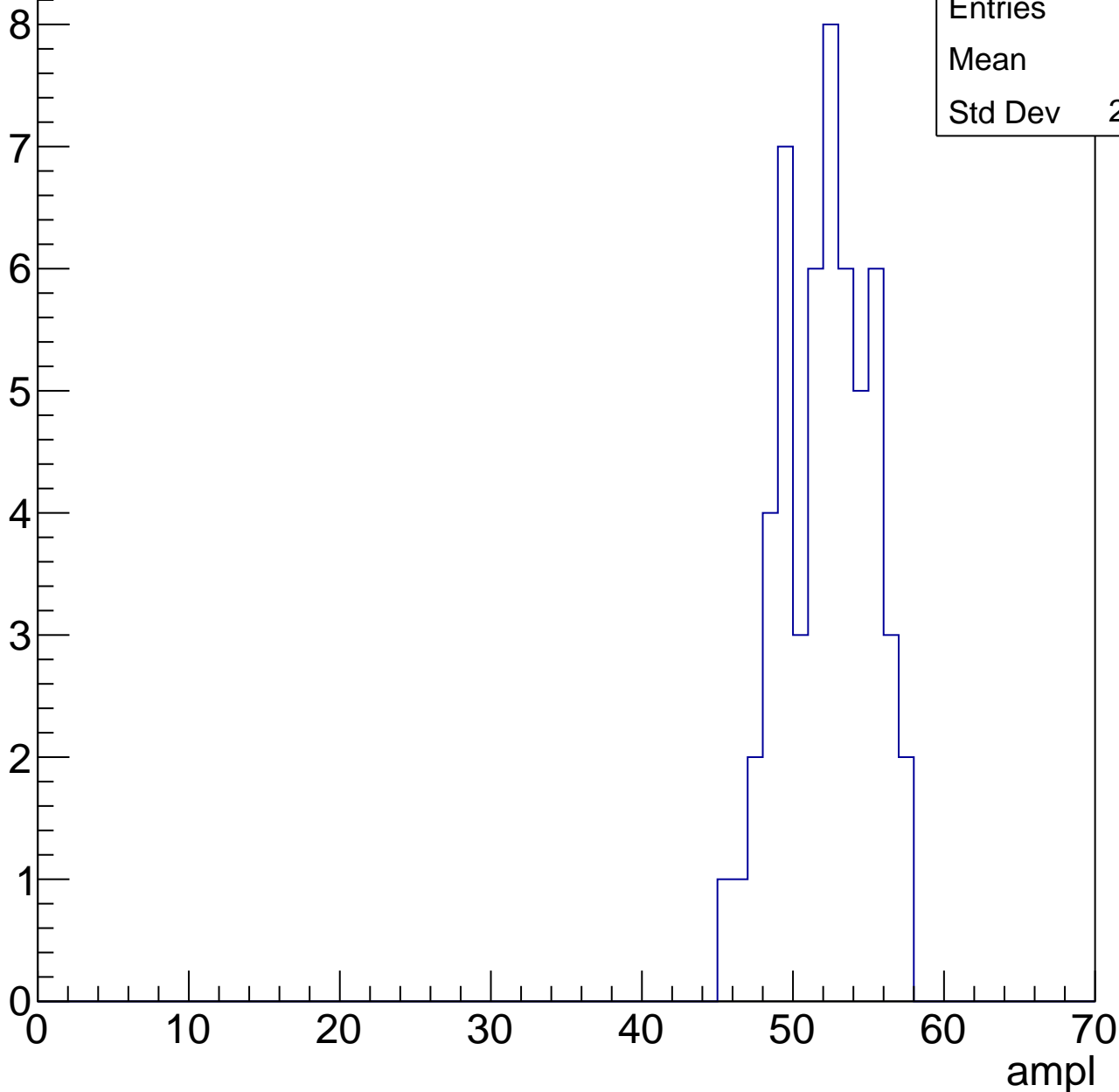


B1L002S, U7-ch82, adc4

calib_packv5_042523_0143.root, FC#10, port B3

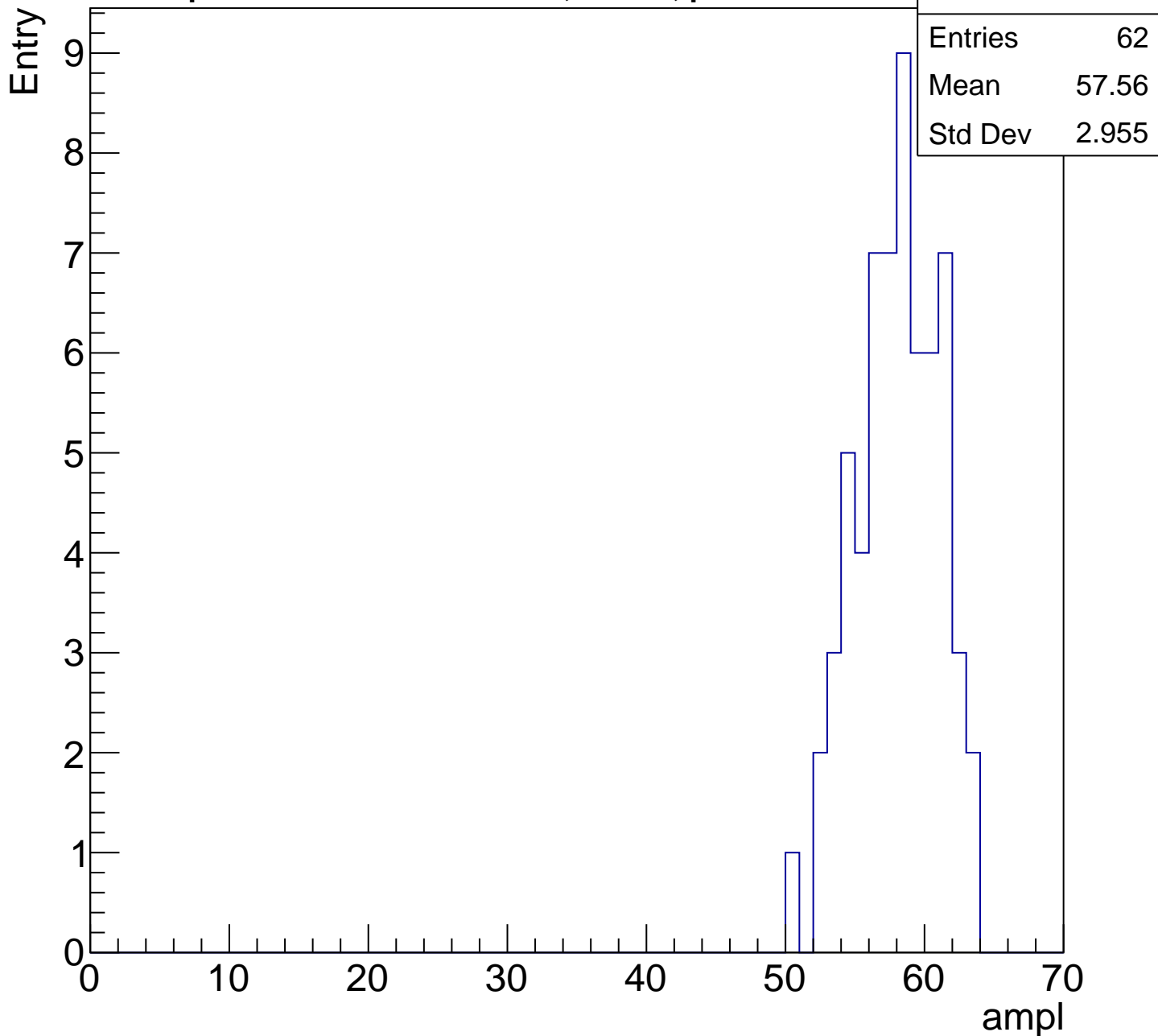
Entry

Entries	54
Mean	51.7
Std Dev	2.897



B1L002S, U7-ch82, adc5

calib_packv5_042523_0143.root, FC#10, port B3

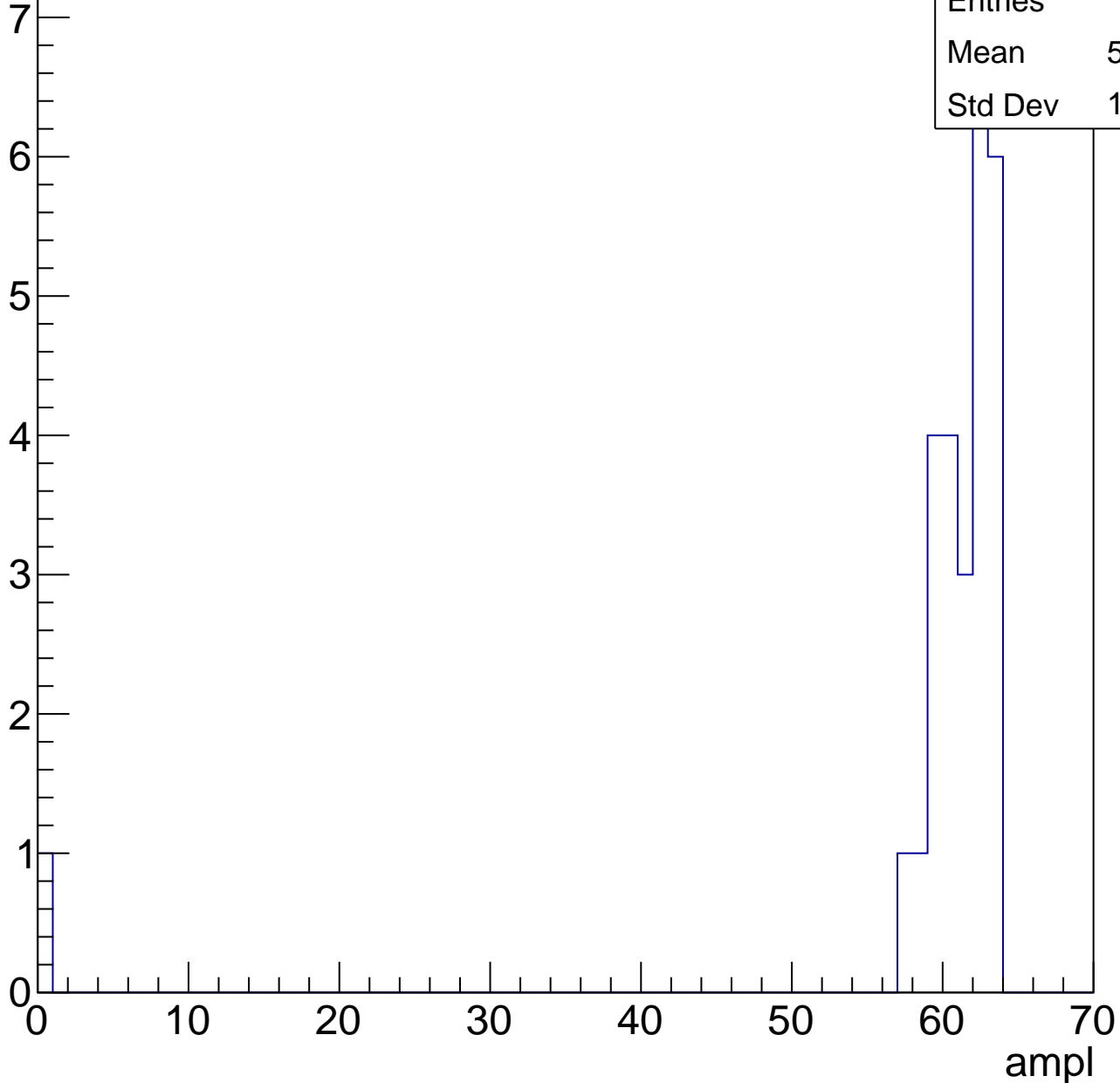


B1L002S, U7-ch82, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.74
Std Dev	11.64



B1L002S, U7-ch82, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch83, adc0

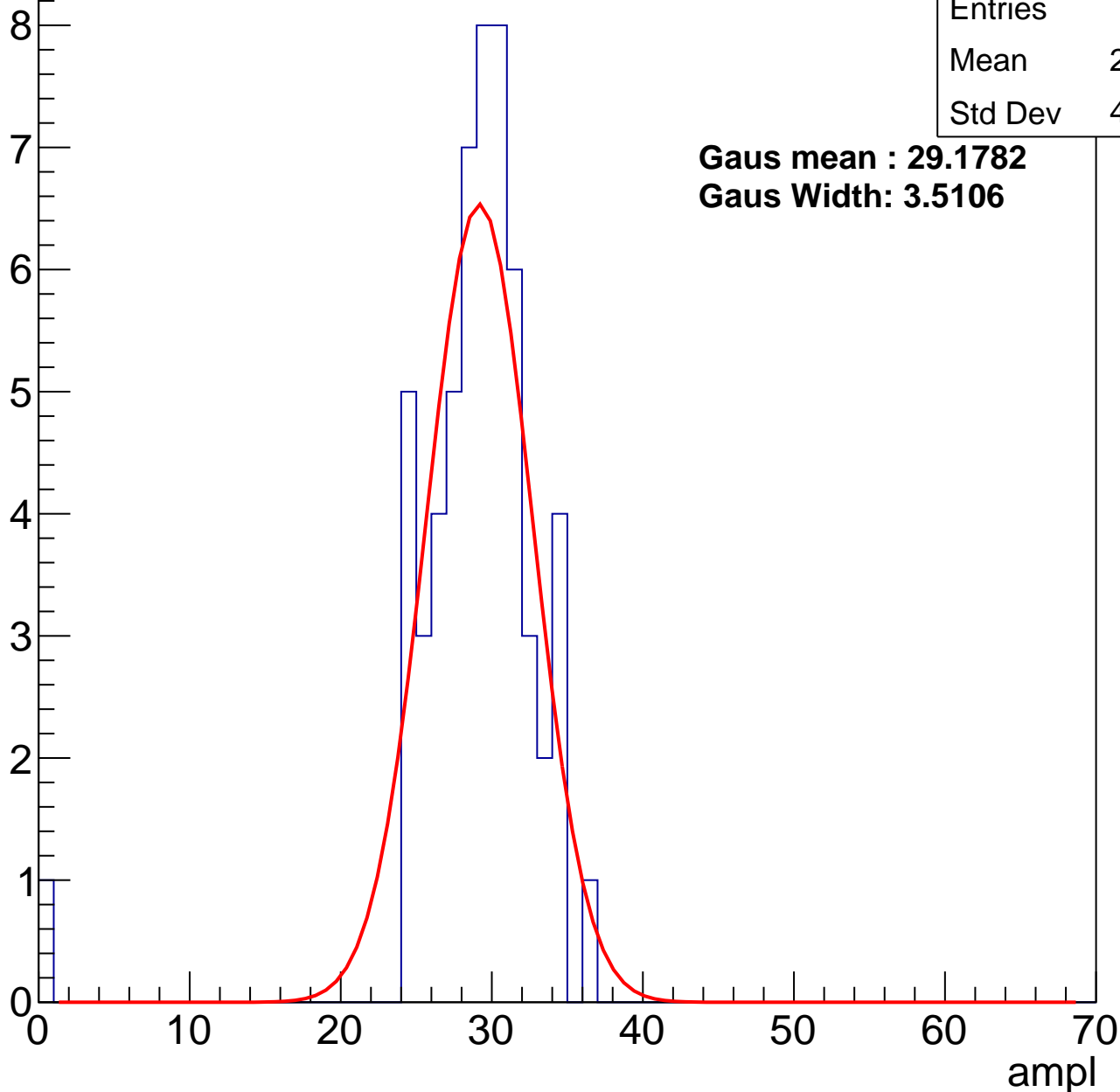
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.46
Std Dev	4.776

Gaus mean : 29.1782

Gaus Width: 3.5106



B1L002S, U7-ch83, adc1

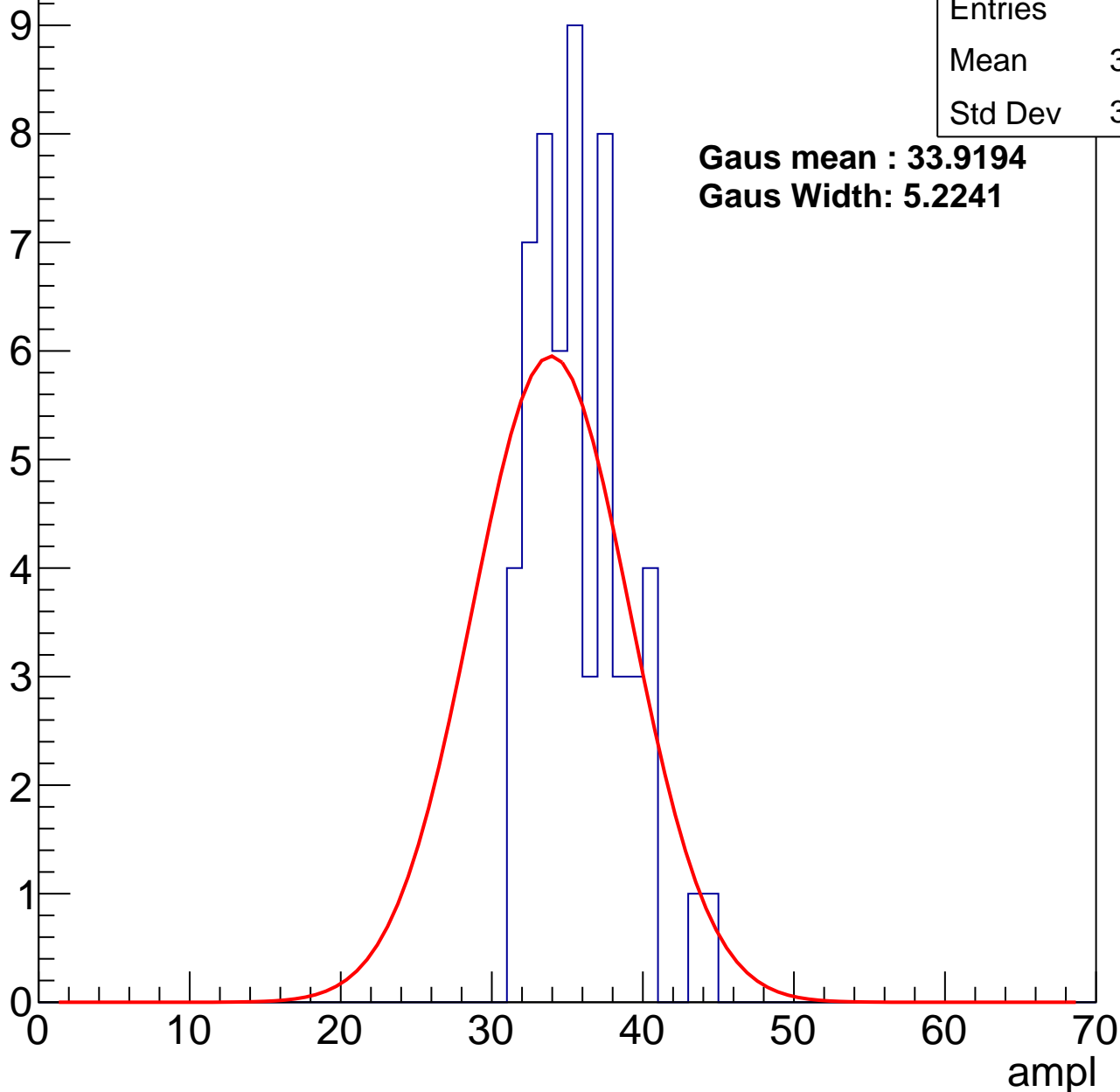
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	35.32
Std Dev	3.004

Gaus mean : 33.9194

Gaus Width: 5.2241



B1L002S, U7-ch83, adc2

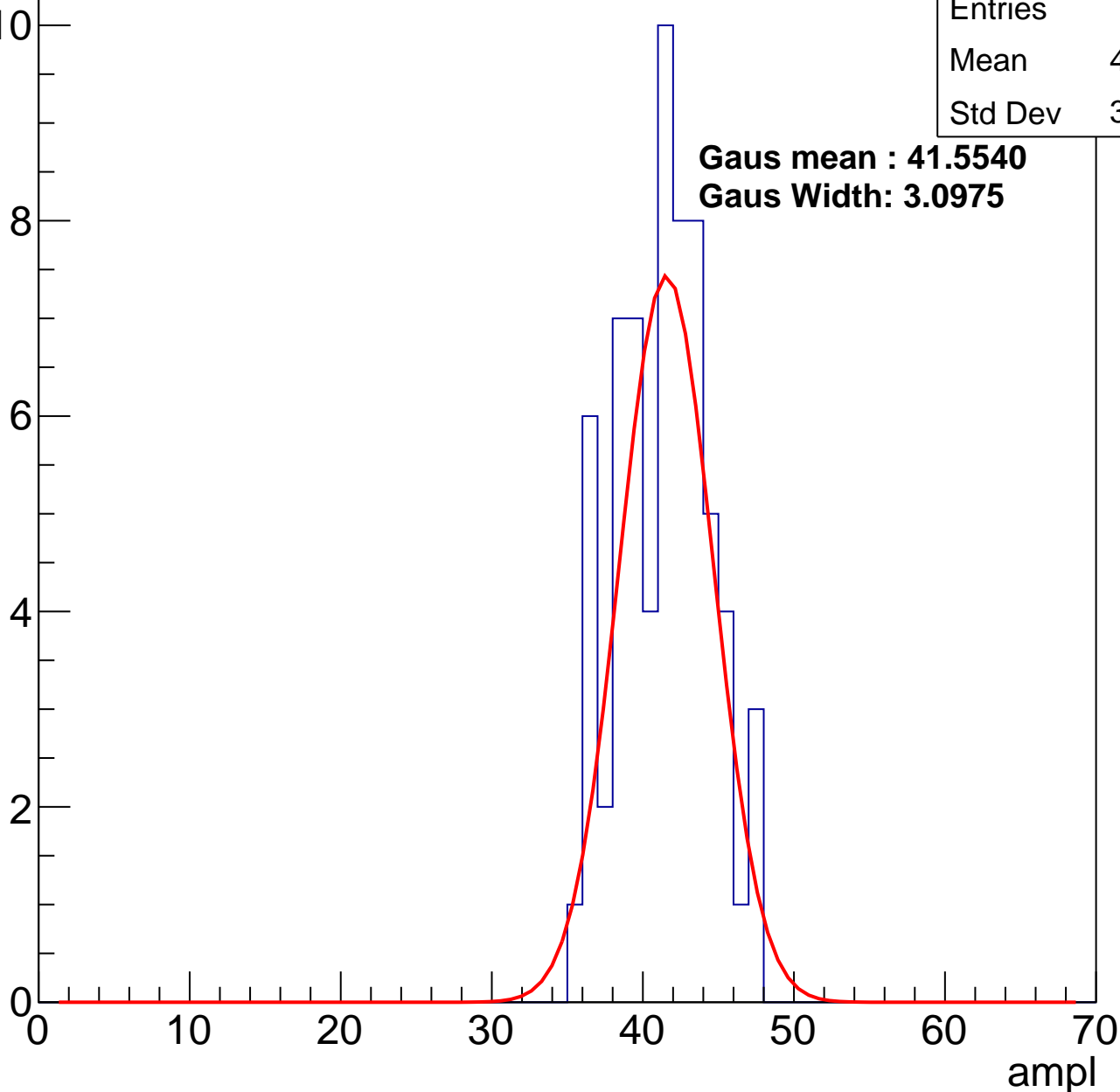
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.92
Std Dev	3.002

Gaus mean : 41.5540

Gaus Width: 3.0975

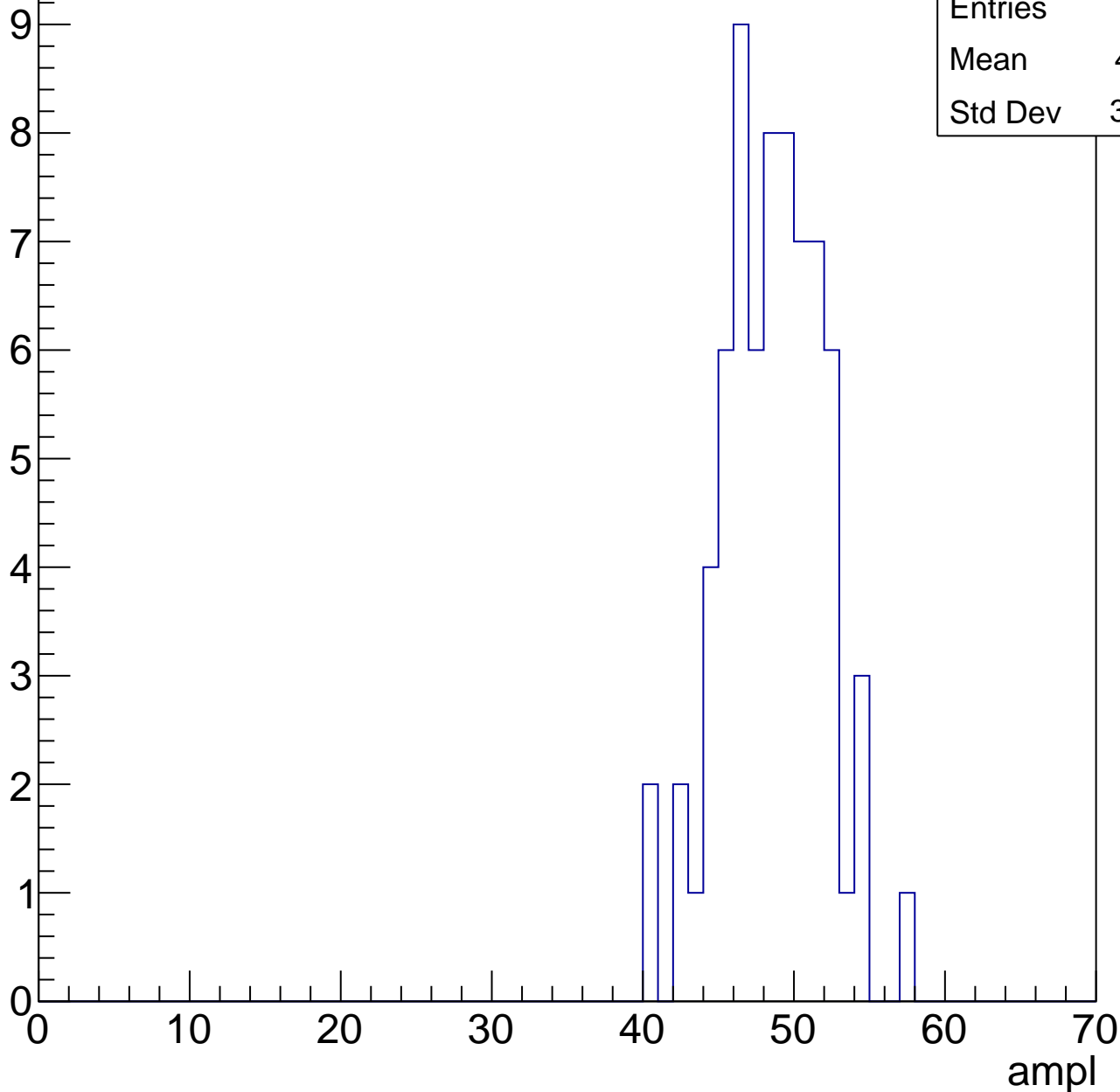


B1L002S, U7-ch83, adc3

calib_packv5_042523_0143.root, FC#10, port B3

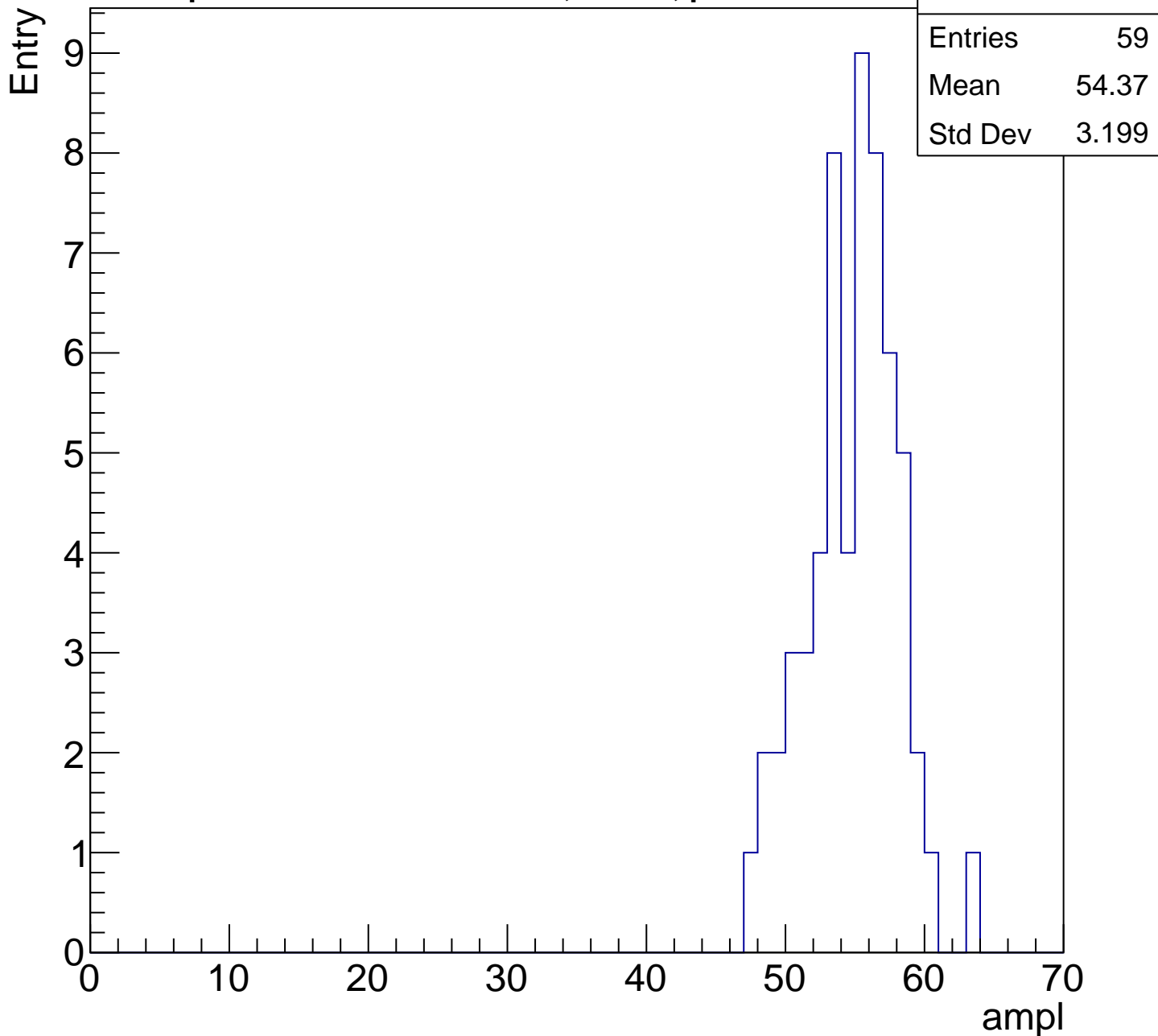
Entry

Entries	71
Mean	48.11
Std Dev	3.342



B1L002S, U7-ch83, adc4

calib_packv5_042523_0143.root, FC#10, port B3

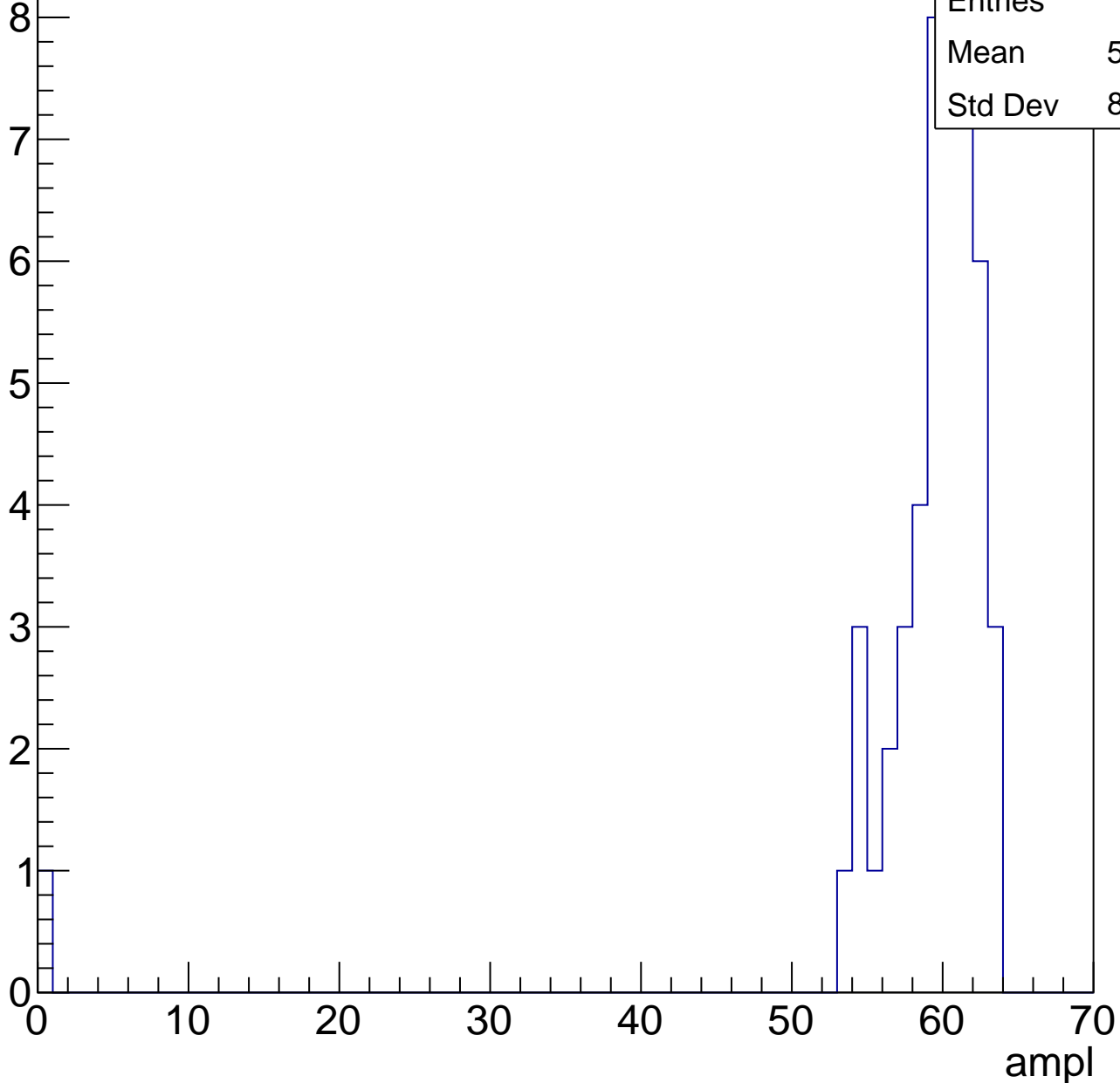


B1L002S, U7-ch83, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.04
Std Dev	8.827

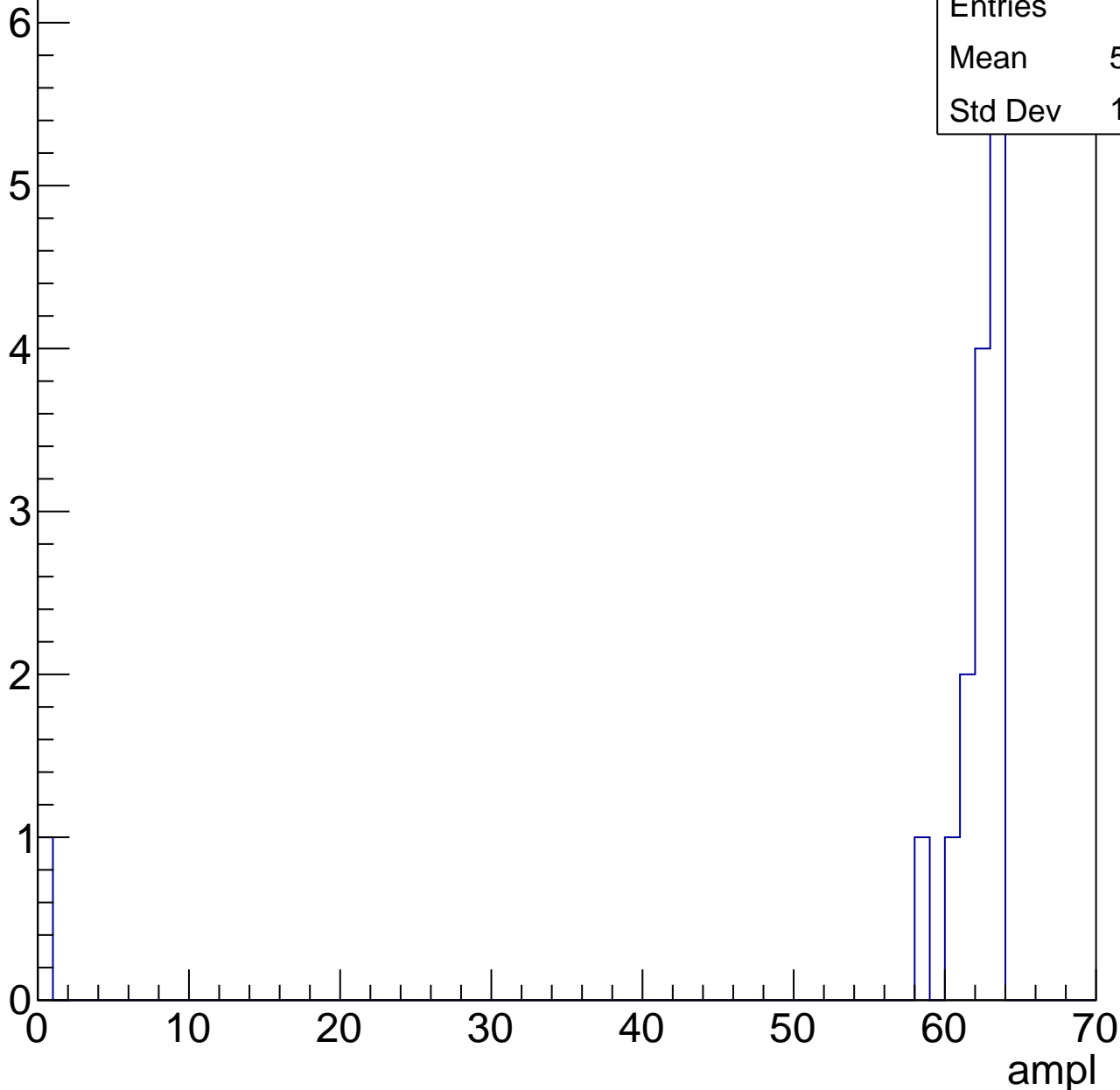


B1L002S, U7-ch83, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	15
Mean	57.73
Std Dev	15.49



B1L002S, U7-ch83, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch84, adc0

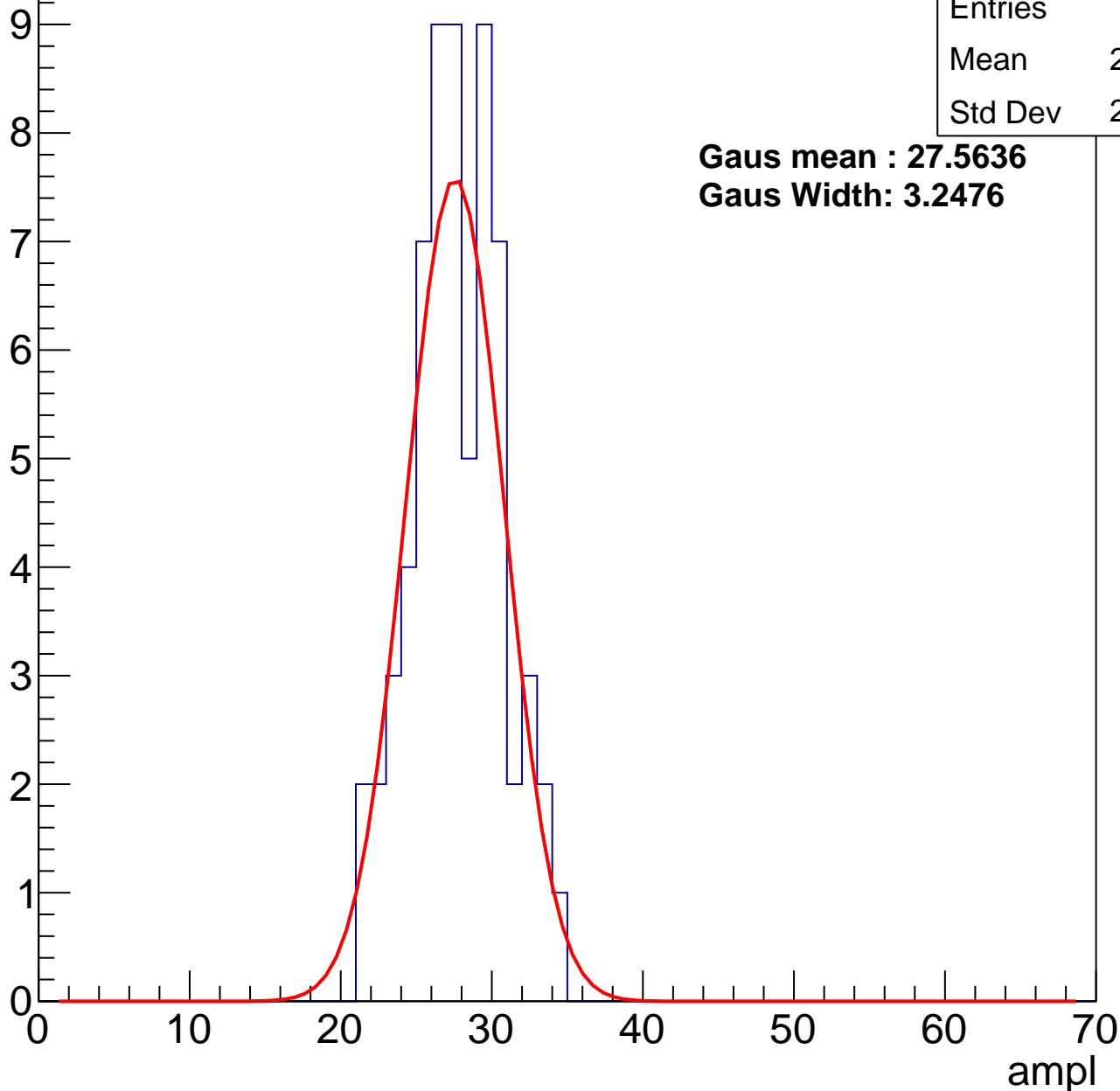
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.26
Std Dev	2.963

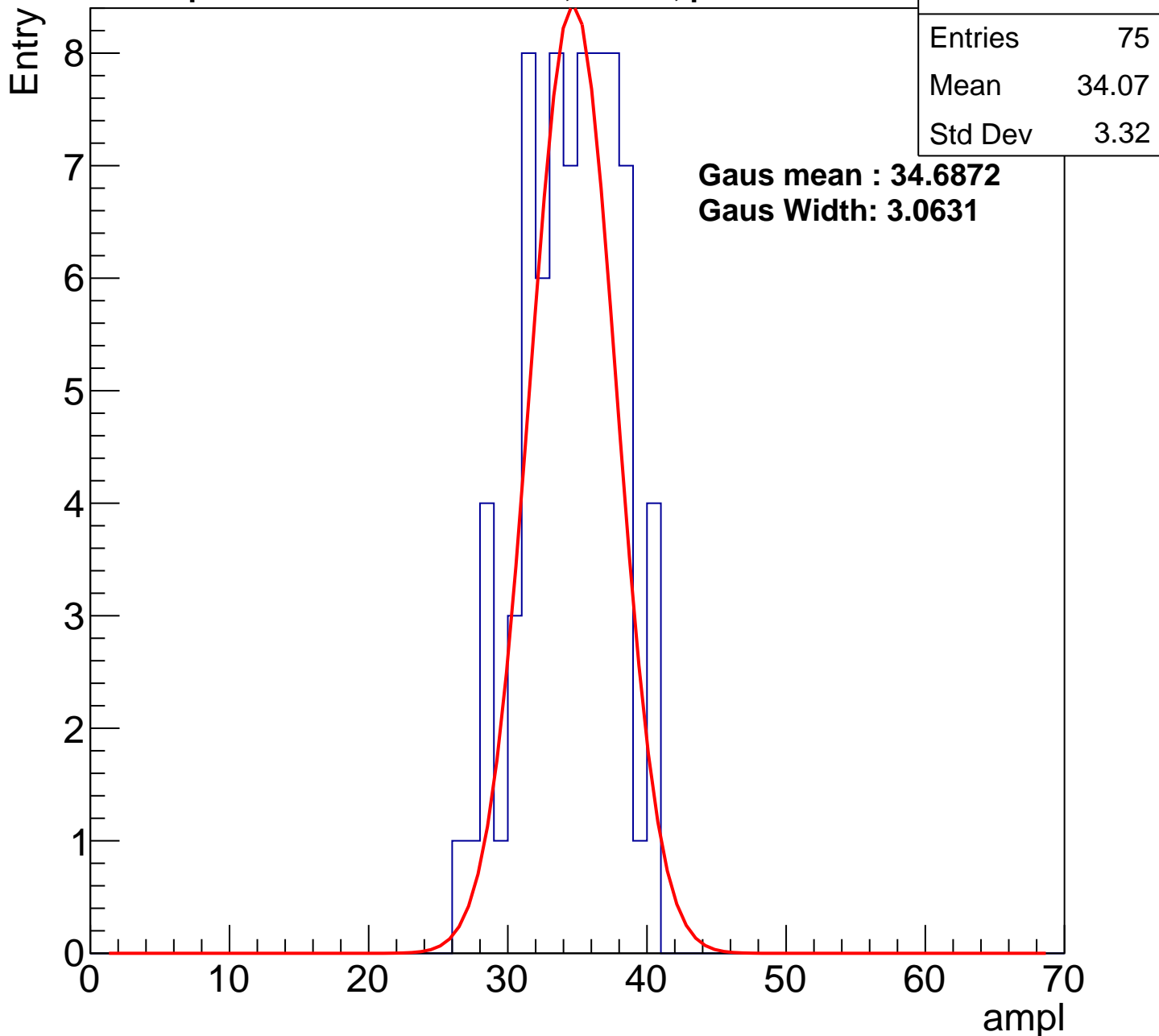
Gaus mean : 27.5636

Gaus Width: 3.2476



B1L002S, U7-ch84, adc1

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch84, adc2

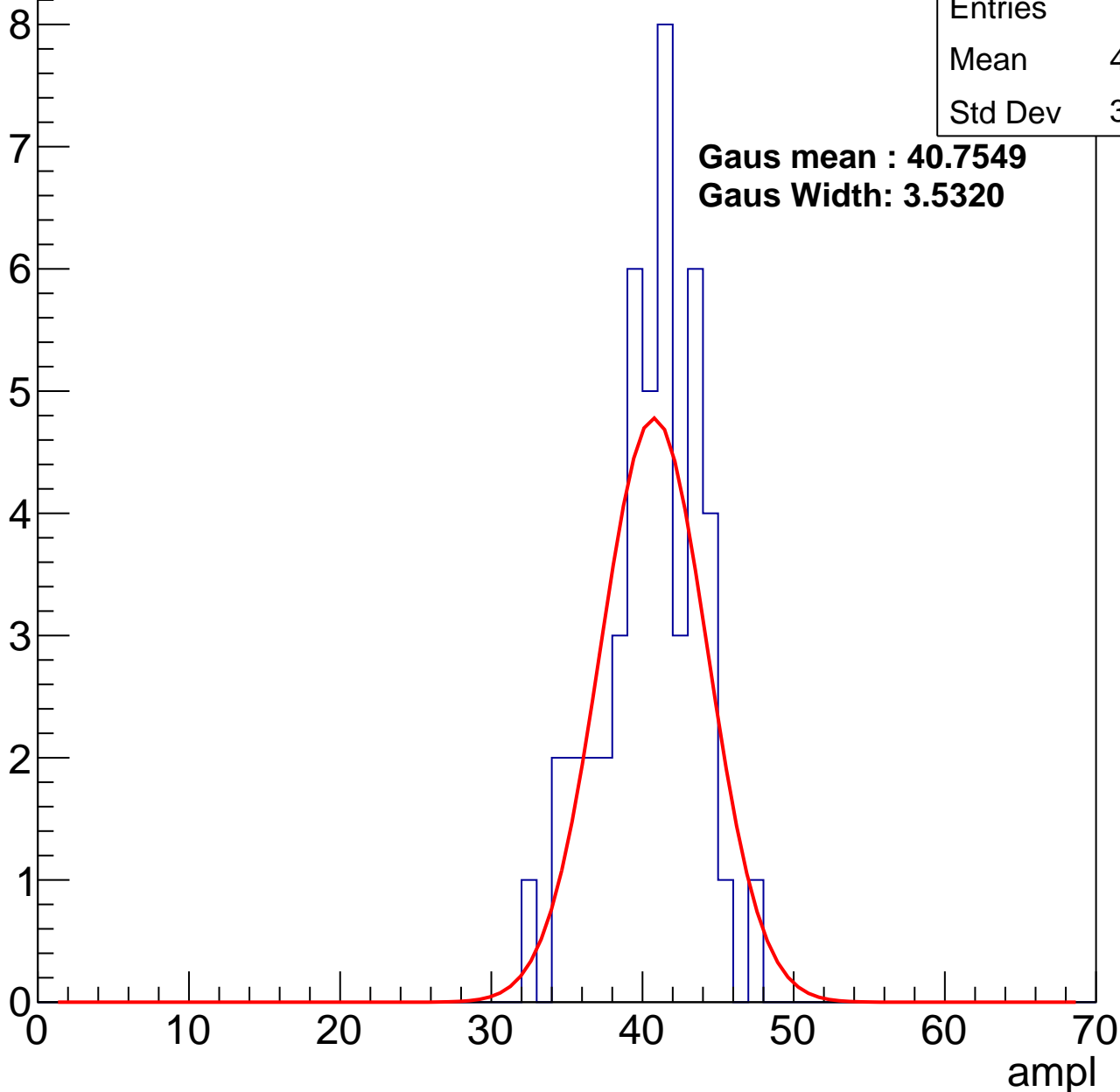
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	40.09
Std Dev	3.182

Gaus mean : 40.7549

Gaus Width: 3.5320

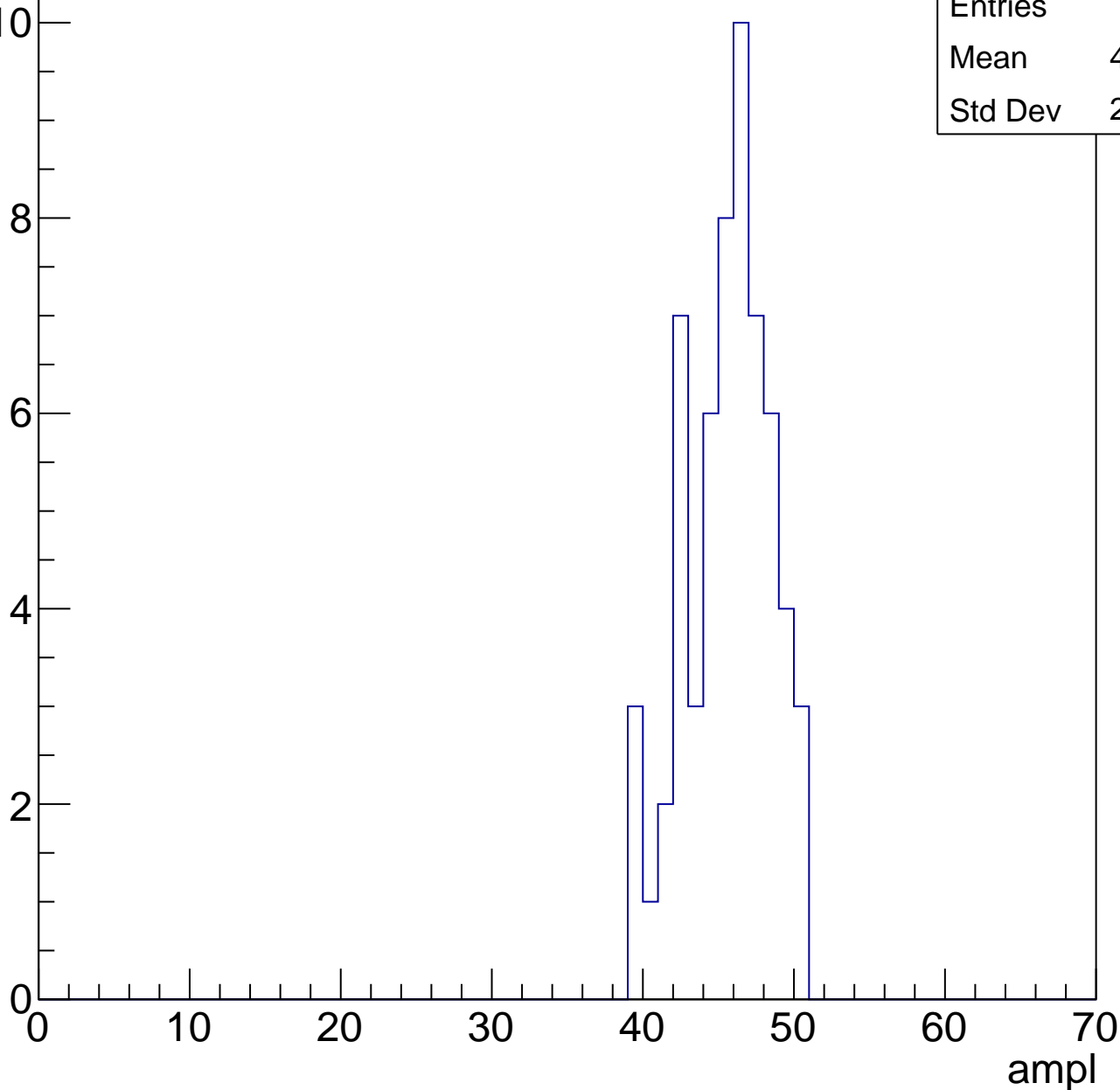


B1L002S, U7-ch84, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.15
Std Dev	2.816

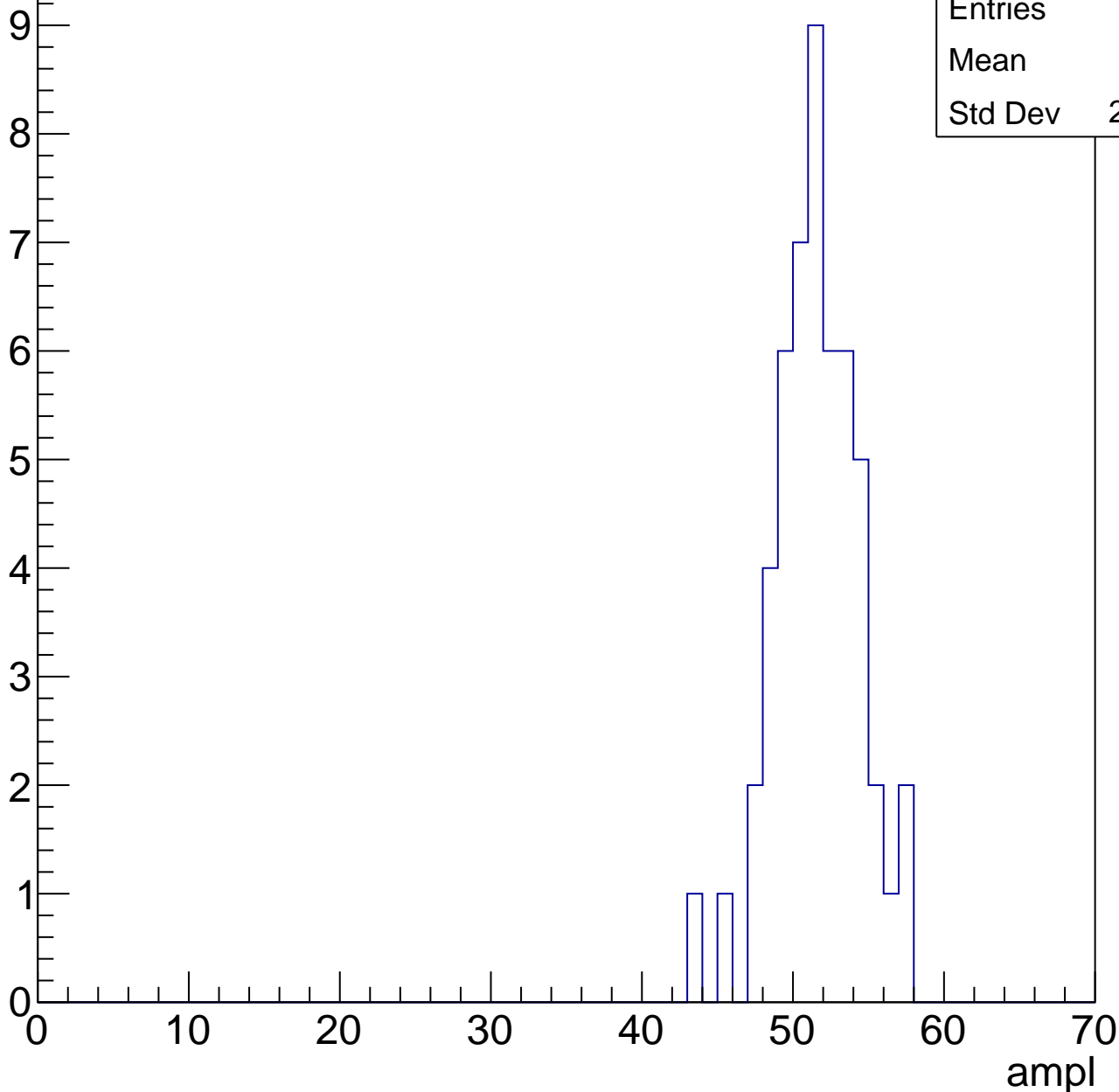


B1L002S, U7-ch84, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.1
Std Dev	2.782



B1L002S, U7-ch84, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	71
Mean	57.17
Std Dev	3.007

Entry

10

8

6

4

2

0

0

10

20

30

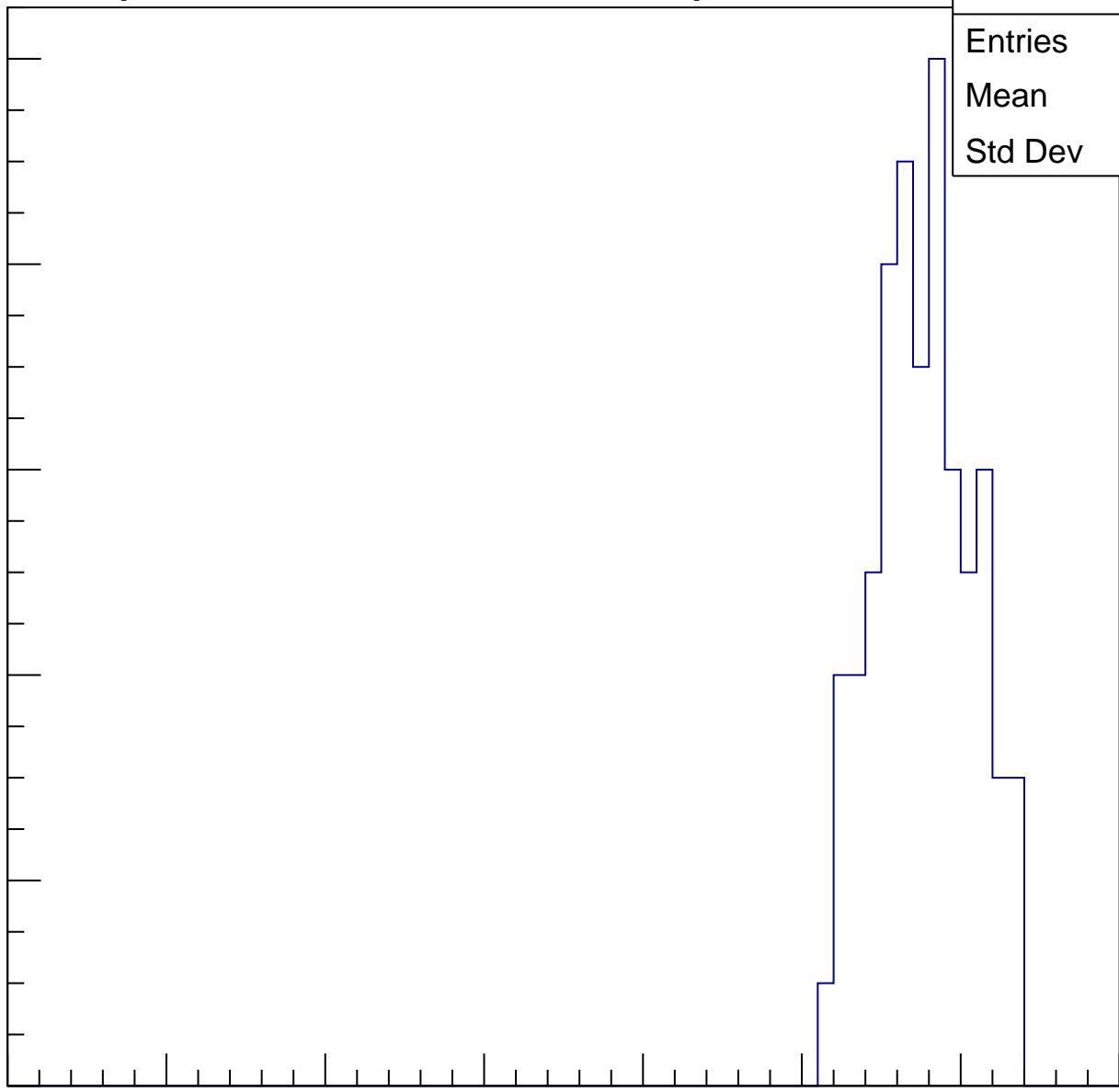
40

50

60

70

ampl

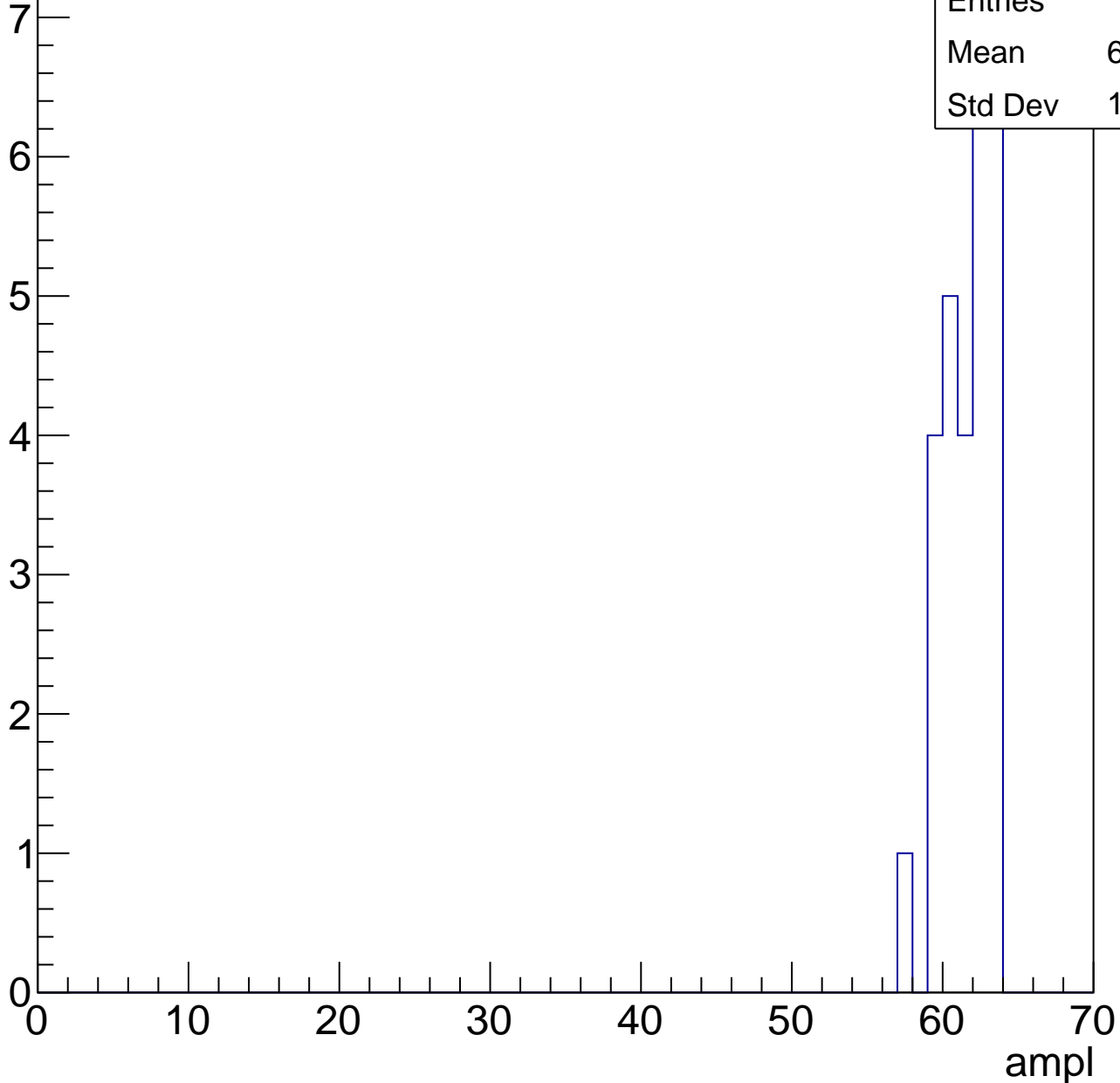


B1L002S, U7-ch84, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	28
Mean	61.14
Std Dev	1.597



B1L002S, U7-ch84, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch85, adc0

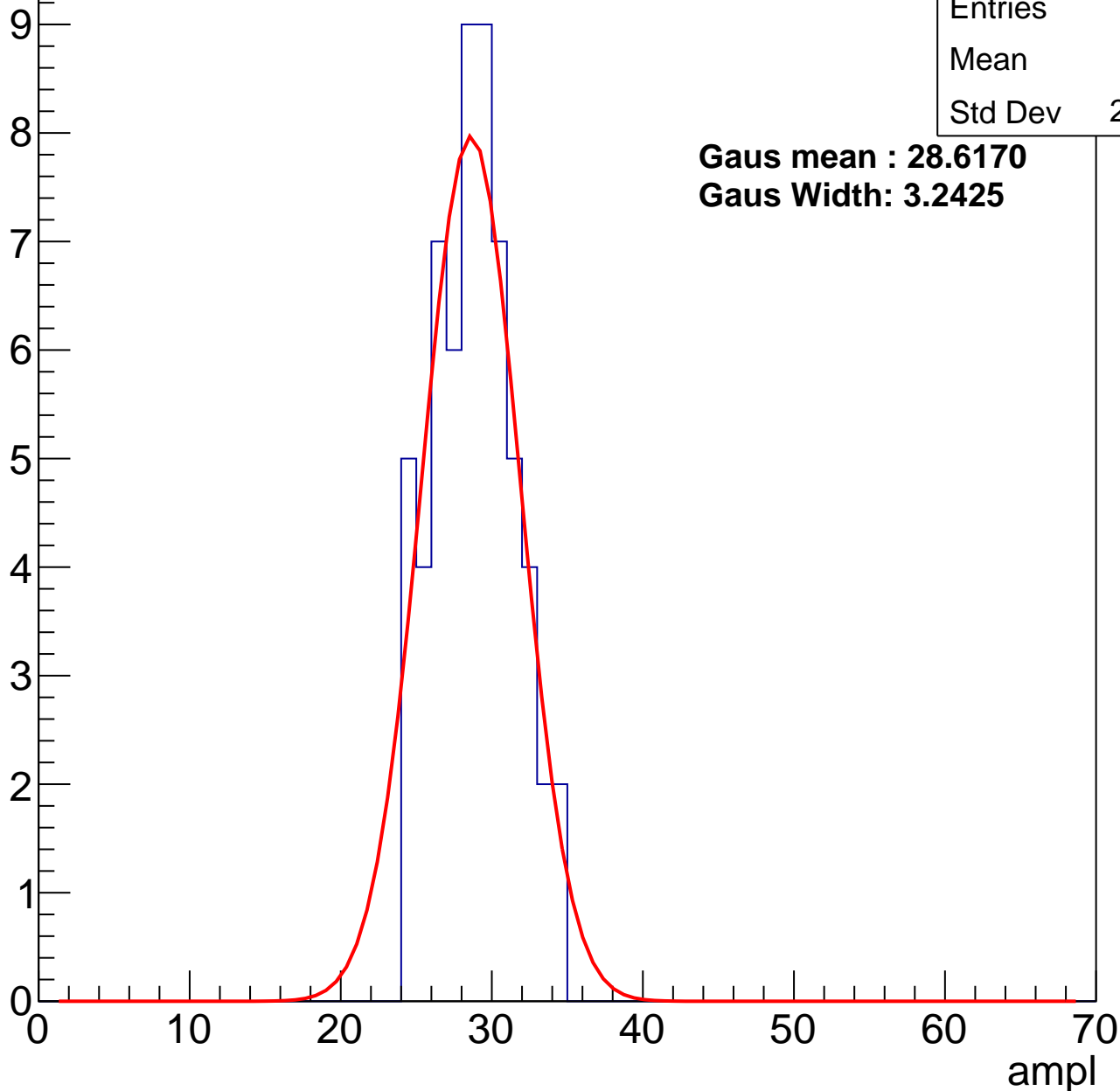
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.4
Std Dev	2.609

Gaus mean : 28.6170

Gaus Width: 3.2425



B1L002S, U7-ch85, adc1

calib_packv5_042523_0143.root, FC#10, port B3

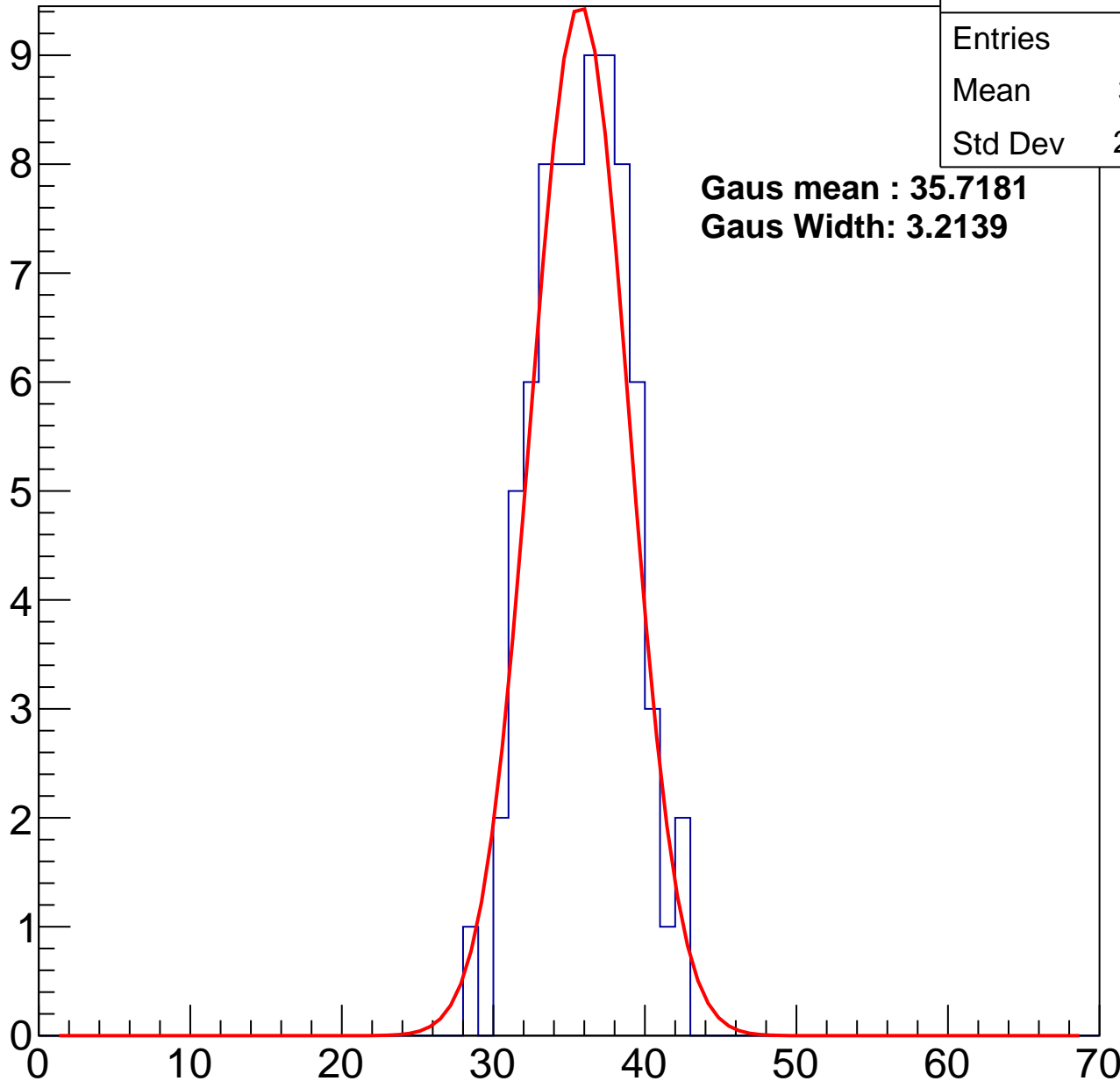
Entry

9
8
7
6
5
4
3
2
1
0

Entries	76
Mean	35.41
Std Dev	2.996

Gaus mean : 35.7181
Gaus Width: 3.2139

ampl



B1L002S, U7-ch85, adc2

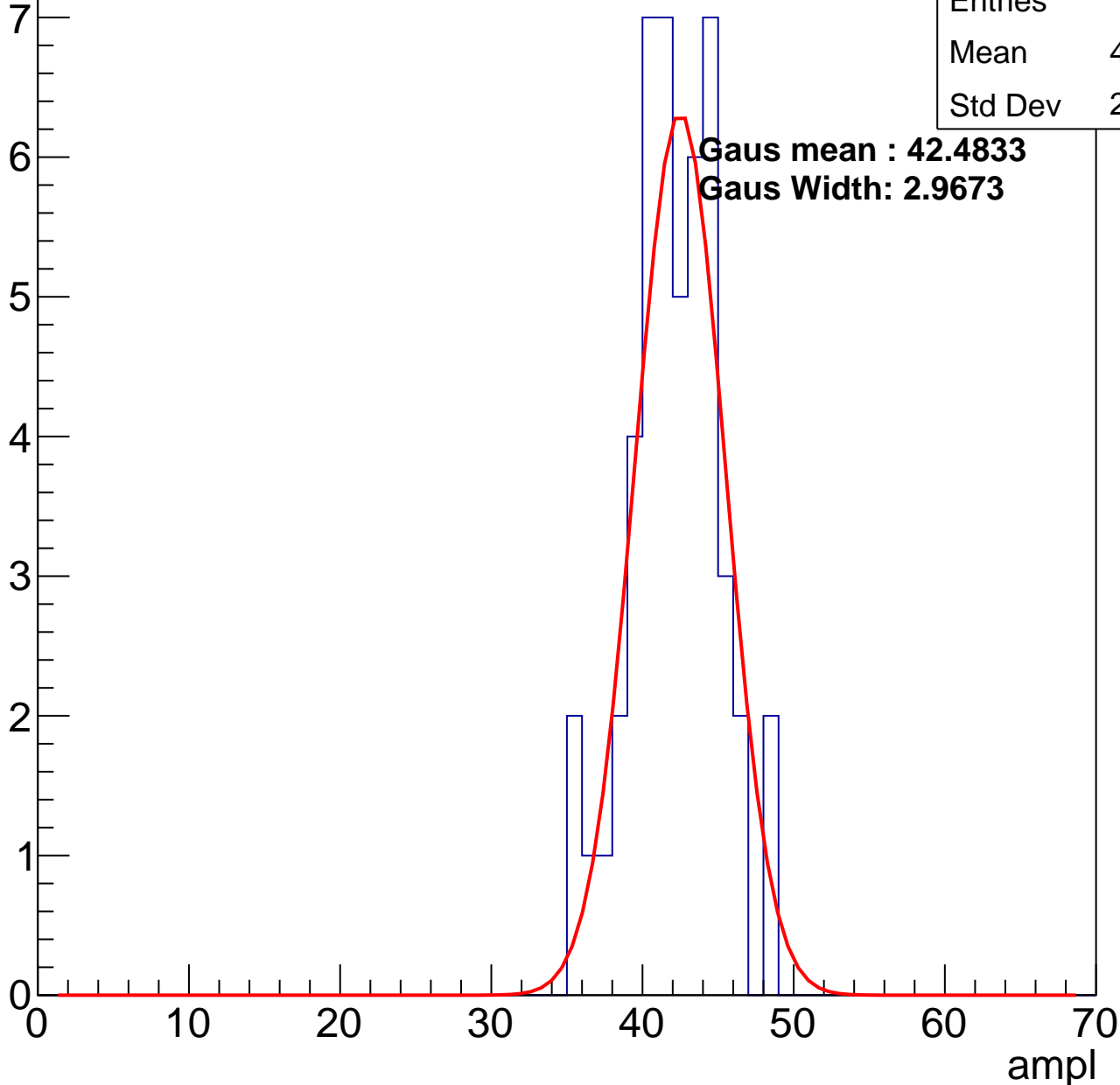
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	41.65
Std Dev	2.932

Gaus mean : 42.4833

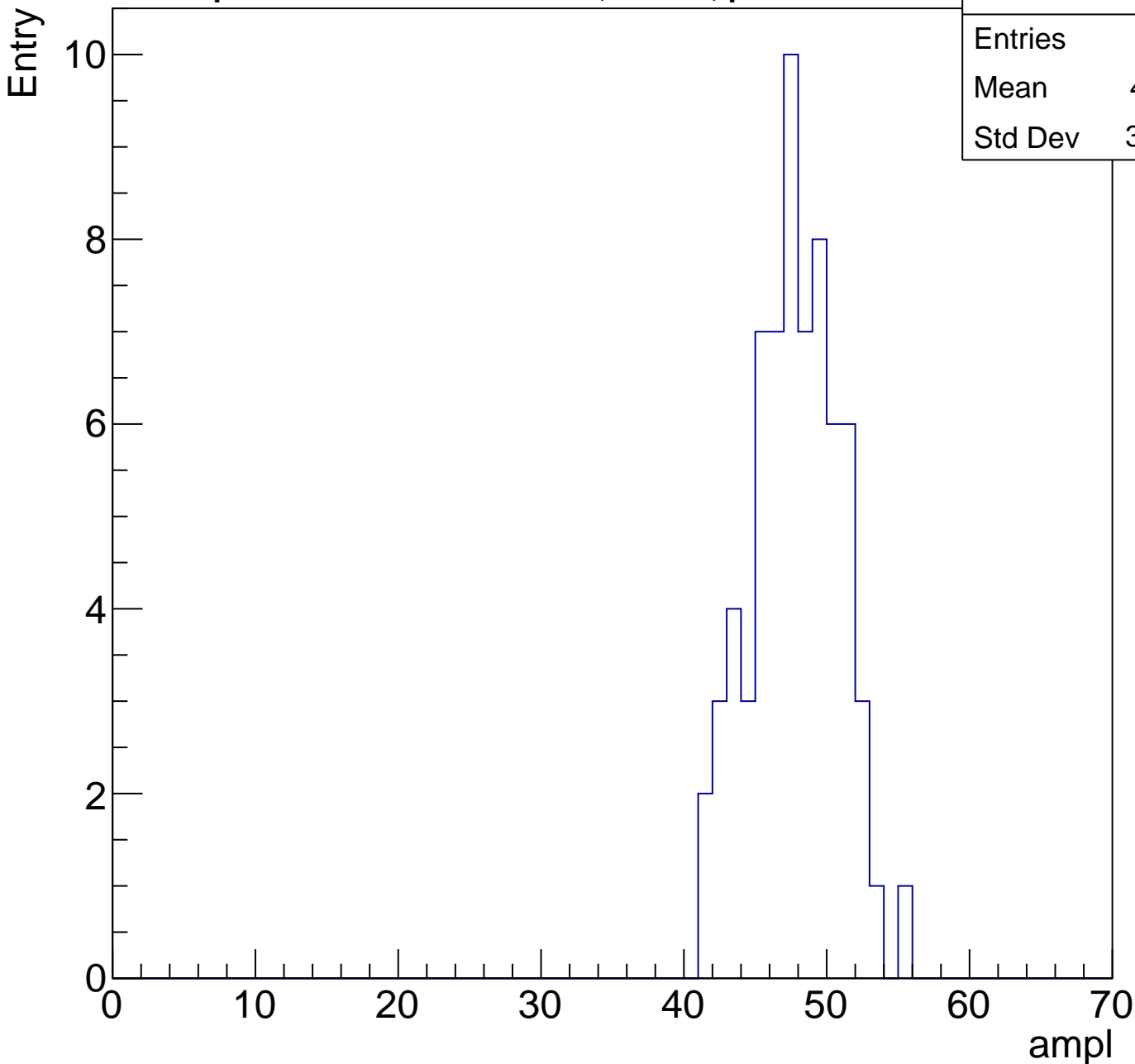
Gaus Width: 2.9673



B1L002S, U7-ch85, adc3

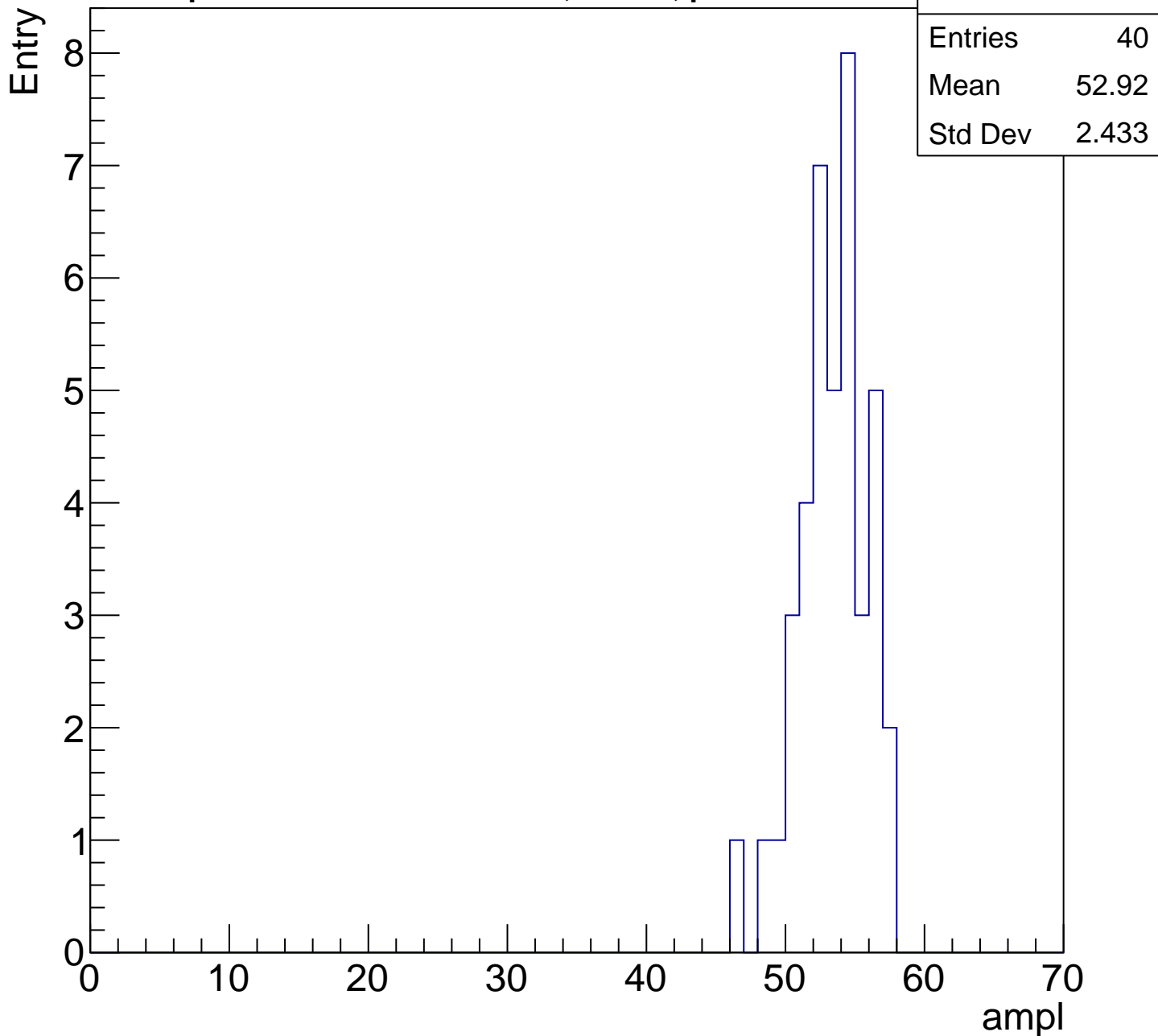
calib_packv5_042523_0143.root, FC#10, port B3

Entries	68
Mean	47.31
Std Dev	3.045



B1L002S, U7-ch85, adc4

calib_packv5_042523_0143.root, FC#10, port B3

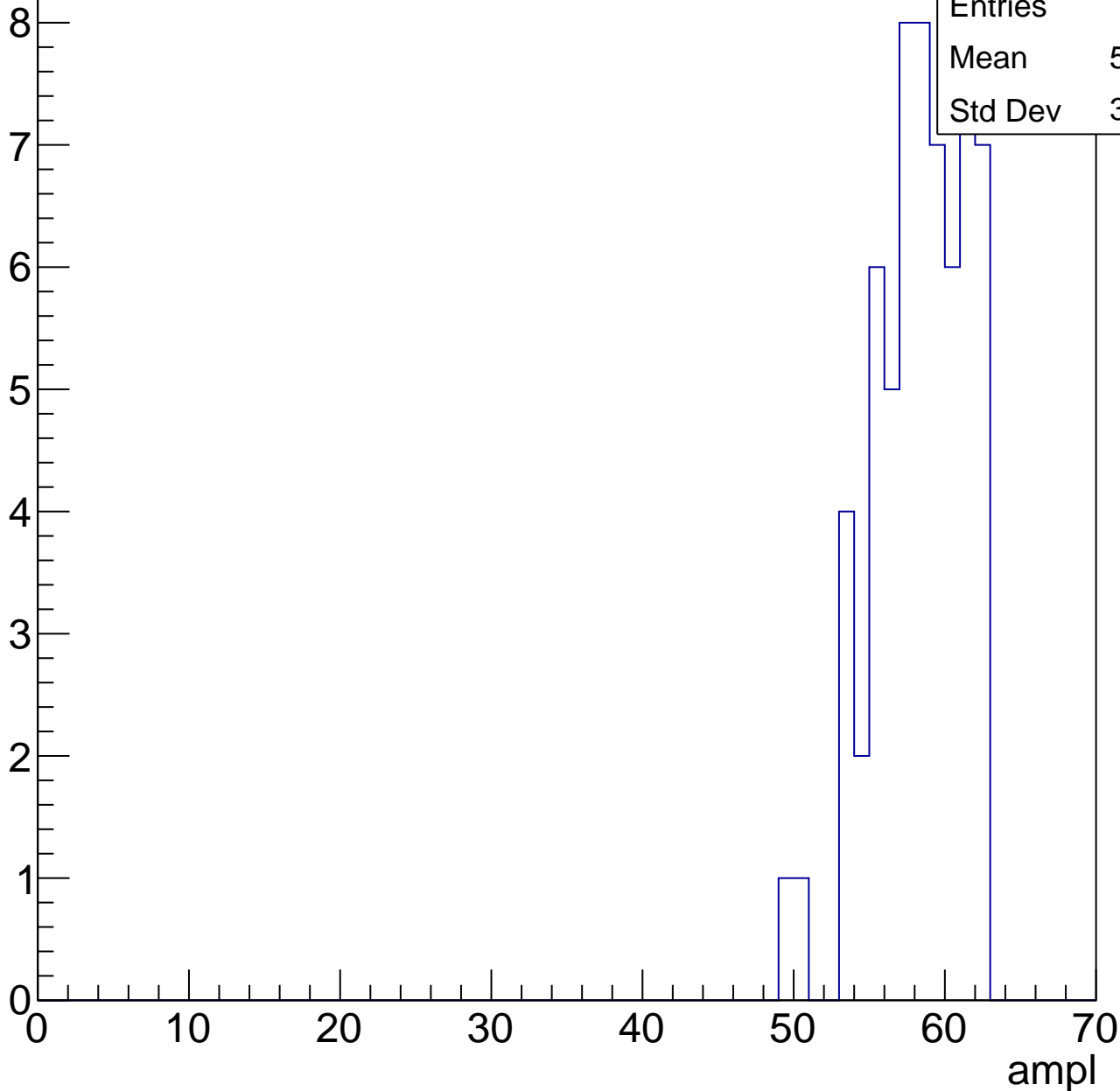


B1L002S, U7-ch85, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	57.84
Std Dev	3.014

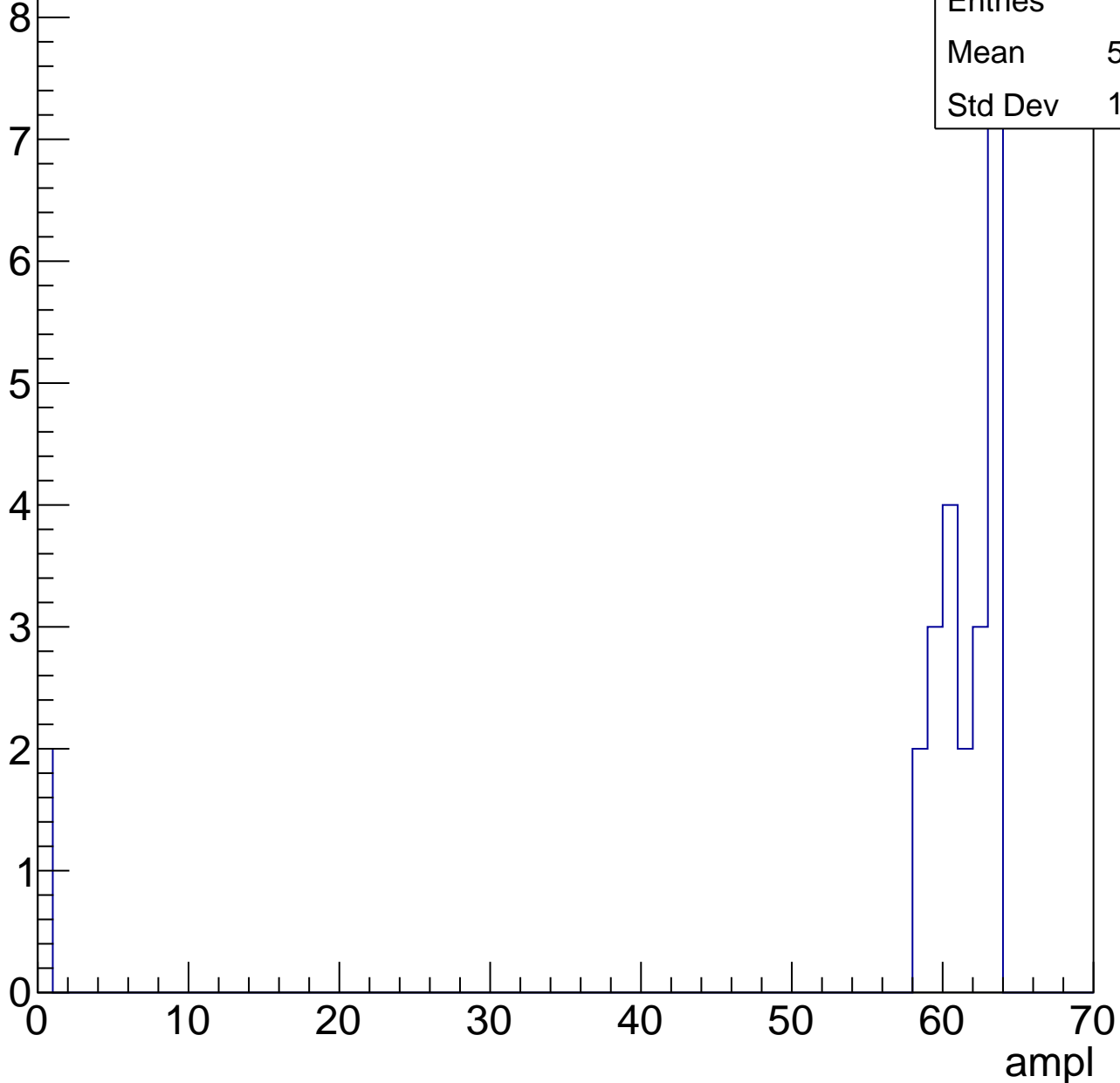


B1L002S, U7-ch85, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

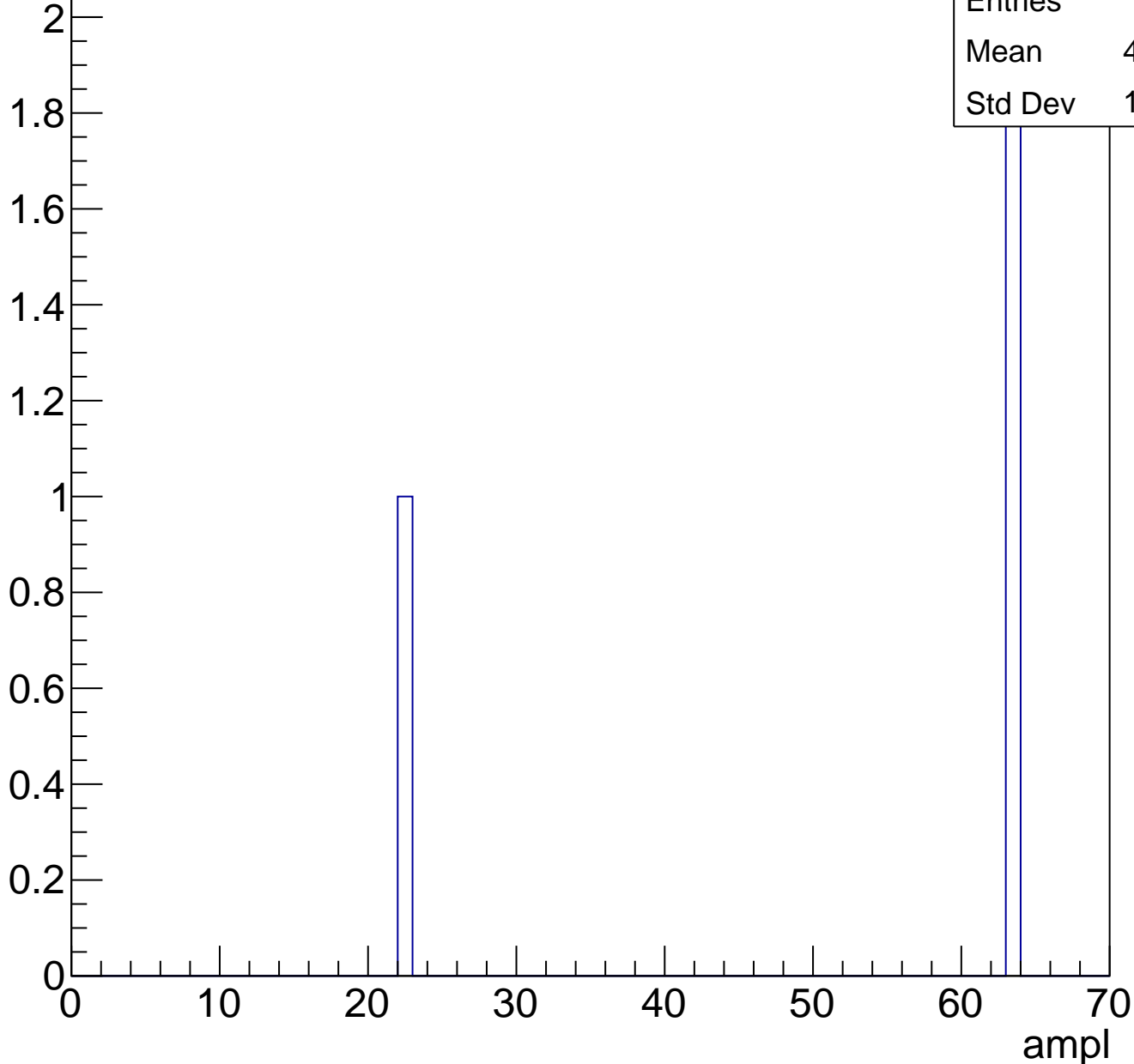
Entries	24
Mean	56.04
Std Dev	16.98



B1L002S, U7-ch85, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch86, adc0

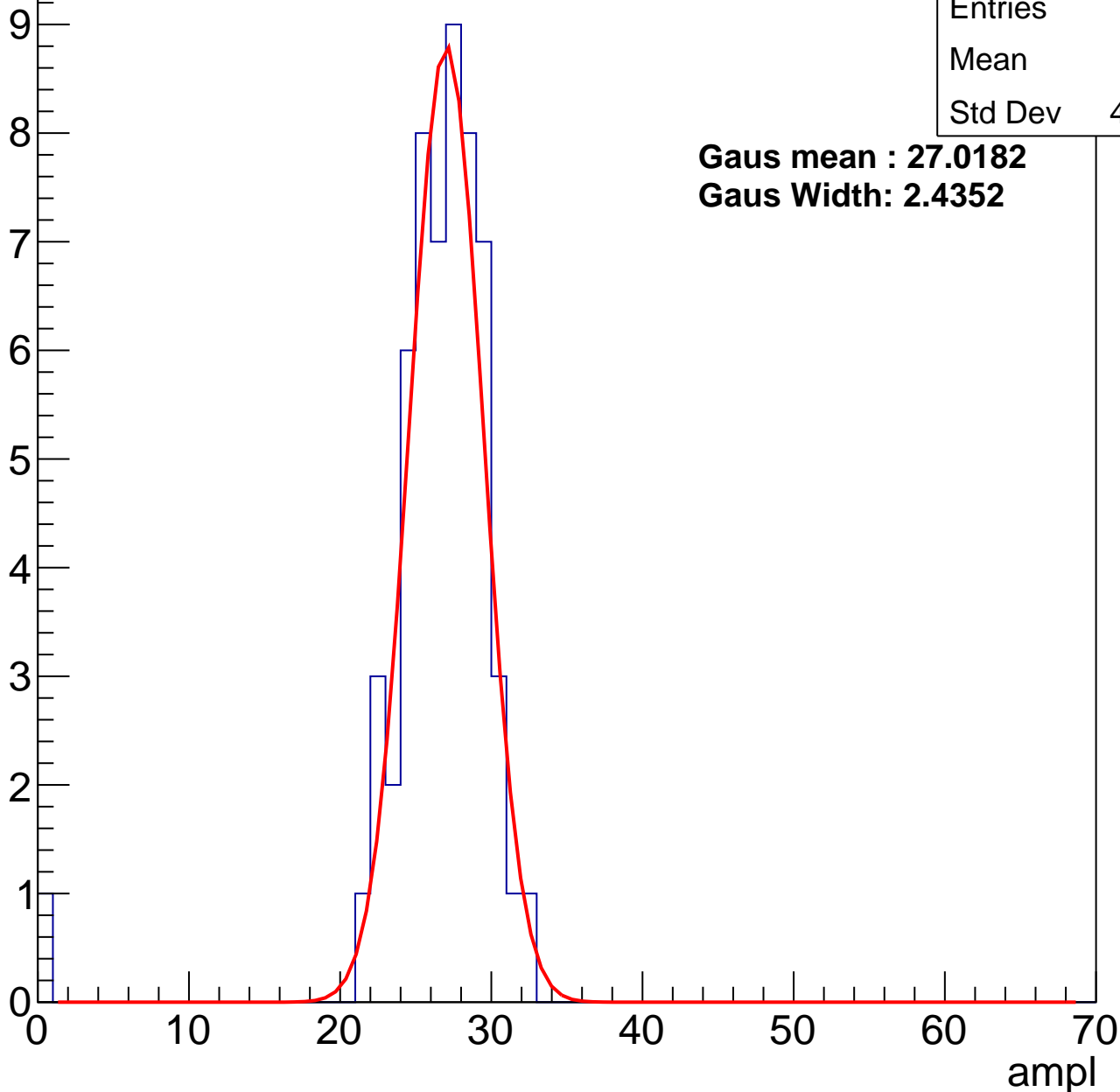
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	26
Std Dev	4.214

Gaus mean : 27.0182

Gaus Width: 2.4352



B1L002S, U7-ch86, adc1

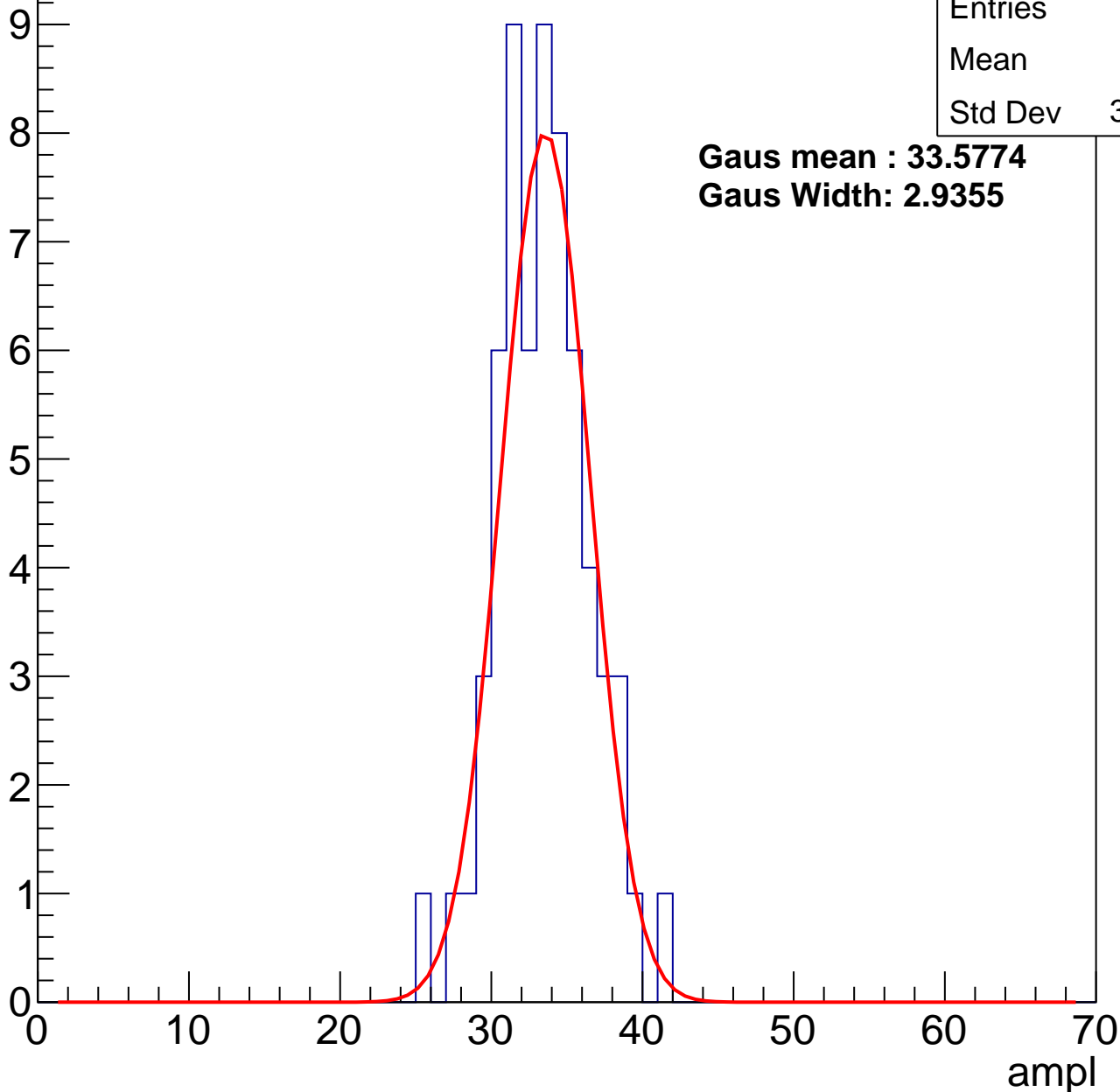
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33
Std Dev	3.005

Gaus mean : 33.5774

Gaus Width: 2.9355



B1L002S, U7-ch86, adc2

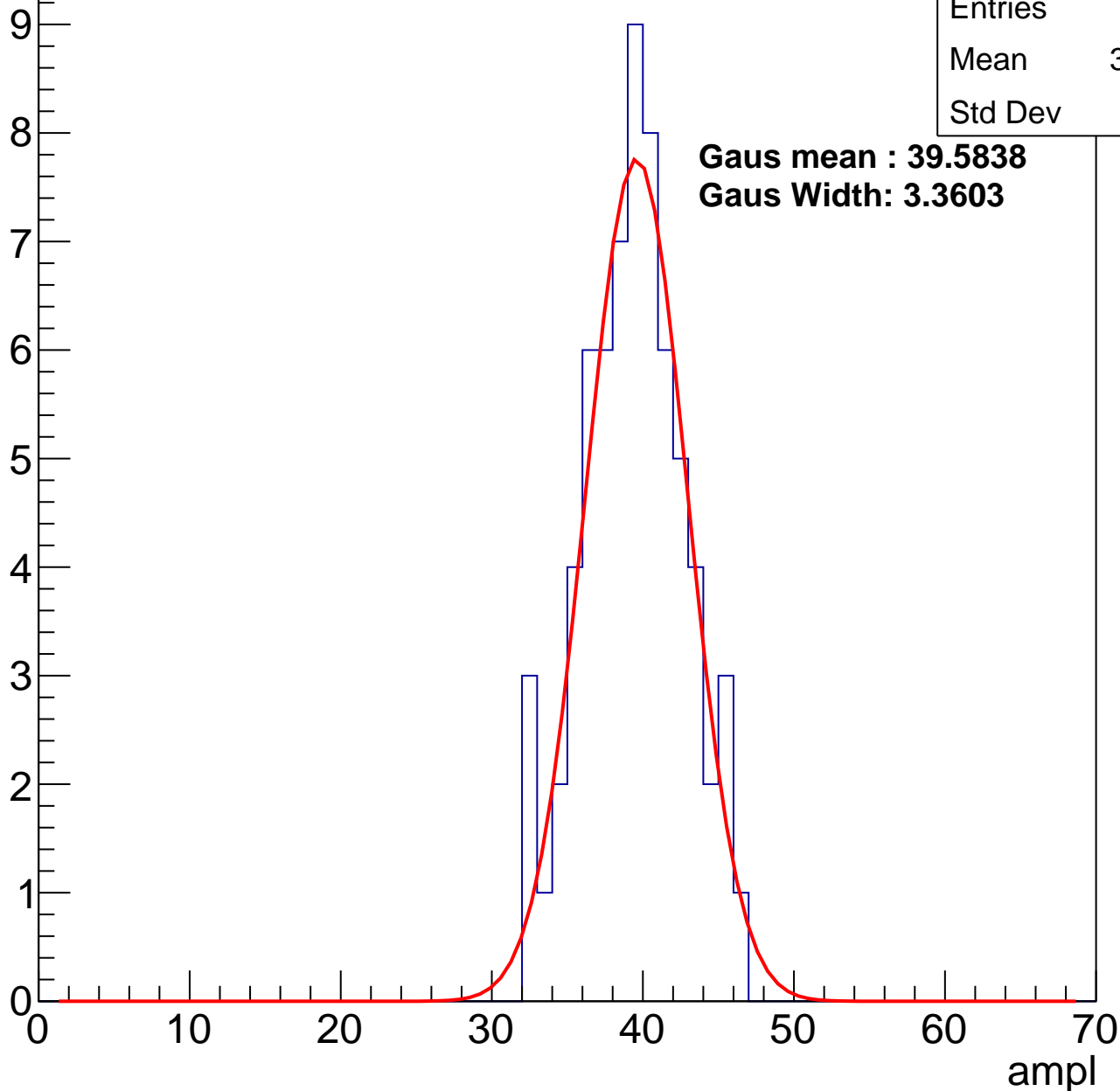
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	38.94
Std Dev	3.3

Gaus mean : 39.5838

Gaus Width: 3.3603

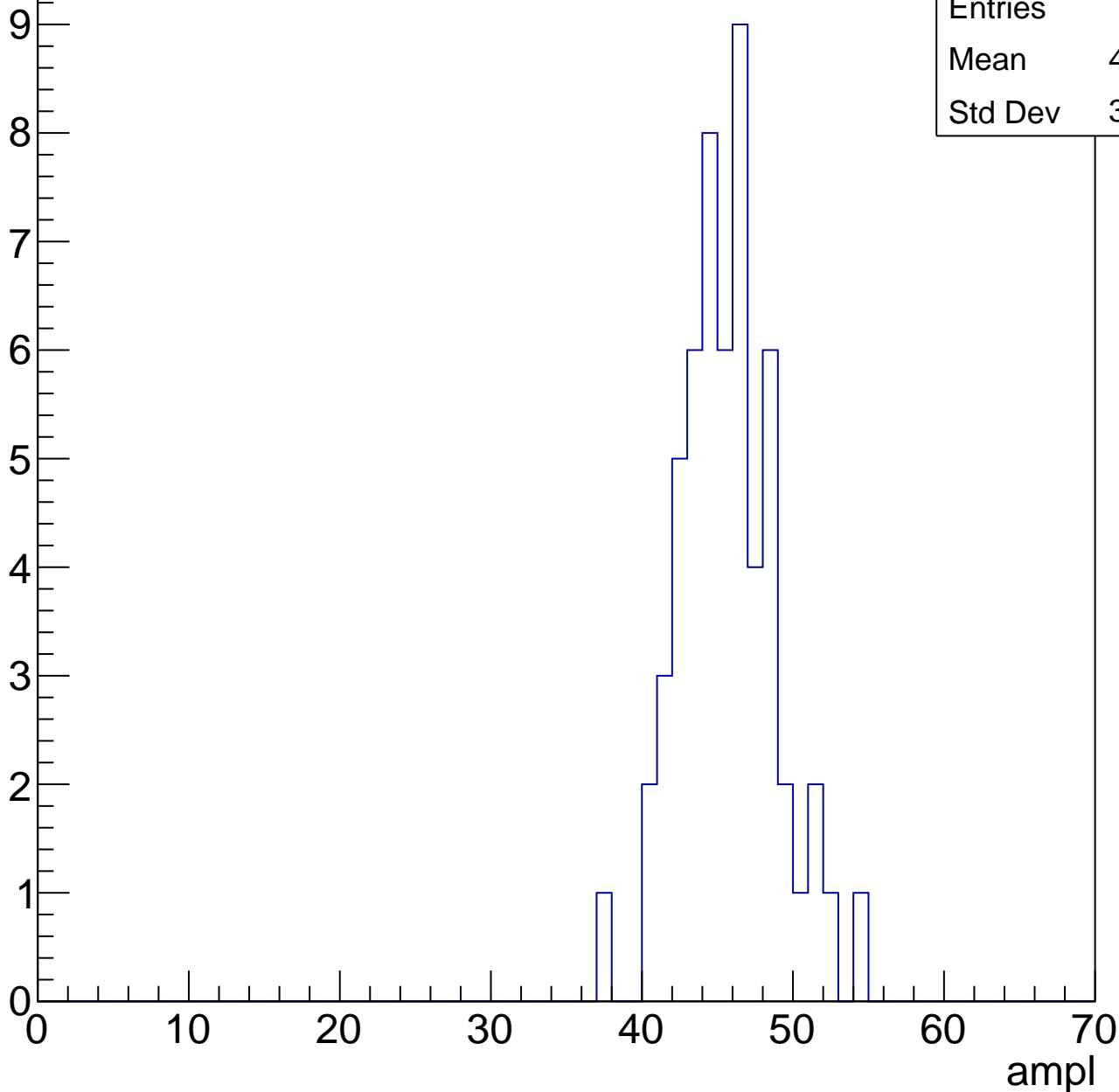


B1L002S, U7-ch86, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	45.19
Std Dev	3.176

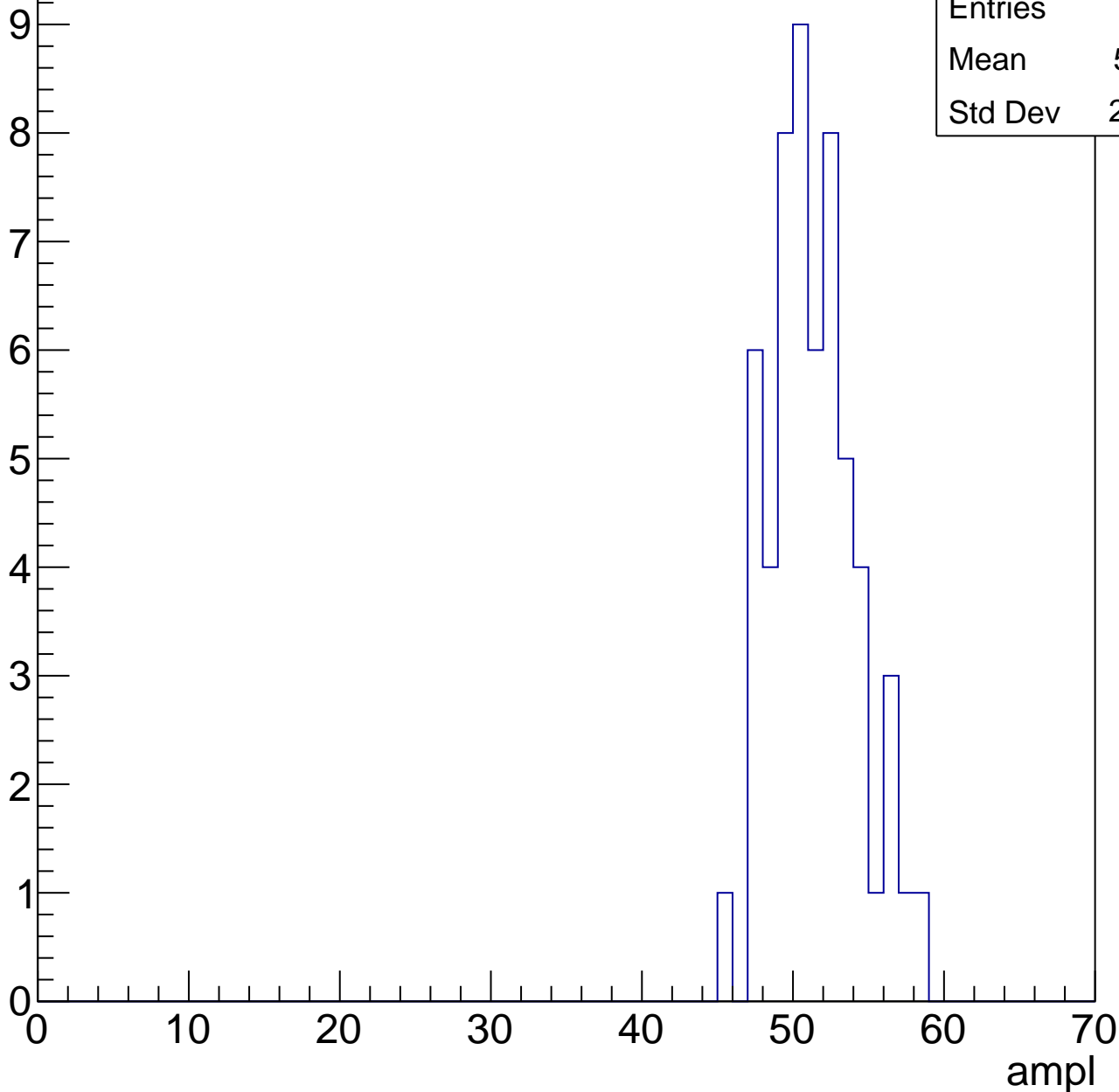


B1L002S, U7-ch86, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

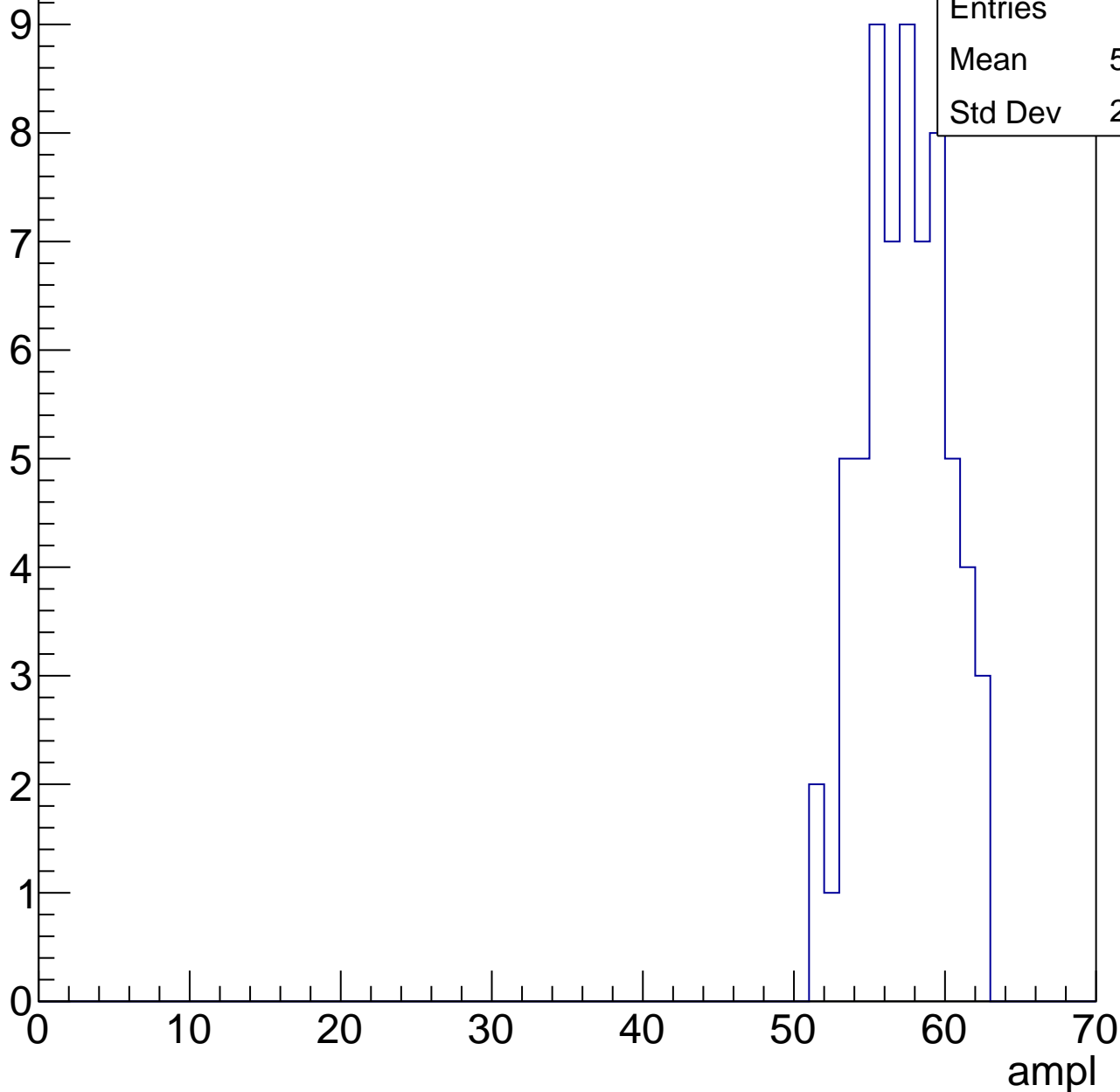
Entries	57
Mean	50.91
Std Dev	2.805



B1L002S, U7-ch86, adc5

calib_packv5_042523_0143.root, FC#10, port B3

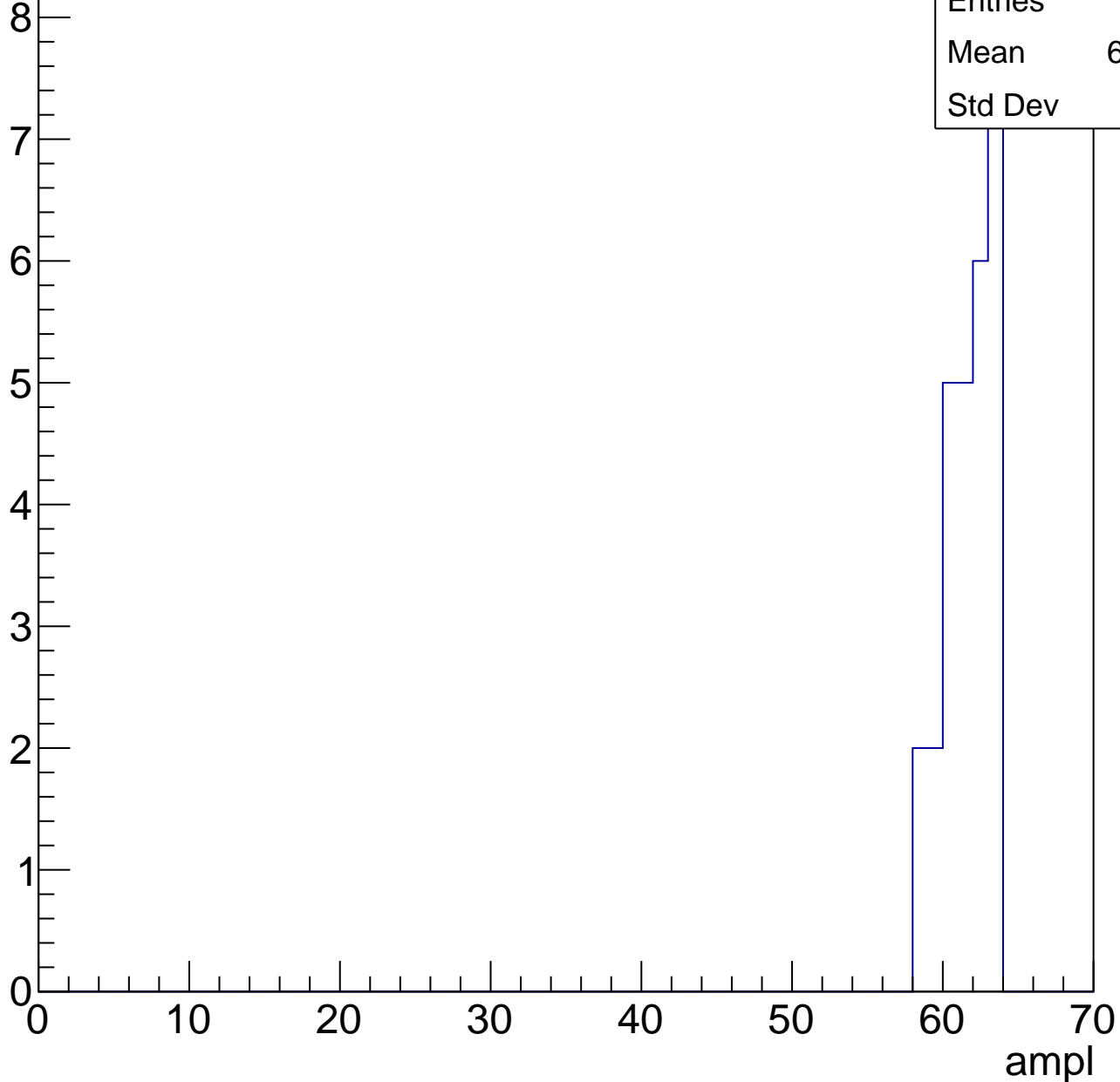
Entry



B1L002S, U7-ch86, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

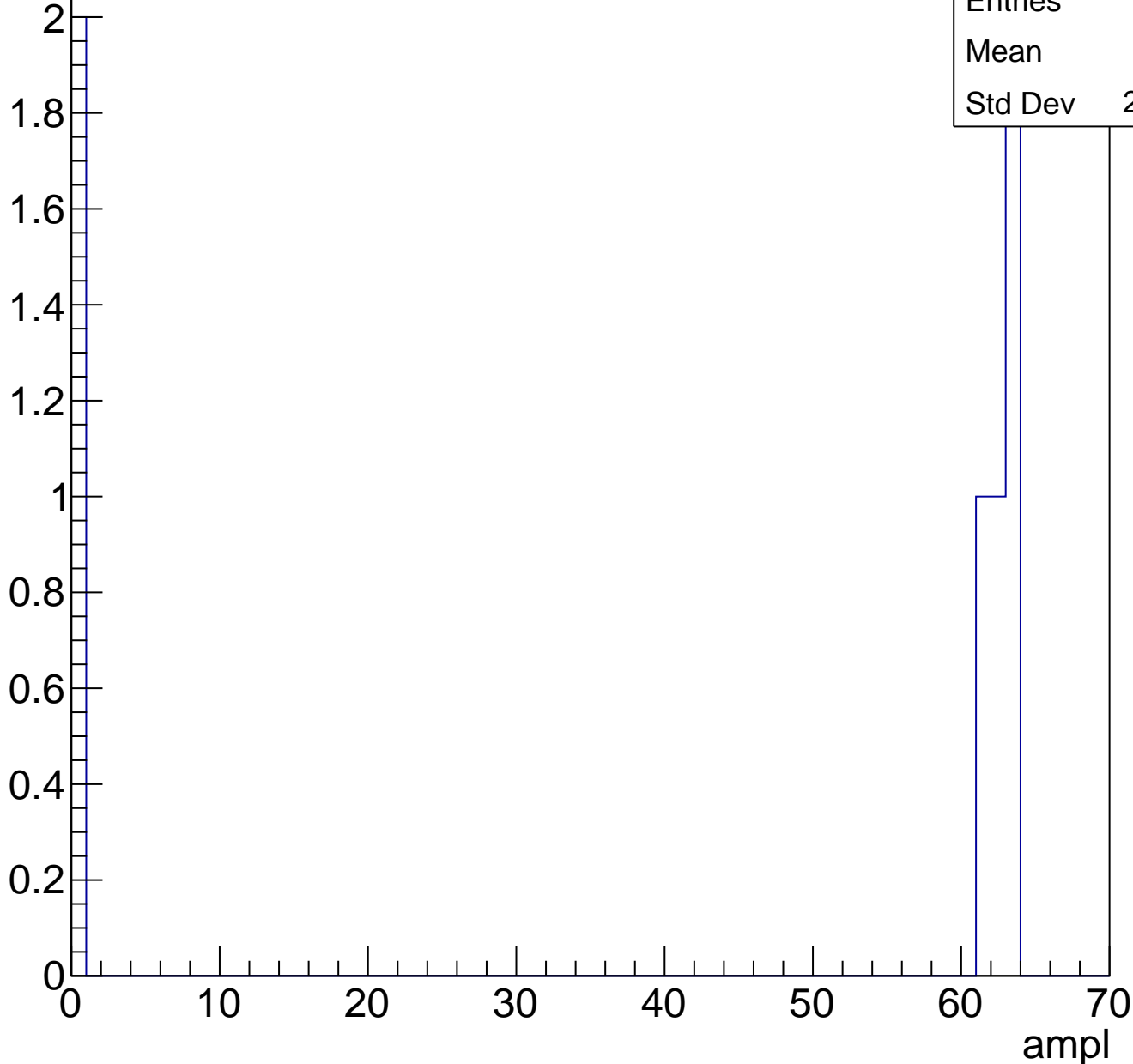


Entries	28
Mean	61.25
Std Dev	1.55

B1L002S, U7-ch86, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch87, adc0

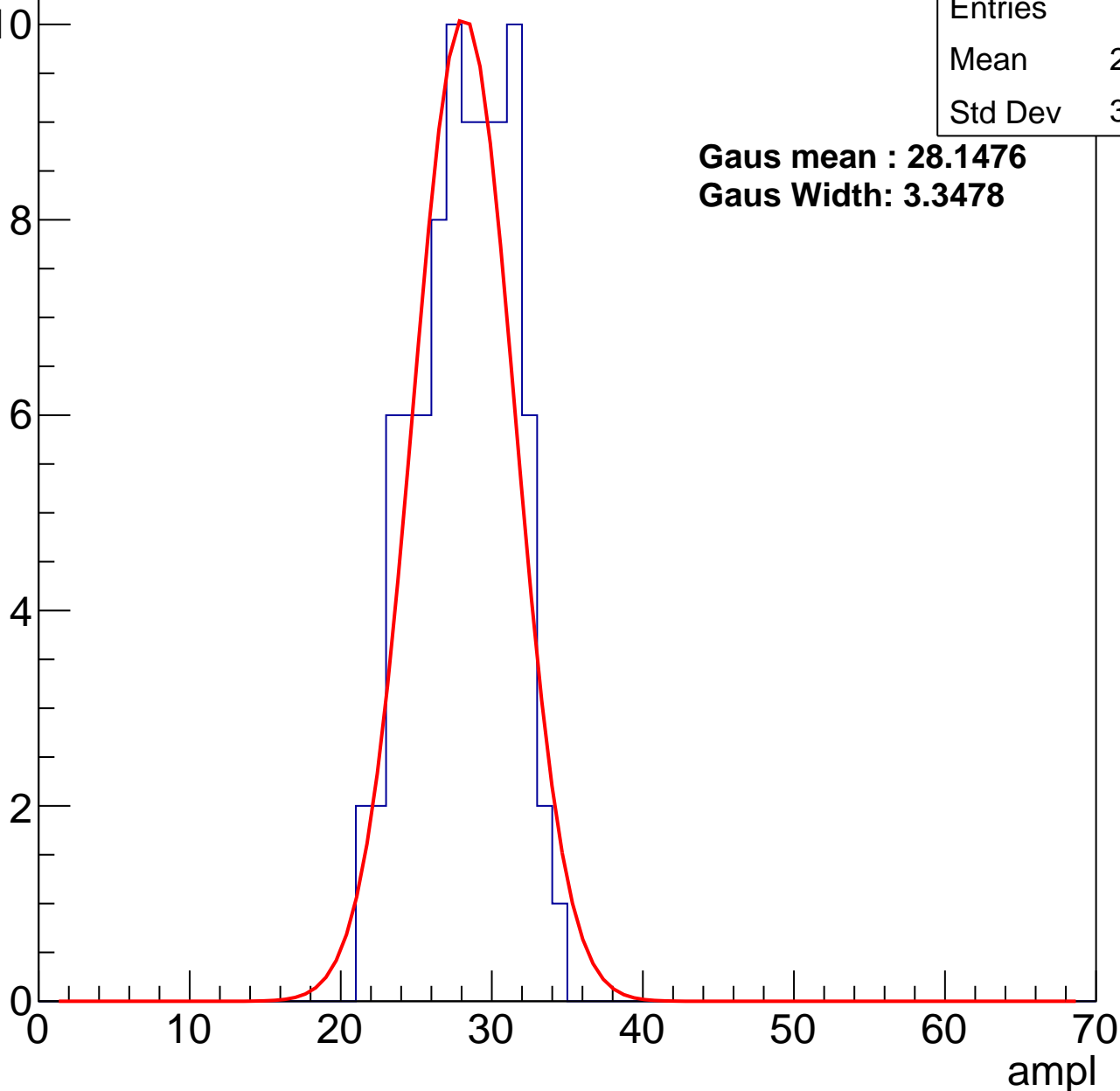
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	86
Mean	27.69
Std Dev	3.089

Gaus mean : 28.1476

Gaus Width: 3.3478



B1L002S, U7-ch87, adc1

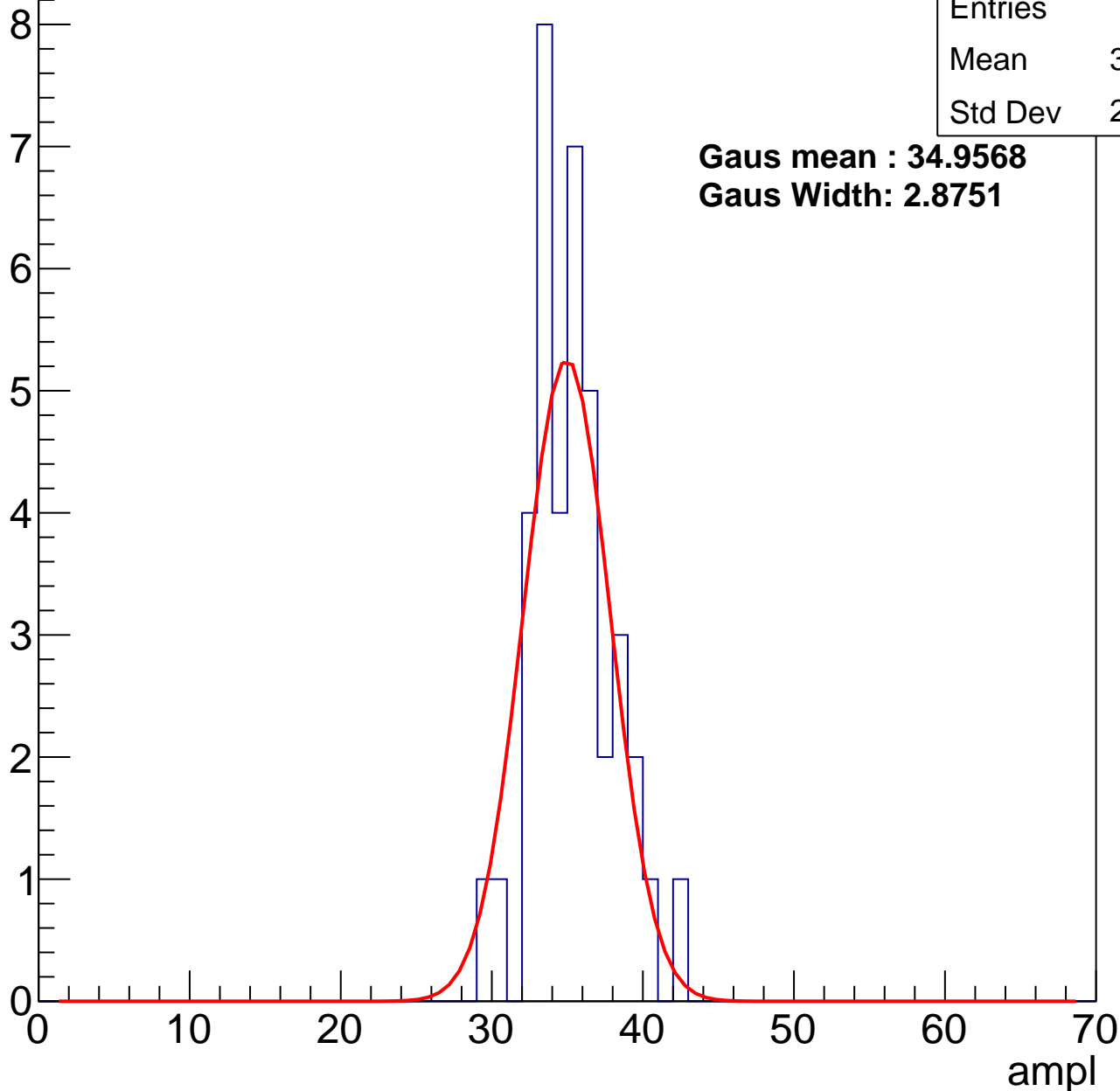
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	39
Mean	34.87
Std Dev	2.672

Gaus mean : 34.9568

Gaus Width: 2.8751



B1L002S, U7-ch87, adc2

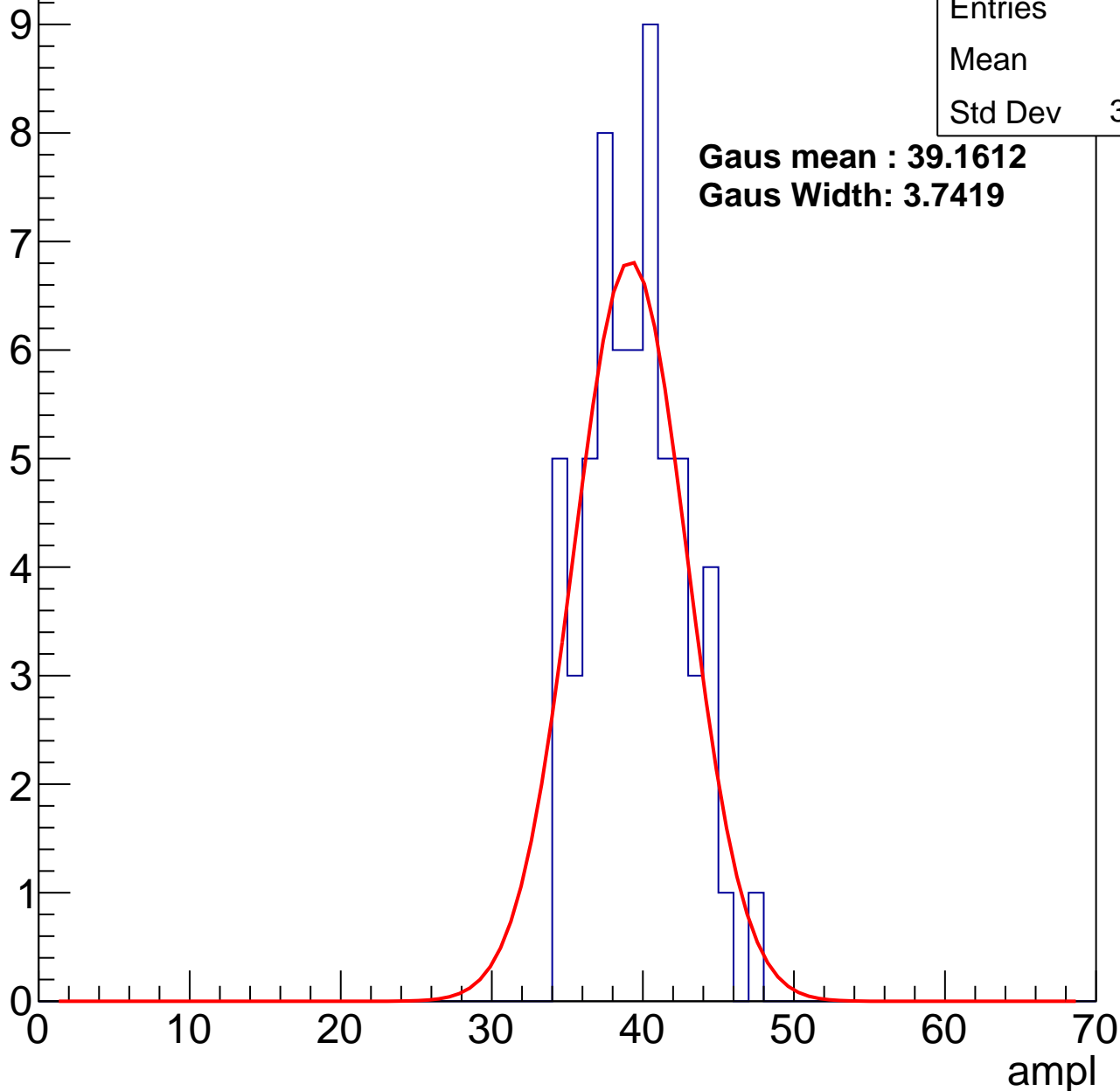
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.1
Std Dev	3.077

Gaus mean : 39.1612

Gaus Width: 3.7419



B1L002S, U7-ch87, adc3

calib_packv5_042523_0143.root, FC#10, port B3

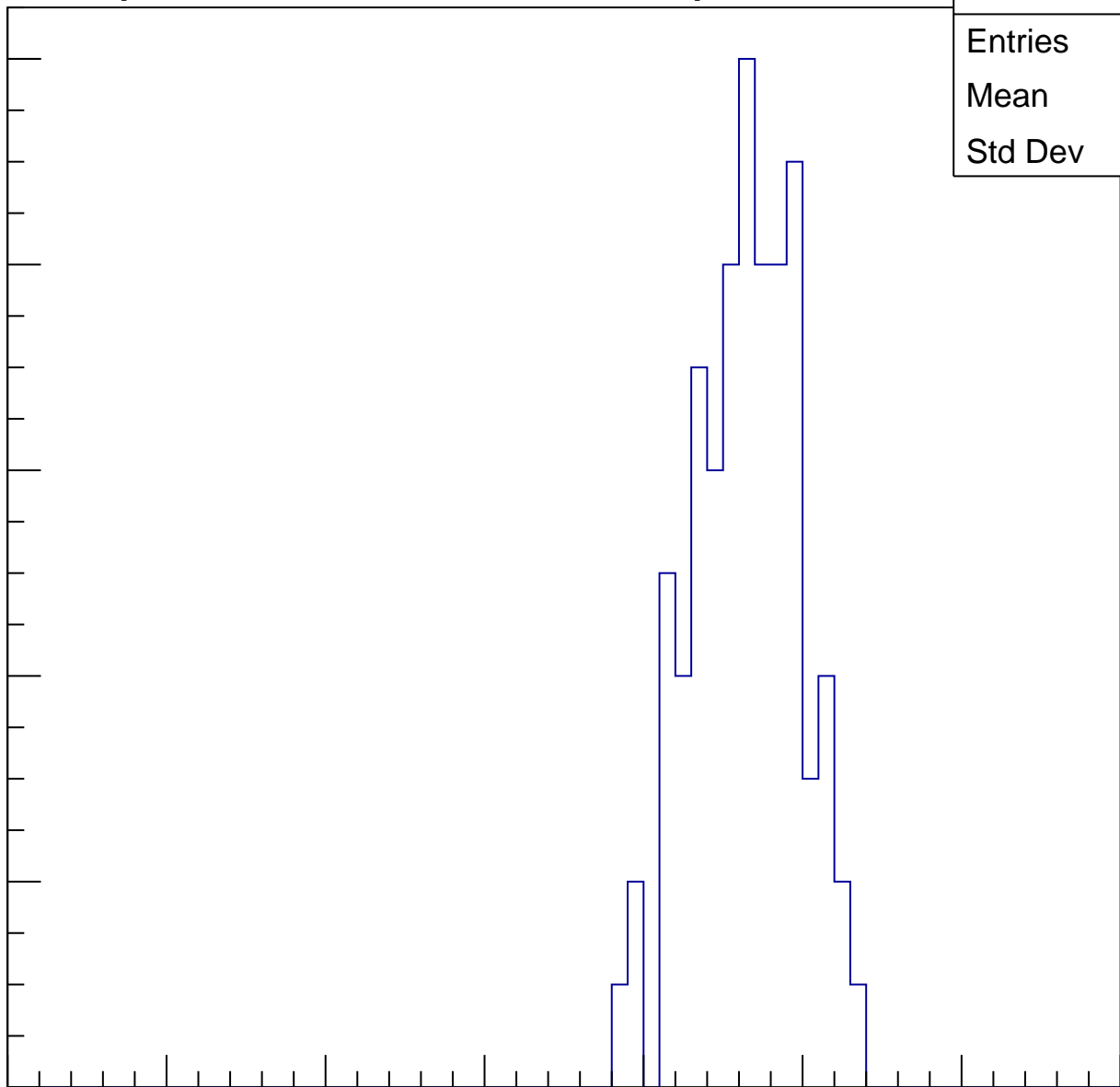
Entries	78
Mean	45.97
Std Dev	3.274

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

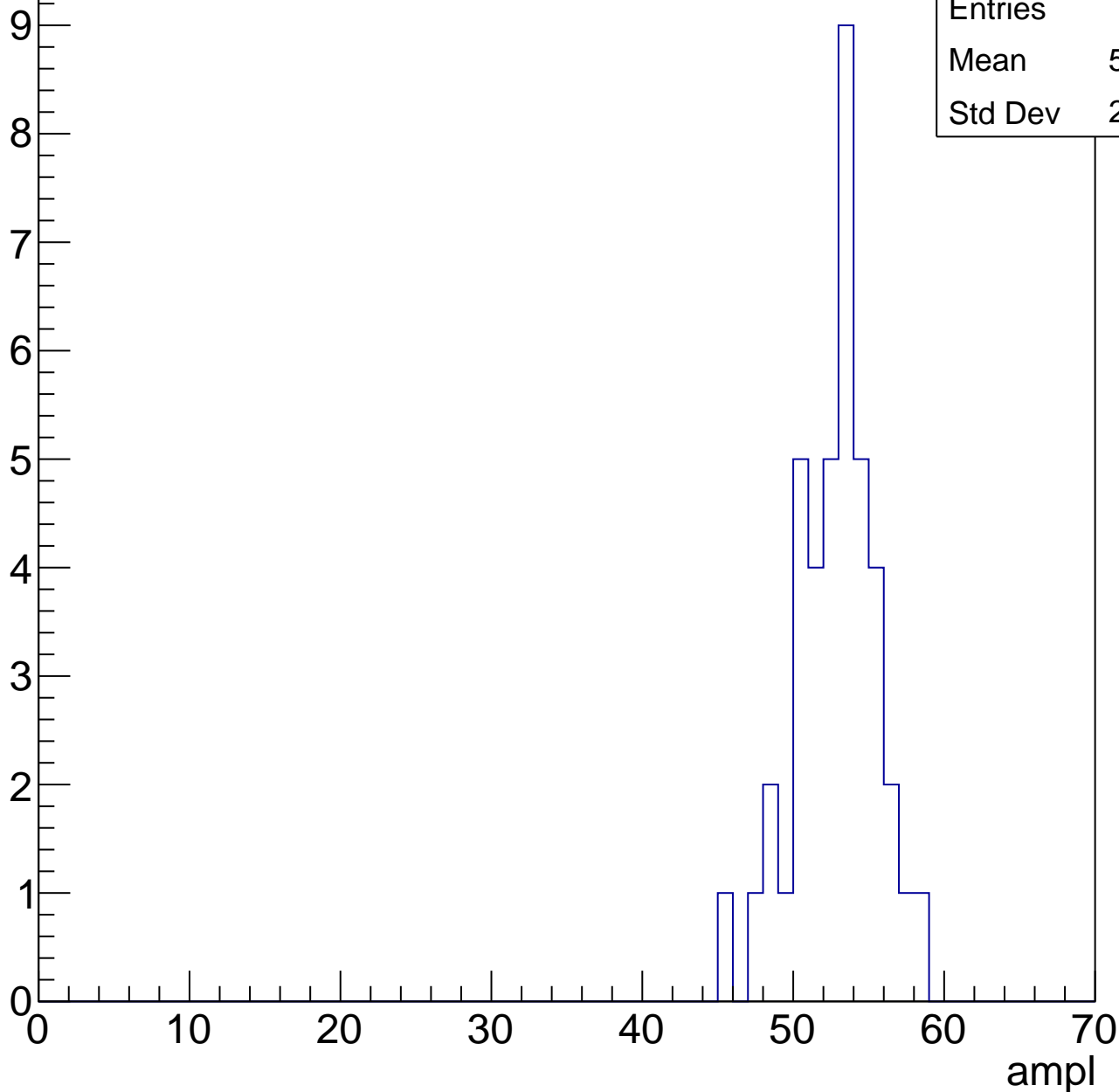
ampl



B1L002S, U7-ch87, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry



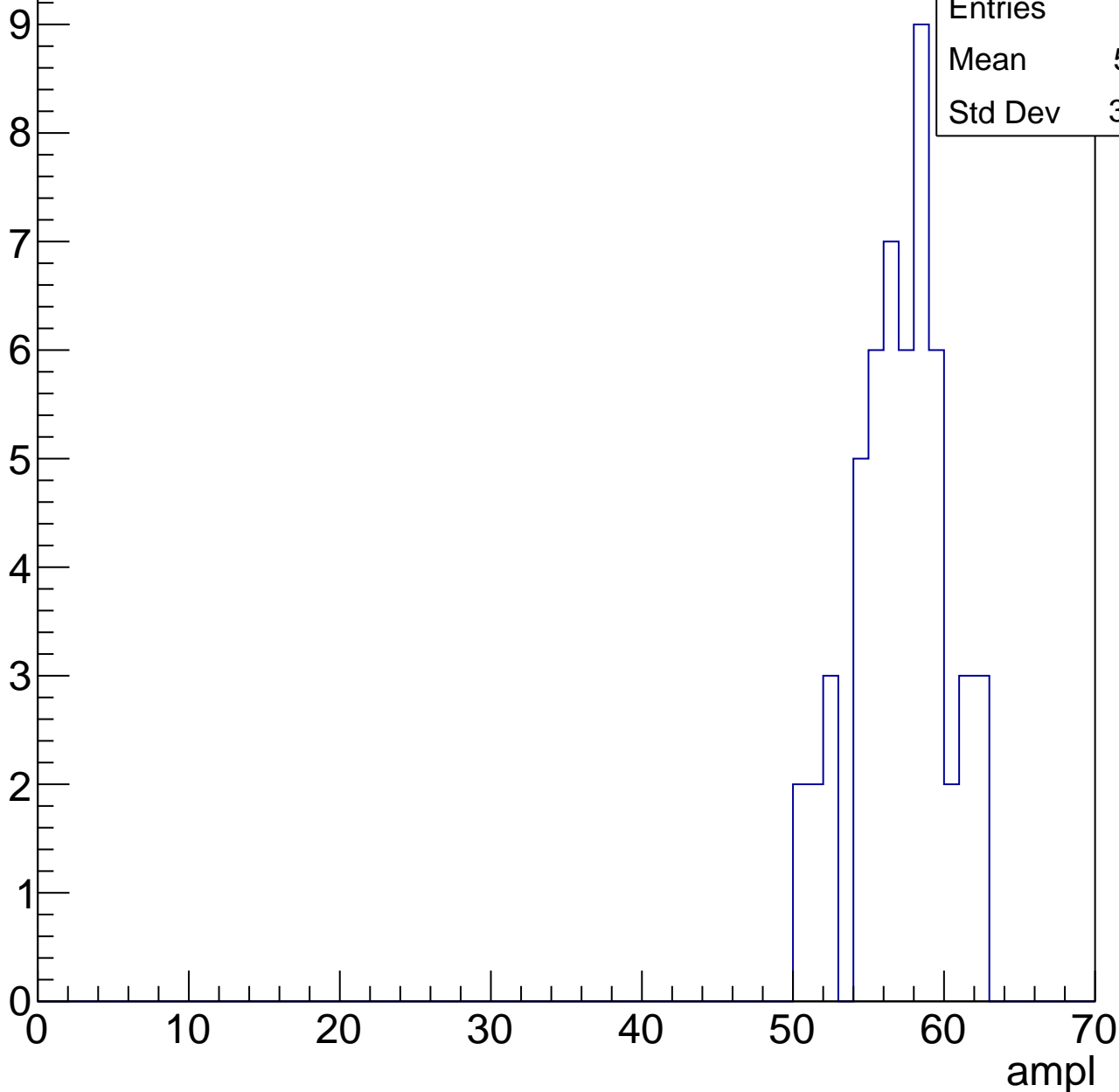
Entries	41
Mean	52.32
Std Dev	2.673

B1L002S, U7-ch87, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	56.61
Std Dev	3.003

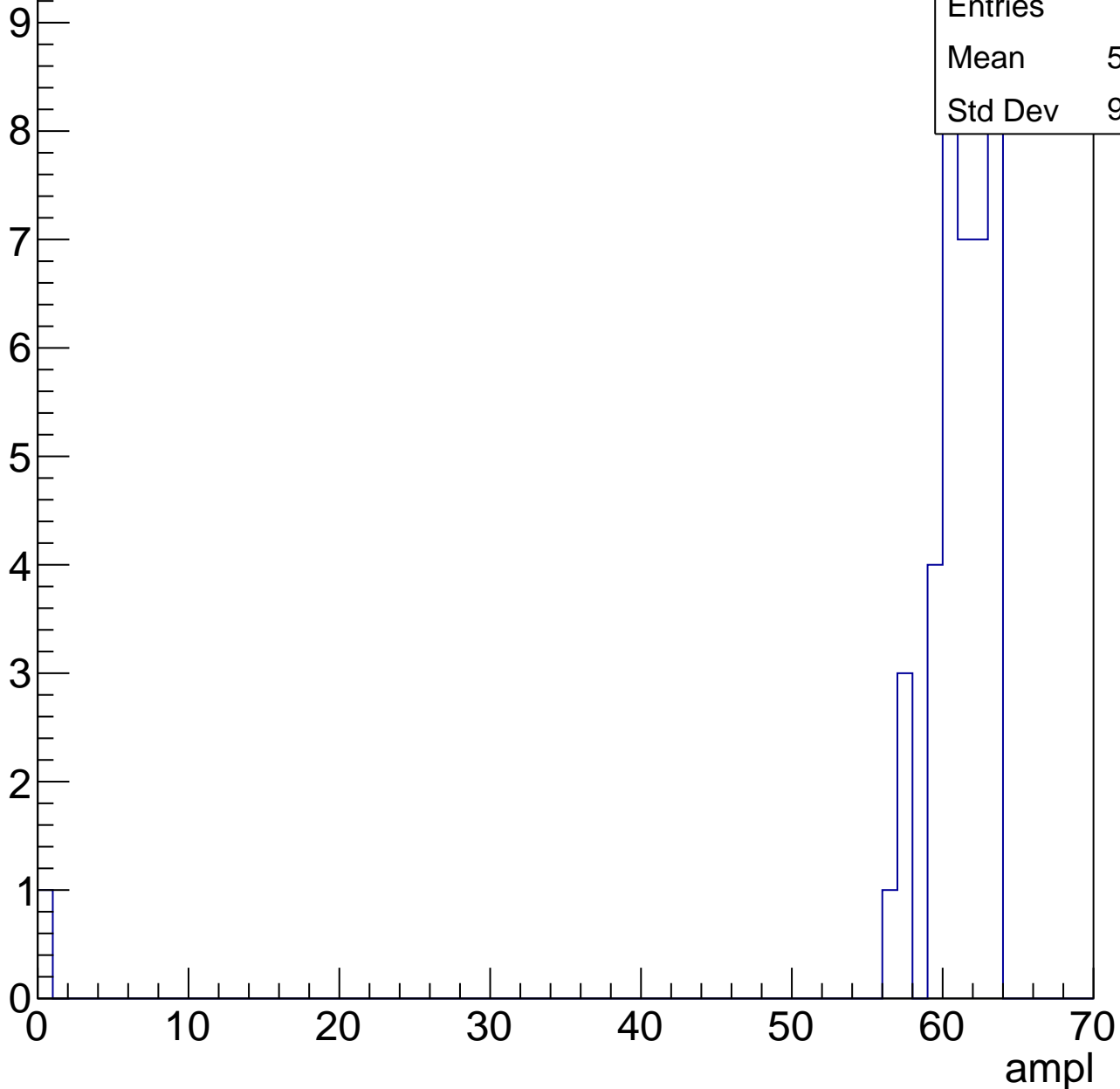


B1L002S, U7-ch87, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

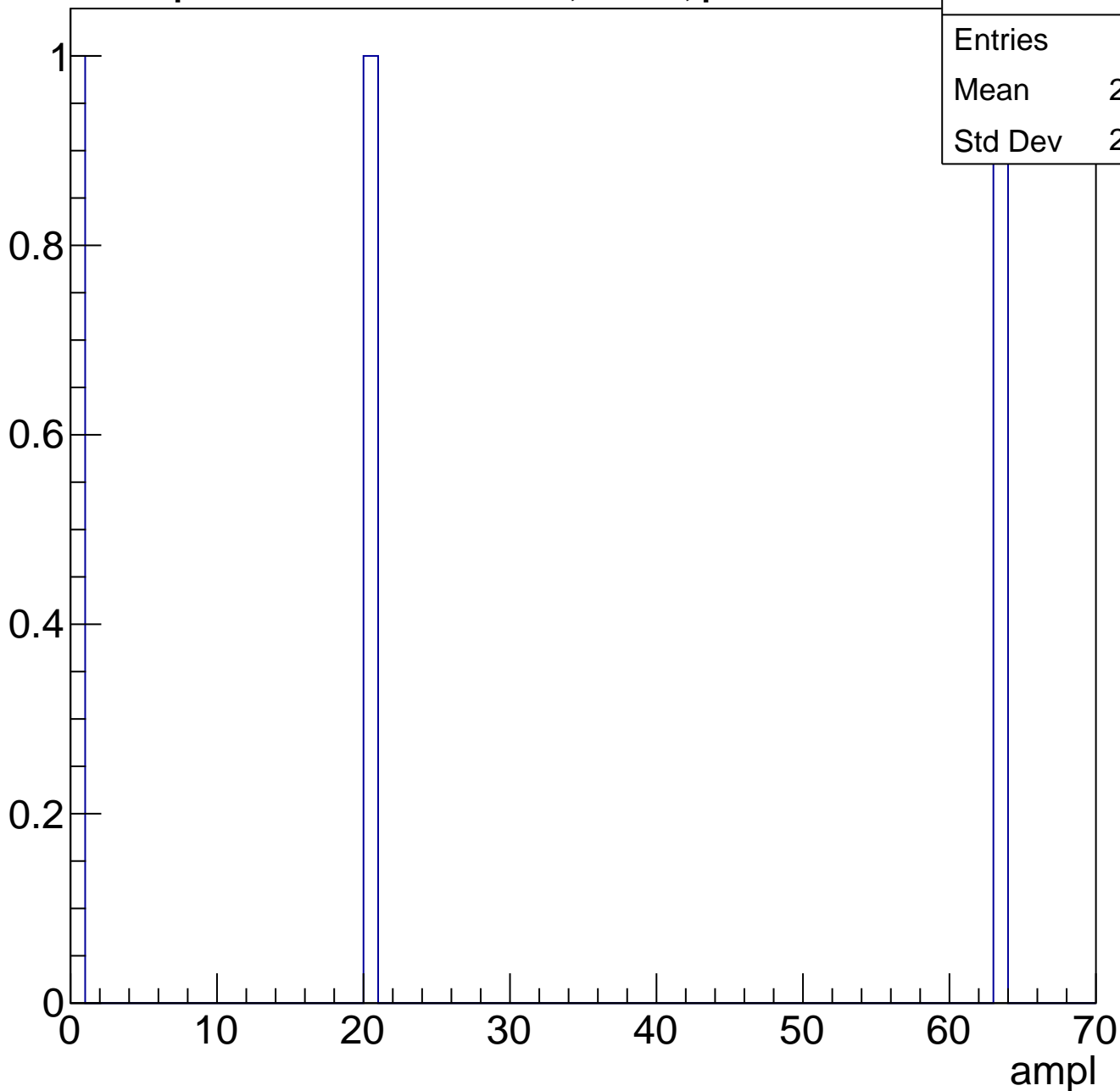
Entries	40
Mean	59.27
Std Dev	9.672



B1L002S, U7-ch87, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B1L002S, U7-ch88, adc0

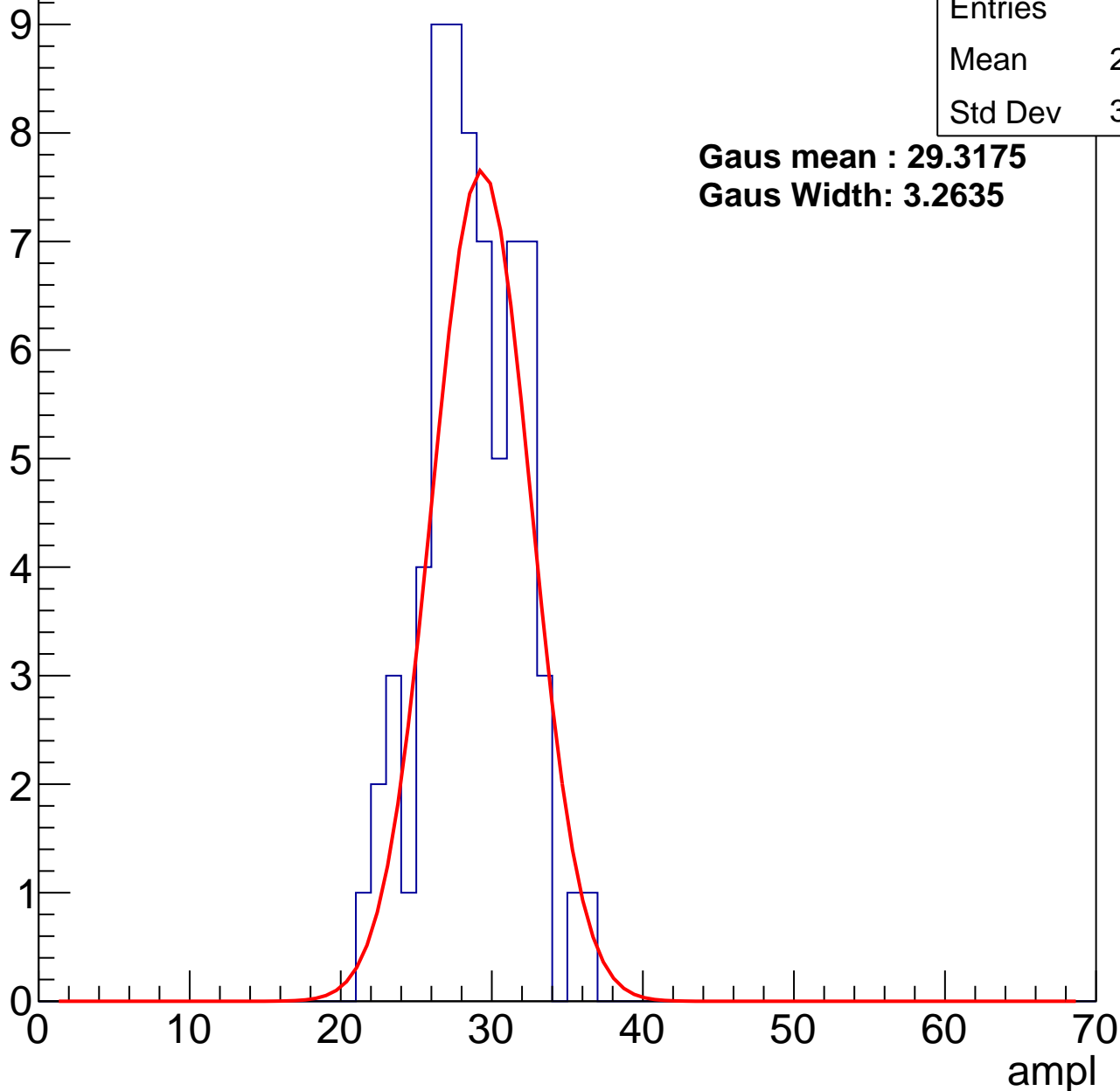
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	28.28
Std Dev	3.157

Gaus mean : 29.3175

Gaus Width: 3.2635



B1L002S, U7-ch88, adc1

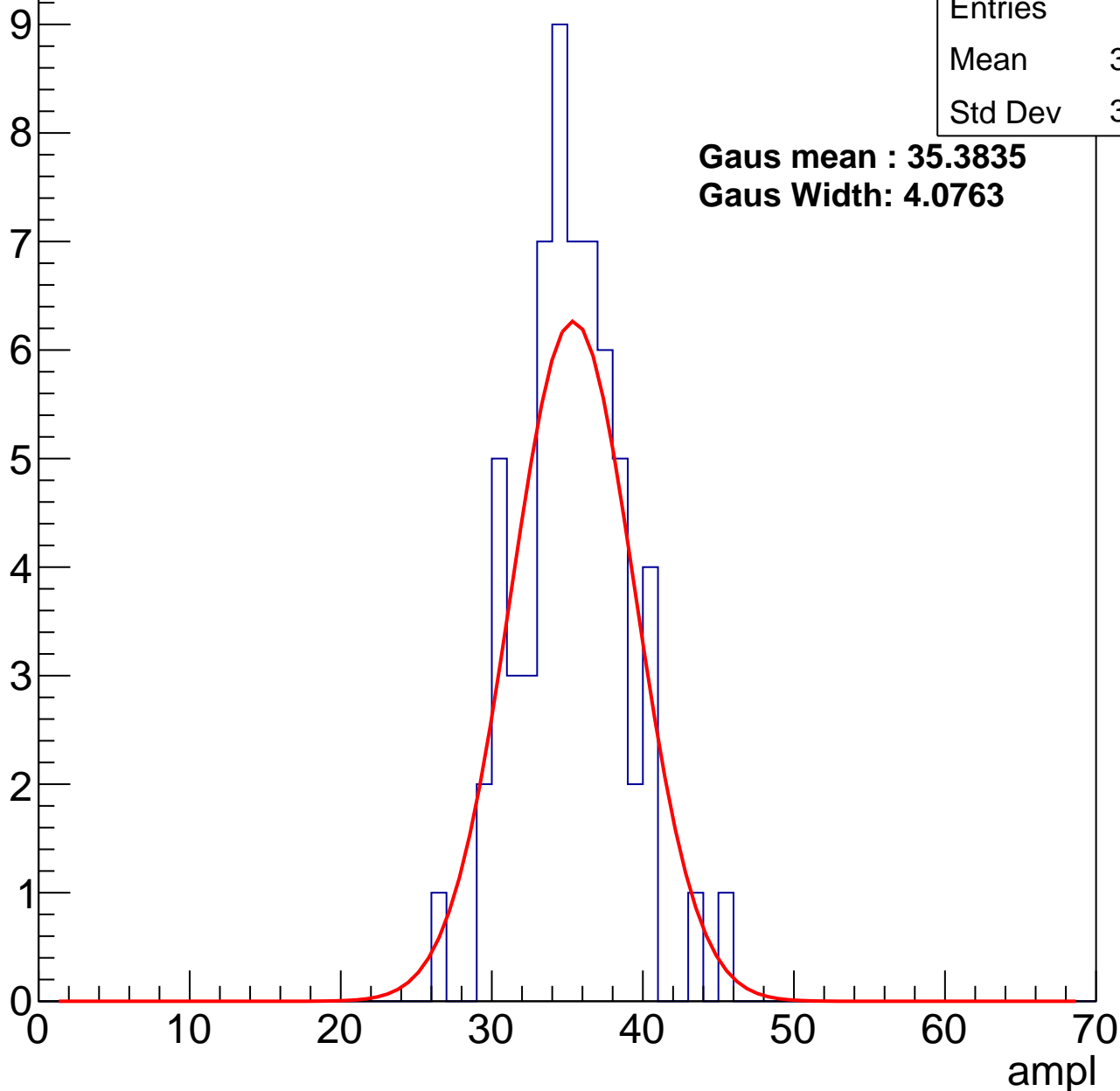
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.84
Std Dev	3.474

Gaus mean : 35.3835

Gaus Width: 4.0763



B1L002S, U7-ch88, adc2

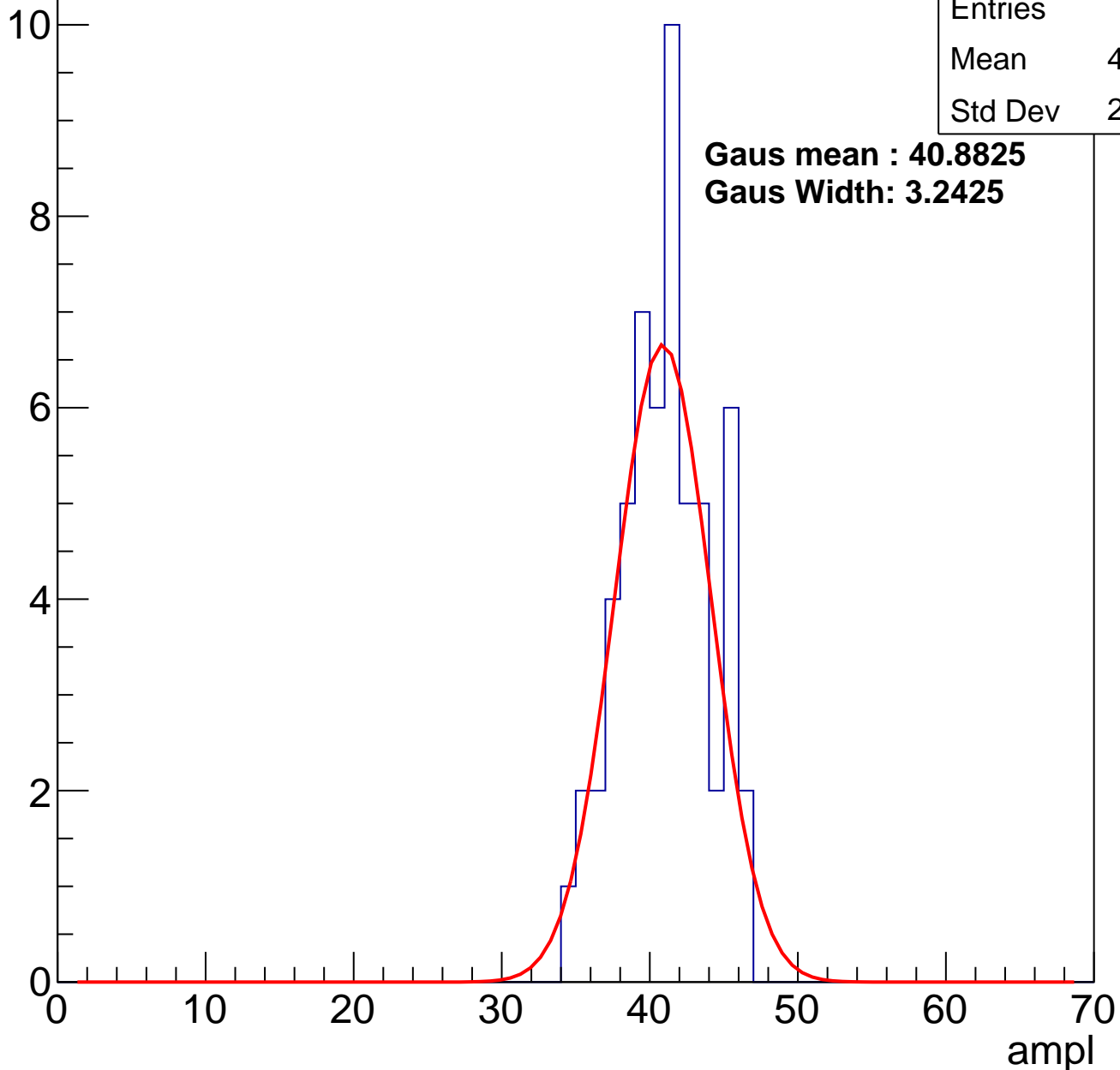
calib_packv5_042523_0143.root, FC#10, port B3

Entries	57
Mean	40.56
Std Dev	2.938

Gaus mean : 40.8825

Gaus Width: 3.2425

Entry

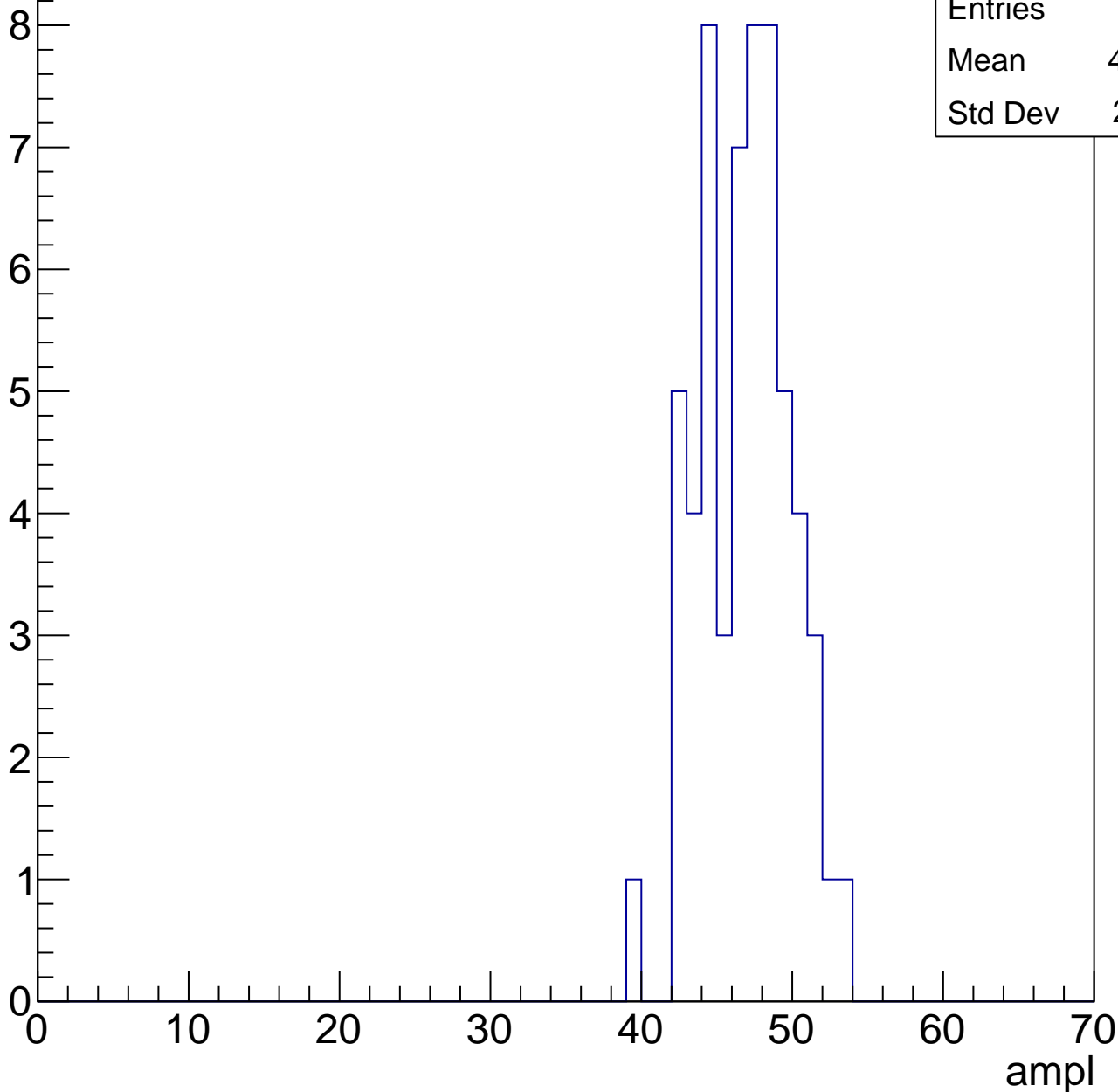


B1L002S, U7-ch88, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	46.43
Std Dev	2.931

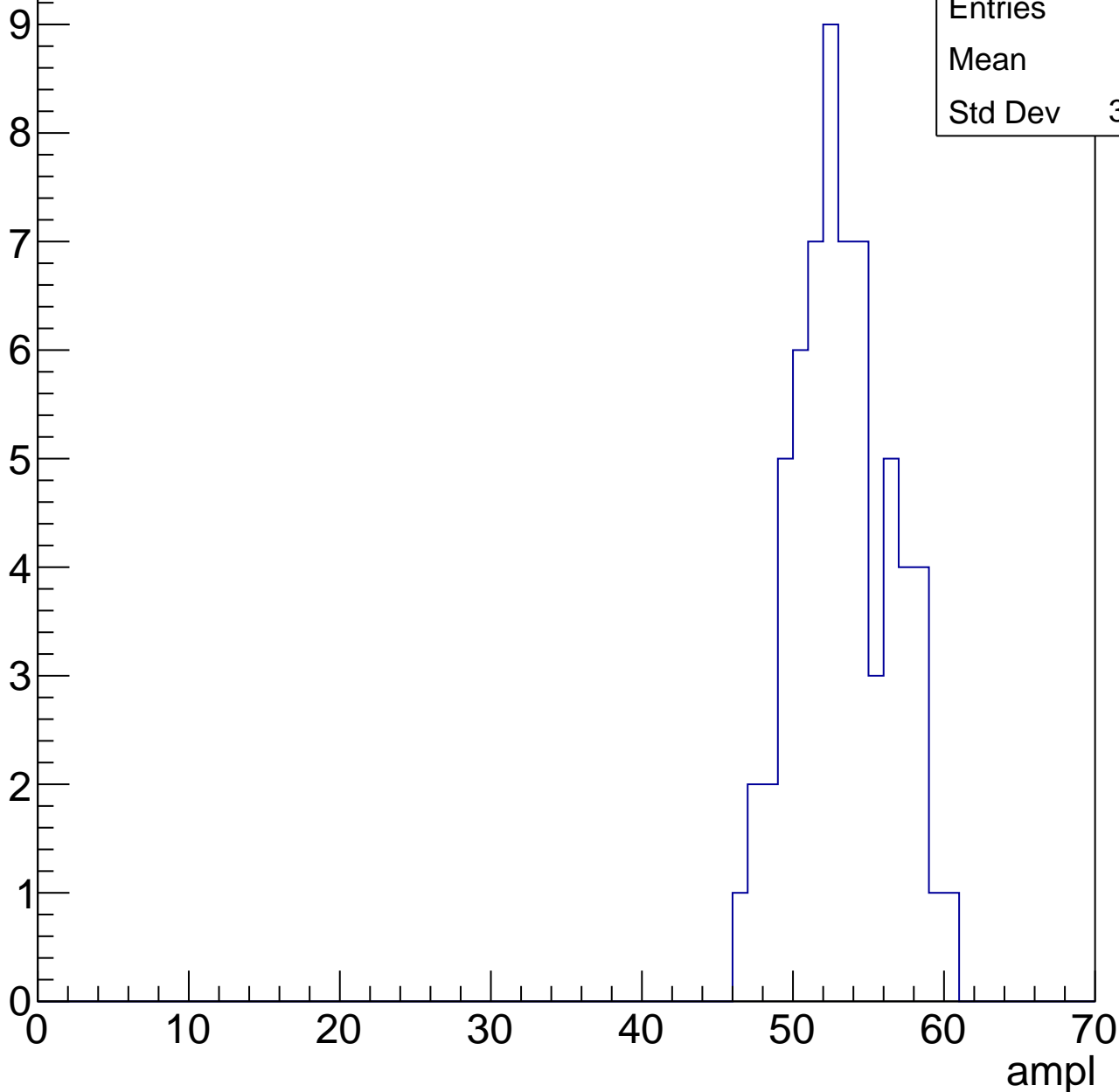


B1L002S, U7-ch88, adc4

calib_packv5_042523_0143.root, FC#10, port B3

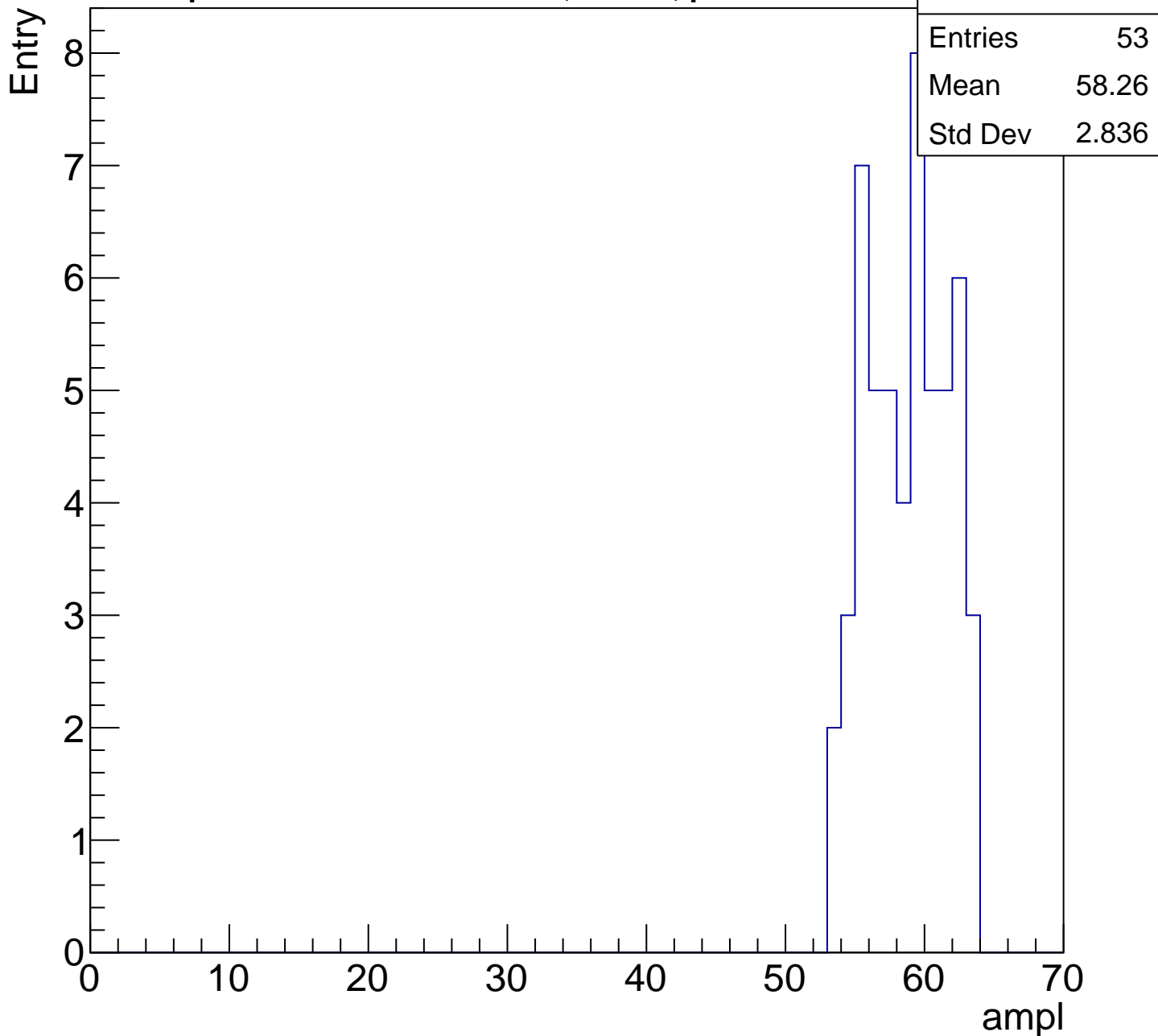
Entry

Entries	64
Mean	52.8
Std Dev	3.193



B1L002S, U7-ch88, adc5

calib_packv5_042523_0143.root, FC#10, port B3

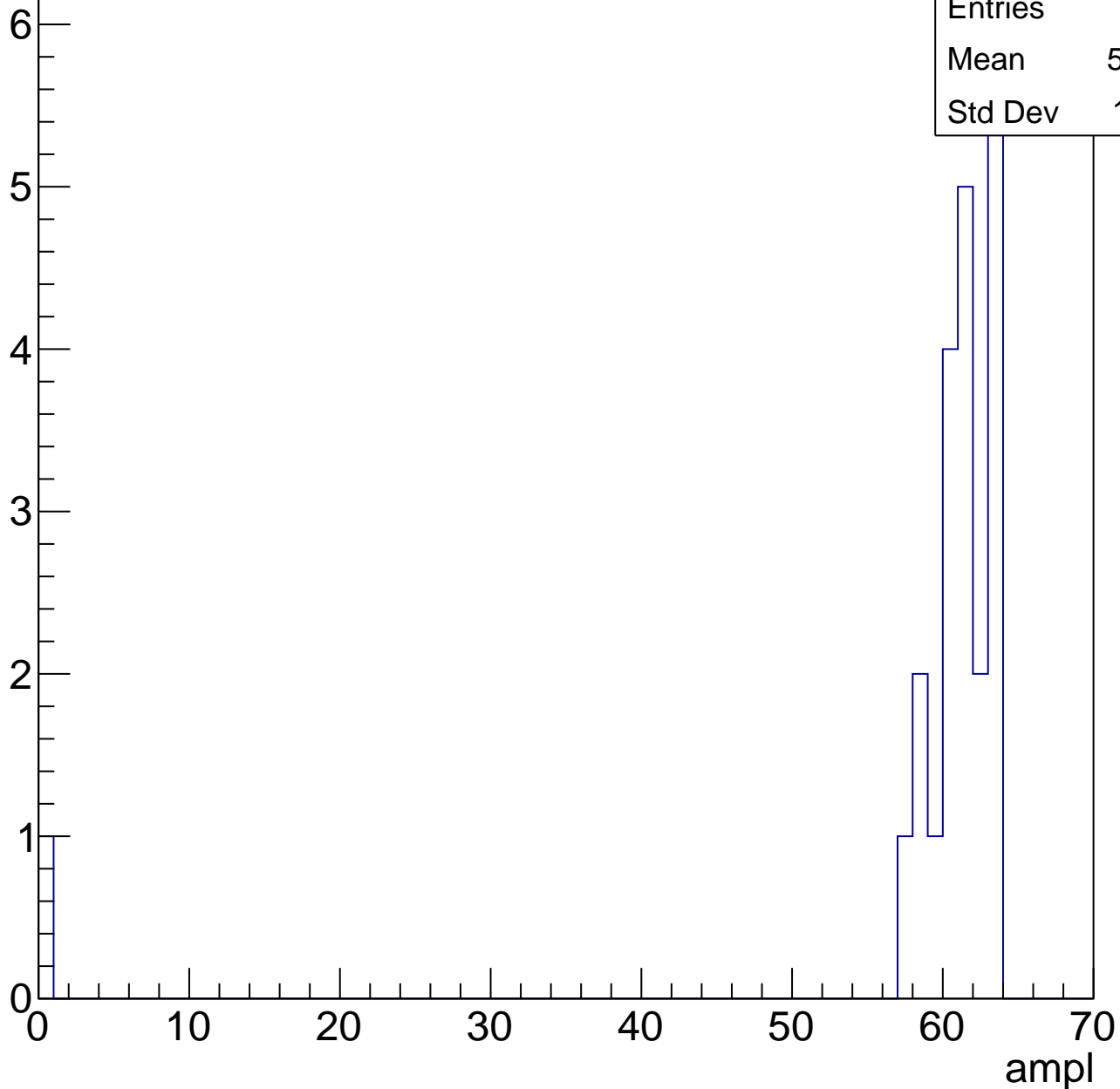


B1L002S, U7-ch88, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

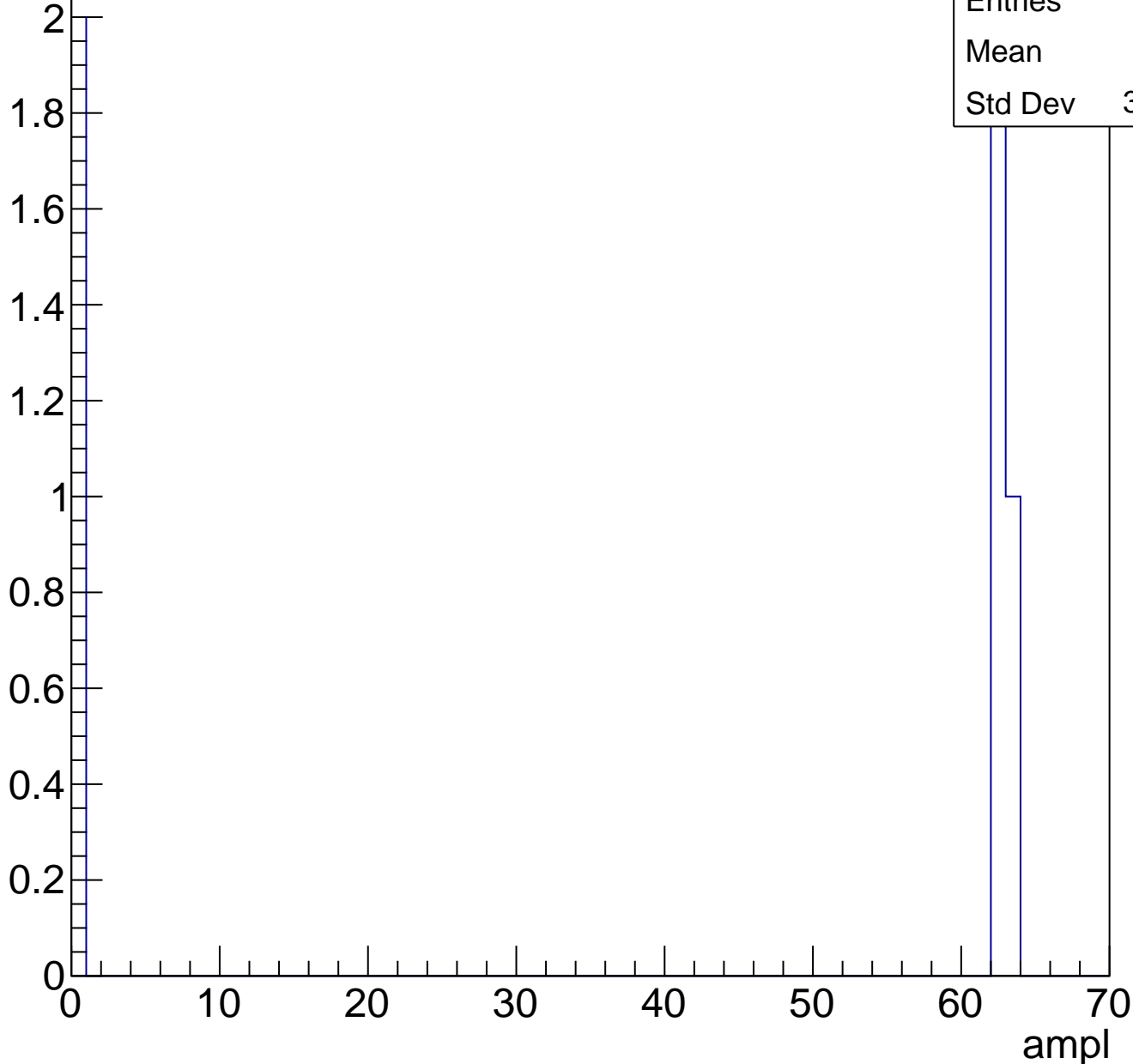
Entries	22
Mean	58.14
Std Dev	12.81



B1L002S, U7-ch88, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch89, adc0

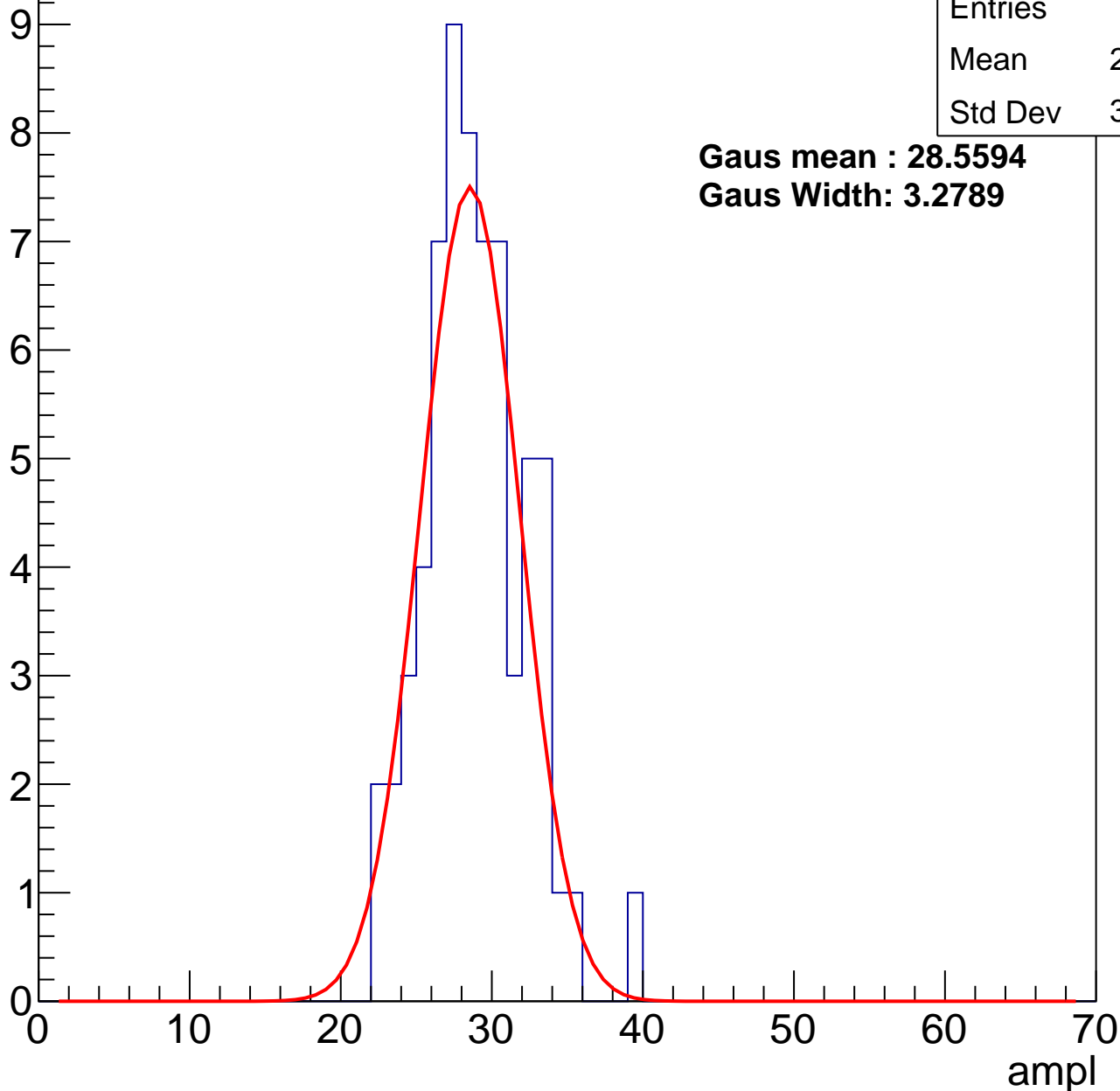
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.46
Std Dev	3.287

Gaus mean : 28.5594

Gaus Width: 3.2789



B1L002S, U7-ch89, adc1

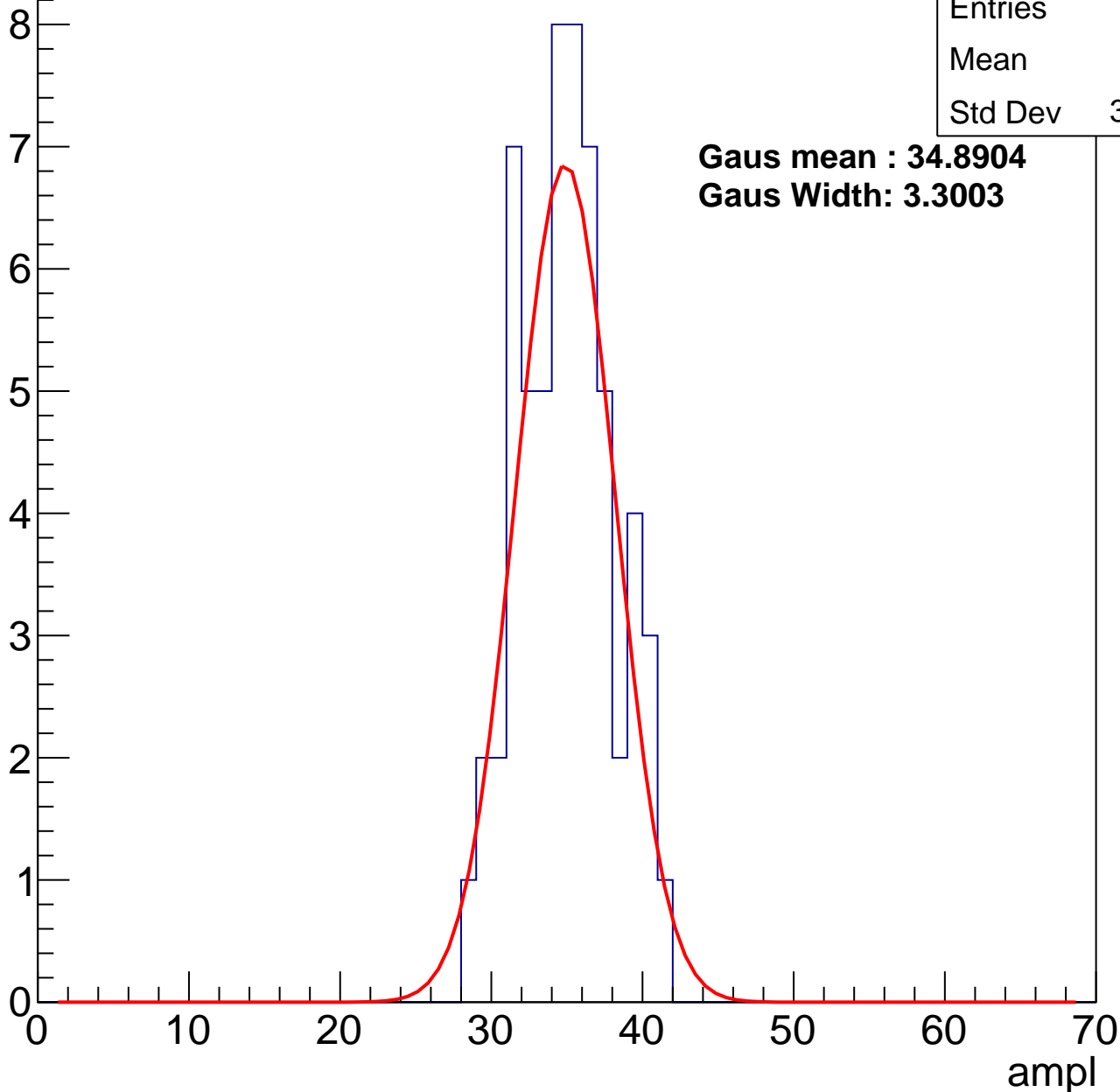
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.5
Std Dev	3.058

Gaus mean : 34.8904

Gaus Width: 3.3003



B1L002S, U7-ch89, adc2

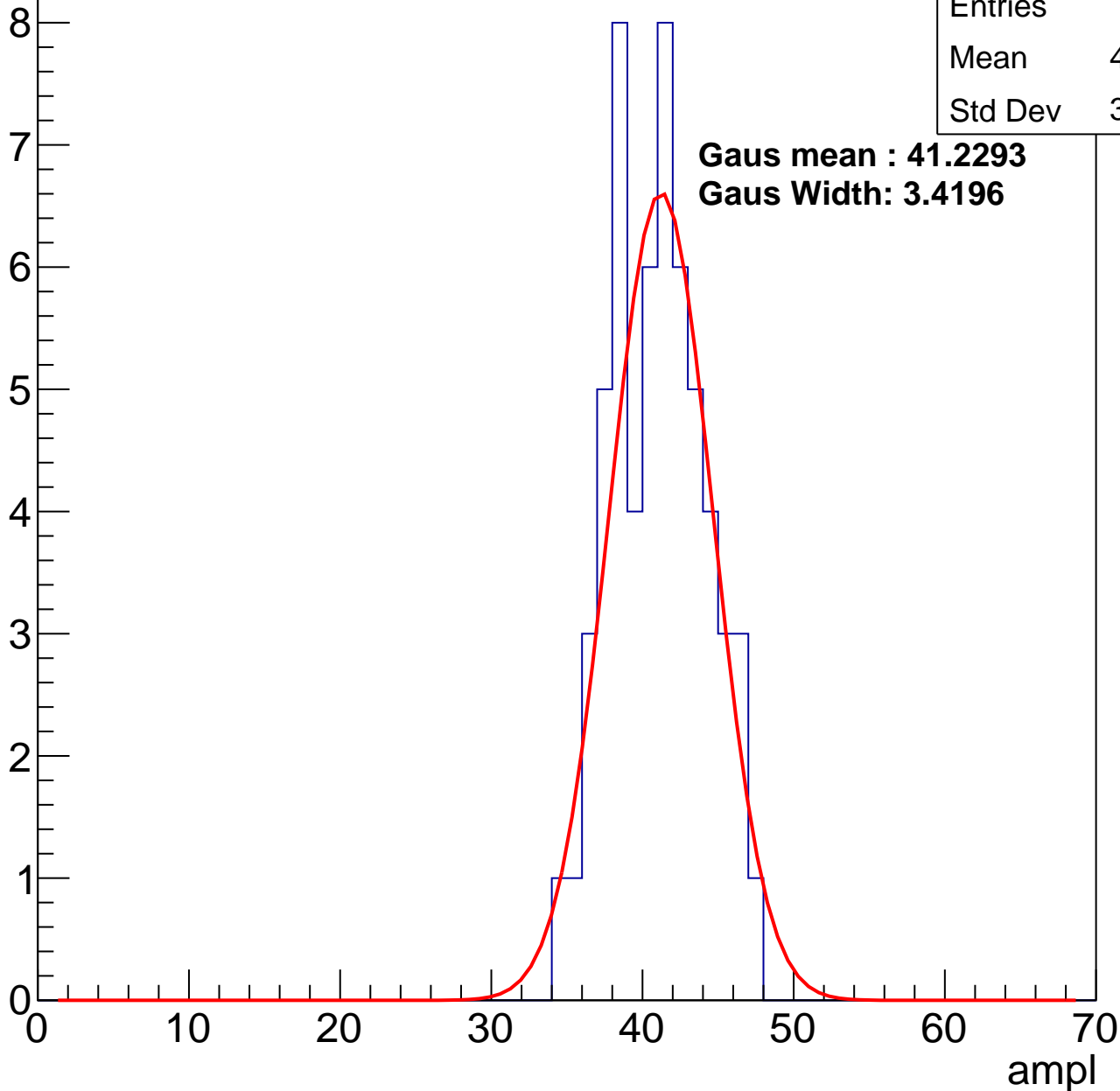
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.57
Std Dev	3.063

Gaus mean : 41.2293

Gaus Width: 3.4196



B1L002S, U7-ch89, adc3

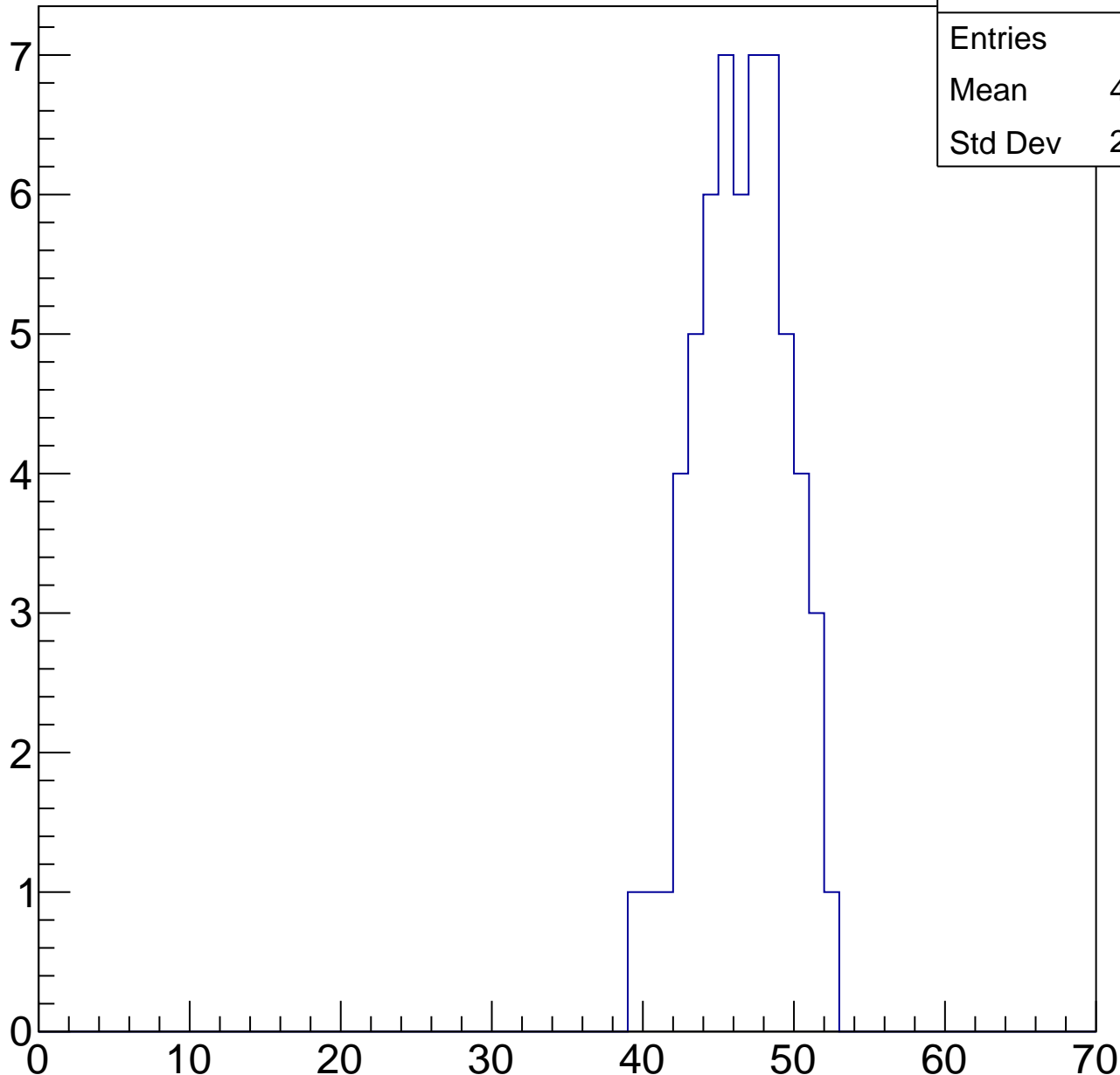
calib_packv5_042523_0143.root, FC#10, port B3

Entry

7
6
5
4
3
2
1
0

Entries	58
Mean	46.09
Std Dev	2.949

ampl

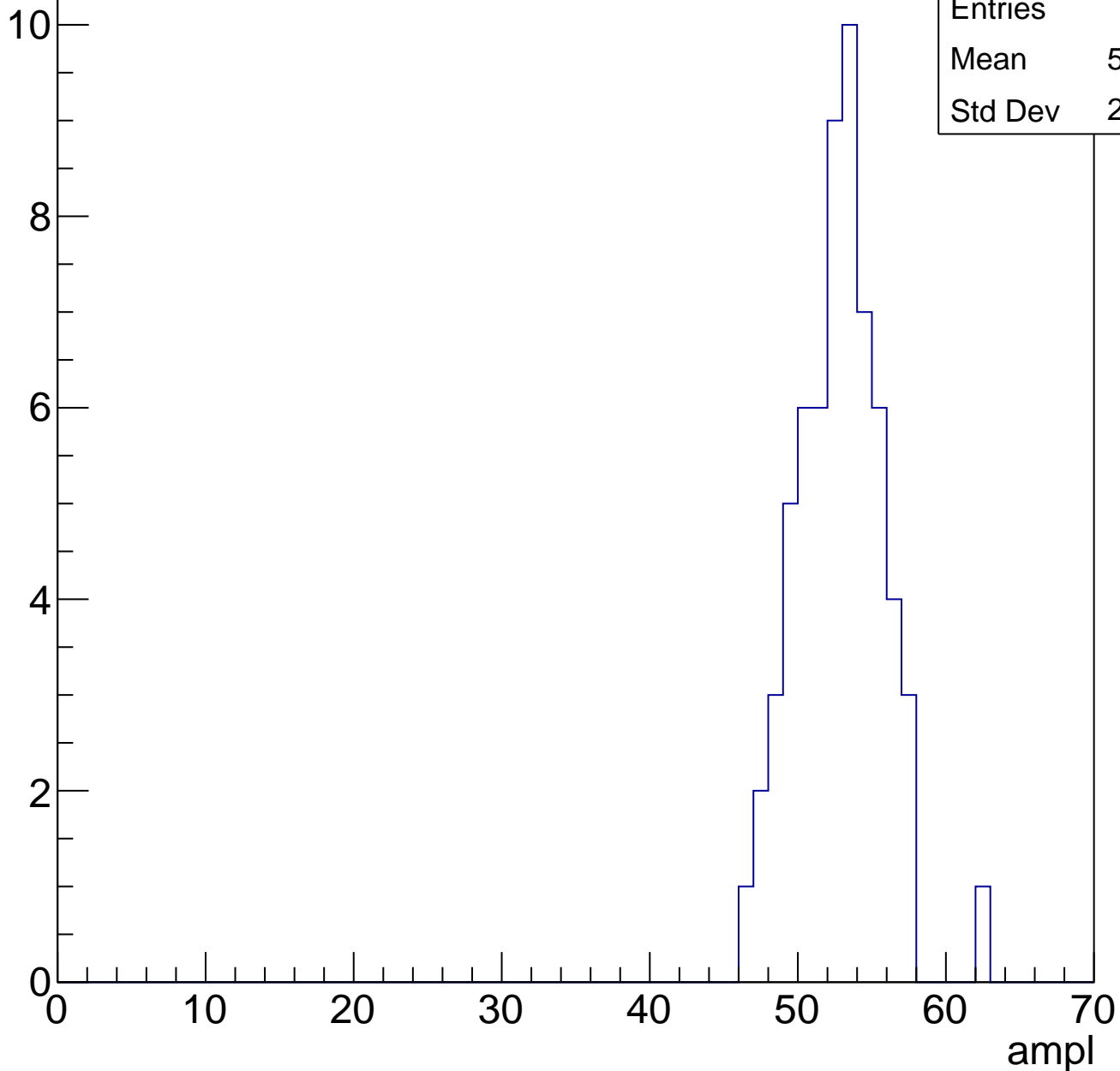


B1L002S, U7-ch89, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	63
Mean	52.35
Std Dev	2.907

Entry



B1L002S, U7-ch89, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	58.62
Std Dev	2.516

Entry

10

8

6

4

2

0

0

10

20

30

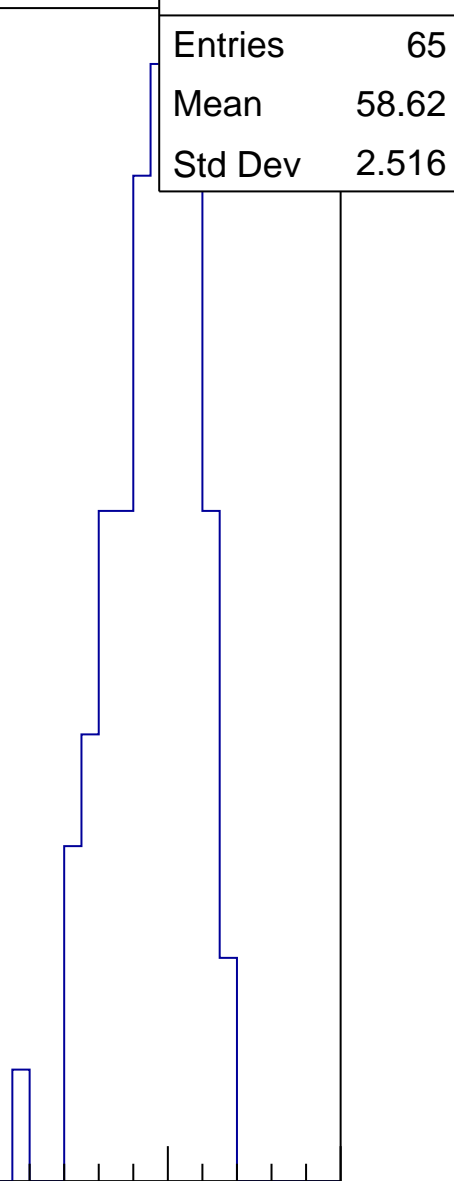
40

50

60

70

ampl

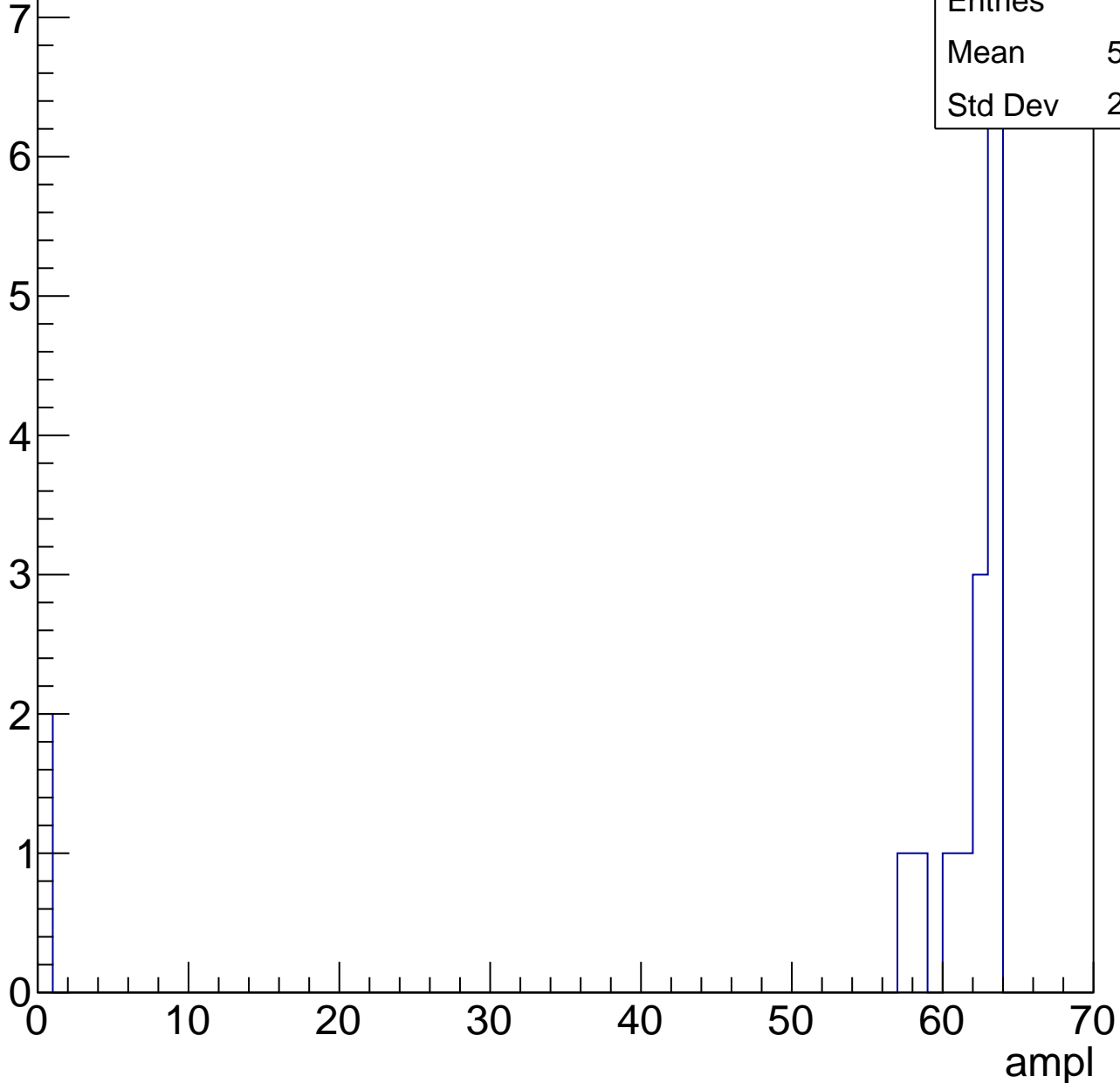


B1L002S, U7-ch89, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	16
Mean	53.94
Std Dev	20.46



B1L002S, U7-ch89, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch90, adc0

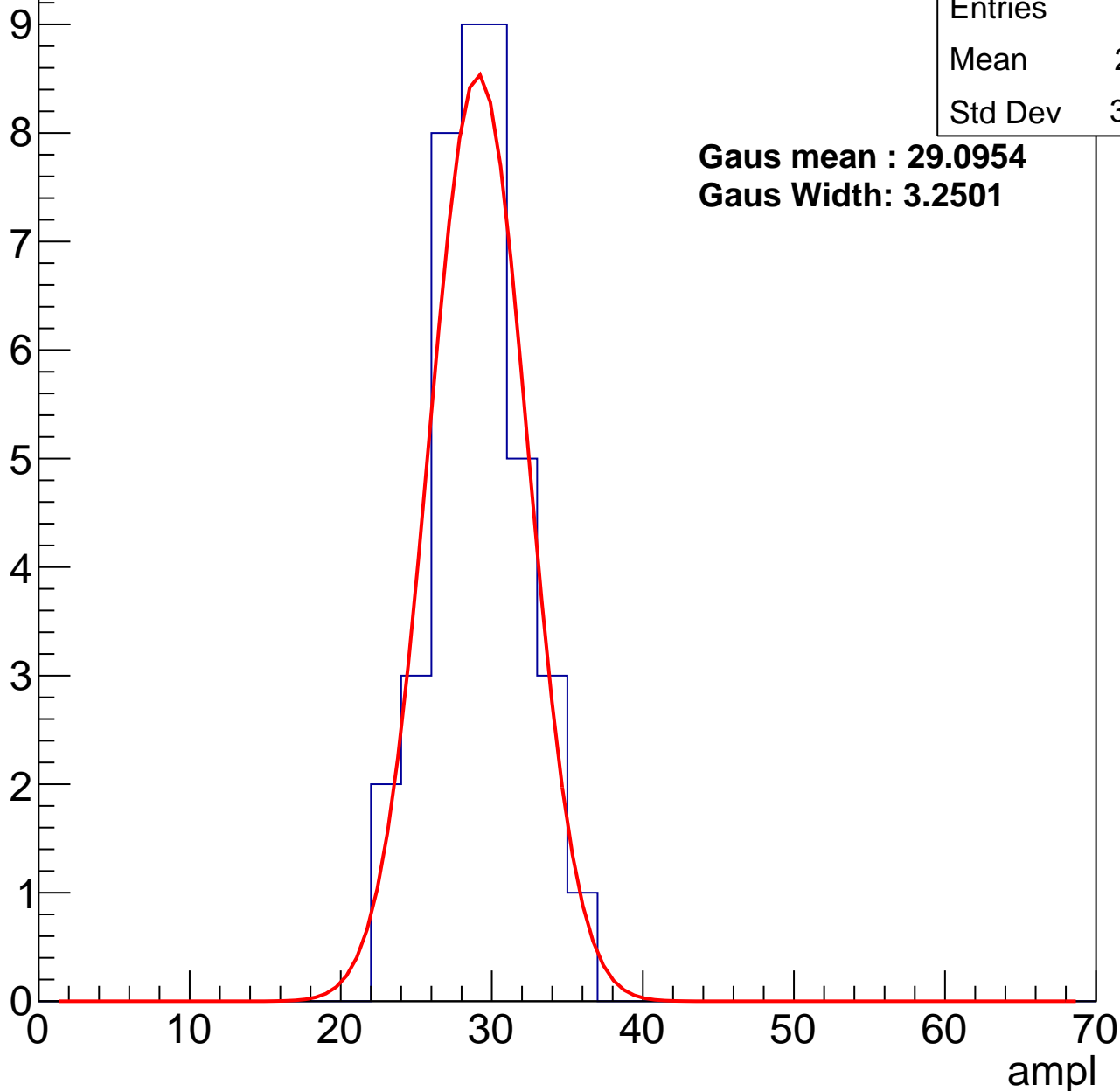
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.61
Std Dev	3.088

Gaus mean : 29.0954

Gaus Width: 3.2501



B1L002S, U7-ch90, adc1

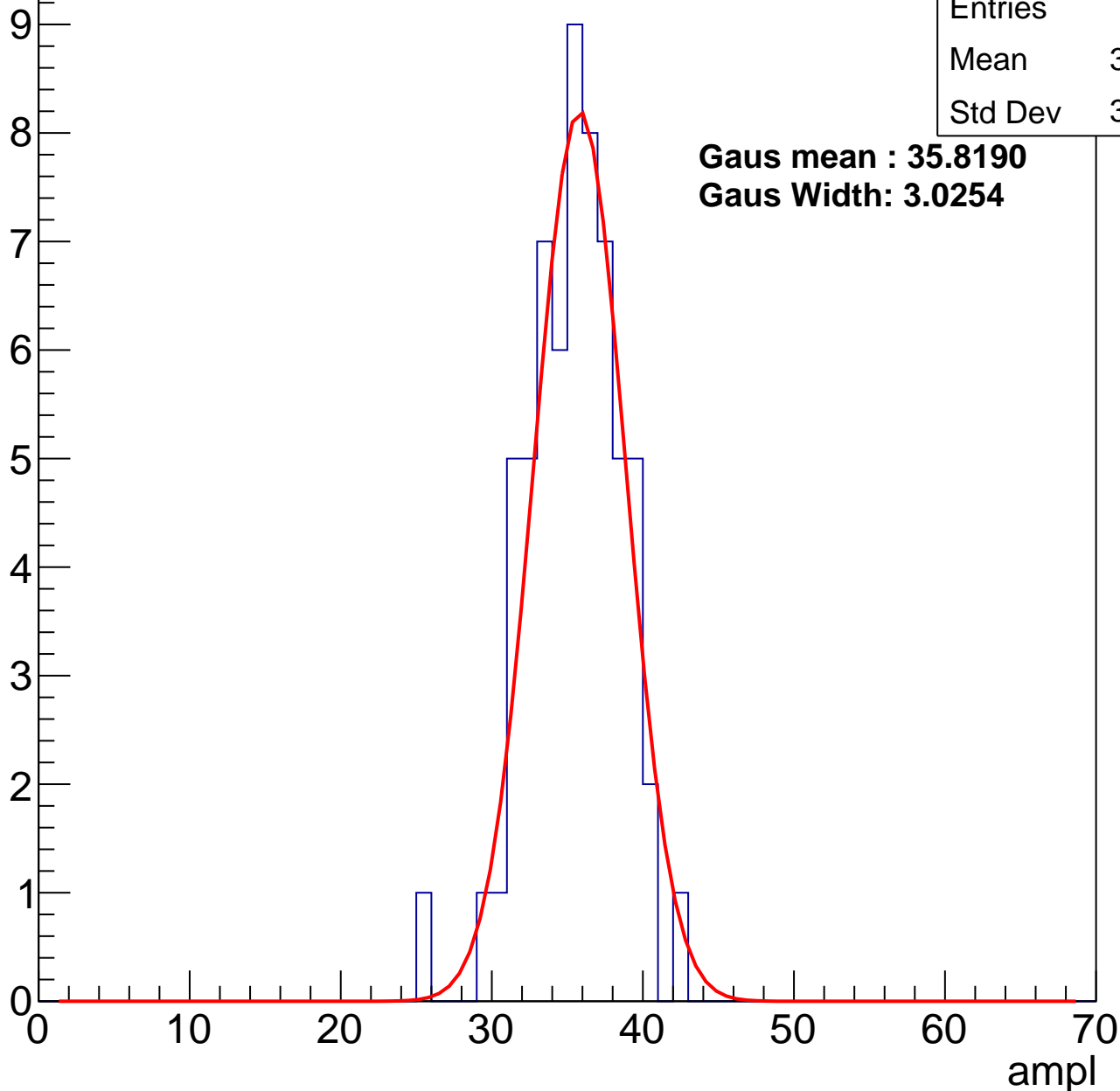
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.97
Std Dev	3.034

Gaus mean : 35.8190

Gaus Width: 3.0254



B1L002S, U7-ch90, adc2

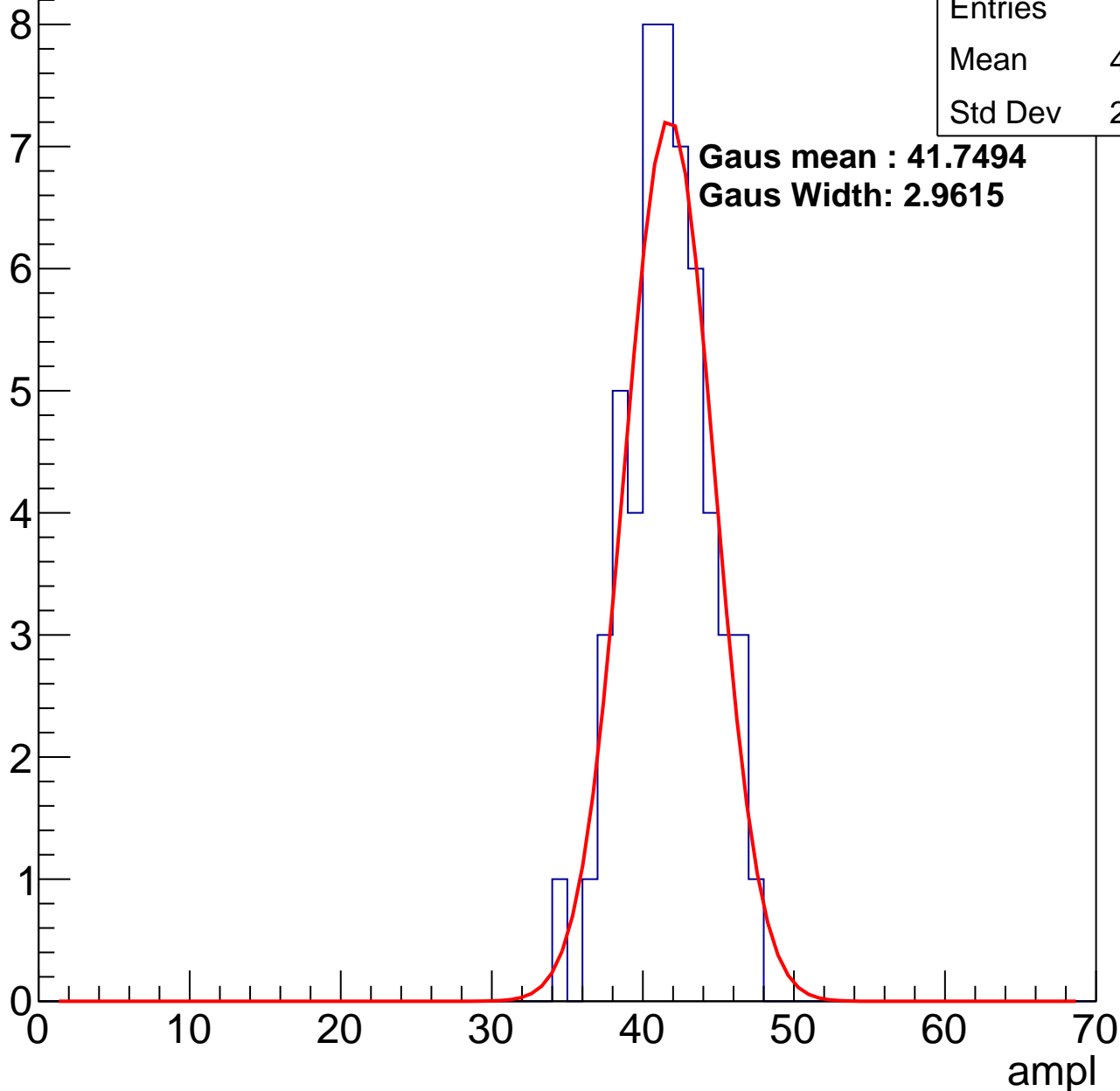
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	41.17
Std Dev	2.774

Gaus mean : 41.7494

Gaus Width: 2.9615

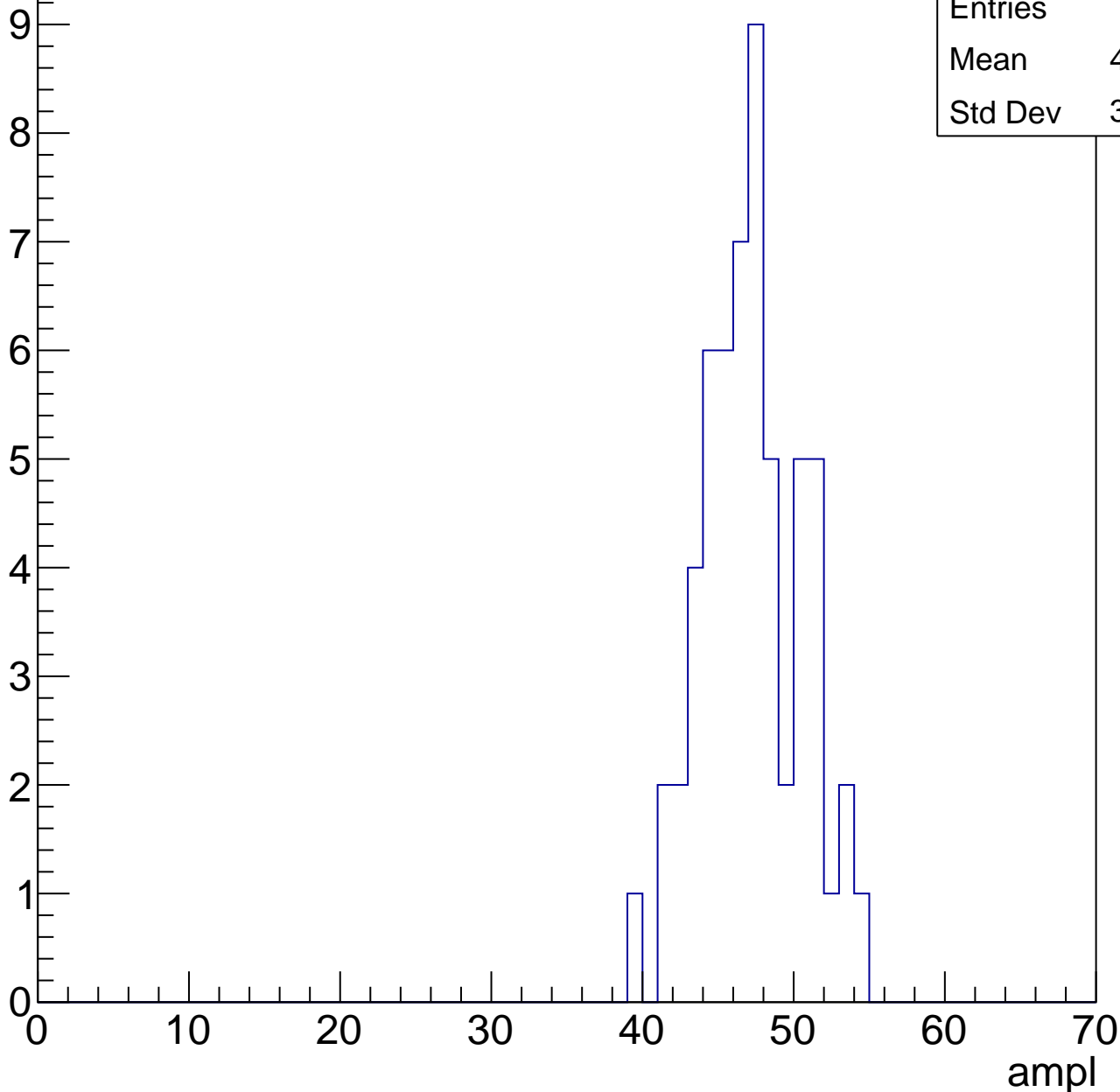


B1L002S, U7-ch90, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	46.74
Std Dev	3.257

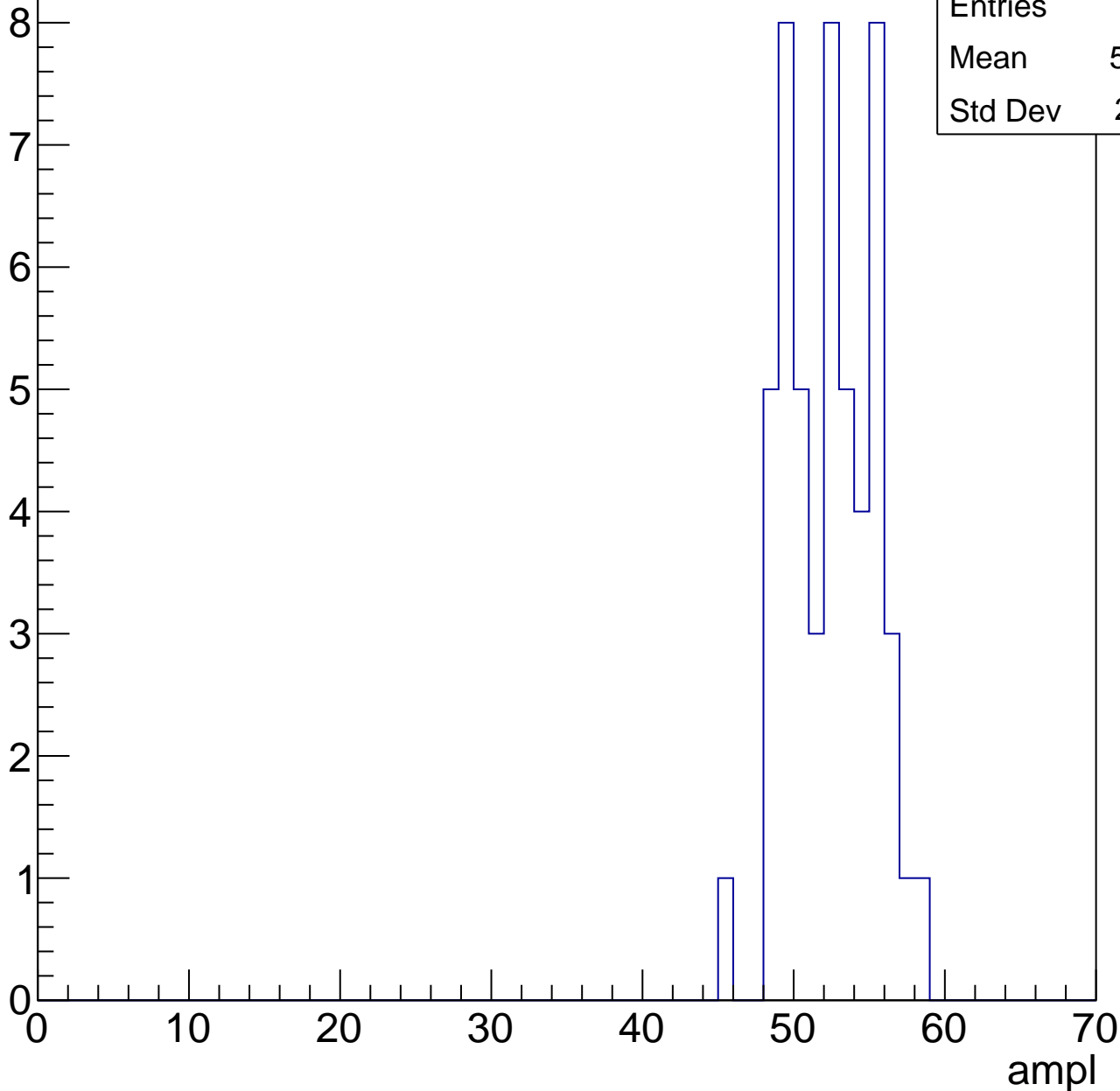


B1L002S, U7-ch90, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.92
Std Dev	2.861

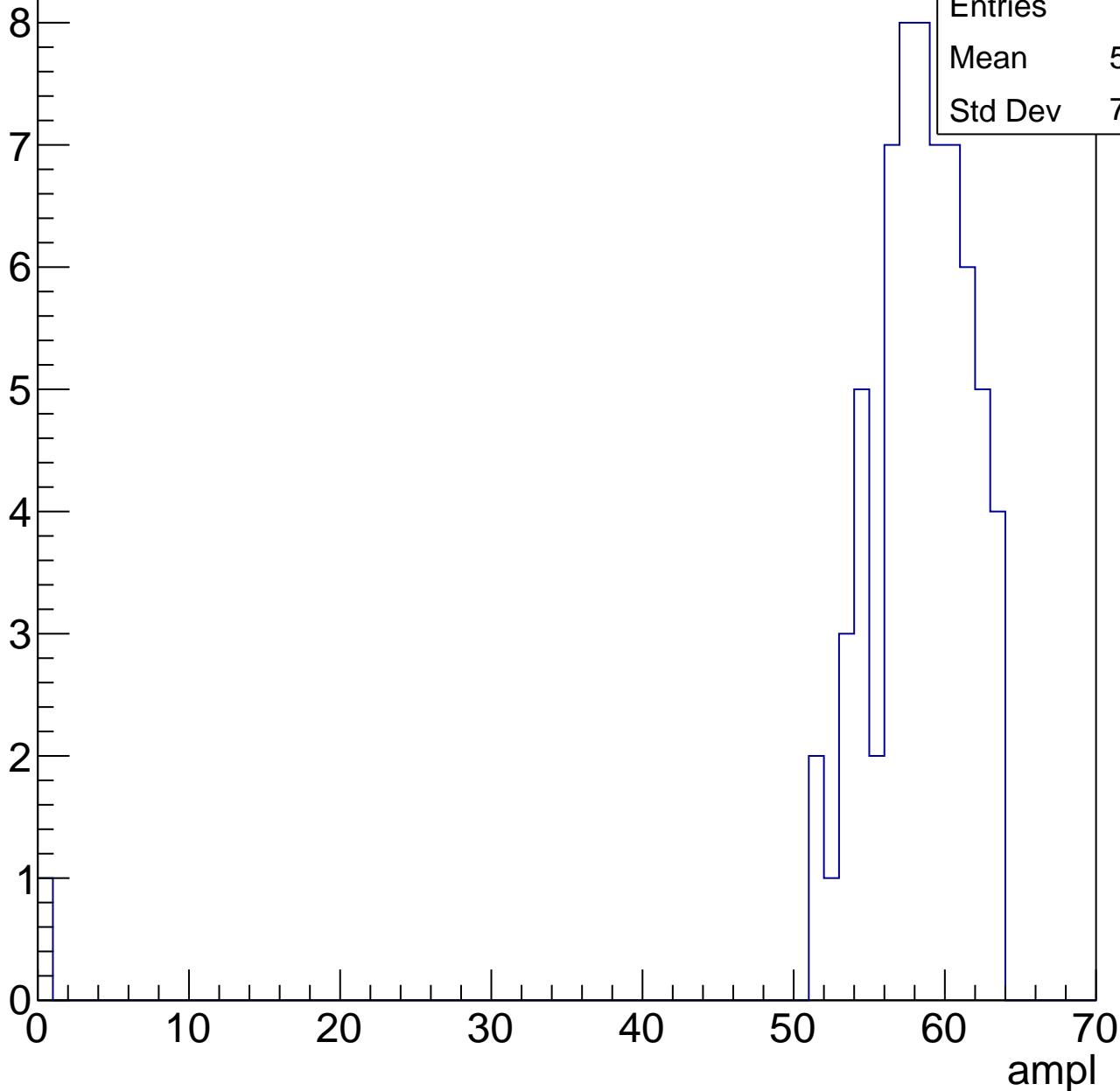


B1L002S, U7-ch90, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.06
Std Dev	7.705

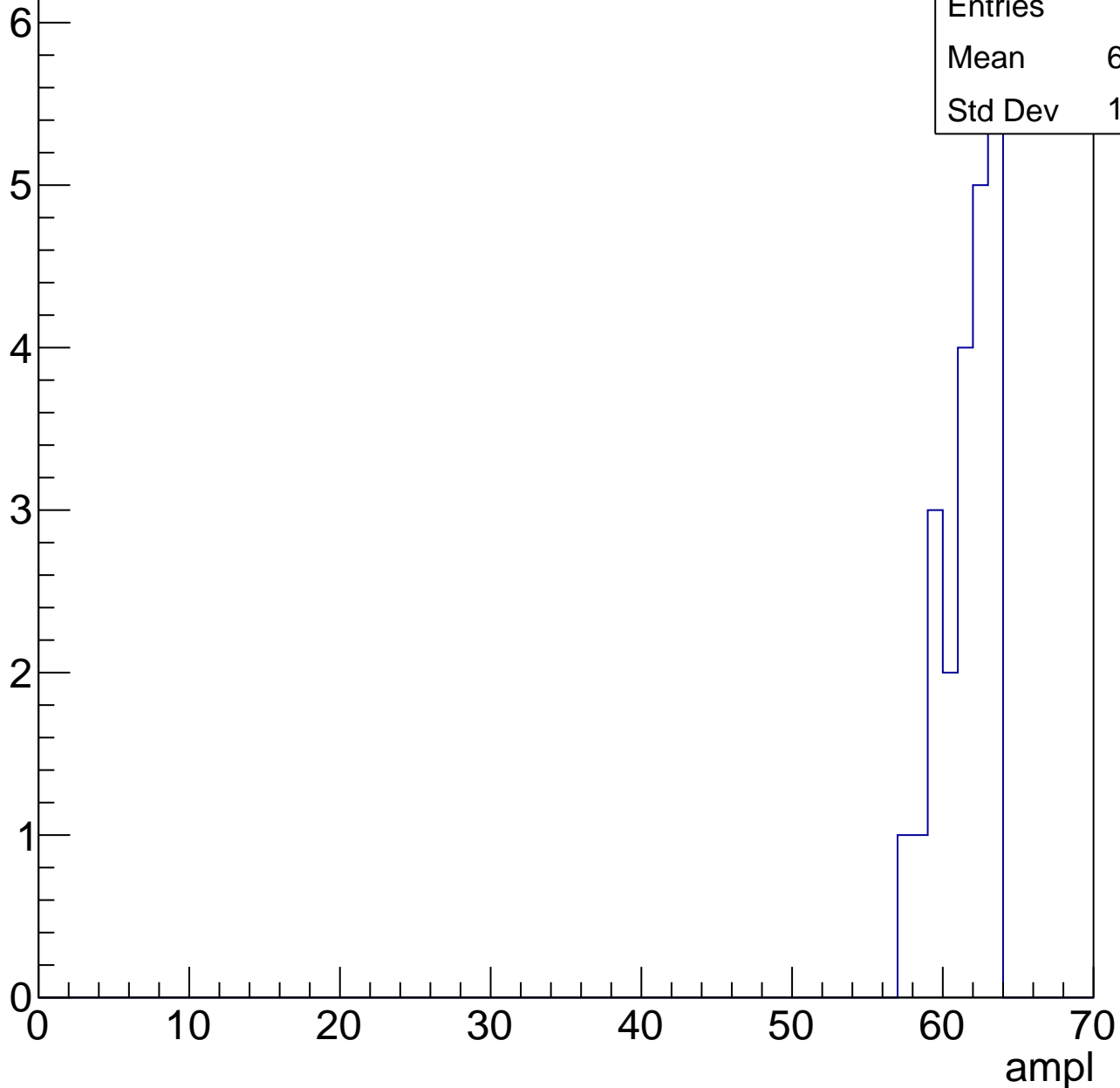


B1L002S, U7-ch90, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

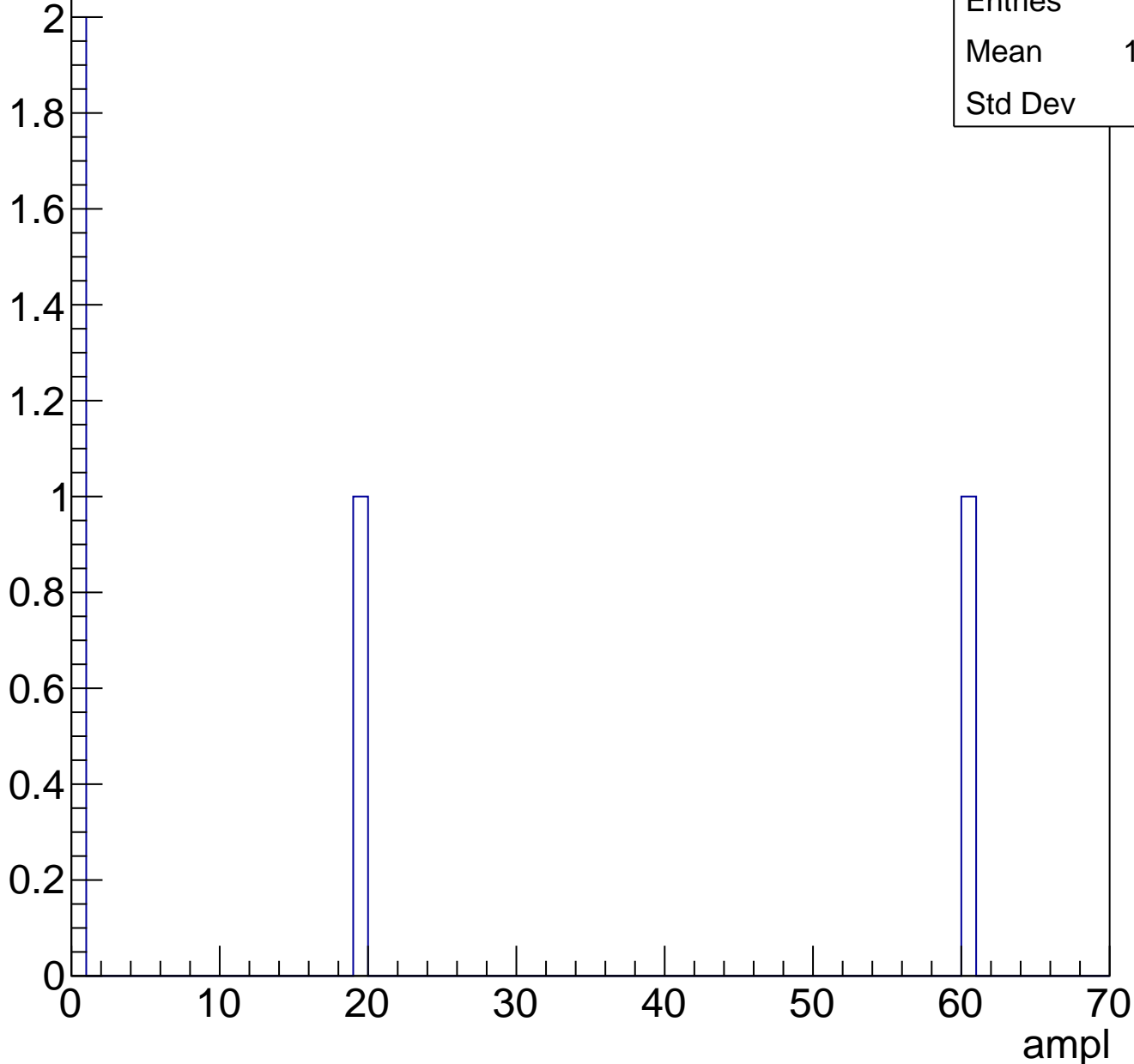
Entries	22
Mean	61.09
Std Dev	1.756



B1L002S, U7-ch90, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch91, adc0

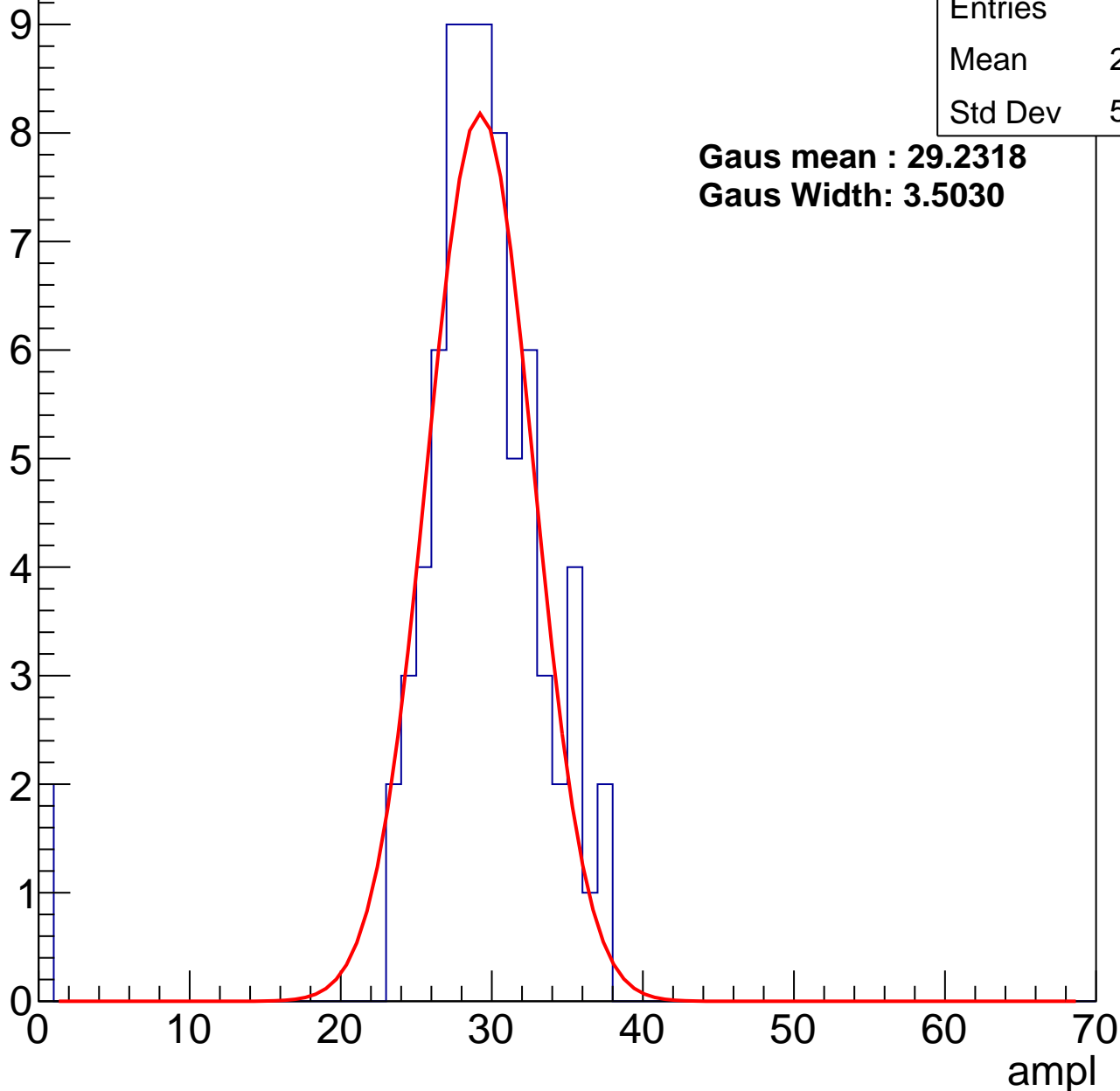
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	28.45
Std Dev	5.739

Gaus mean : 29.2318

Gaus Width: 3.5030



B1L002S, U7-ch91, adc1

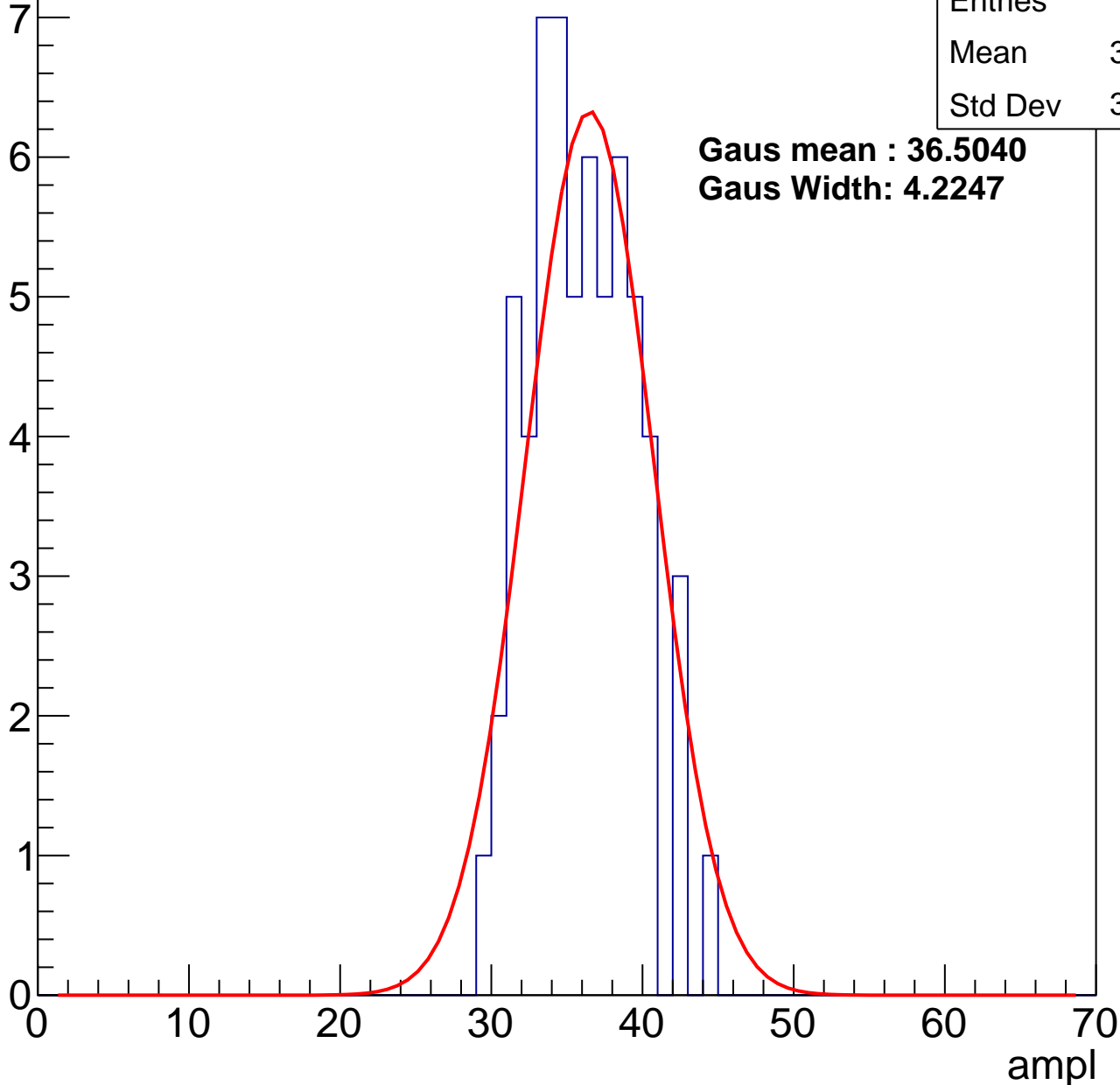
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.57
Std Dev	3.399

Gaus mean : 36.5040

Gaus Width: 4.2247



B1L002S, U7-ch91, adc2

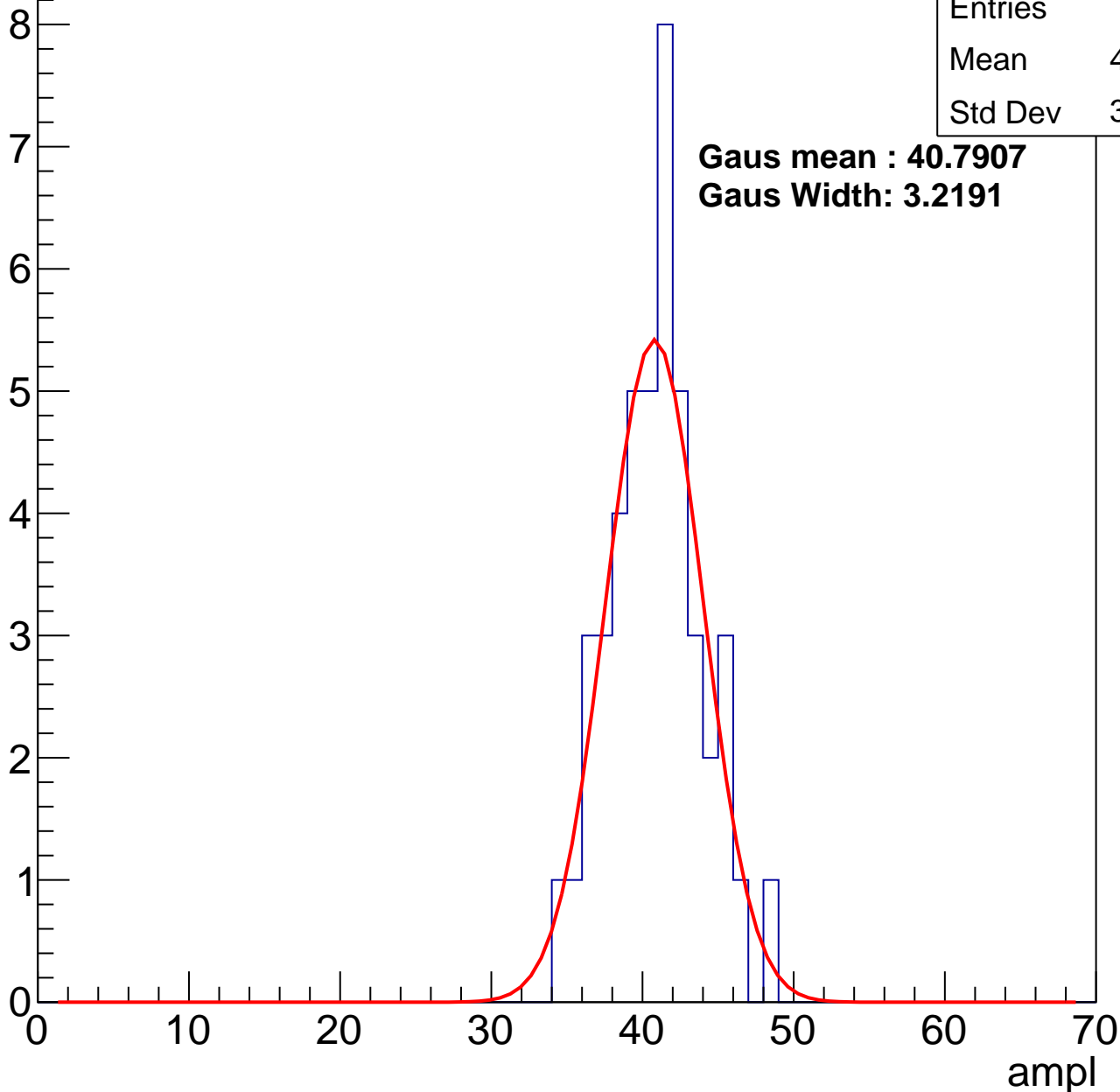
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	40.42
Std Dev	3.022

Gaus mean : 40.7907

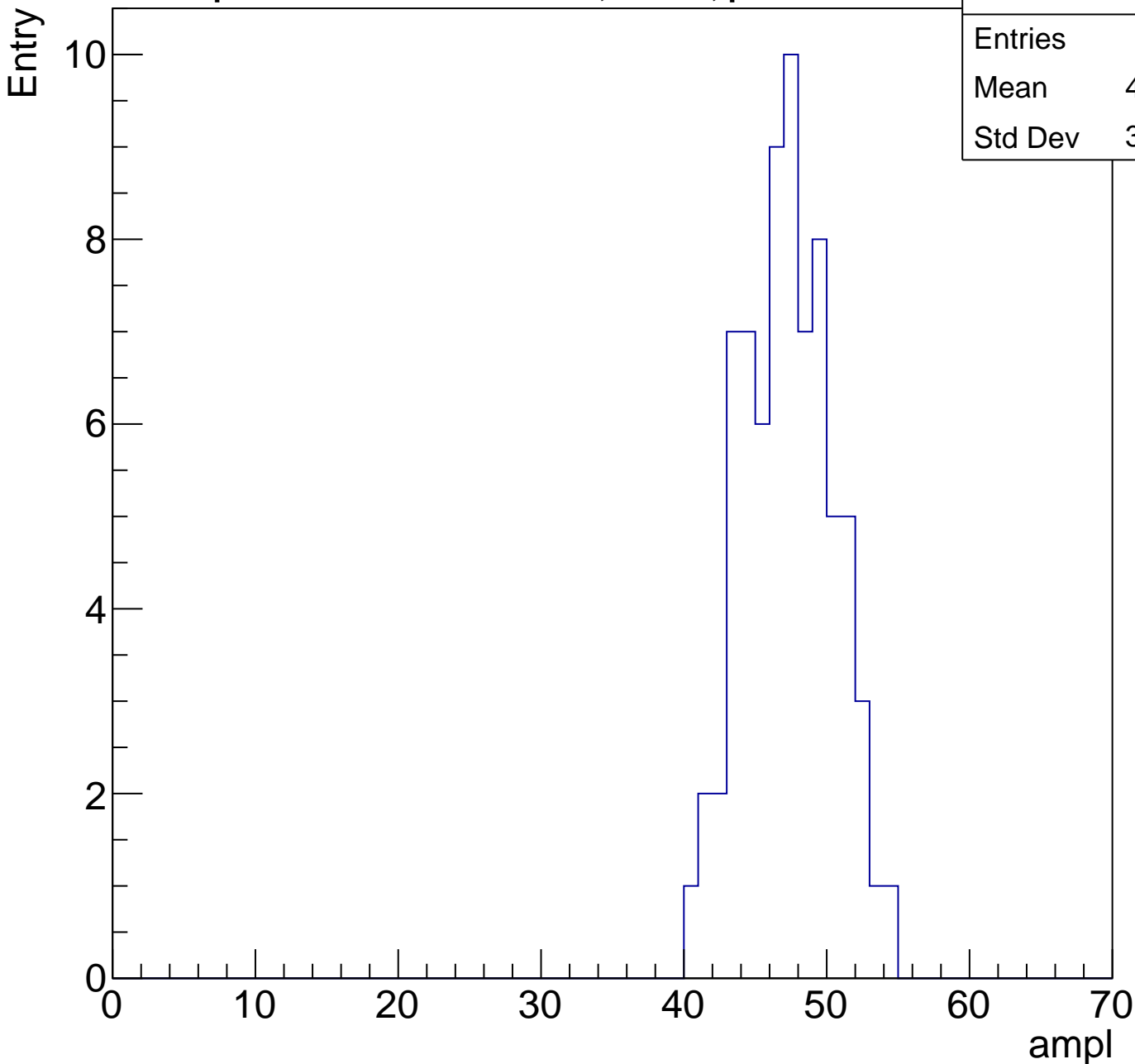
Gaus Width: 3.2191



B1L002S, U7-ch91, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	74
Mean	46.82
Std Dev	3.077

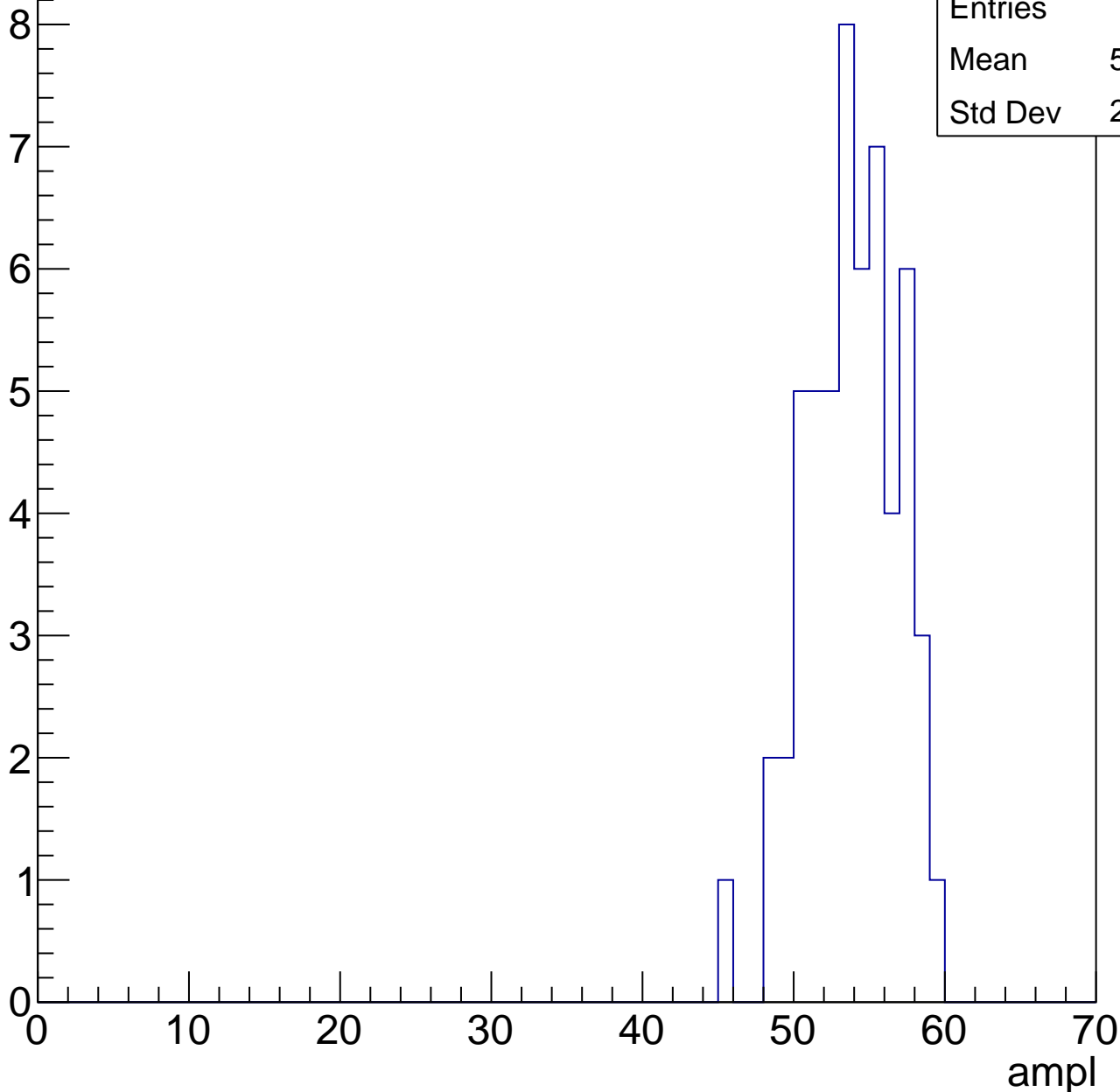


B1L002S, U7-ch91, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.38
Std Dev	2.969

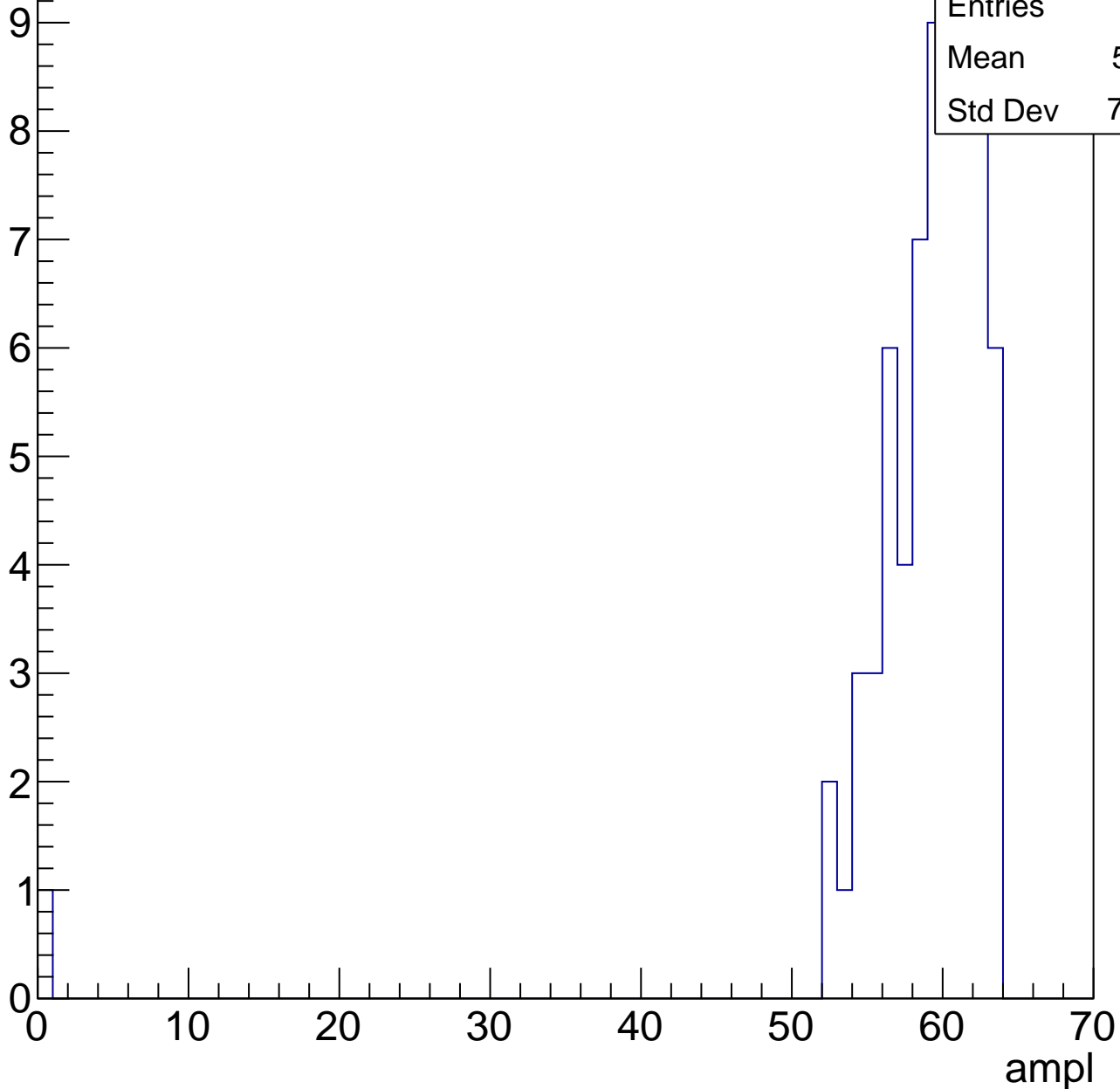


B1L002S, U7-ch91, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

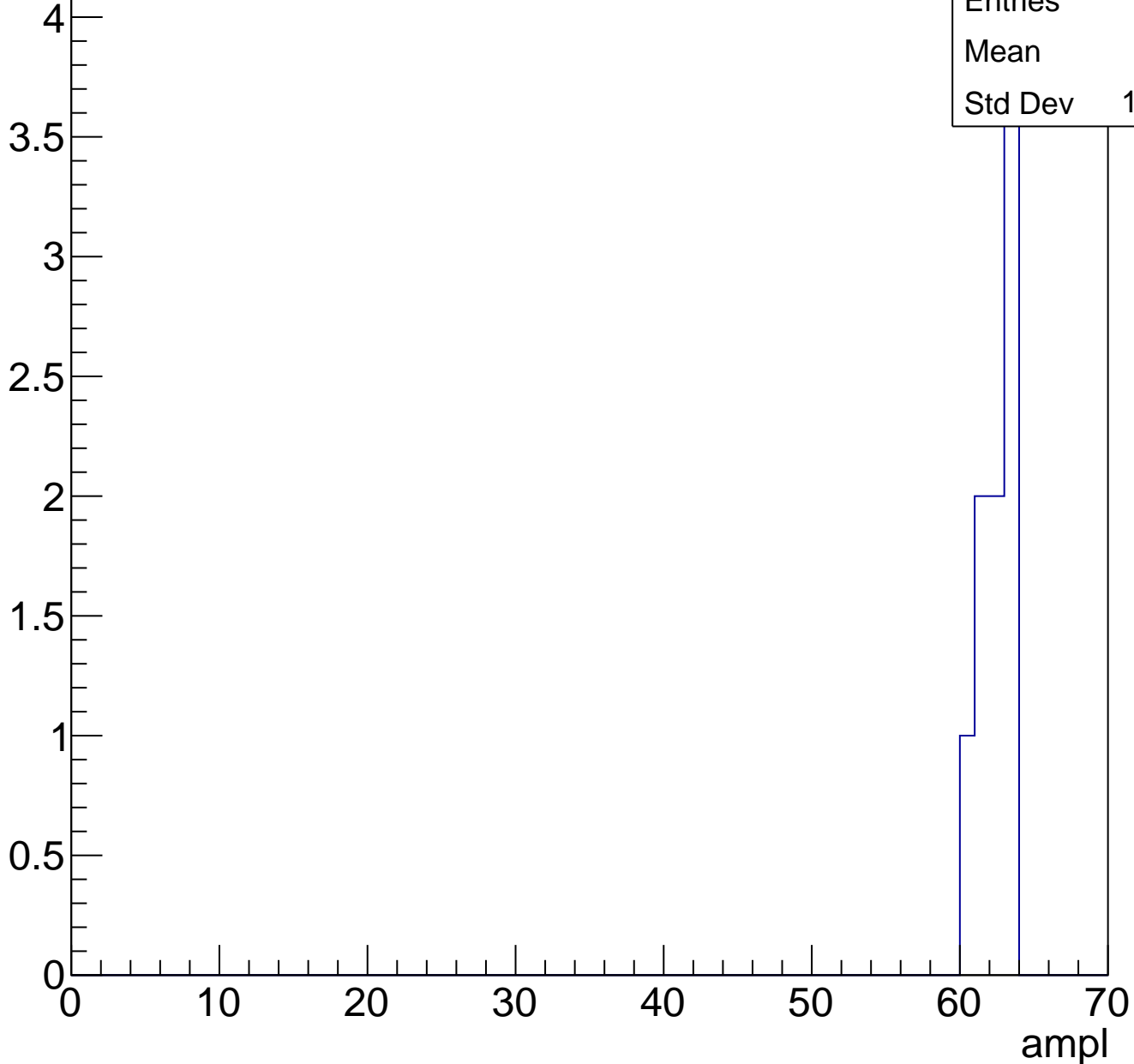
Entries	67
Mean	58.01
Std Dev	7.687



B1L002S, U7-ch91, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch91, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch92, adc0

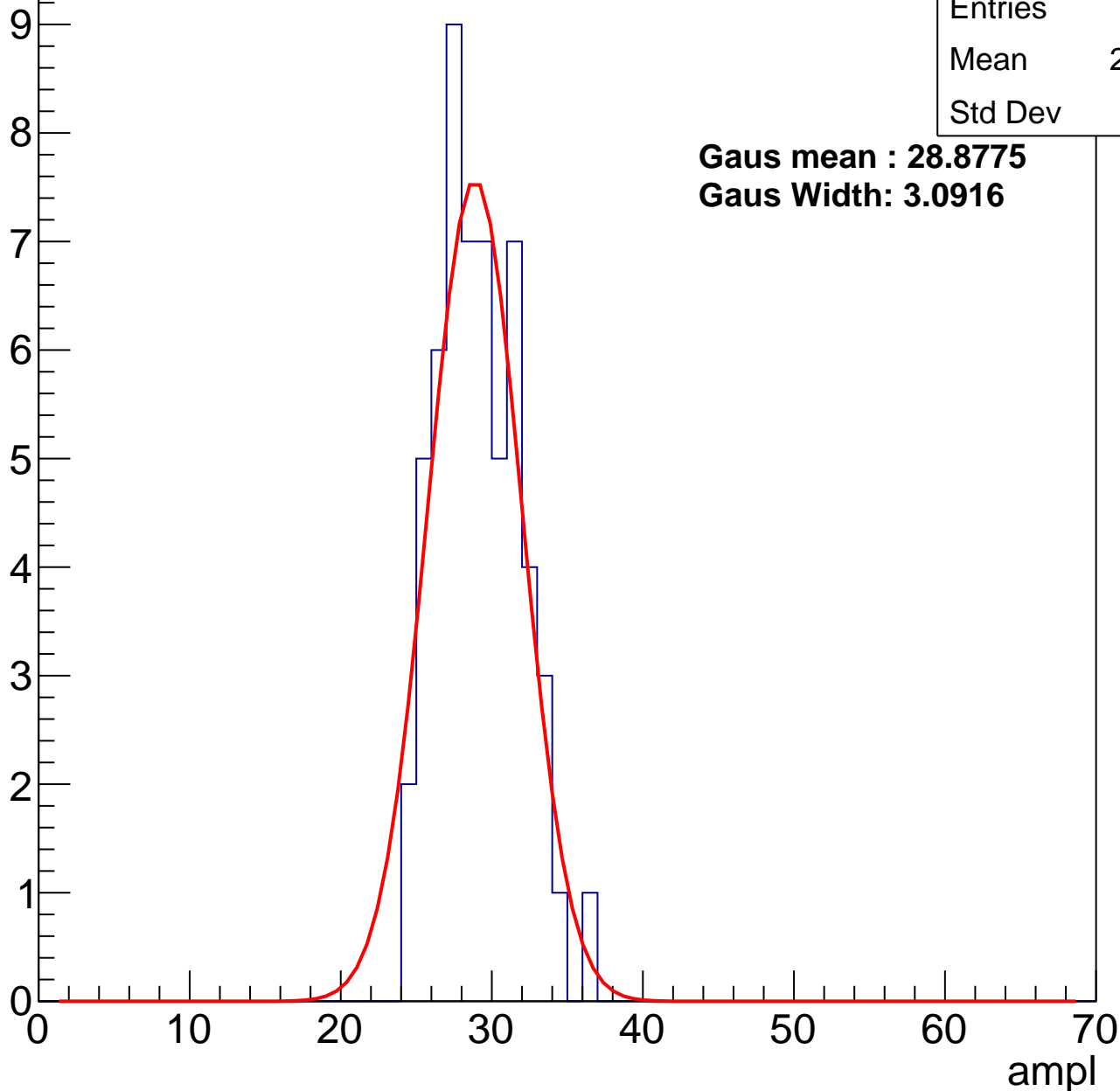
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.68
Std Dev	2.69

Gaus mean : 28.8775

Gaus Width: 3.0916



B1L002S, U7-ch92, adc1

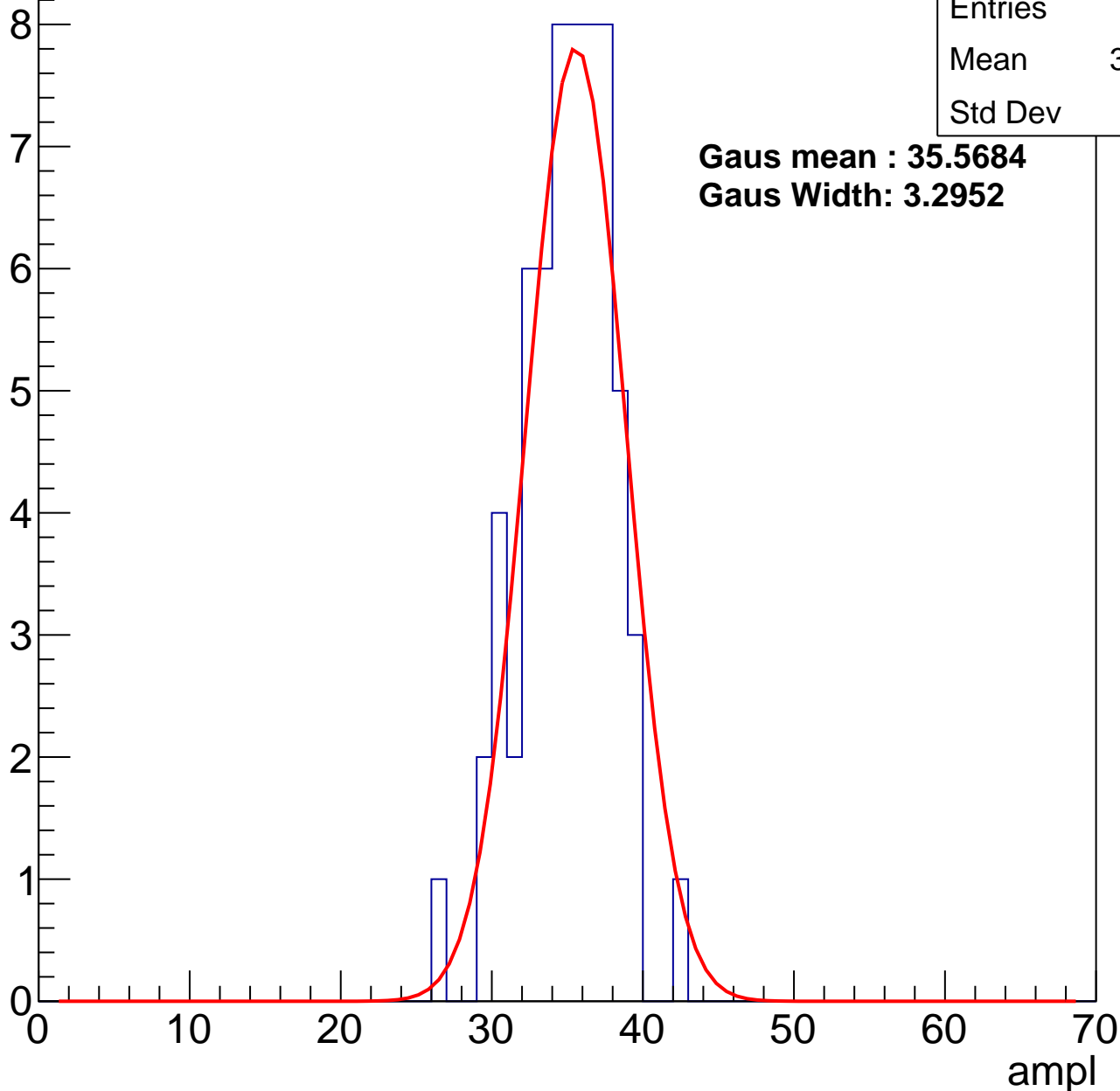
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.53
Std Dev	2.95

Gaus mean : 35.5684

Gaus Width: 3.2952



B1L002S, U7-ch92, adc2

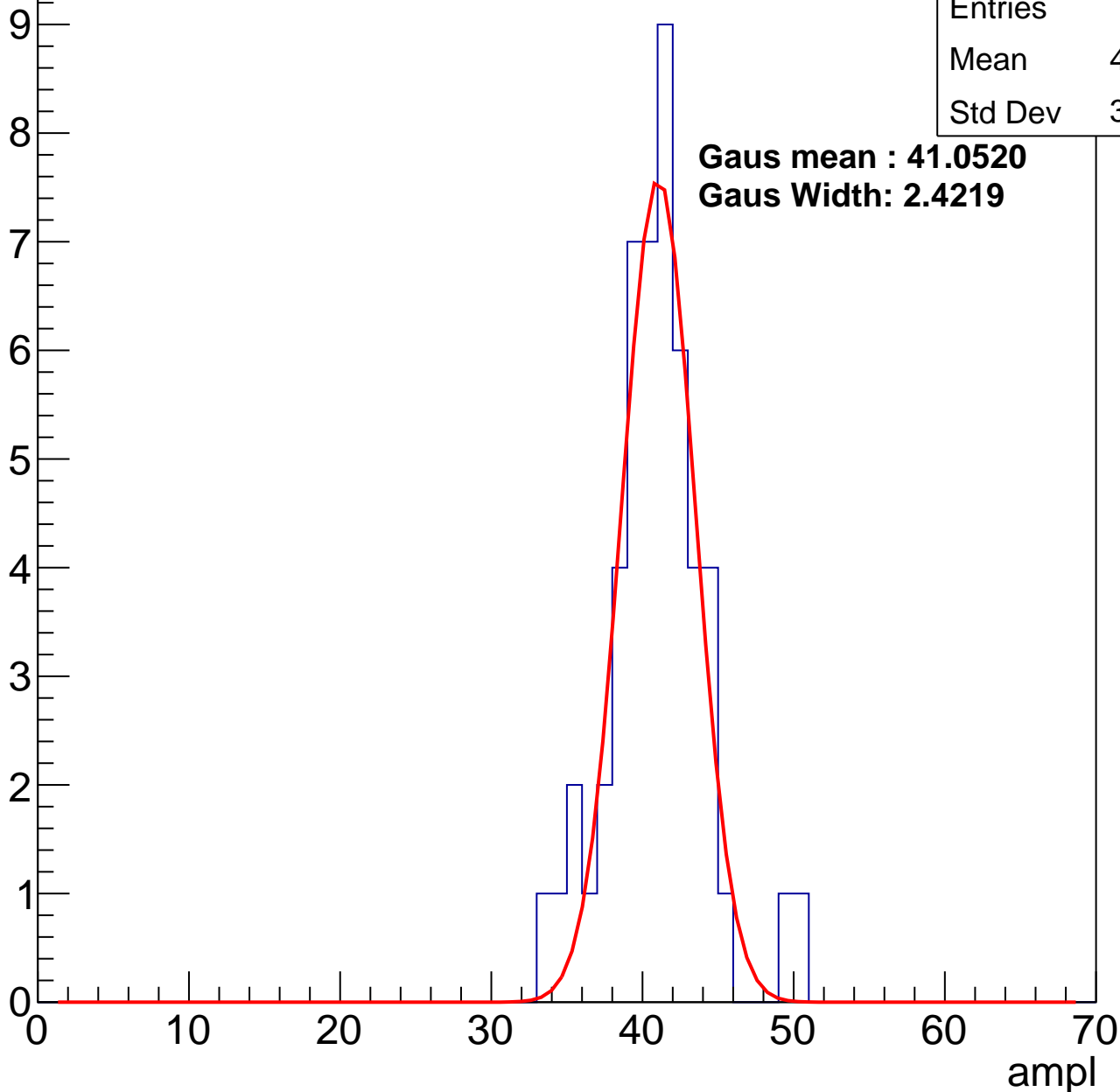
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.49
Std Dev	3.189

Gaus mean : 41.0520

Gaus Width: 2.4219



B1L002S, U7-ch92, adc3

calib_packv5_042523_0143.root, FC#10, port B3

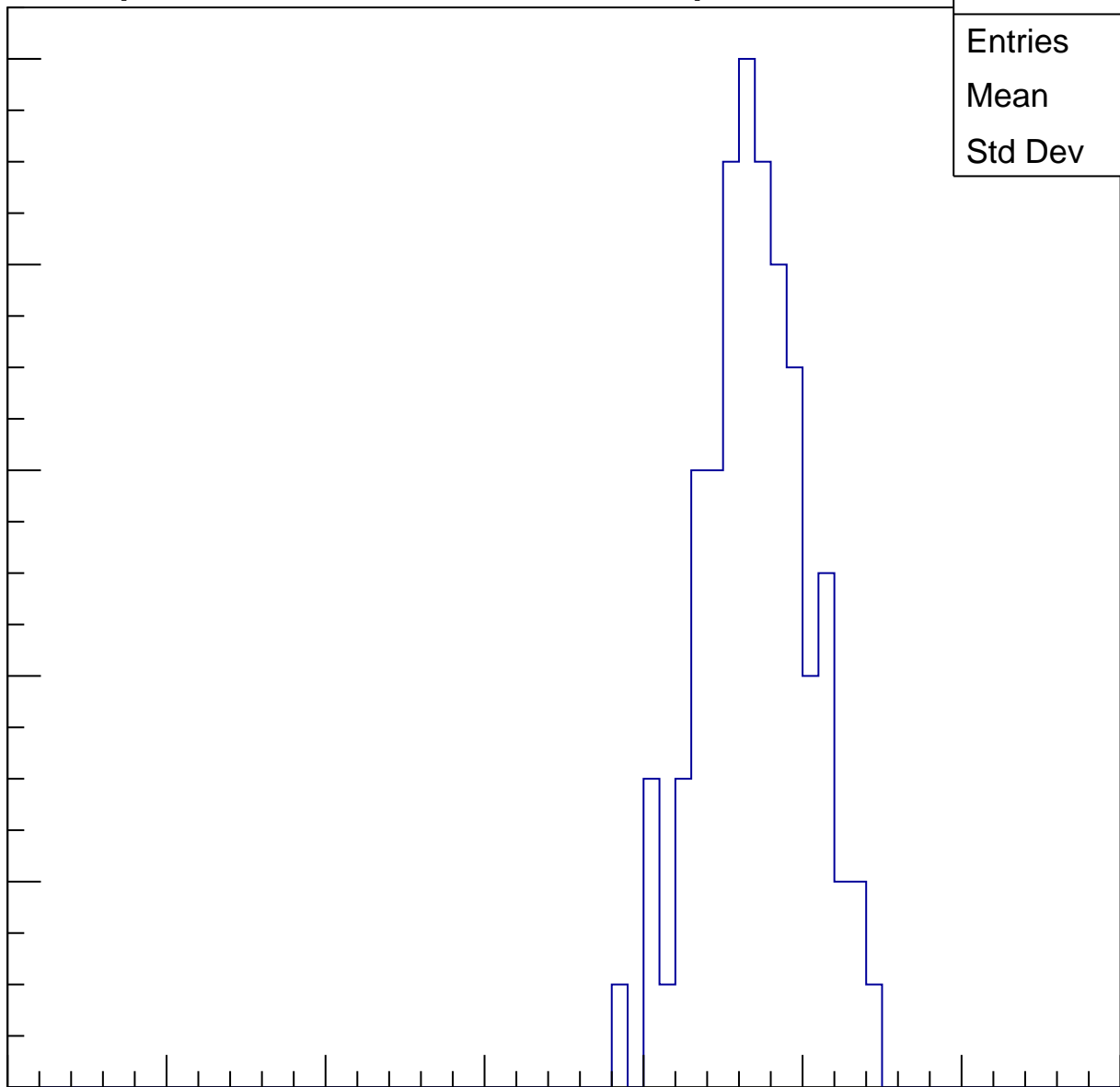
Entries	77
Mean	46.51
Std Dev	3.301

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch92, adc4

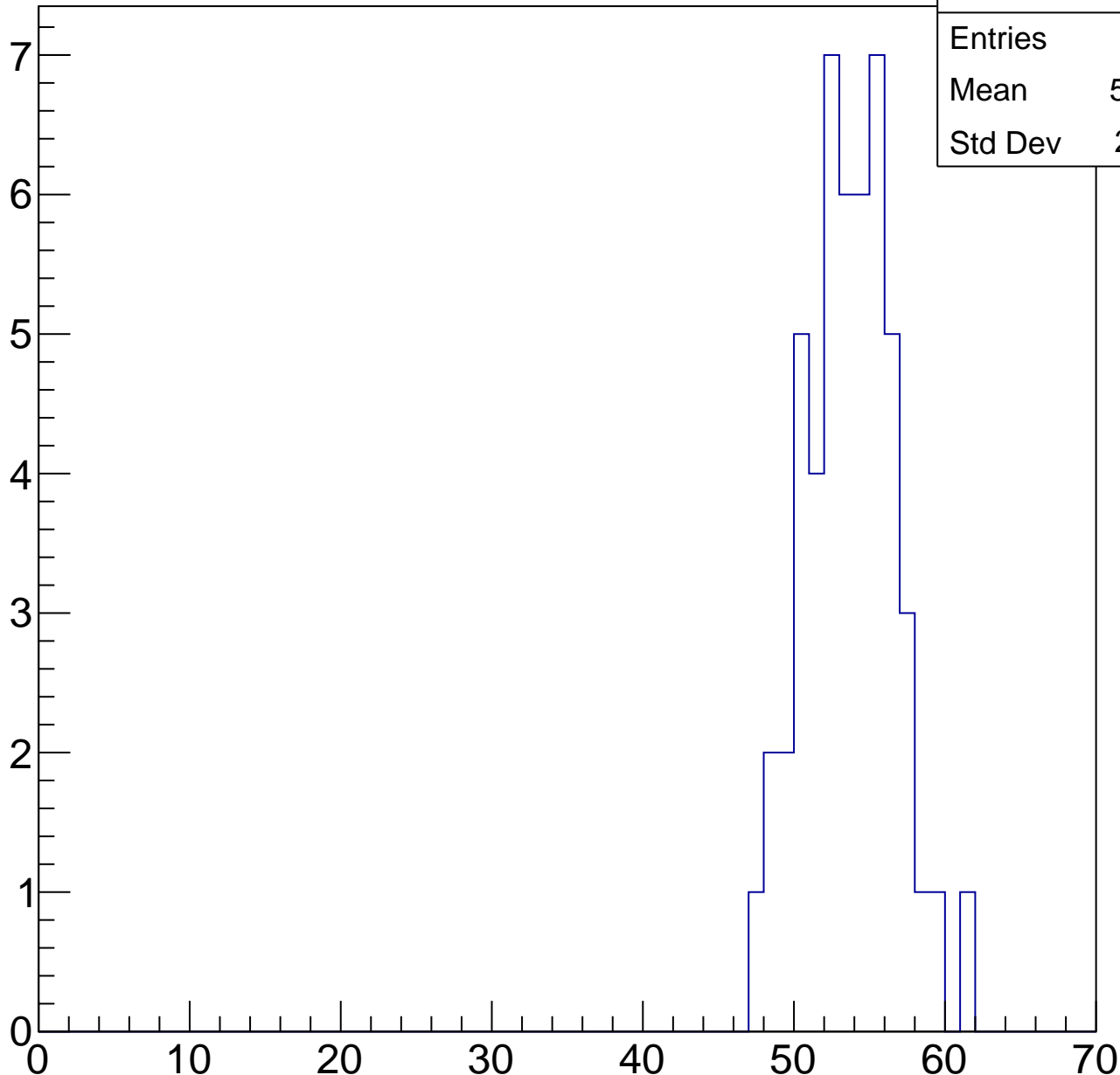
calib_packv5_042523_0143.root, FC#10, port B3

Entry

7
6
5
4
3
2
1
0

Entries	51
Mean	53.24
Std Dev	2.921

ampl

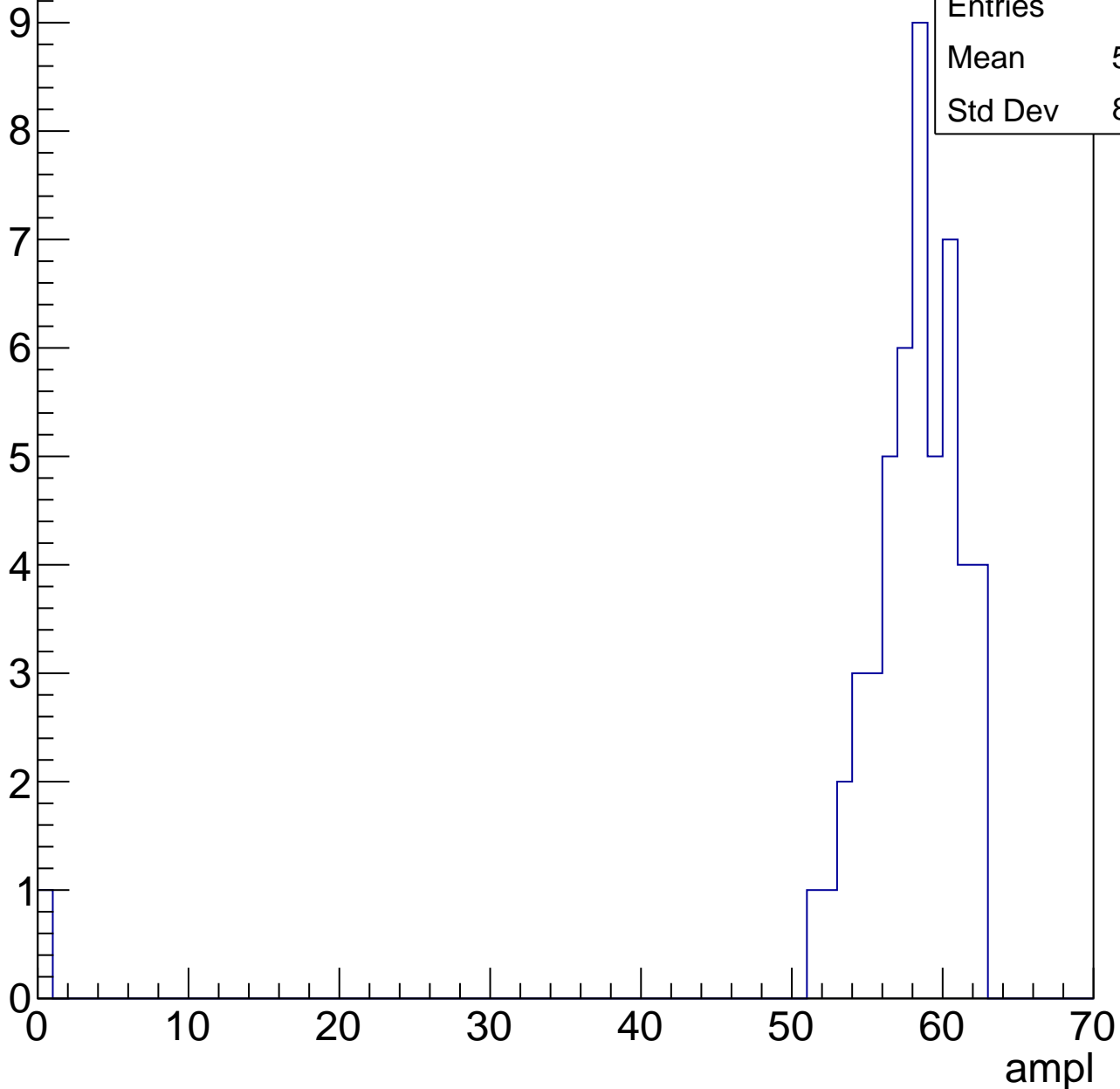


B1L002S, U7-ch92, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	56.61
Std Dev	8.441

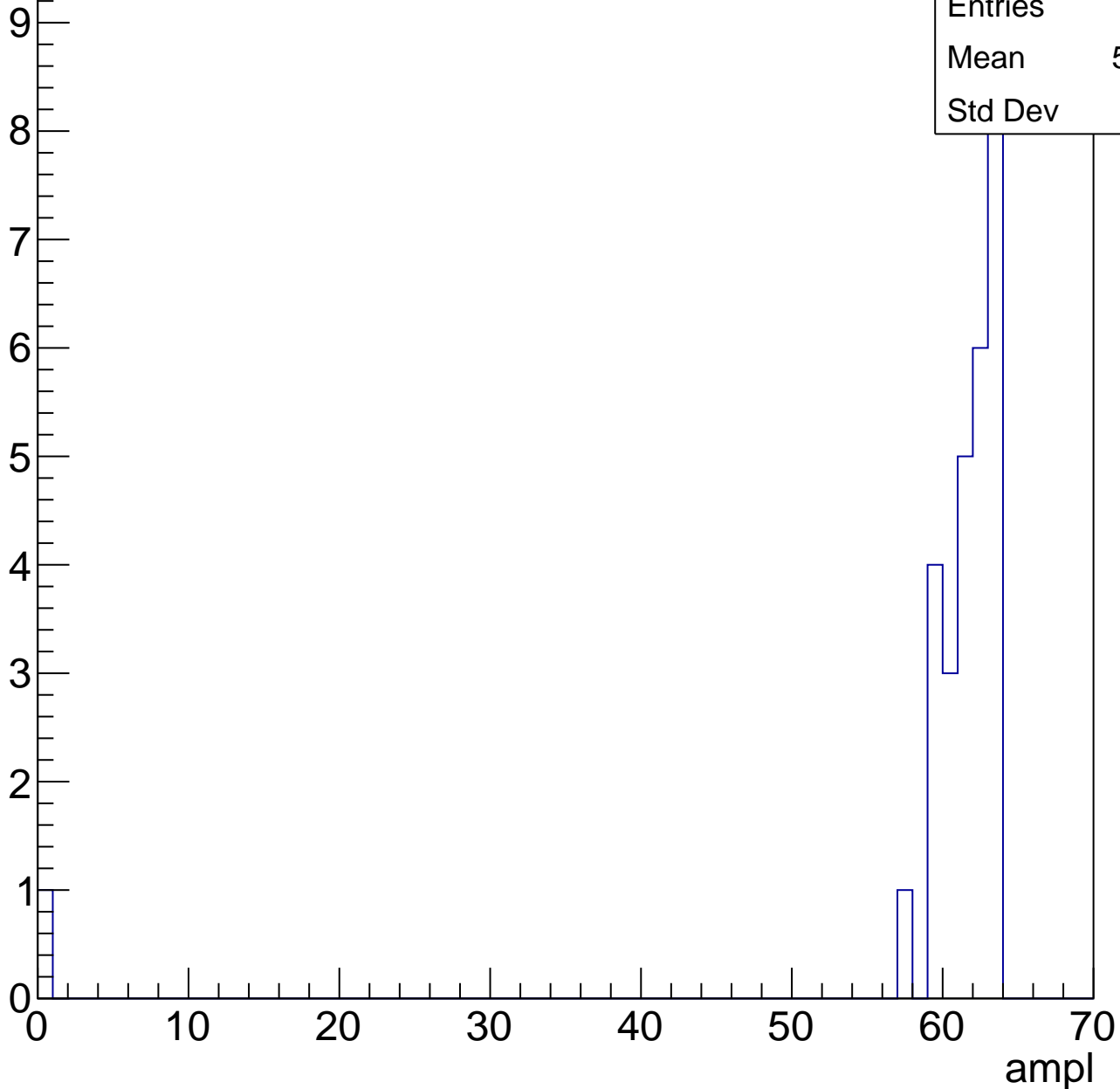


B1L002S, U7-ch92, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

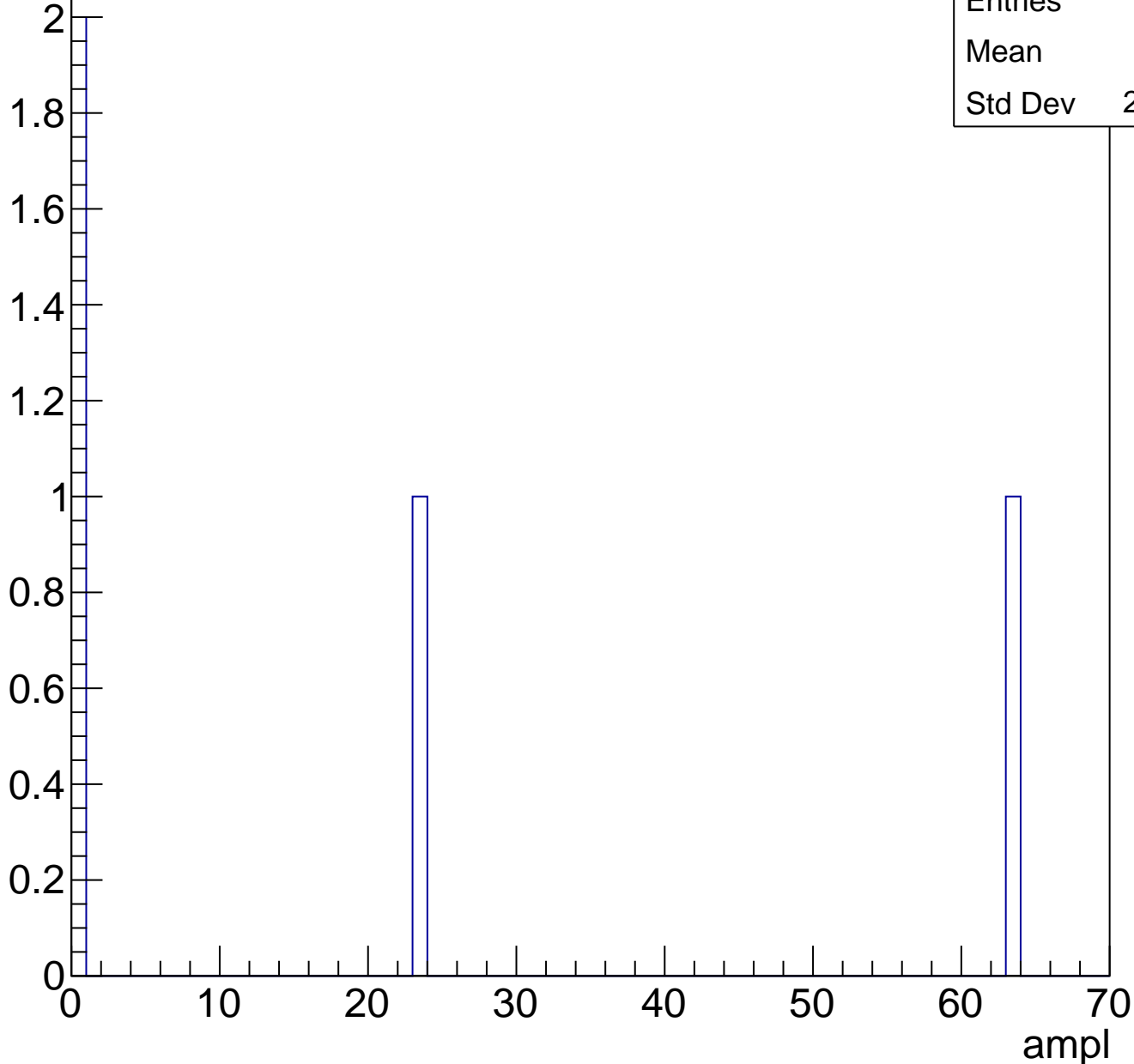
Entries	29
Mean	59.21
Std Dev	11.3



B1L002S, U7-ch92, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	21.5
Std Dev	25.73

B1L002S, U7-ch93, adc0

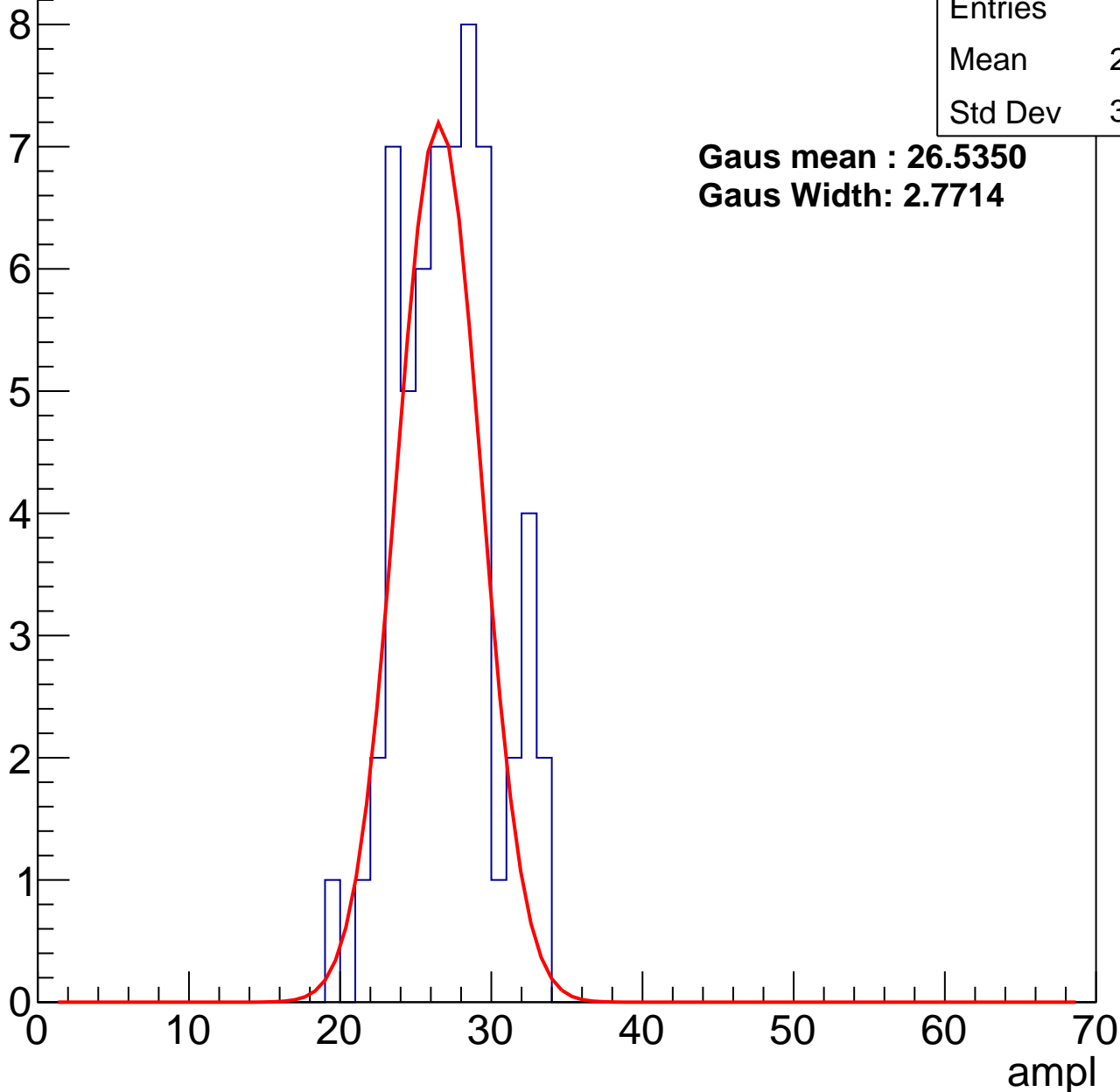
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	26.65
Std Dev	3.108

Gaus mean : 26.5350

Gaus Width: 2.7714



B1L002S, U7-ch93, adc1

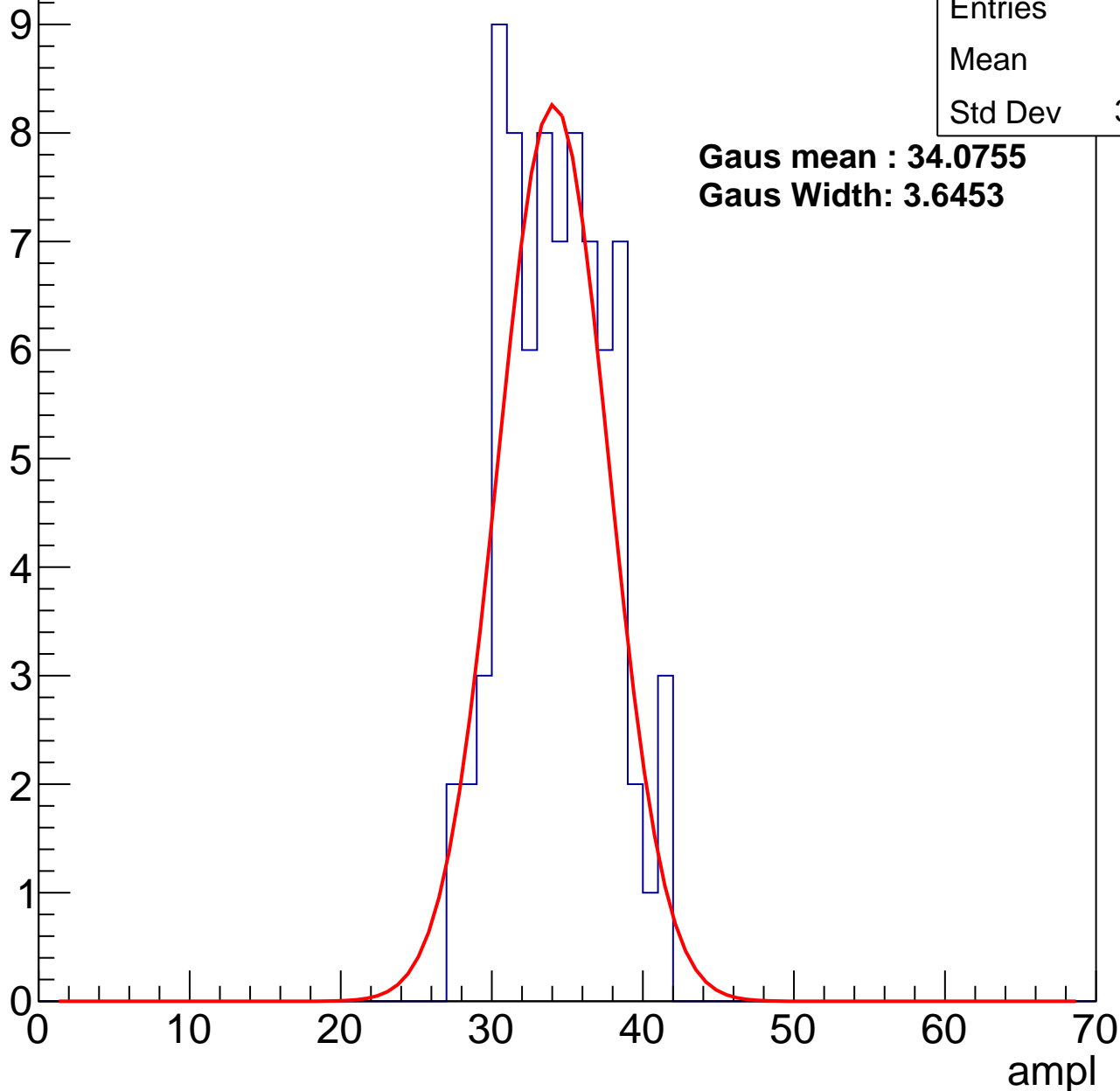
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	79
Mean	33.8
Std Dev	3.421

Gaus mean : 34.0755

Gaus Width: 3.6453



B1L002S, U7-ch93, adc2

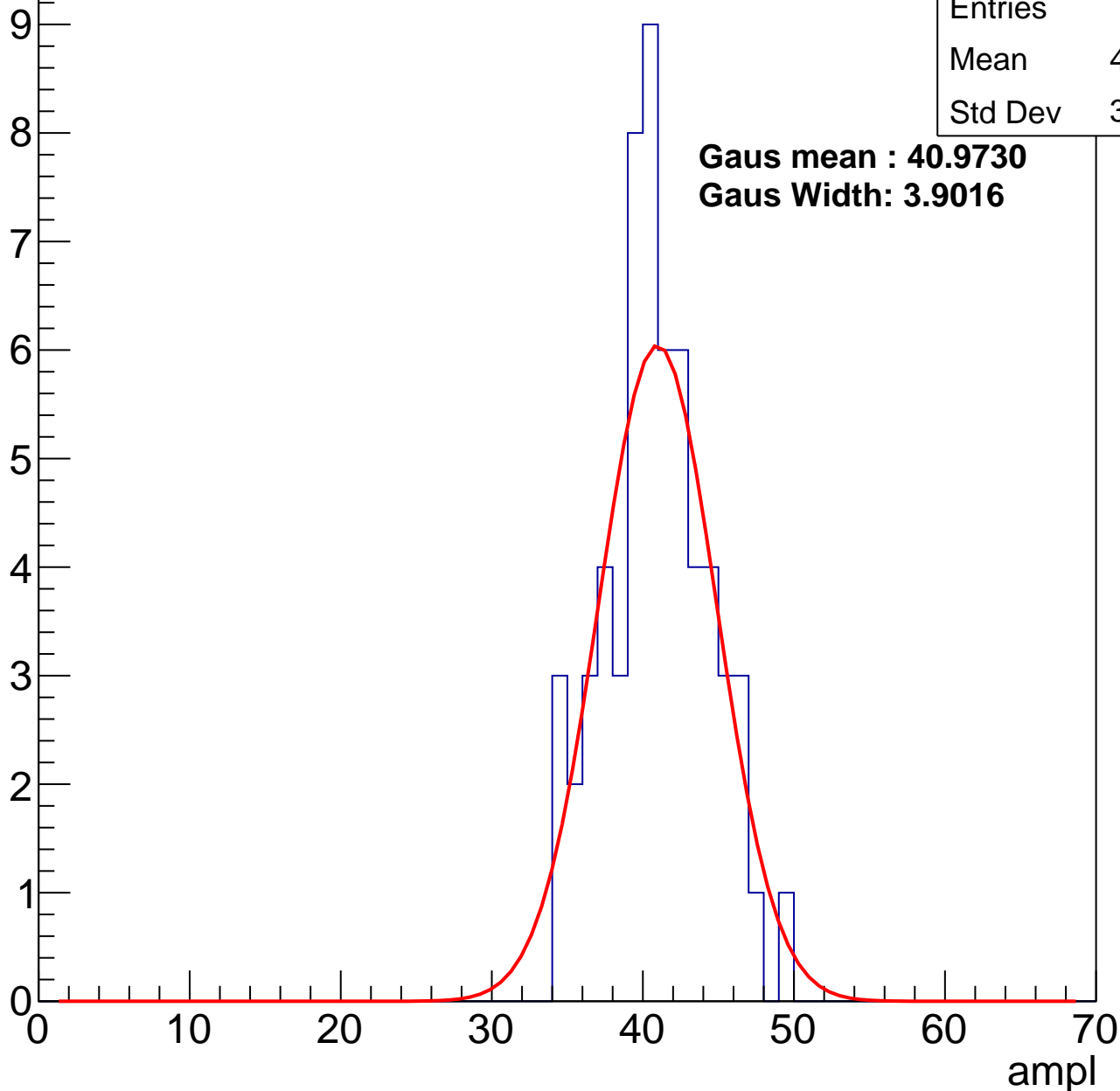
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	40.48
Std Dev	3.394

Gaus mean : 40.9730

Gaus Width: 3.9016

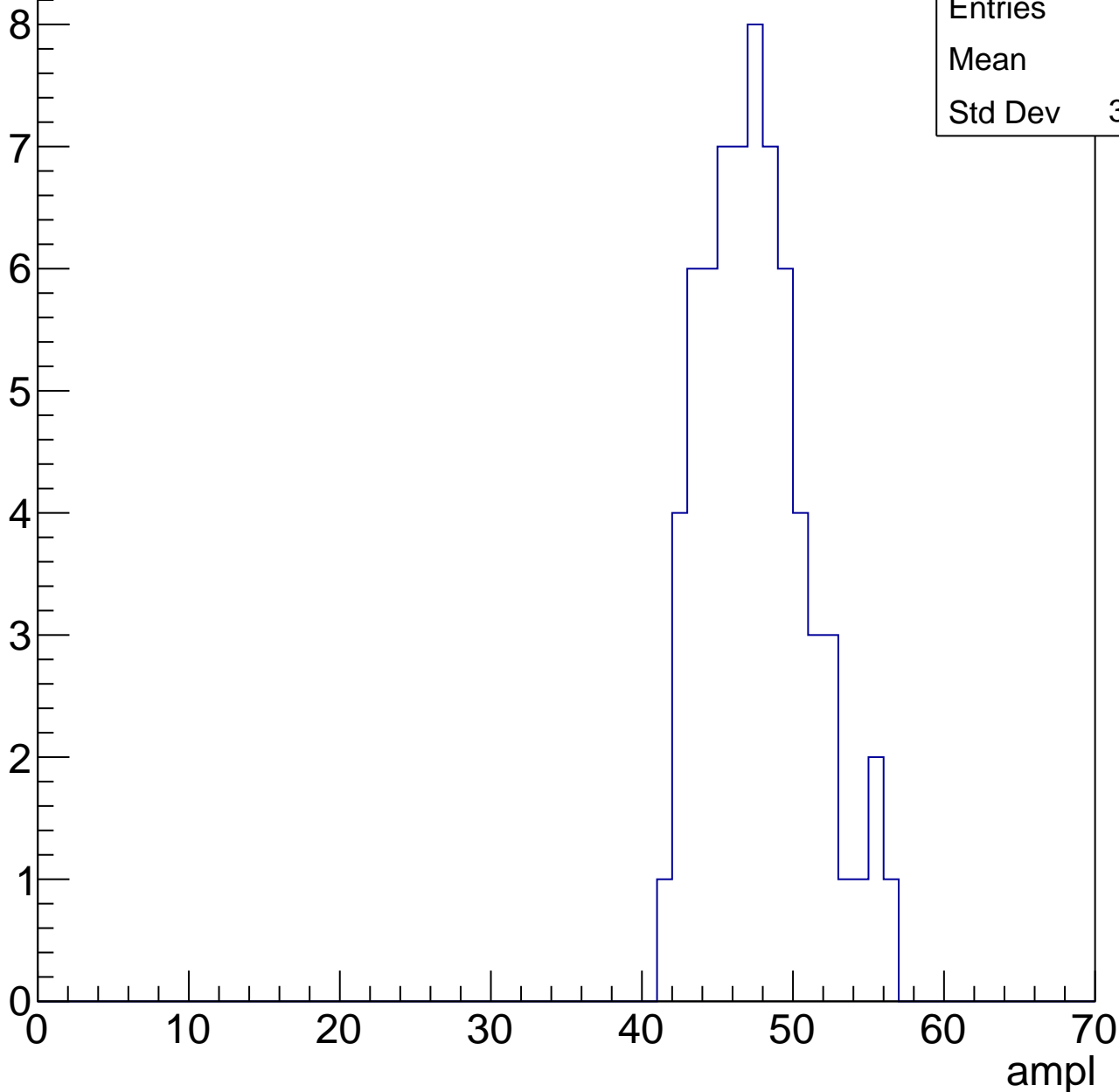


B1L002S, U7-ch93, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	47.1
Std Dev	3.465

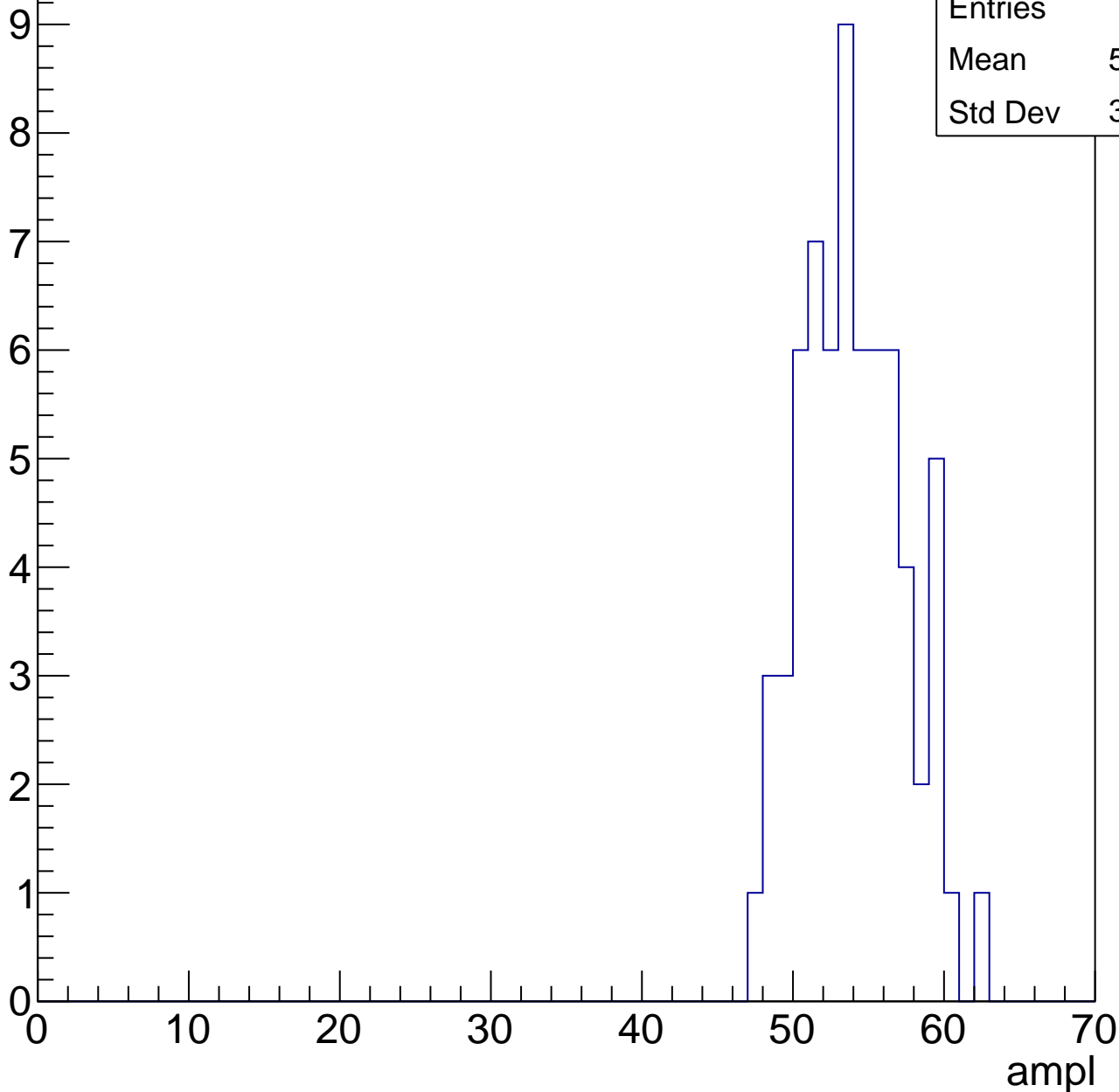


B1L002S, U7-ch93, adc4

calib_packv5_042523_0143.root, FC#10, port B3

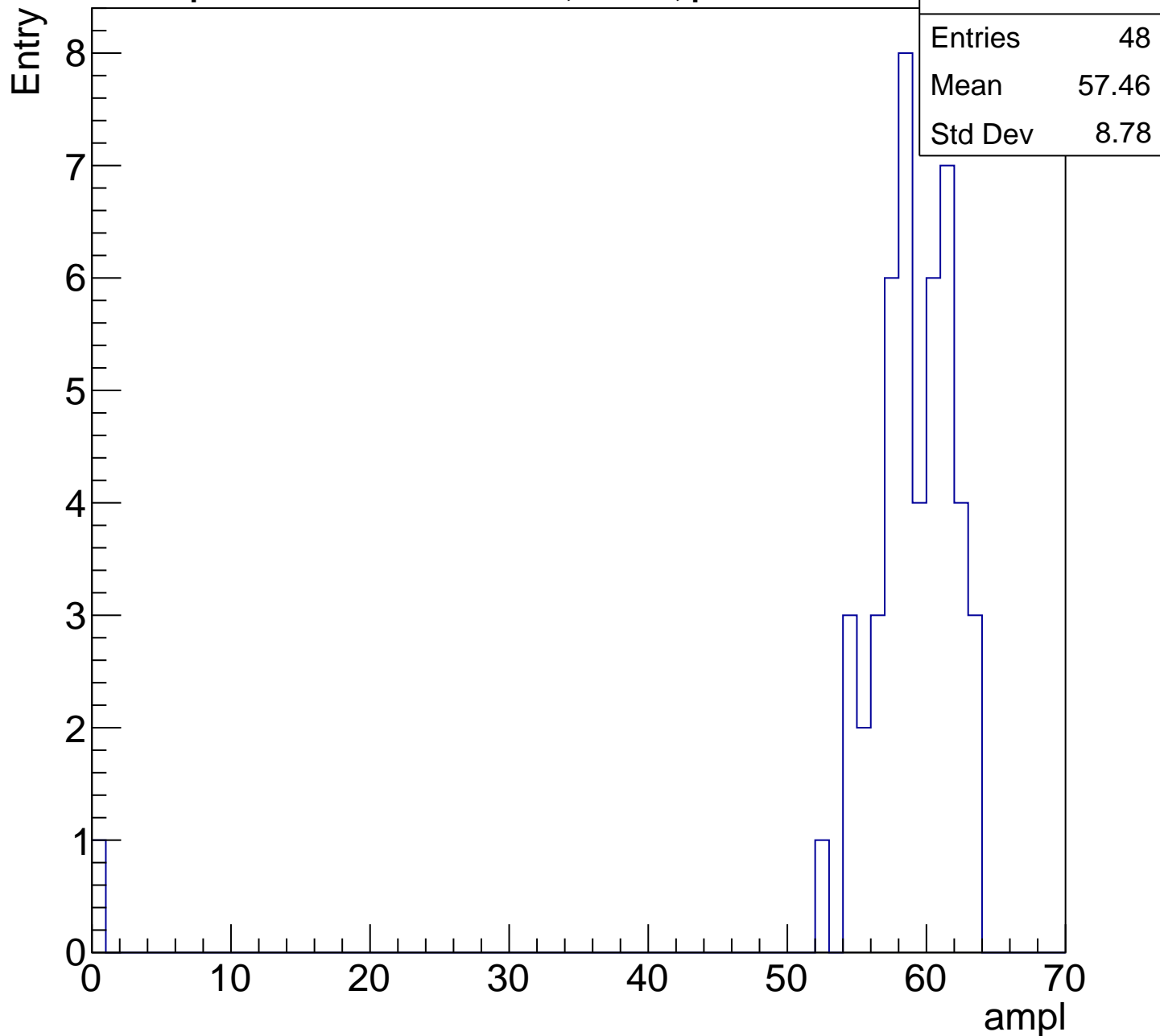
Entry

Entries	66
Mean	53.56
Std Dev	3.335



B1L002S, U7-ch93, adc5

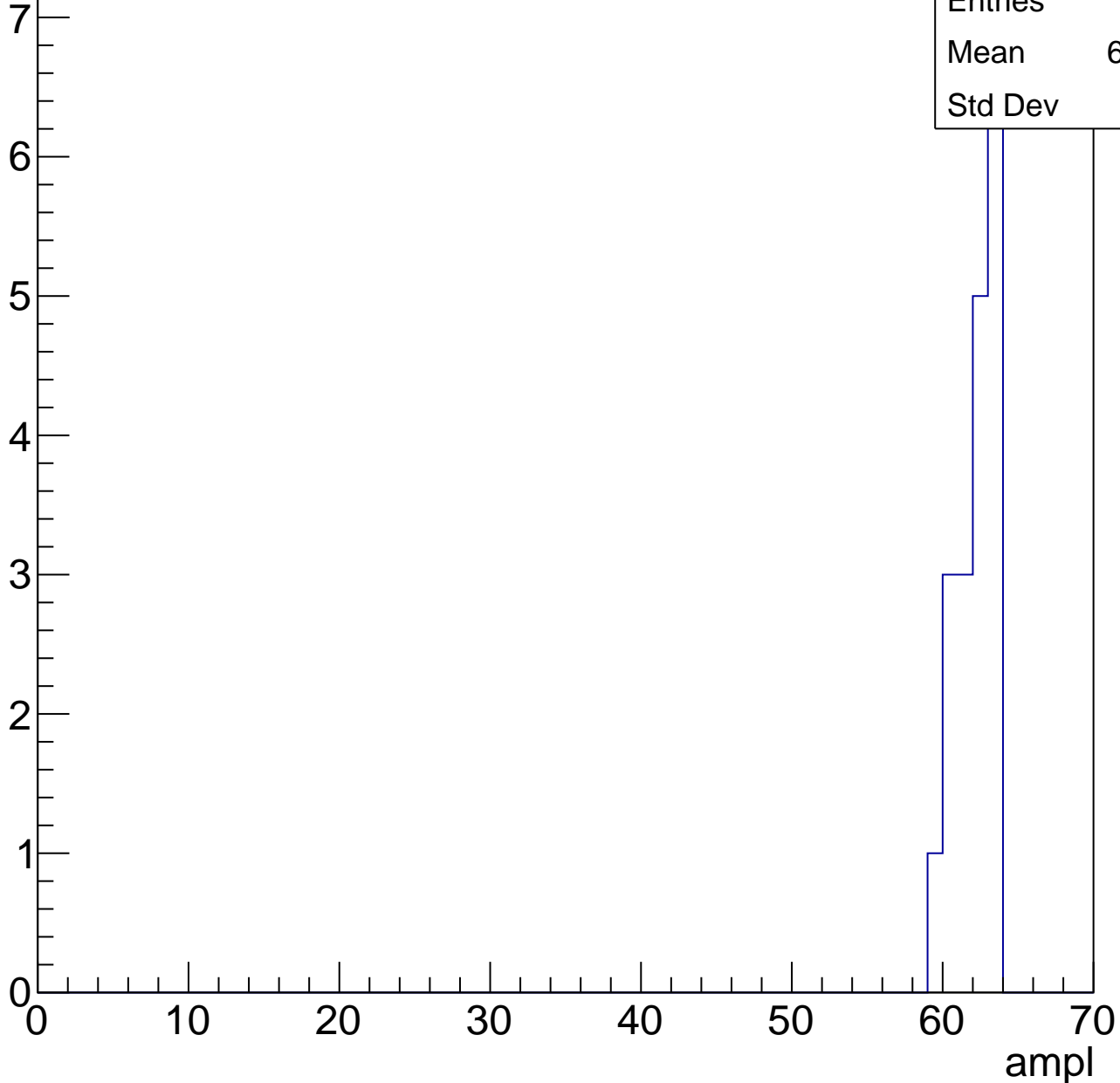
calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch93, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	19
Mean	61.74
Std Dev	1.25

B1L002S, U7-ch93, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch94, adc0

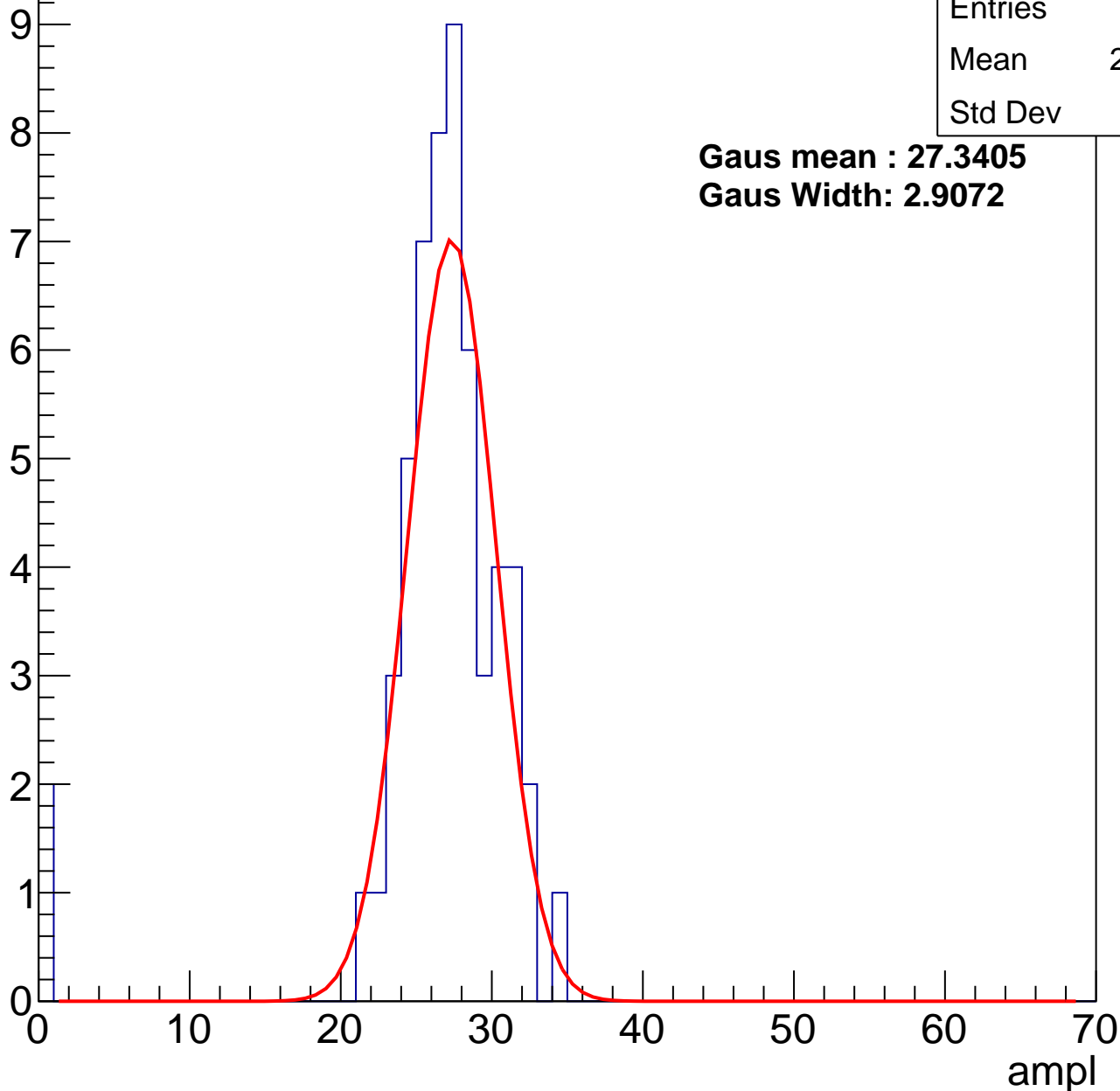
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	25.98
Std Dev	5.68

Gaus mean : 27.3405

Gaus Width: 2.9072



B1L002S, U7-ch94, adc1

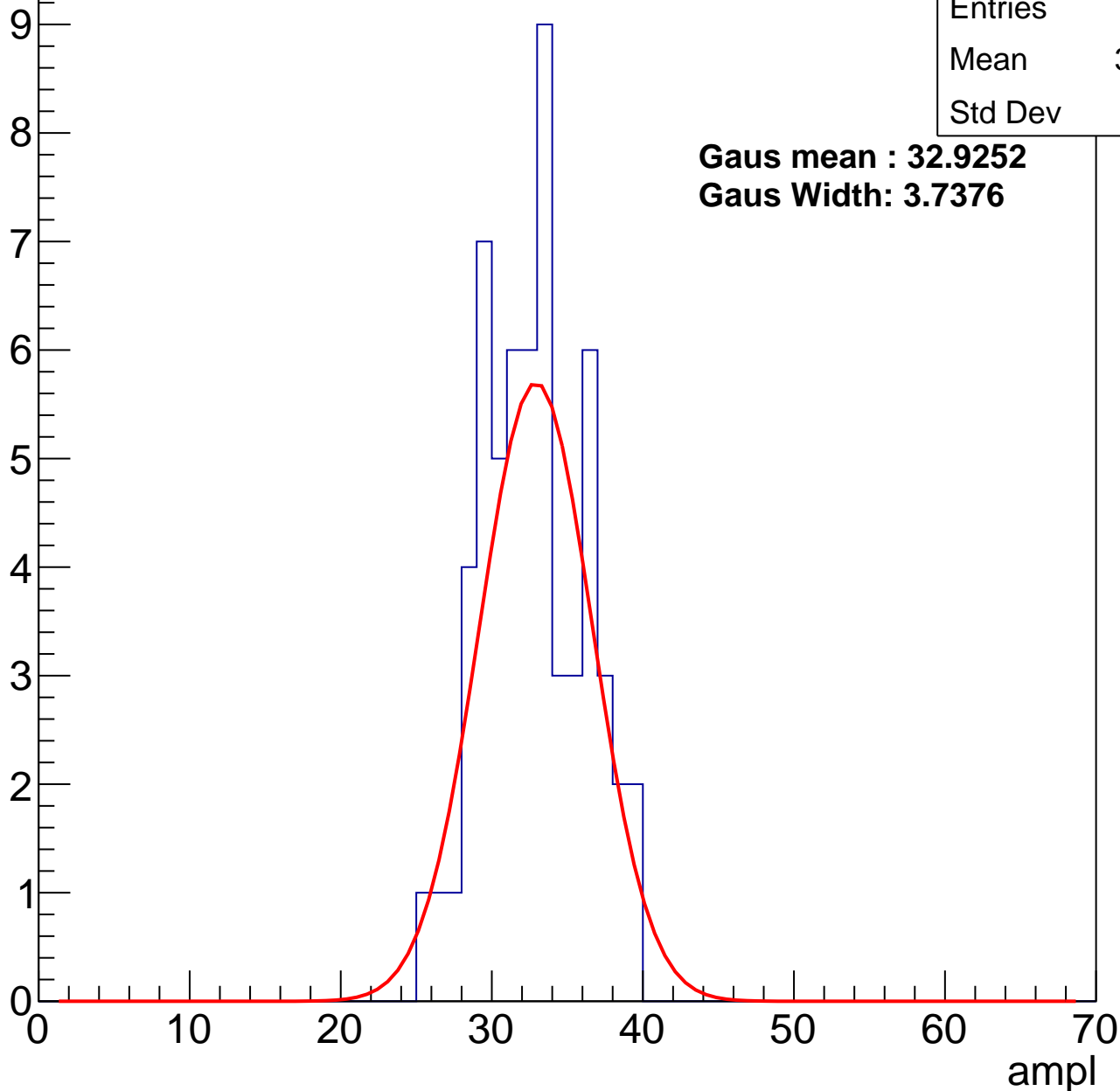
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	32.31
Std Dev	3.31

Gaus mean : 32.9252

Gaus Width: 3.7376



B1L002S, U7-ch94, adc2

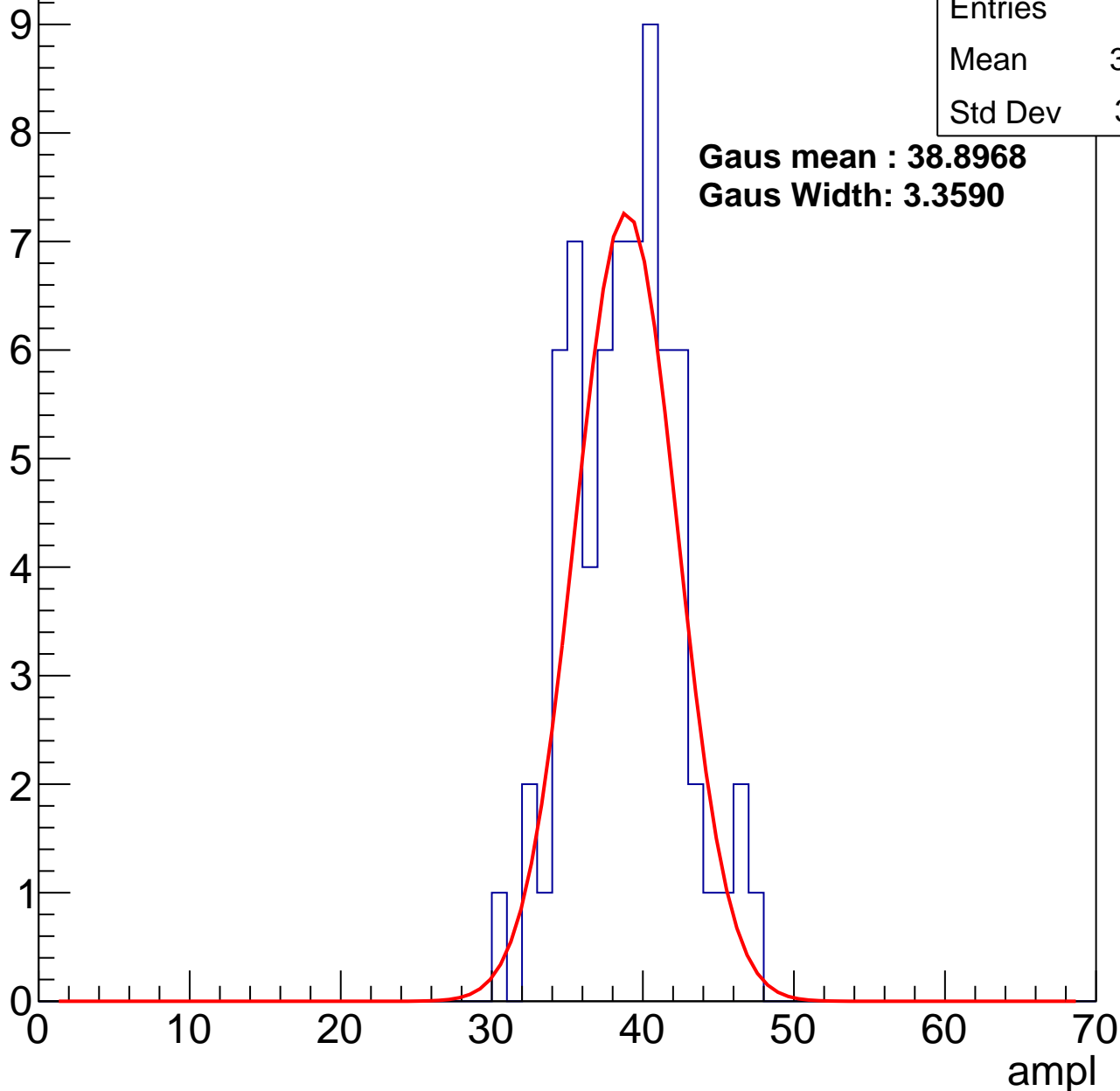
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	38.45
Std Dev	3.541

Gaus mean : 38.8968

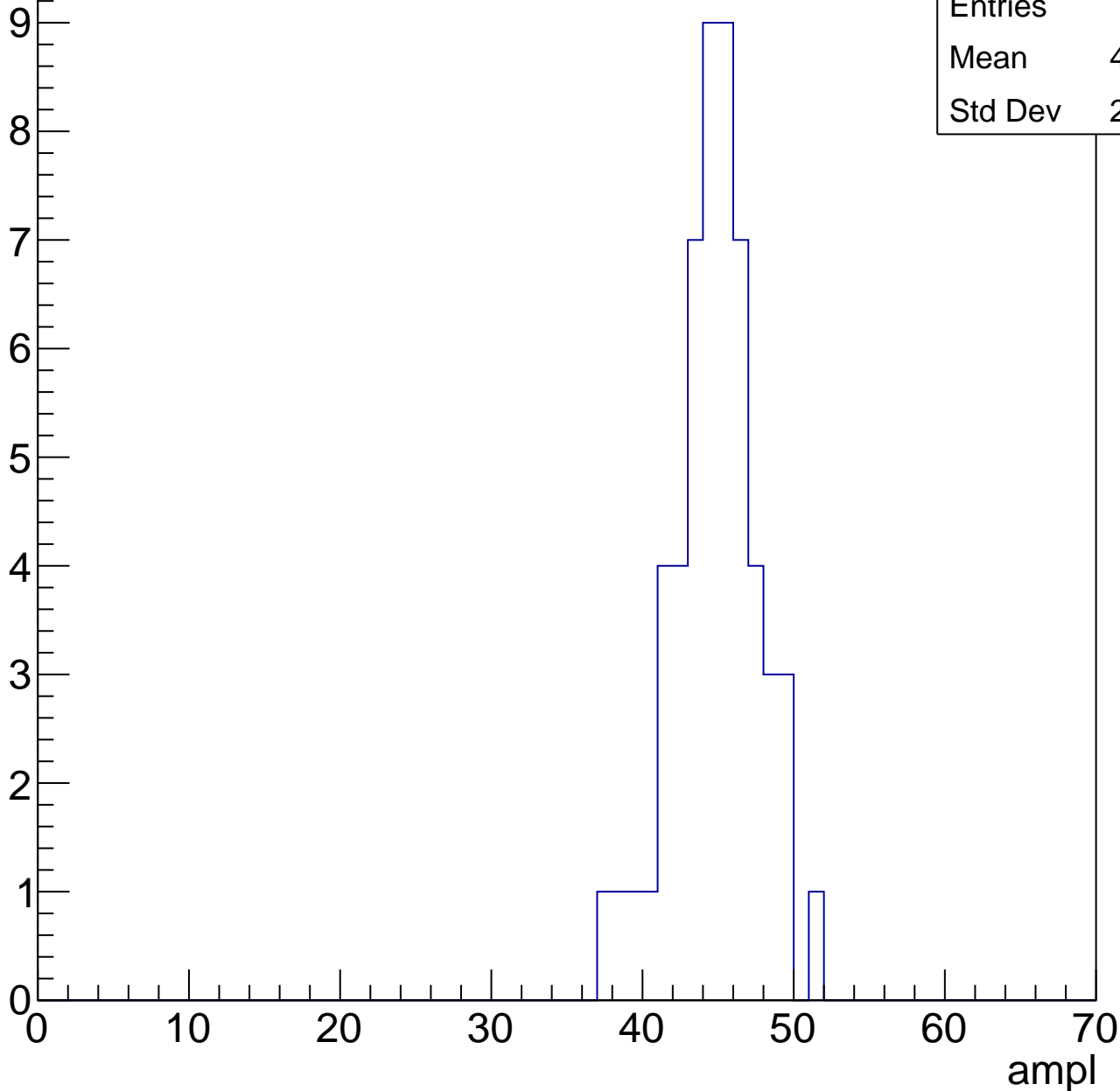
Gaus Width: 3.3590



B1L002S, U7-ch94, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry



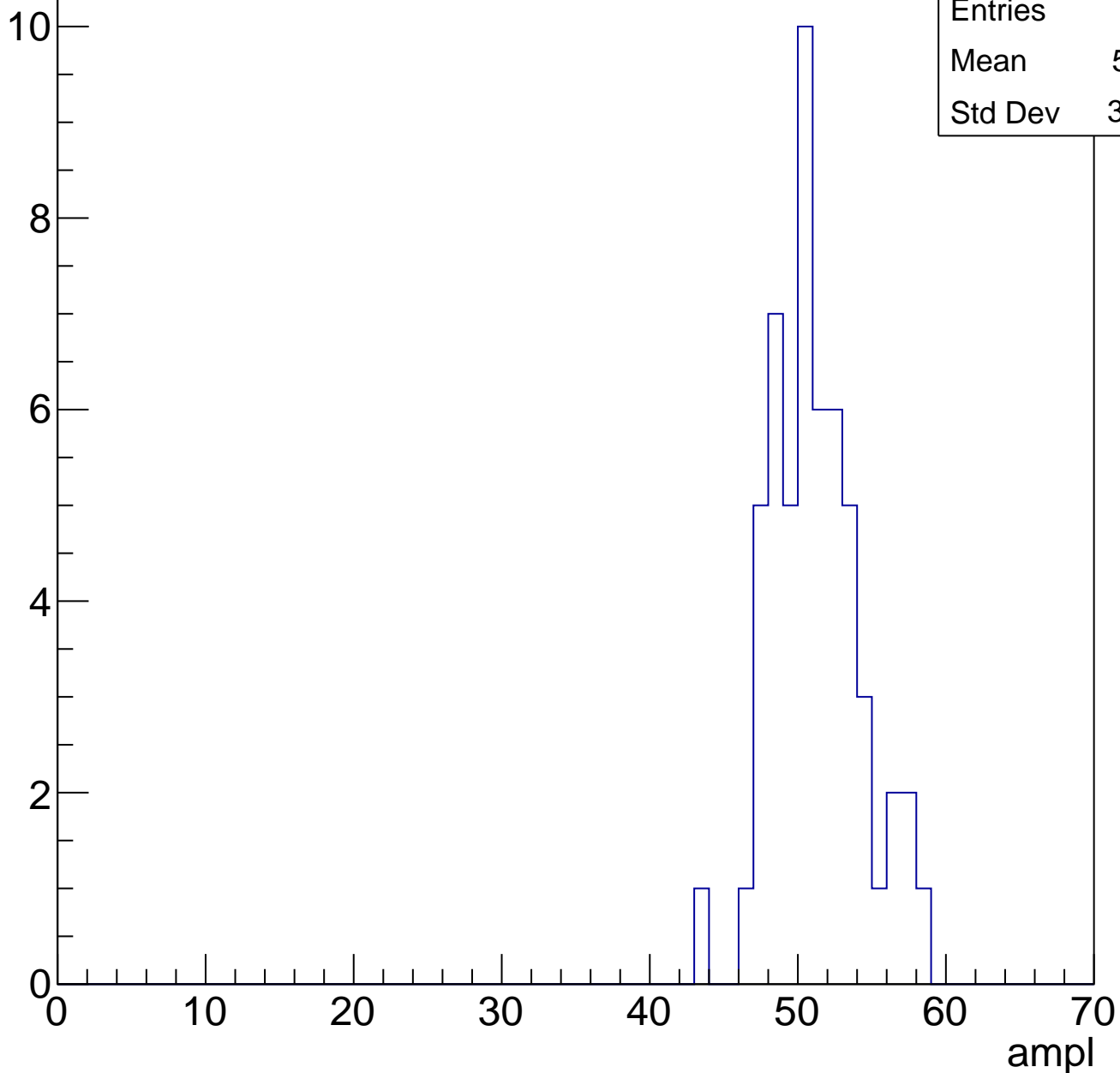
Entries	55
Mean	44.36
Std Dev	2.772

B1L002S, U7-ch94, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	55
Mean	50.71
Std Dev	3.007

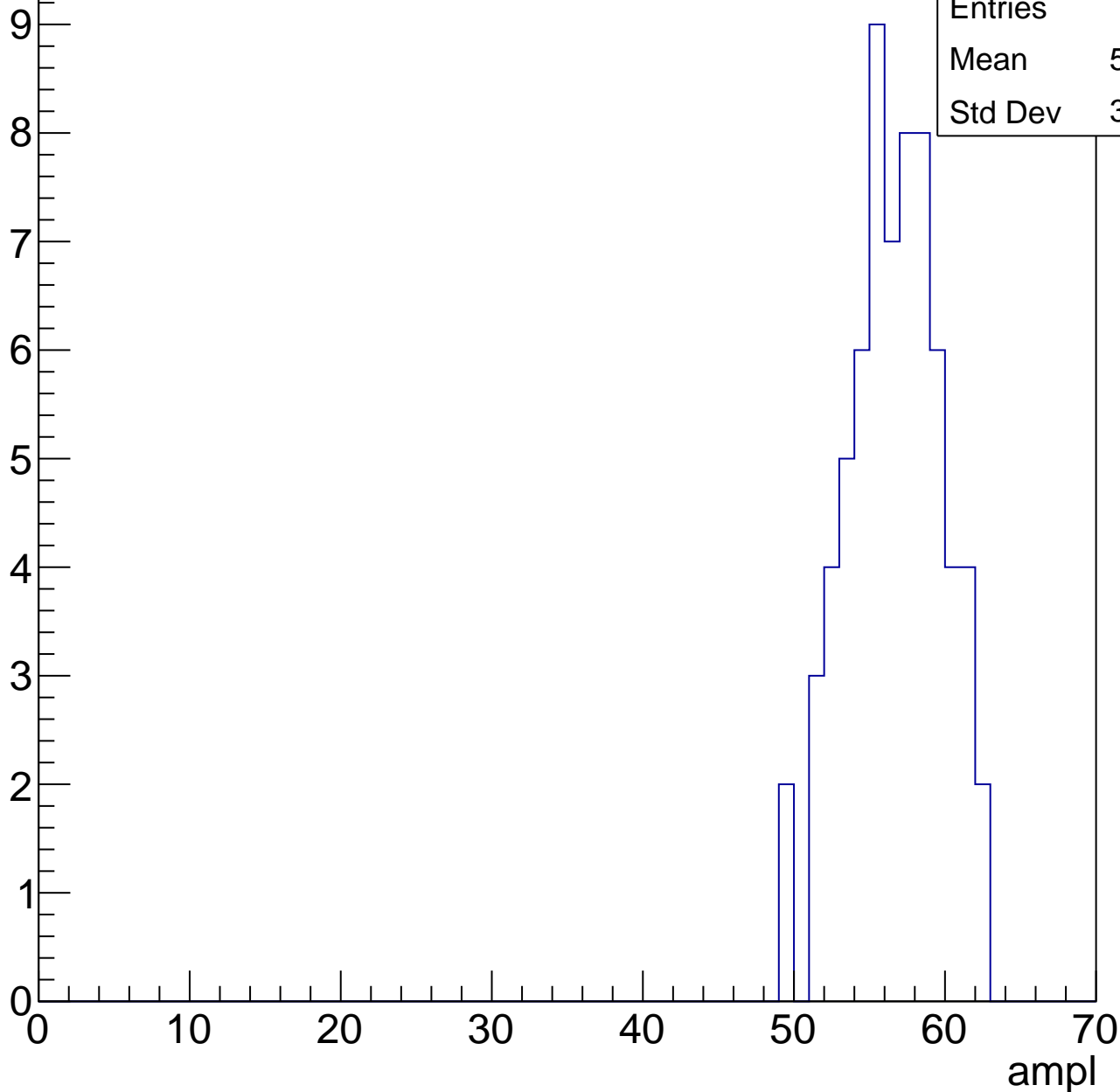
Entry



B1L002S, U7-ch94, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



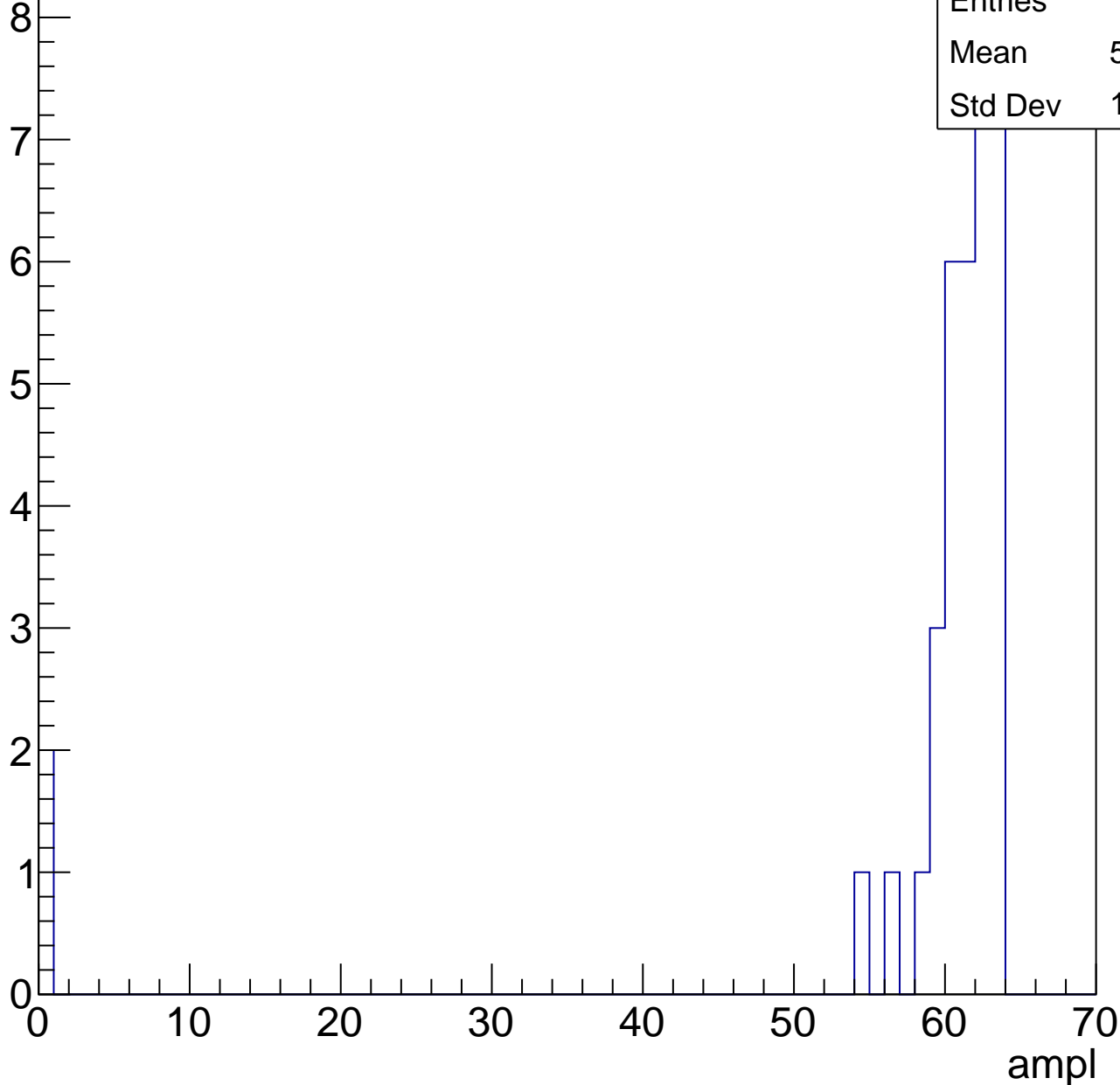
Entries	68
Mean	56.13
Std Dev	3.077

B1L002S, U7-ch94, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

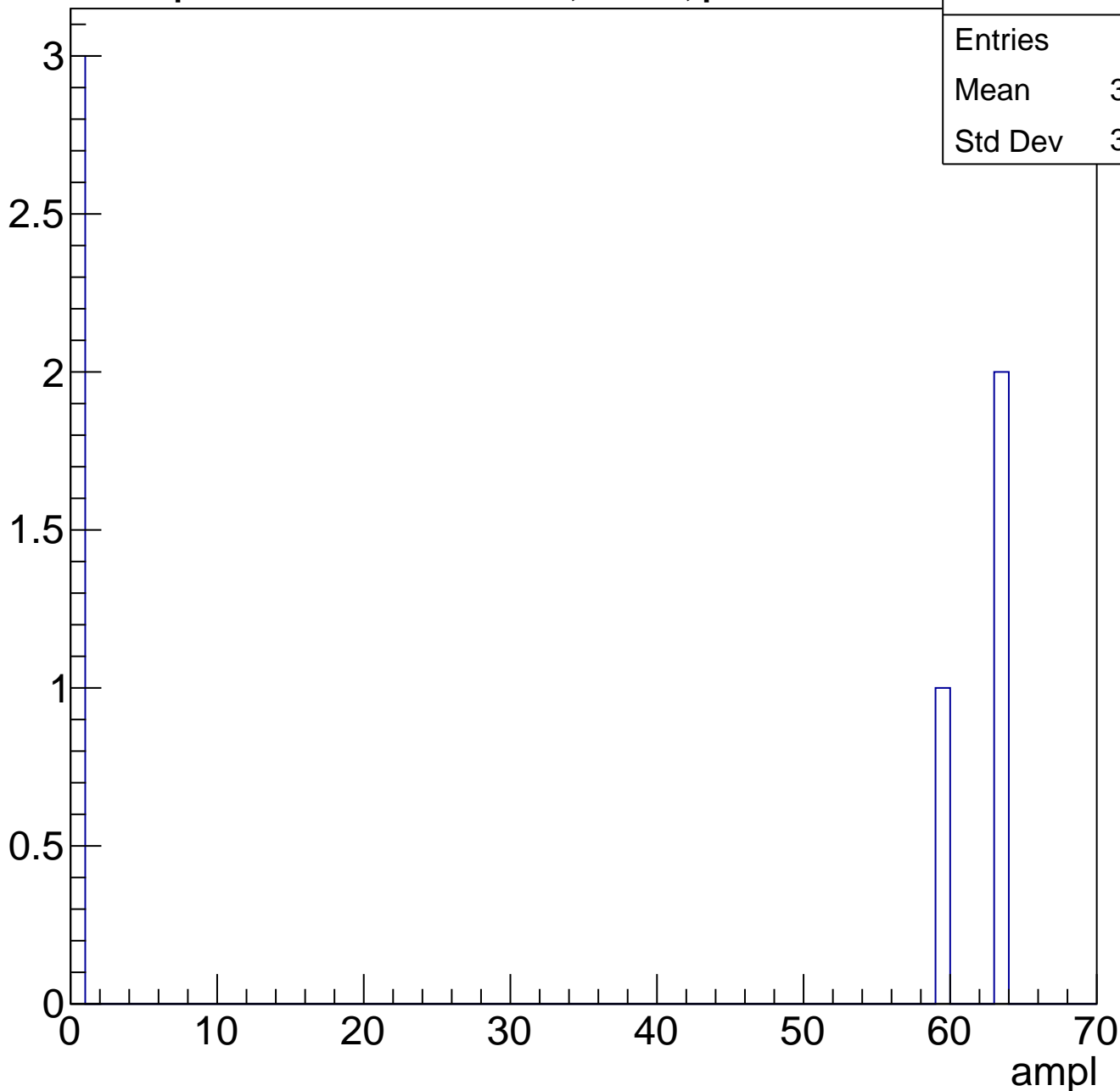
Entries	36
Mean	57.53
Std Dev	14.09



B1L002S, U7-ch94, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	6
Mean	30.83
Std Dev	30.86

B1L002S, U7-ch95, adc0

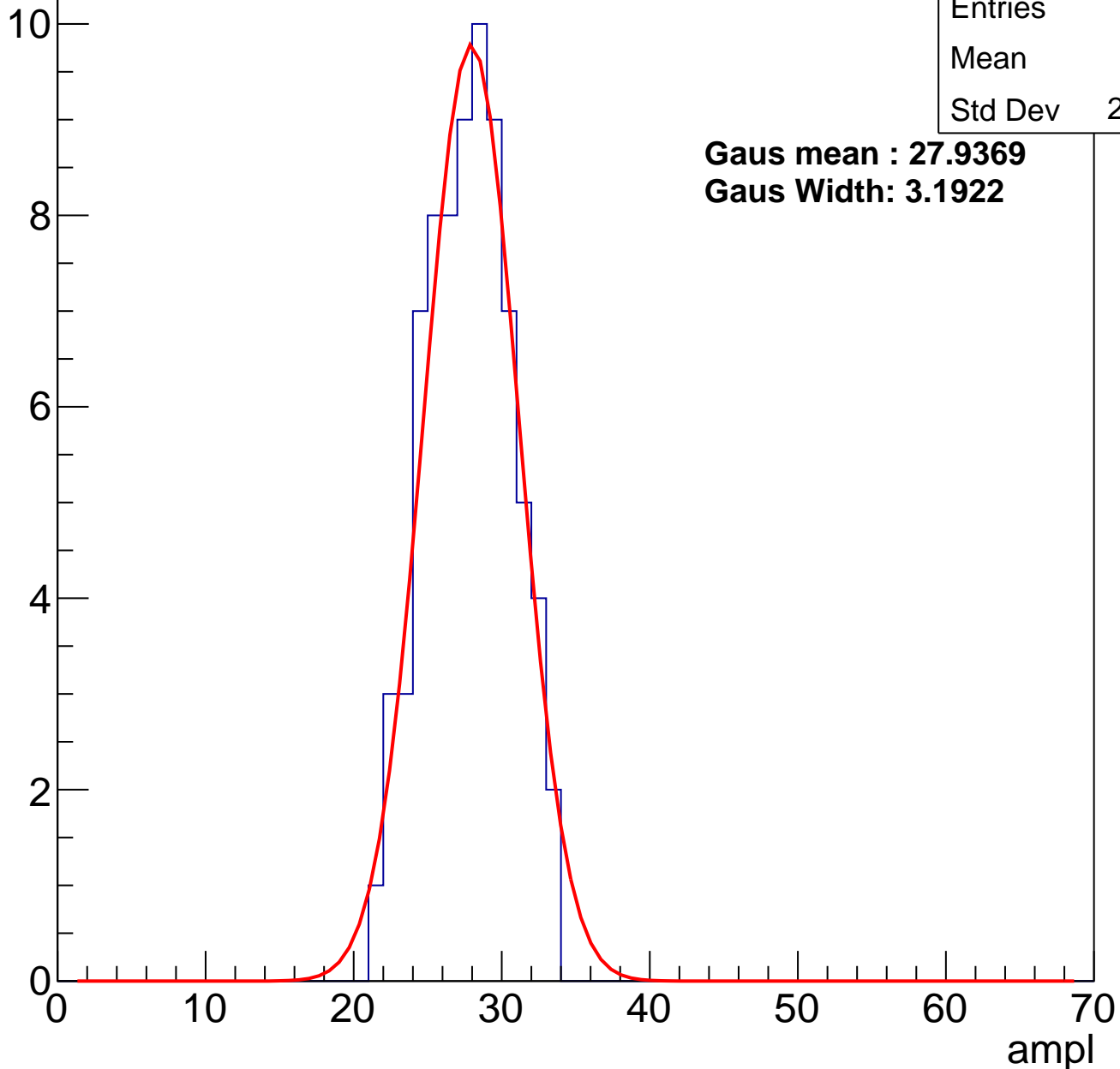
calib_packv5_042523_0143.root, FC#10, port B3

Entries	76
Mean	27.3
Std Dev	2.847

Gaus mean : 27.9369

Gaus Width: 3.1922

Entry



B1L002S, U7-ch95, adc1

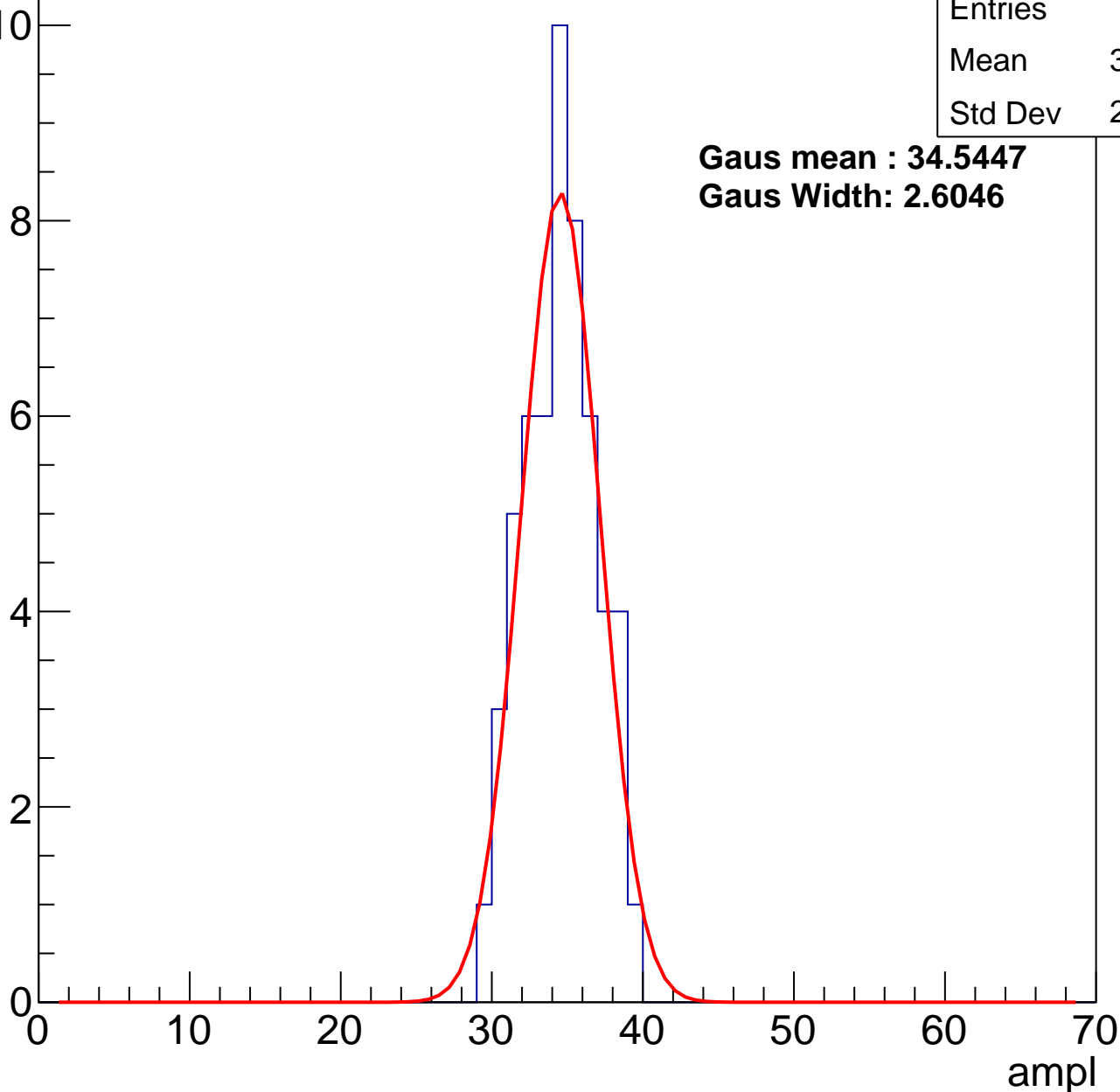
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.06
Std Dev	2.376

Gaus mean : 34.5447

Gaus Width: 2.6046



B1L002S, U7-ch95, adc2

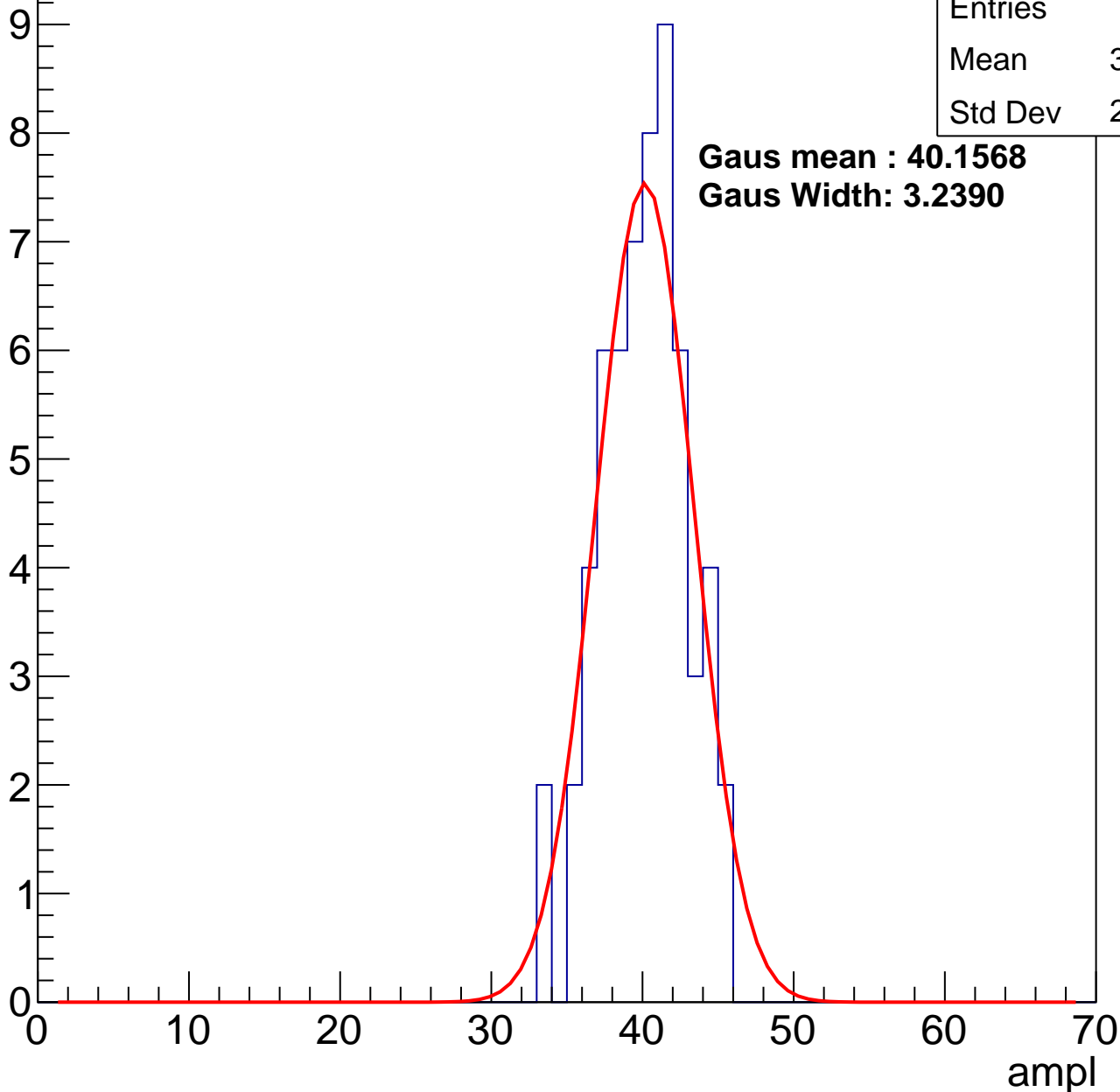
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	39.64
Std Dev	2.803

Gaus mean : 40.1568

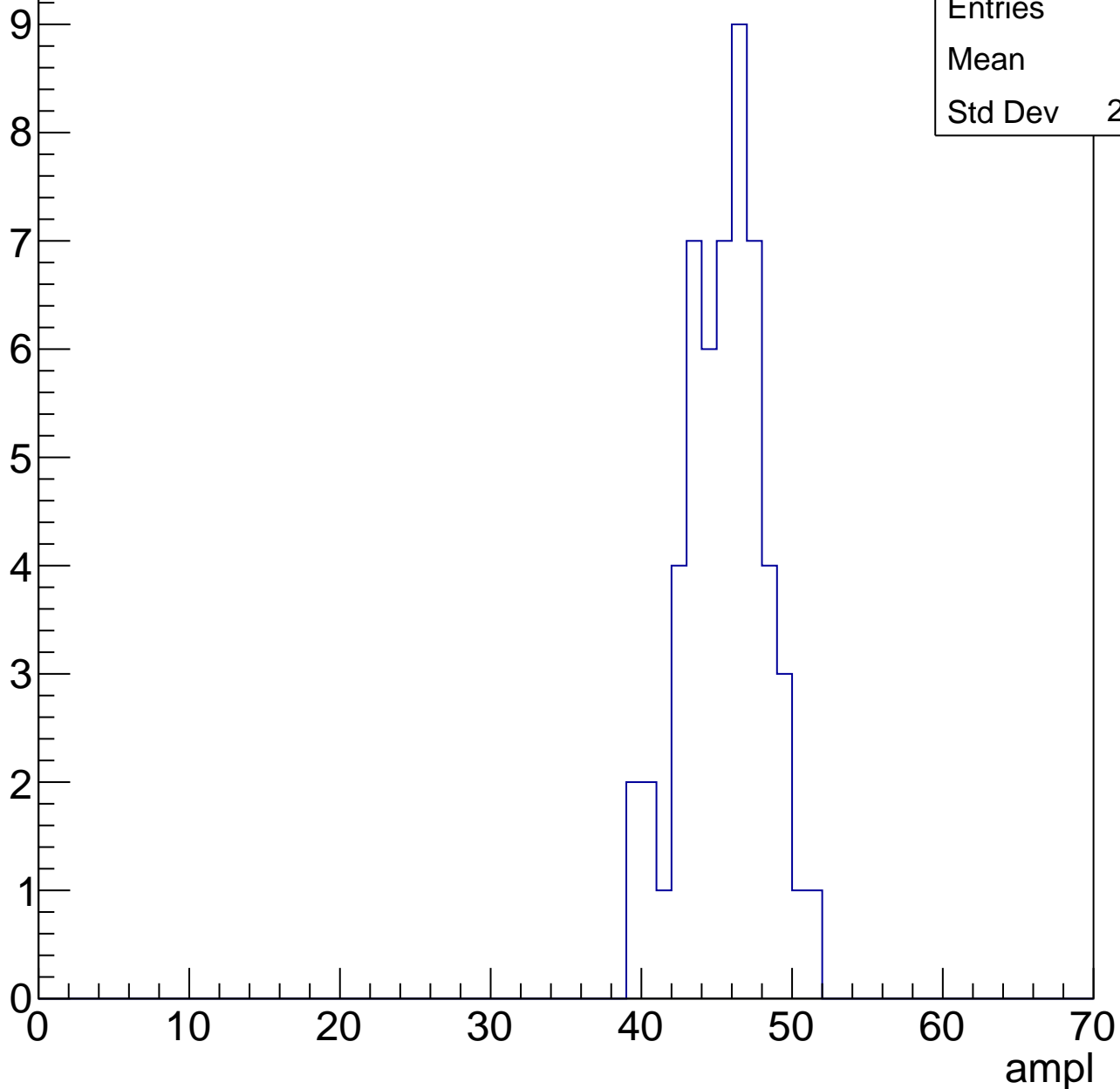
Gaus Width: 3.2390



B1L002S, U7-ch95, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

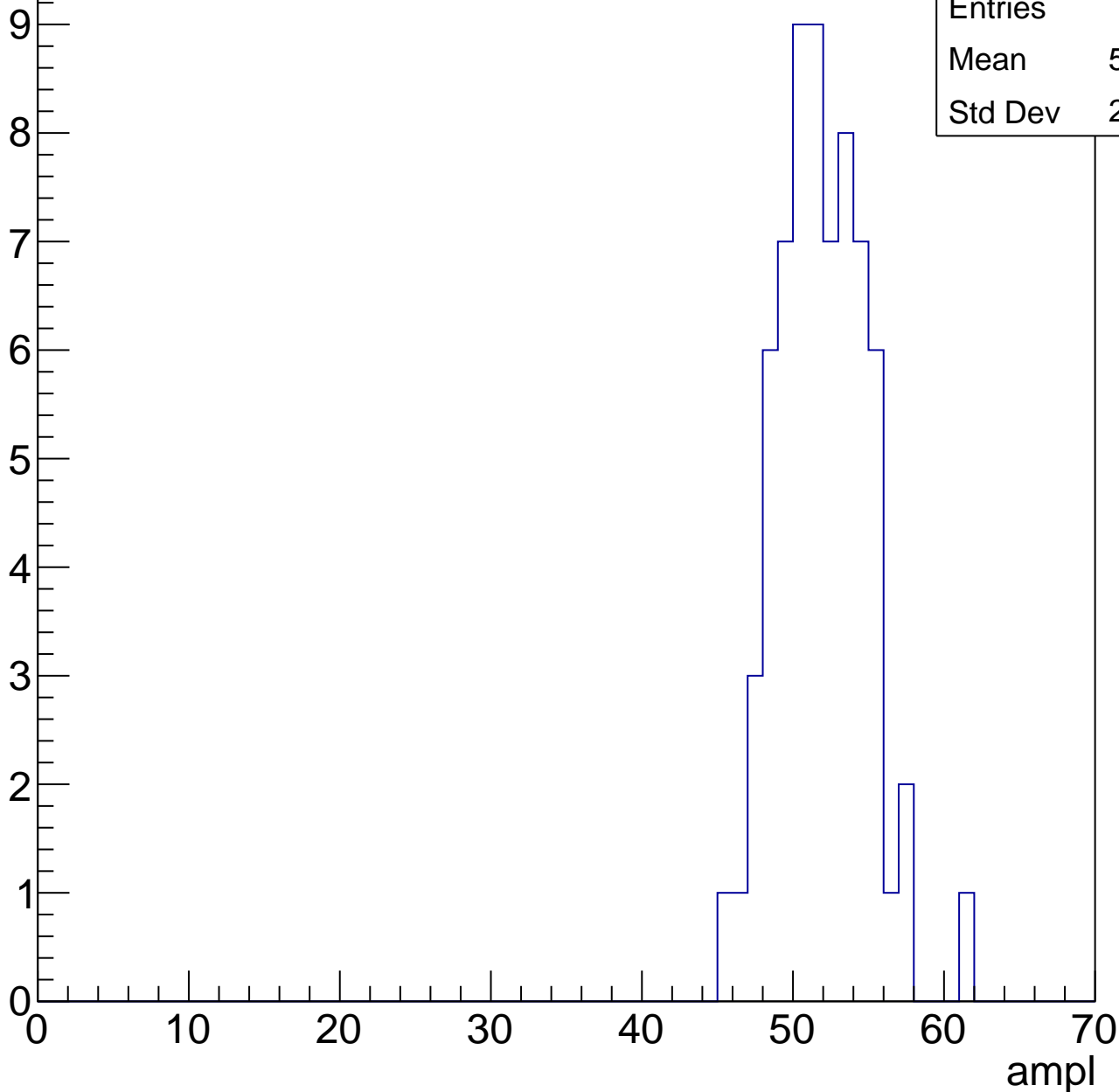


B1L002S, U7-ch95, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	51.46
Std Dev	2.923

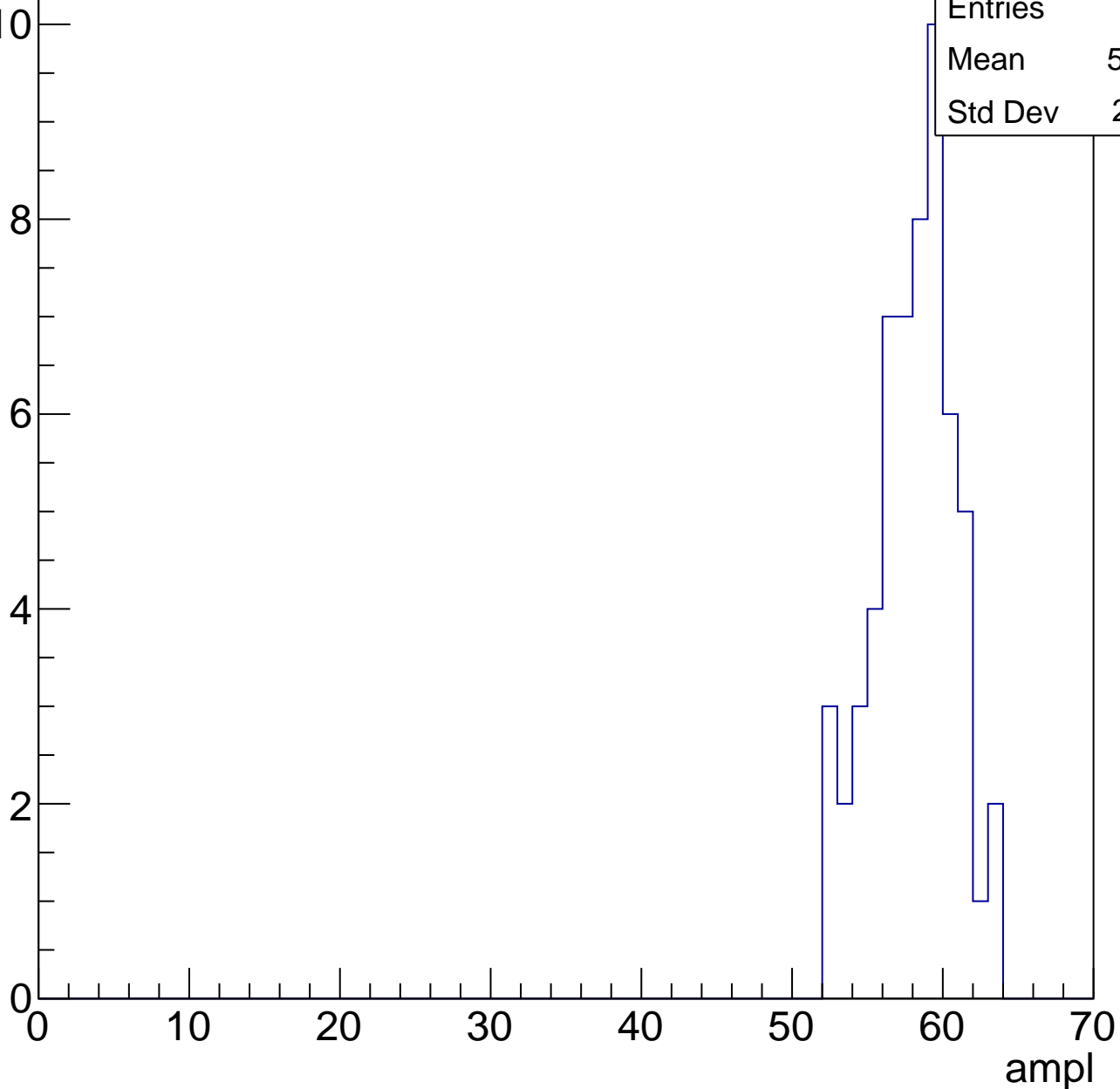


B1L002S, U7-ch95, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.62
Std Dev	2.671

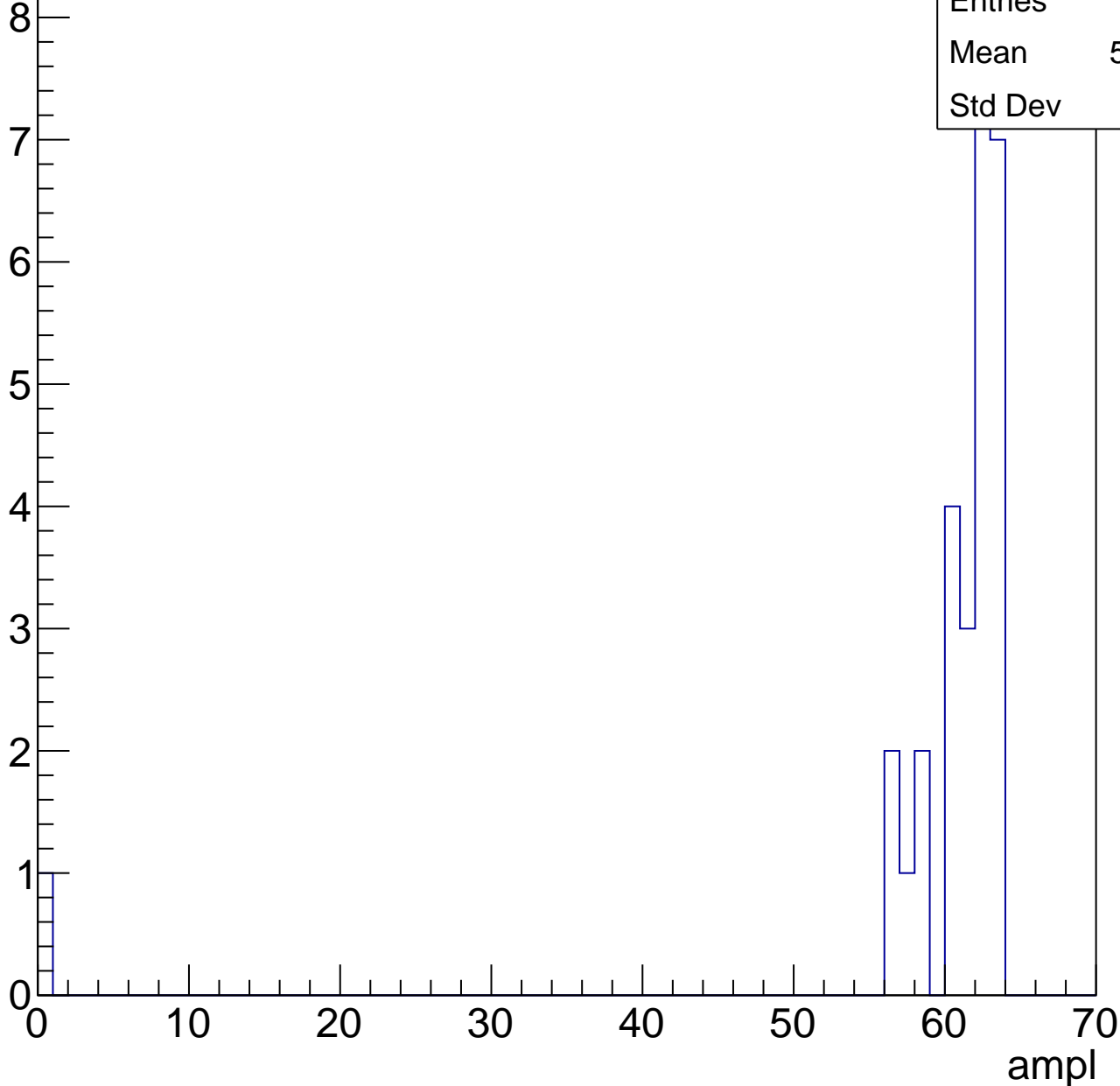


B1L002S, U7-ch95, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

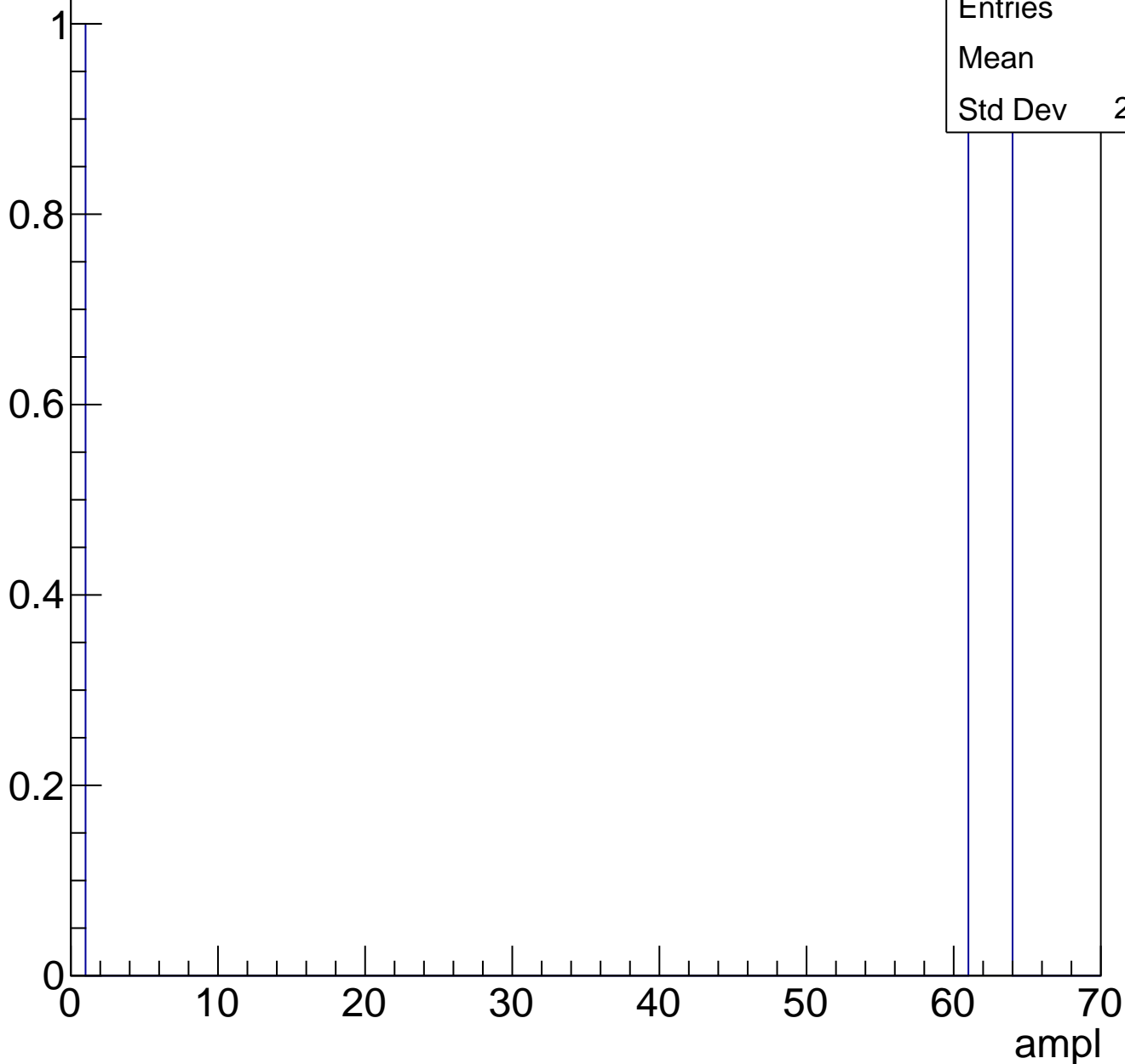
Entries	28
Mean	58.75
Std Dev	11.5



B1L002S, U7-ch95, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch96, adc0

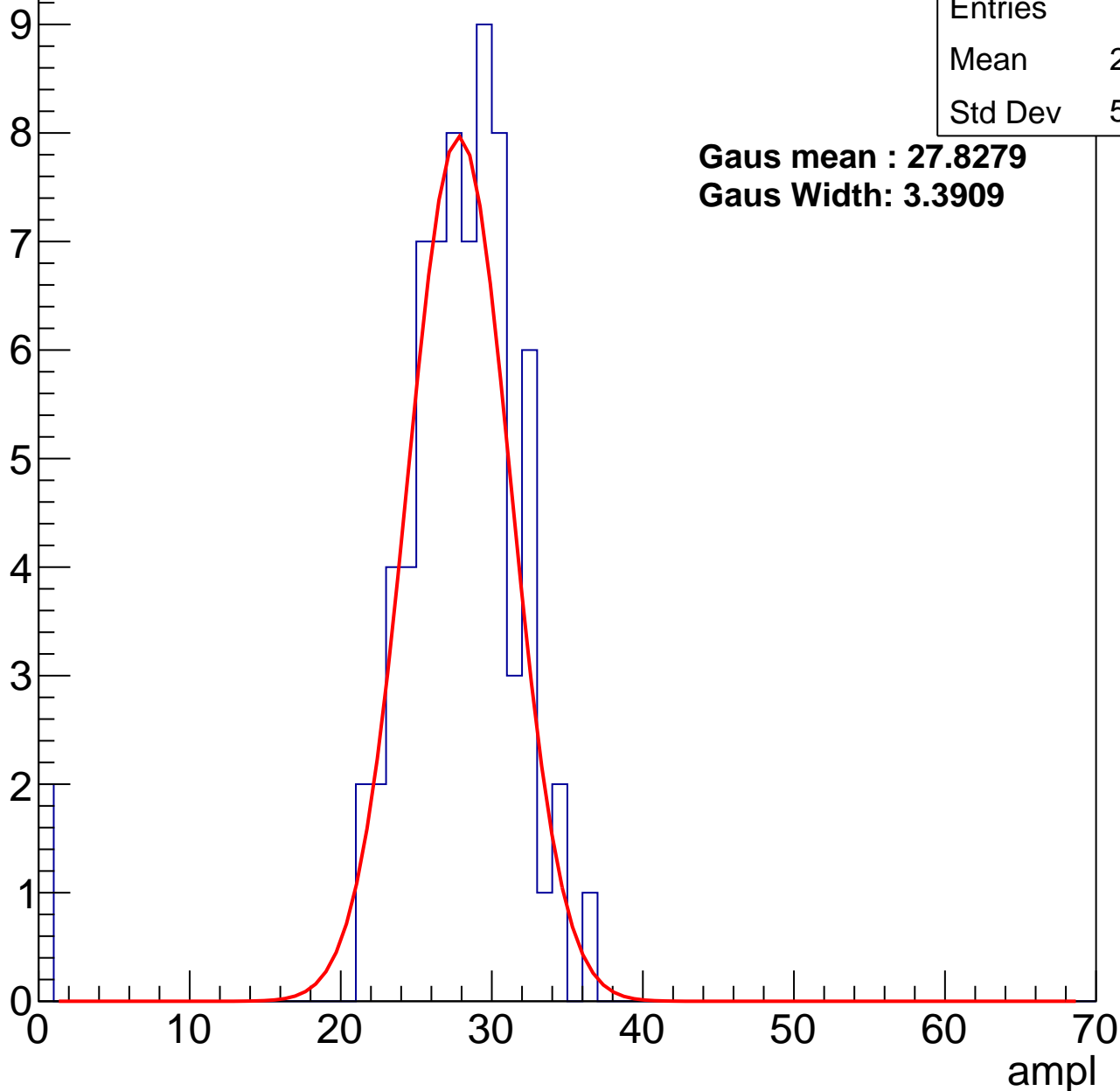
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	73
Mean	26.93
Std Dev	5.543

Gaus mean : 27.8279

Gaus Width: 3.3909



B1L002S, U7-ch96, adc1

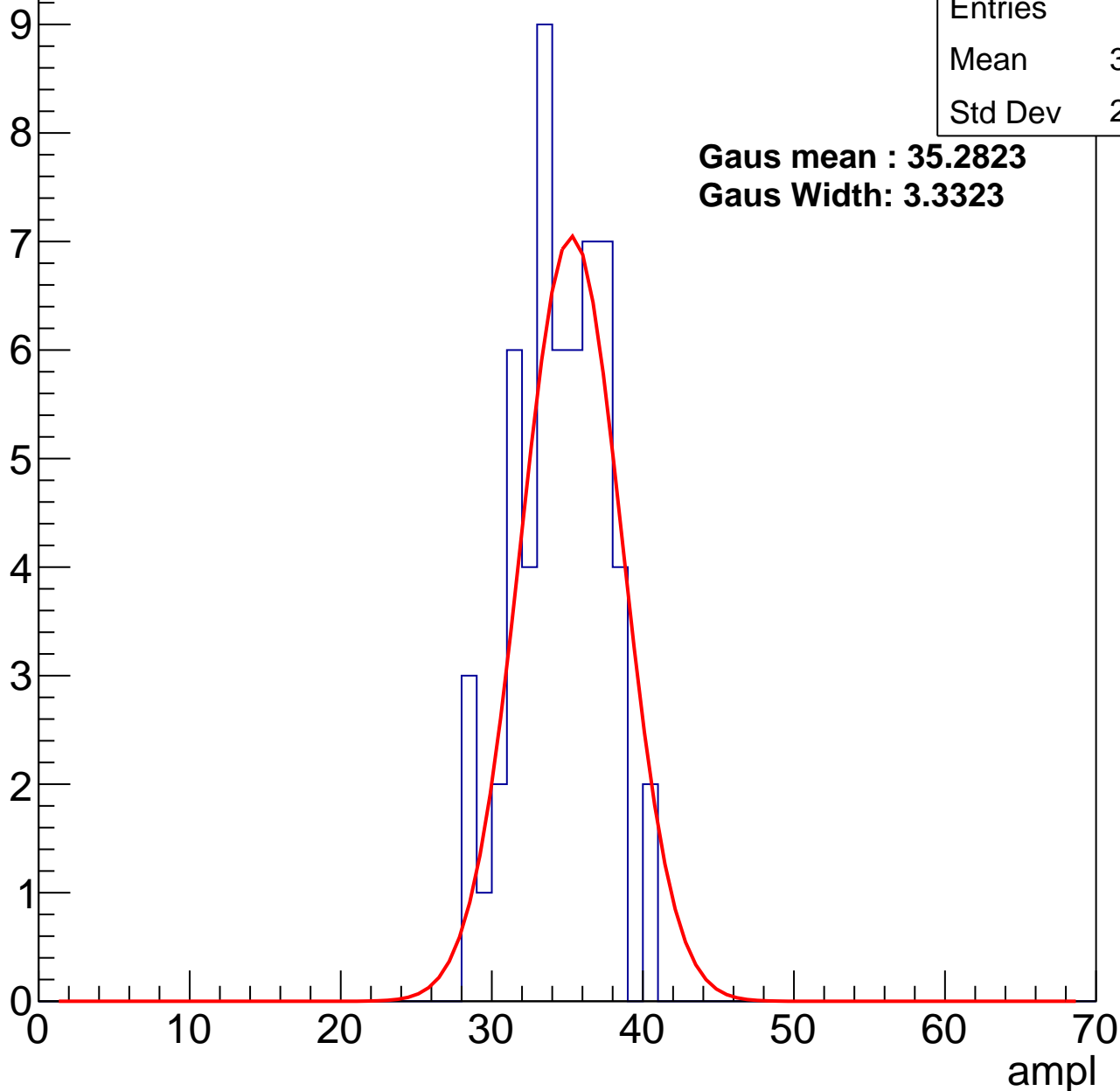
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.05
Std Dev	2.892

Gaus mean : 35.2823

Gaus Width: 3.3323



B1L002S, U7-ch96, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	40.22
Std Dev	3.519

Gaus mean : 40.6876

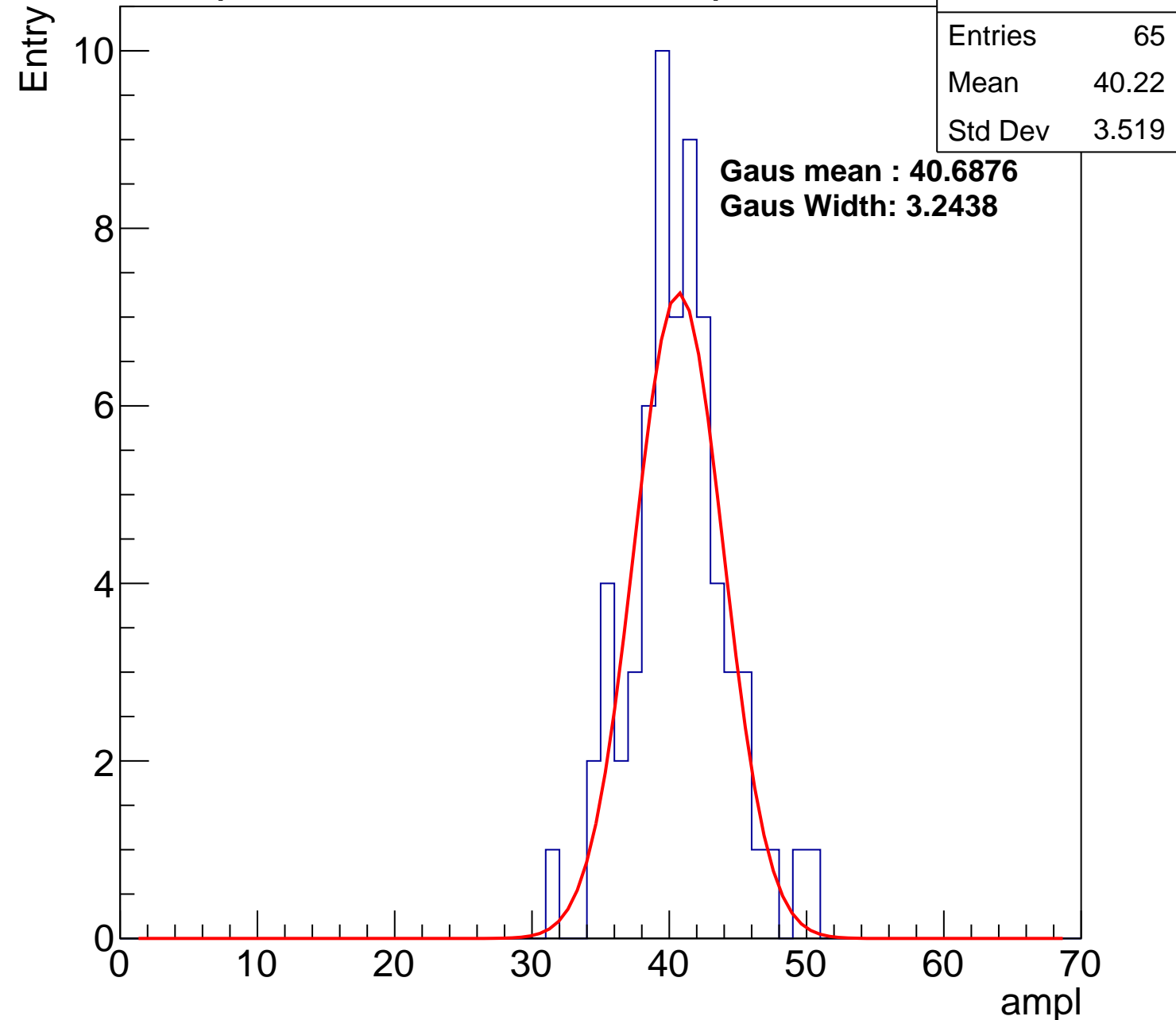
Gaus Width: 3.2438

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

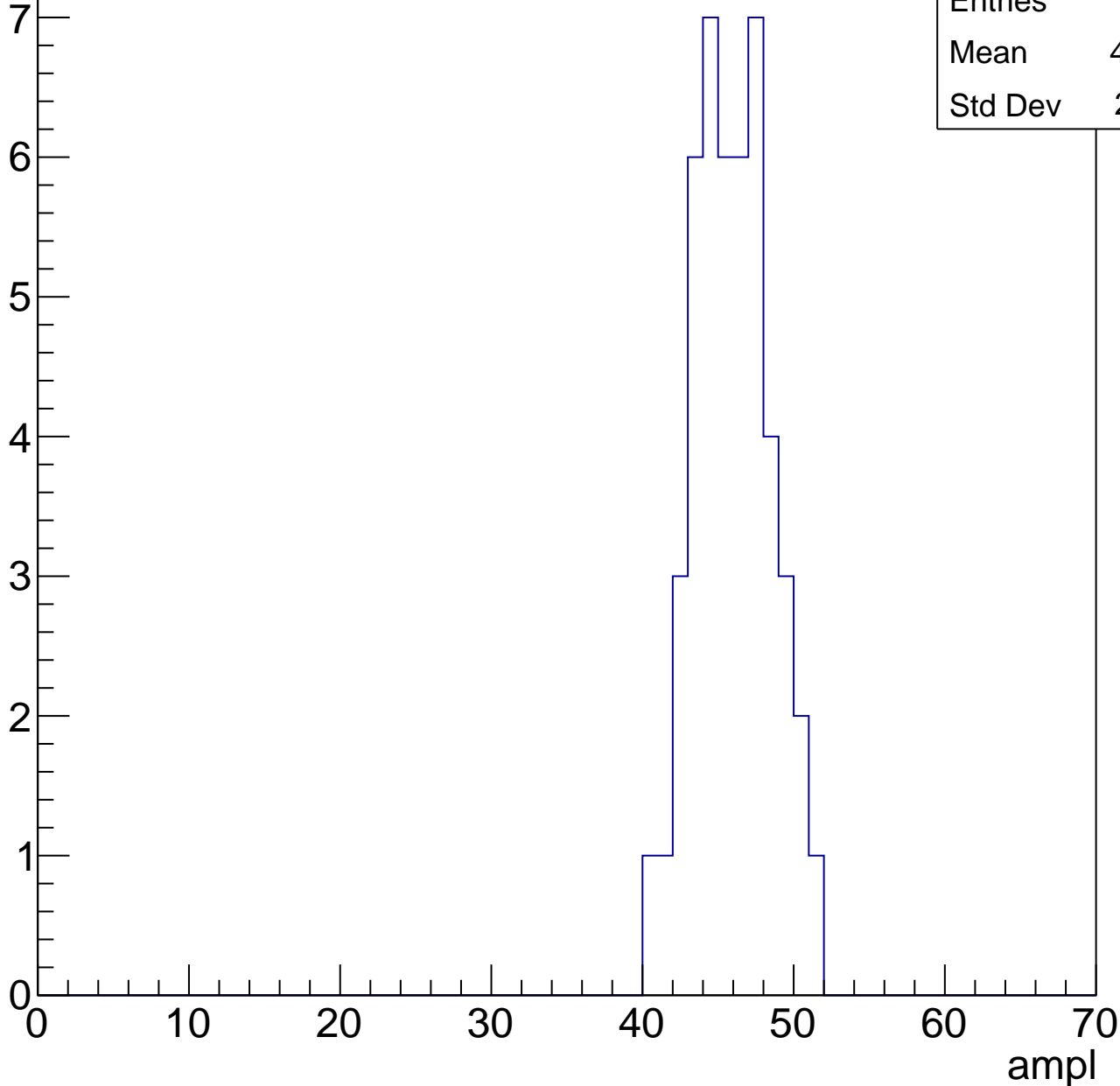


B1L002S, U7-ch96, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	45.49
Std Dev	2.491

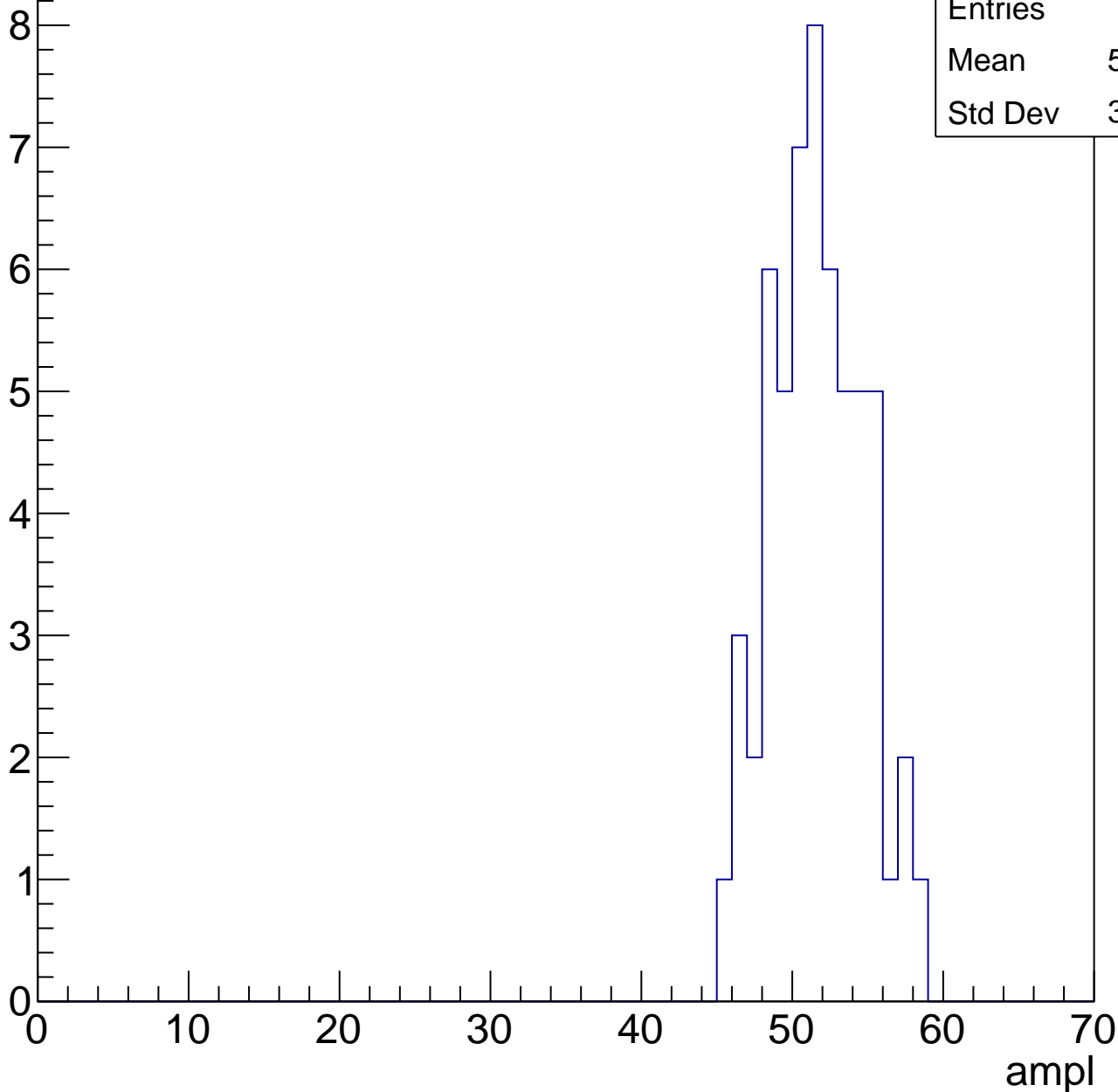


B1L002S, U7-ch96, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	51.19
Std Dev	3.017

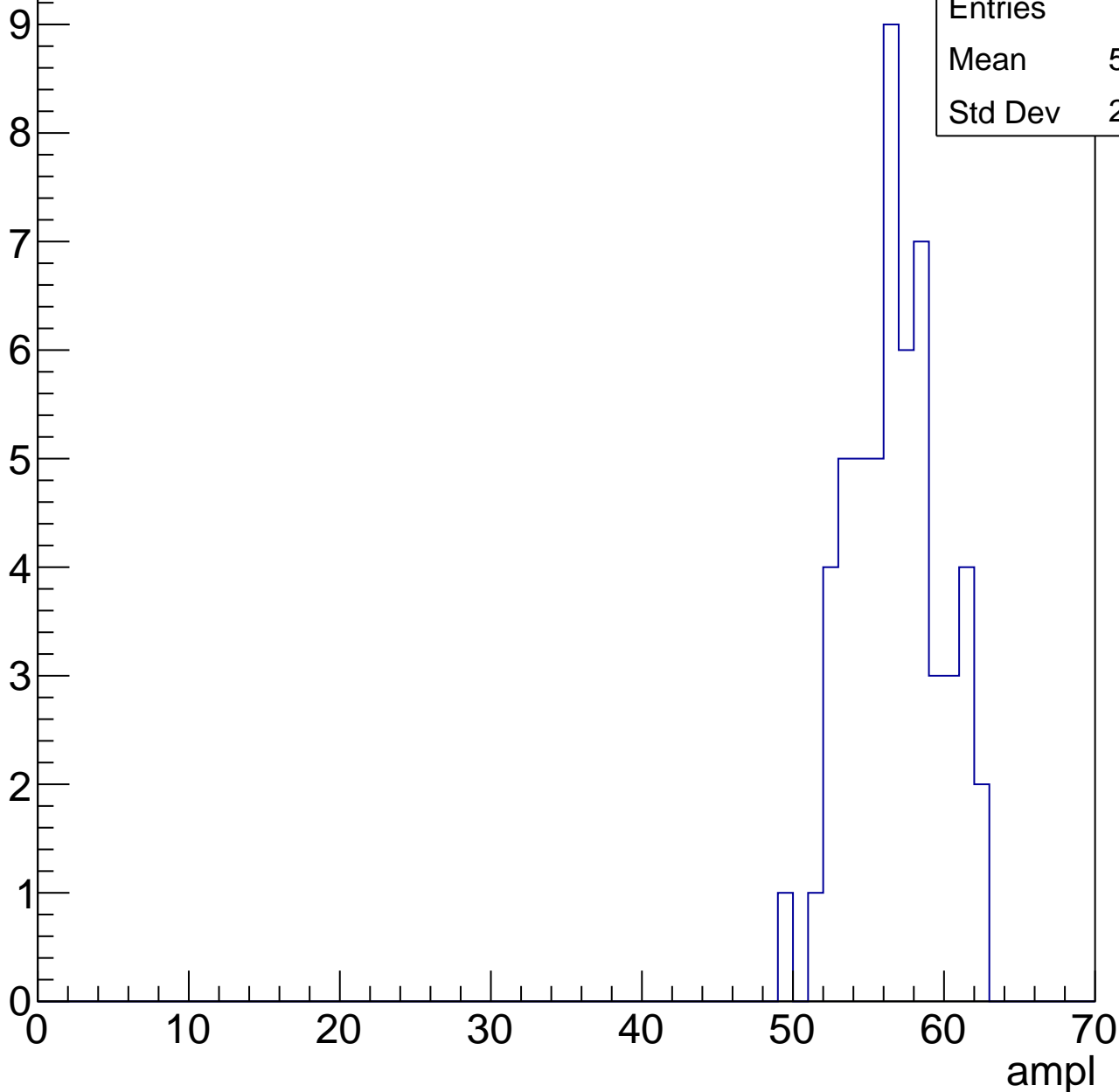


B1L002S, U7-ch96, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

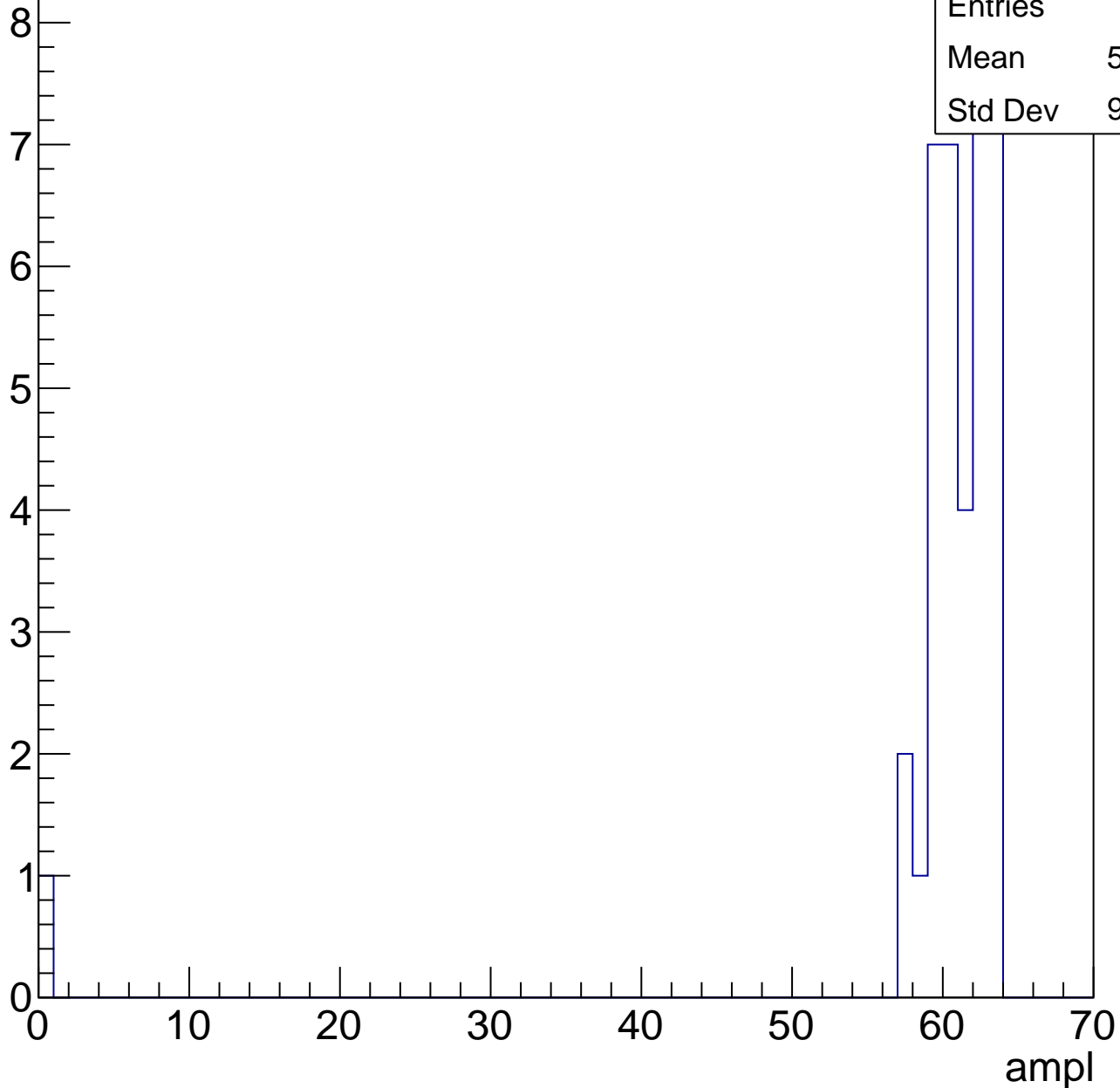
Entries	55
Mean	56.27
Std Dev	2.969



B1L002S, U7-ch96, adc6

calib_packv5_042523_0143.root, FC#10, port B3

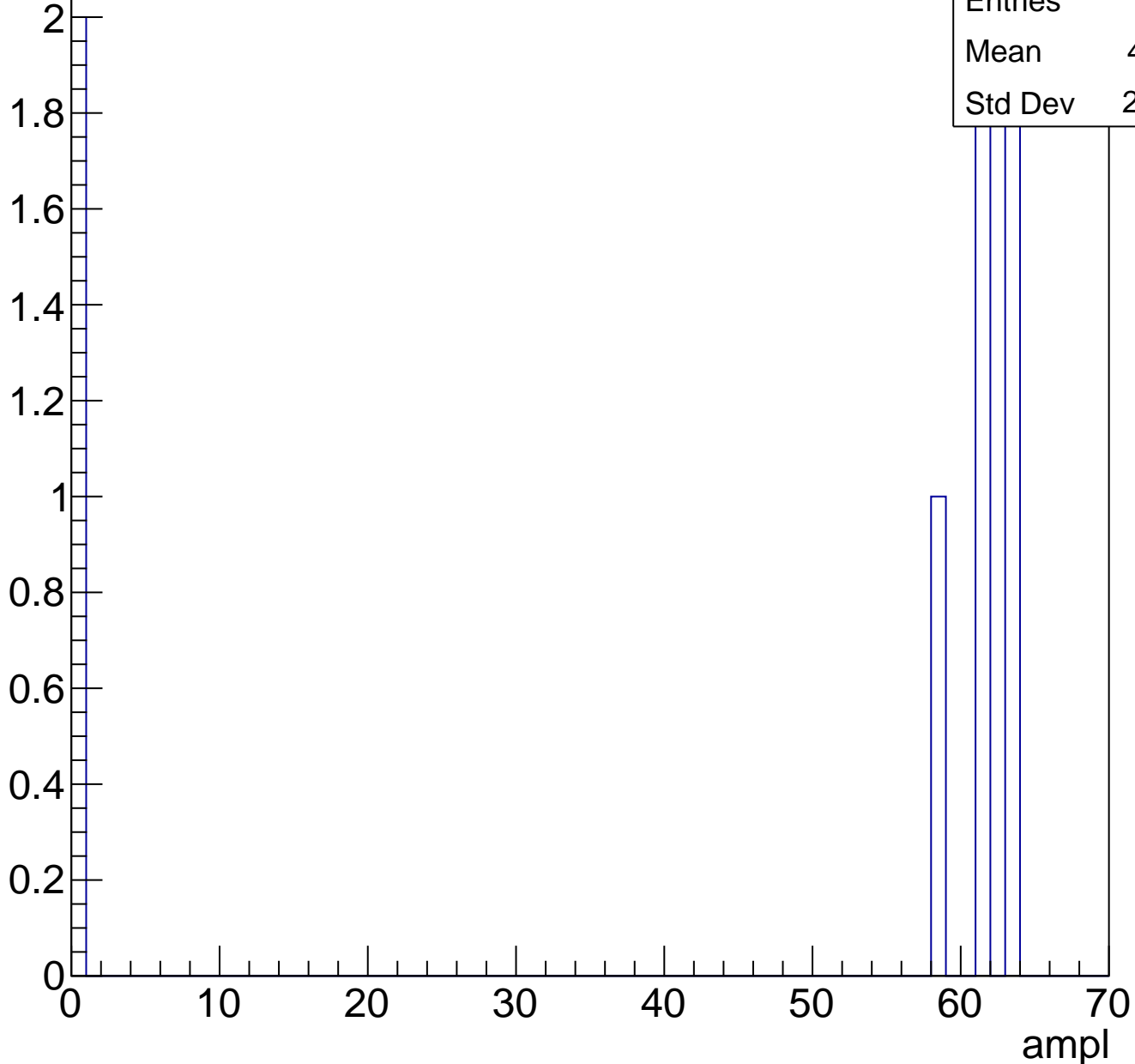
Entry



B1L002S, U7-ch96, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	7
Mean	43.71
Std Dev	27.69

B1L002S, U7-ch97, adc0

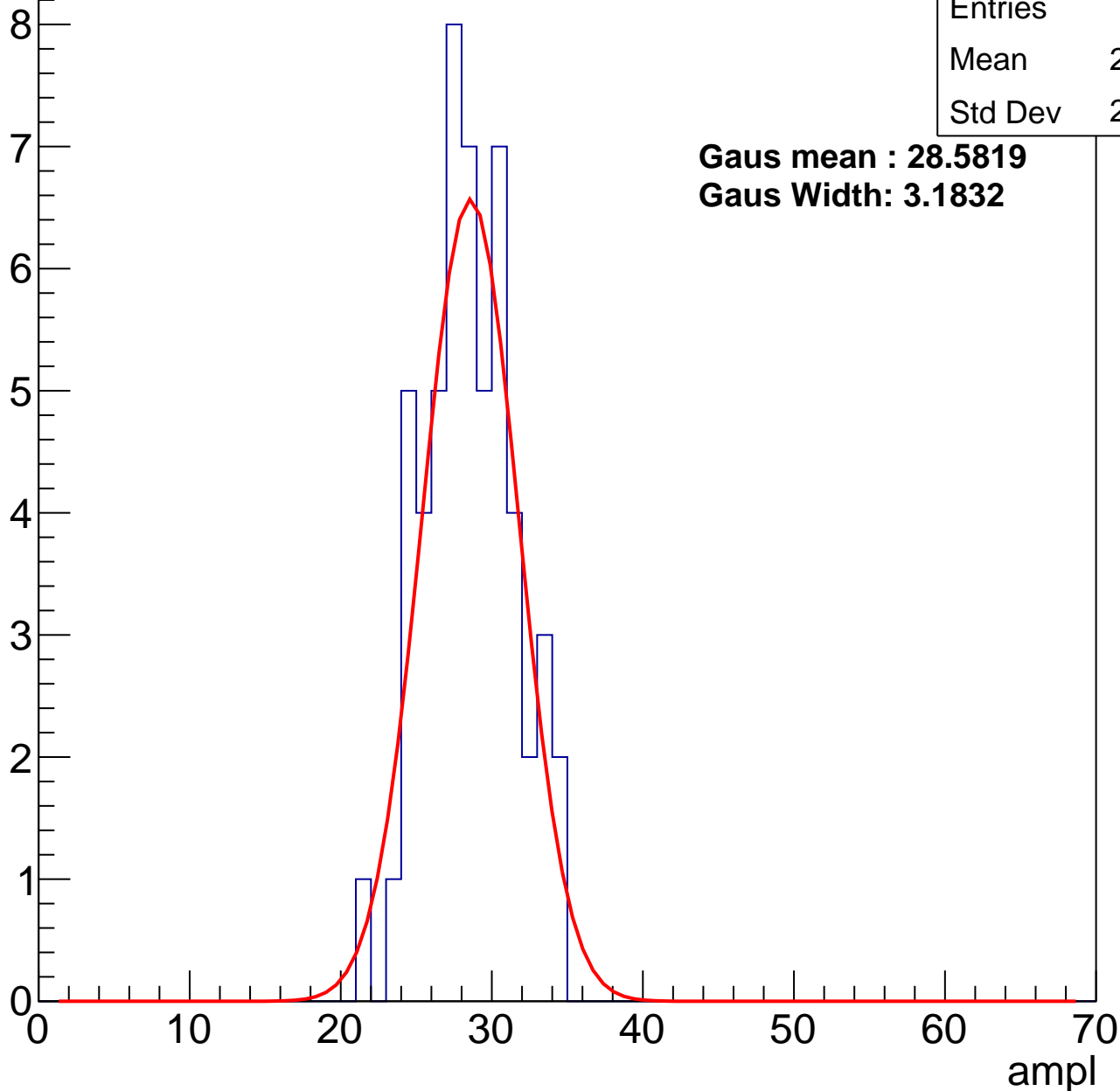
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.07
Std Dev	2.937

Gaus mean : 28.5819

Gaus Width: 3.1832



B1L002S, U7-ch97, adc1

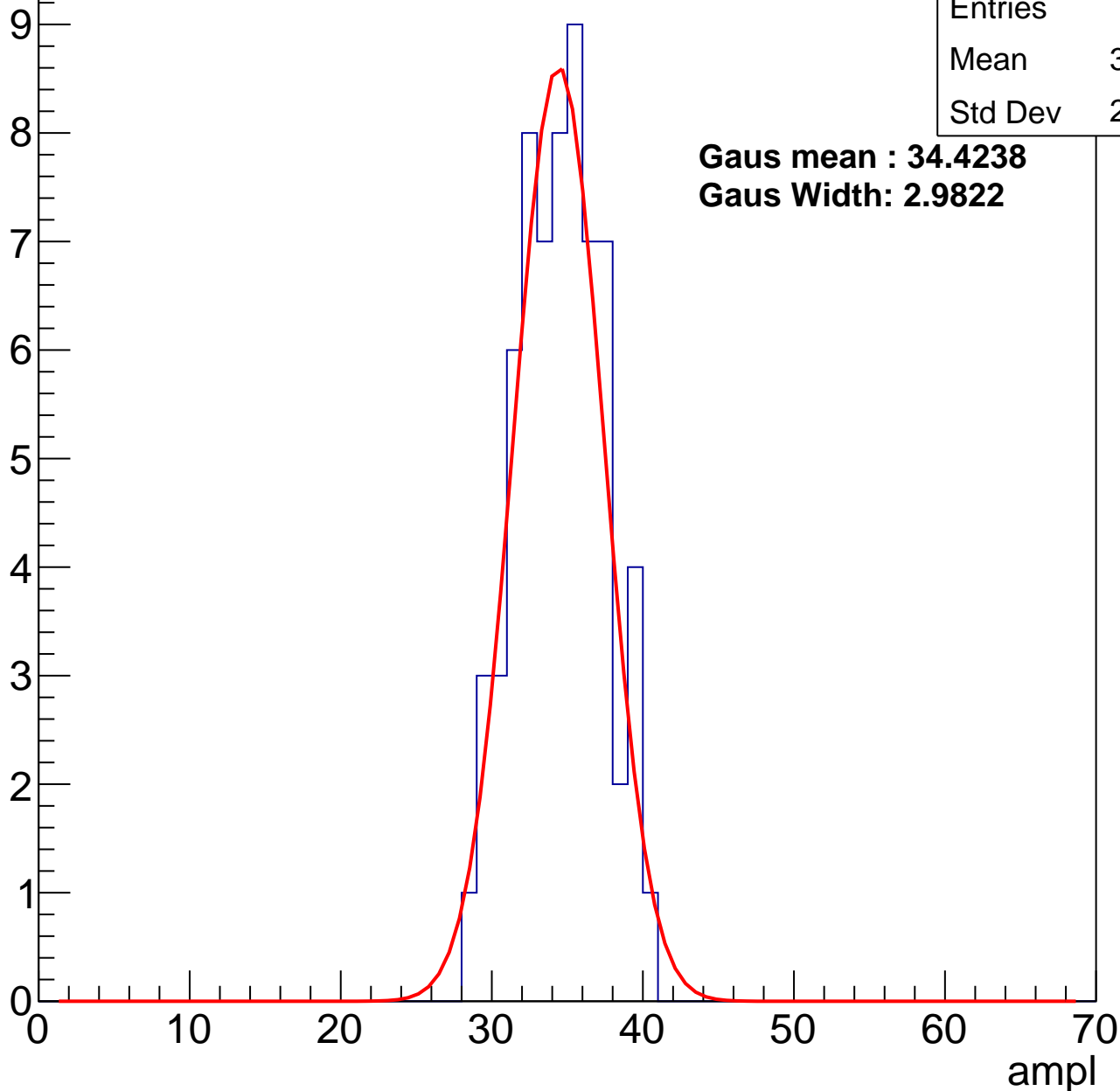
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.06
Std Dev	2.806

Gaus mean : 34.4238

Gaus Width: 2.9822



B1L002S, U7-ch97, adc2

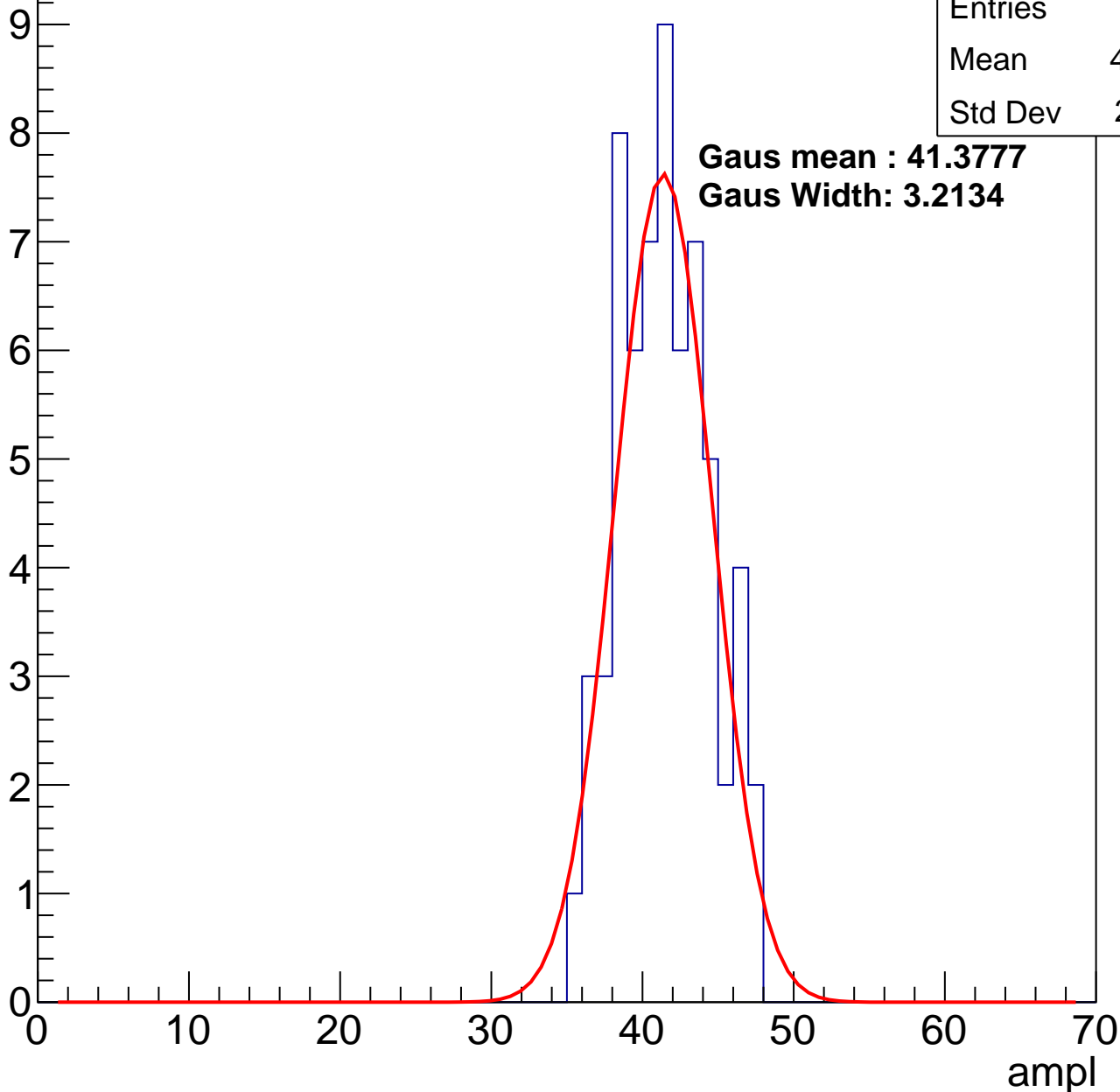
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.98
Std Dev	2.941

Gaus mean : 41.3777

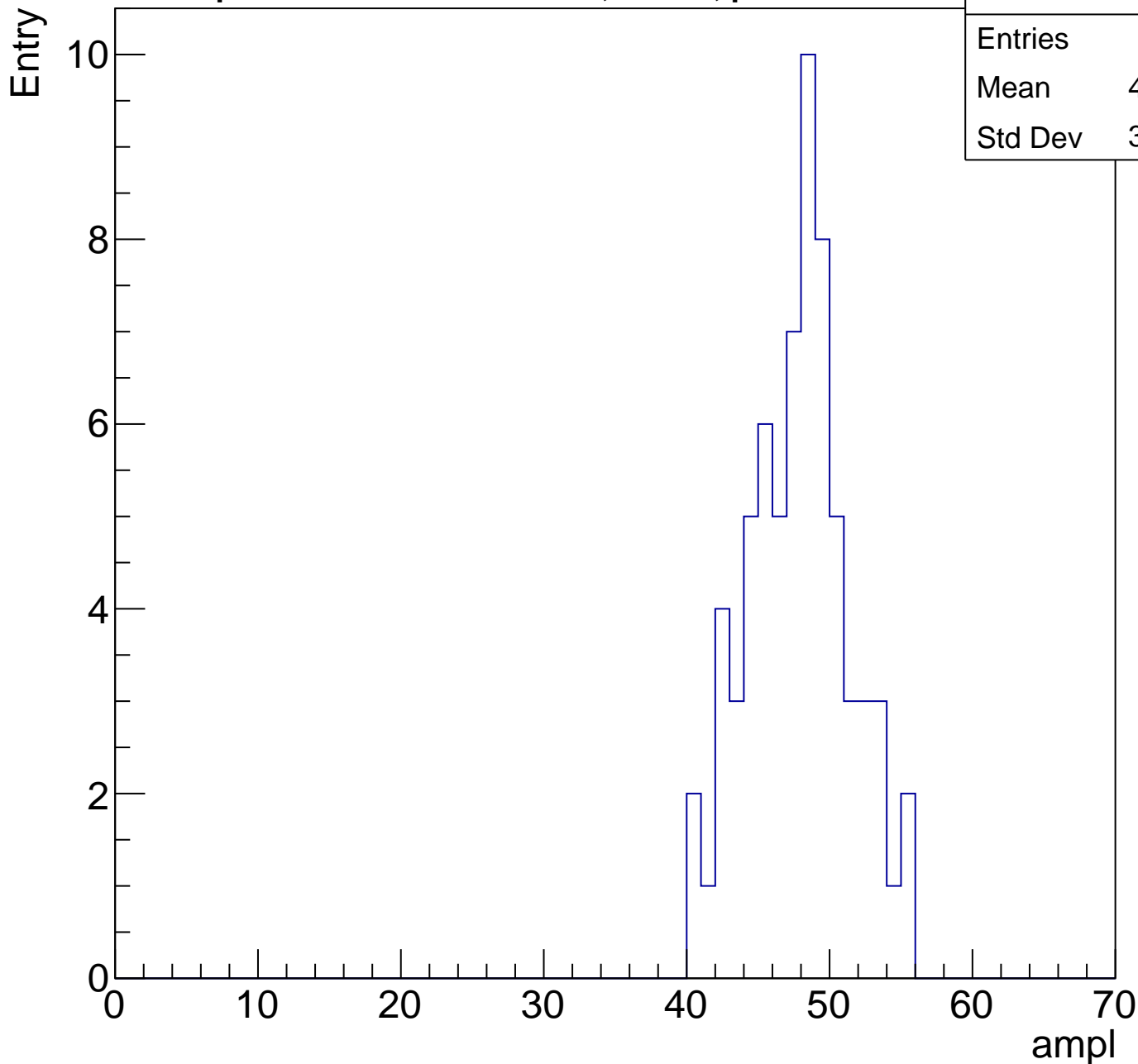
Gaus Width: 3.2134



B1L002S, U7-ch97, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	68
Mean	47.37
Std Dev	3.518

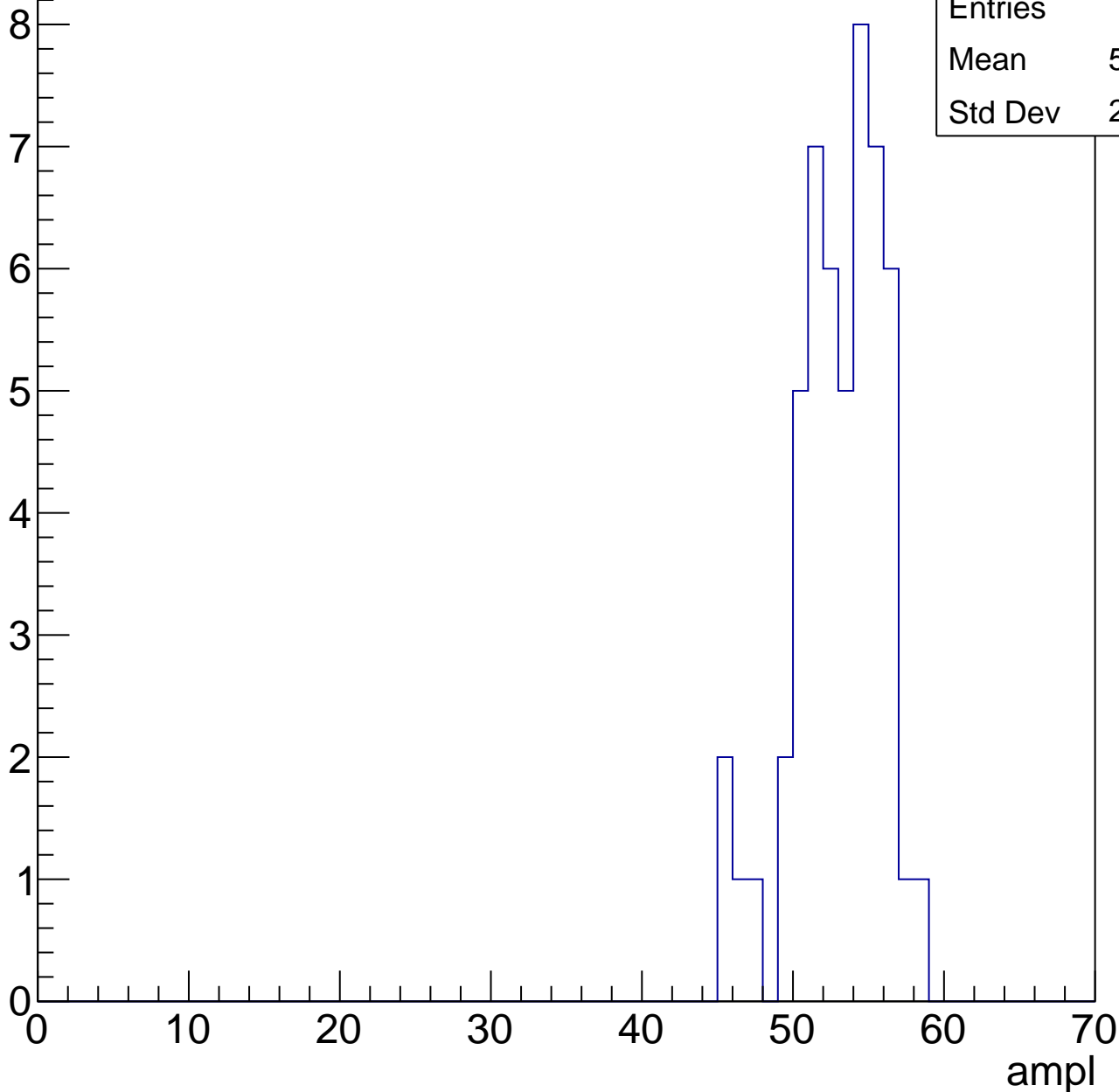


B1L002S, U7-ch97, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.56
Std Dev	2.925

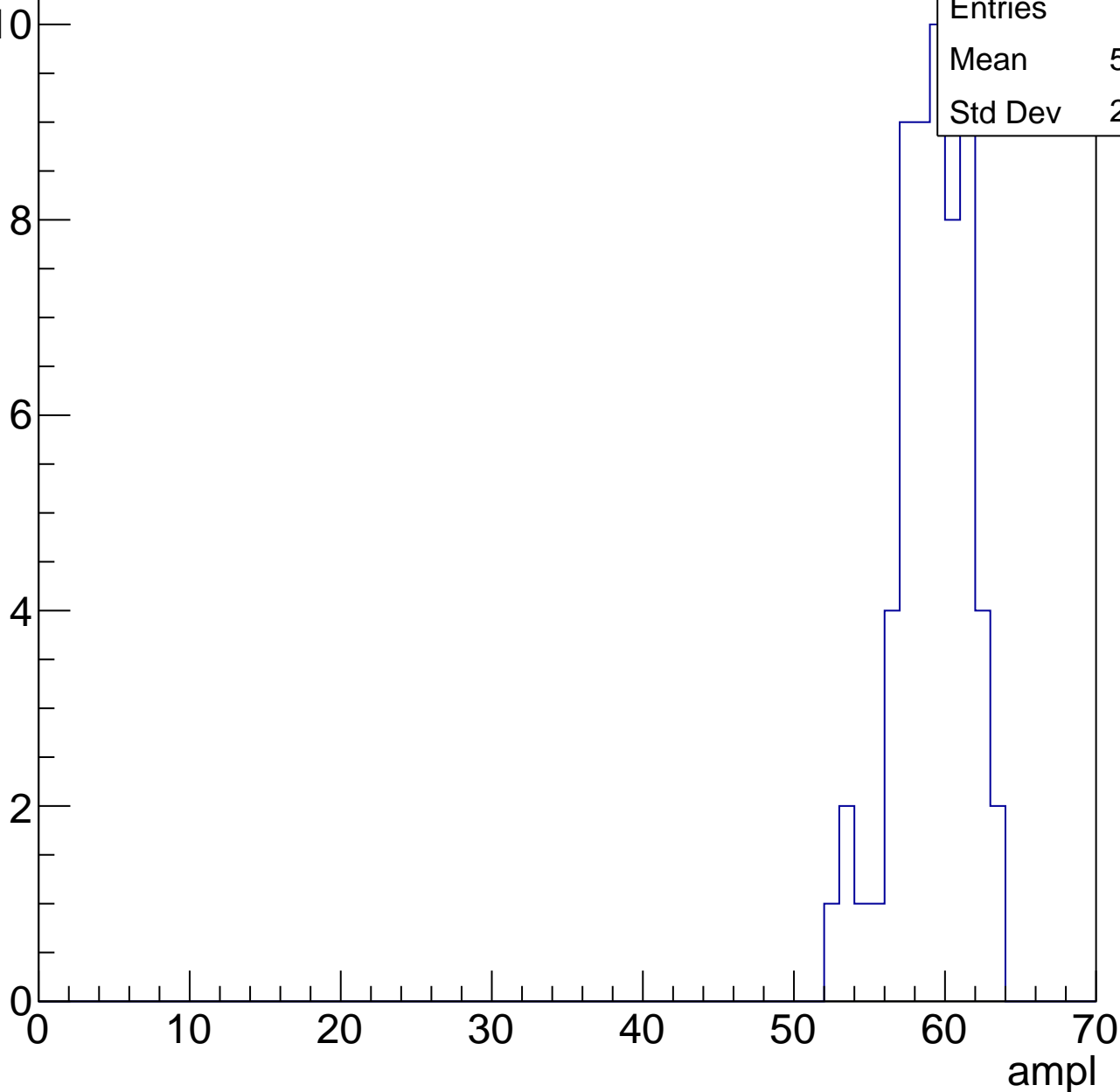


B1L002S, U7-ch97, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.65
Std Dev	2.407

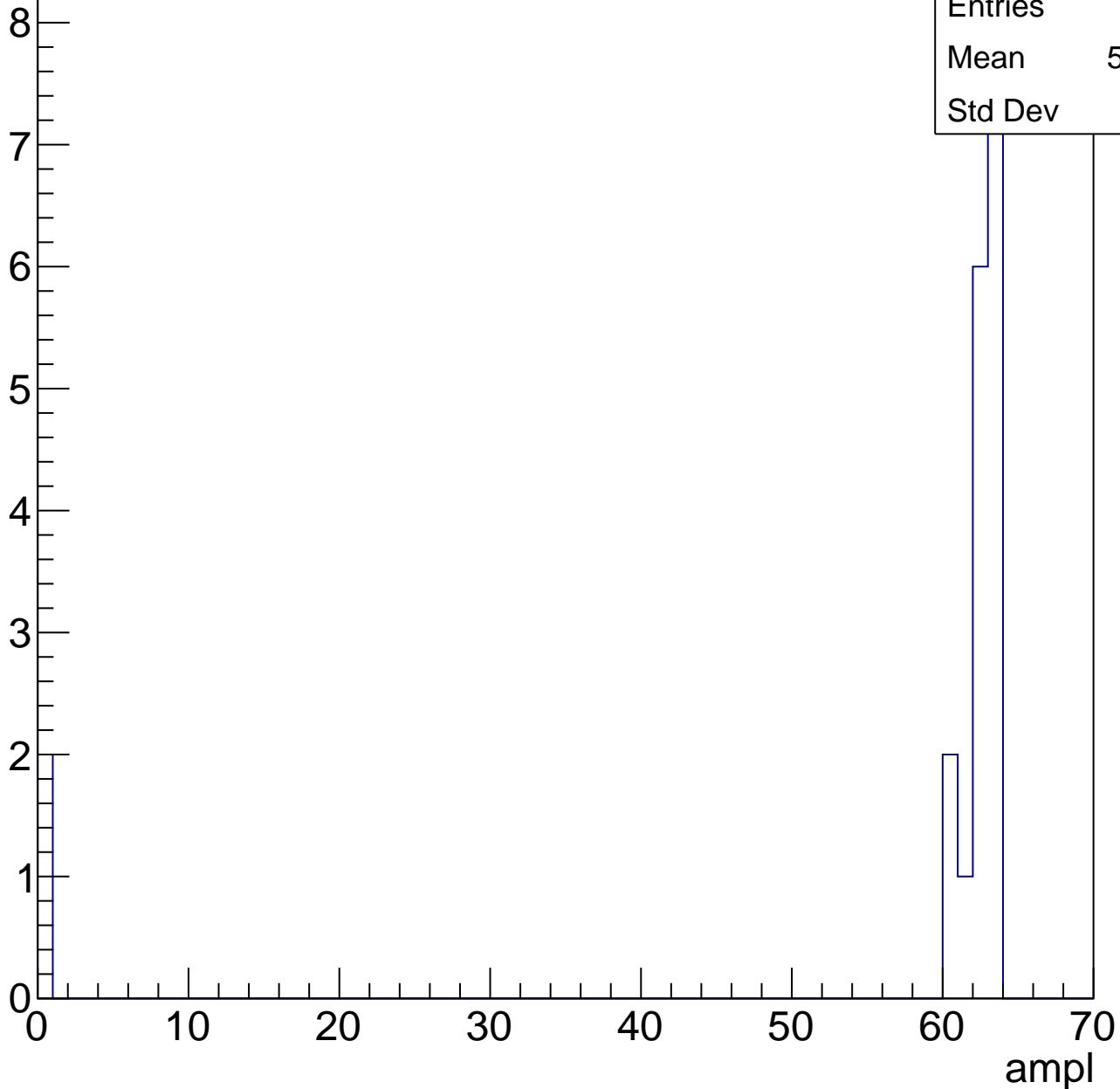


B1L002S, U7-ch97, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	55.63
Std Dev	19.1



B1L002S, U7-ch97, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch98, adc0

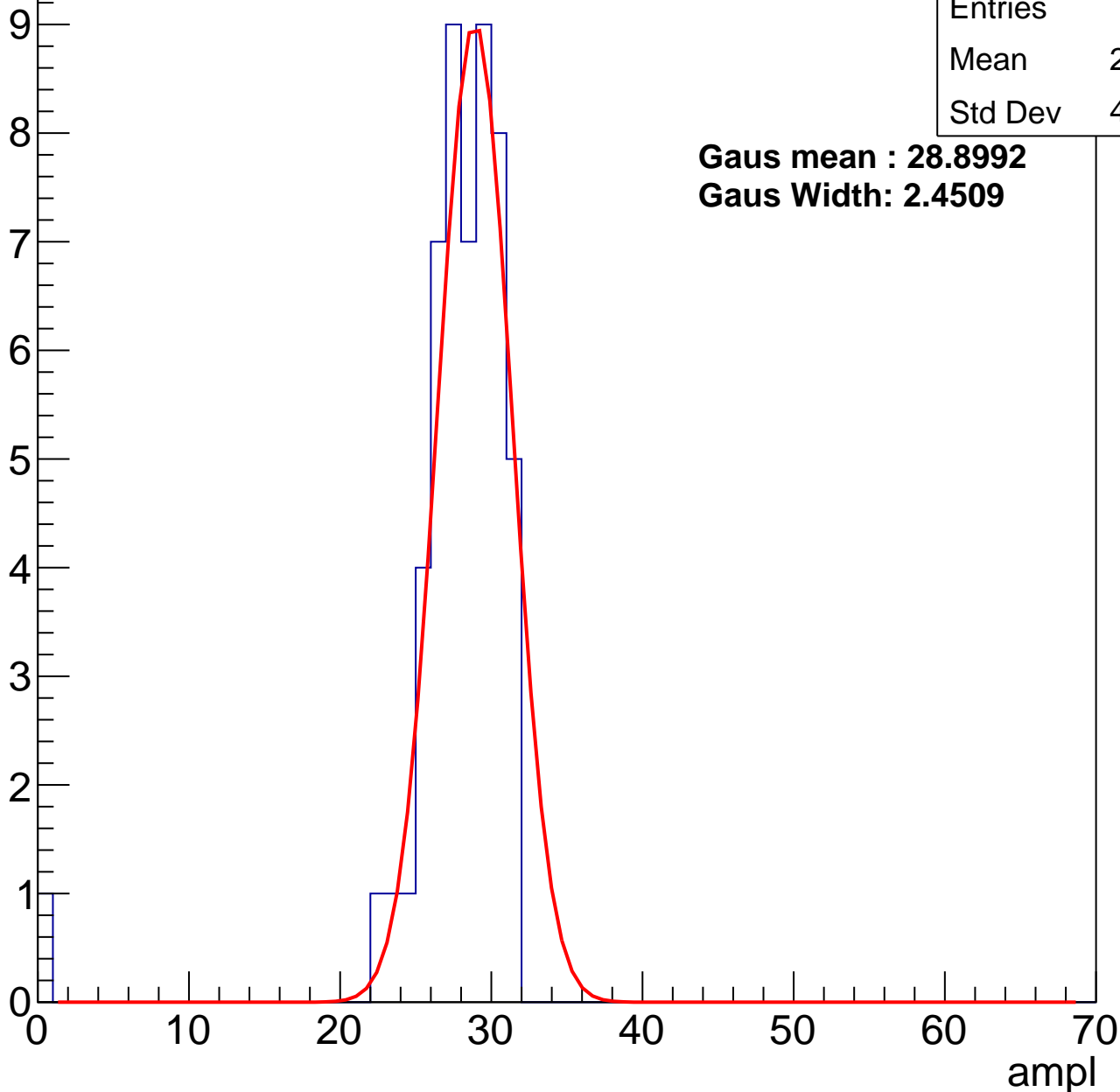
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	27.28
Std Dev	4.328

Gaus mean : 28.8992

Gaus Width: 2.4509



B1L002S, U7-ch98, adc1

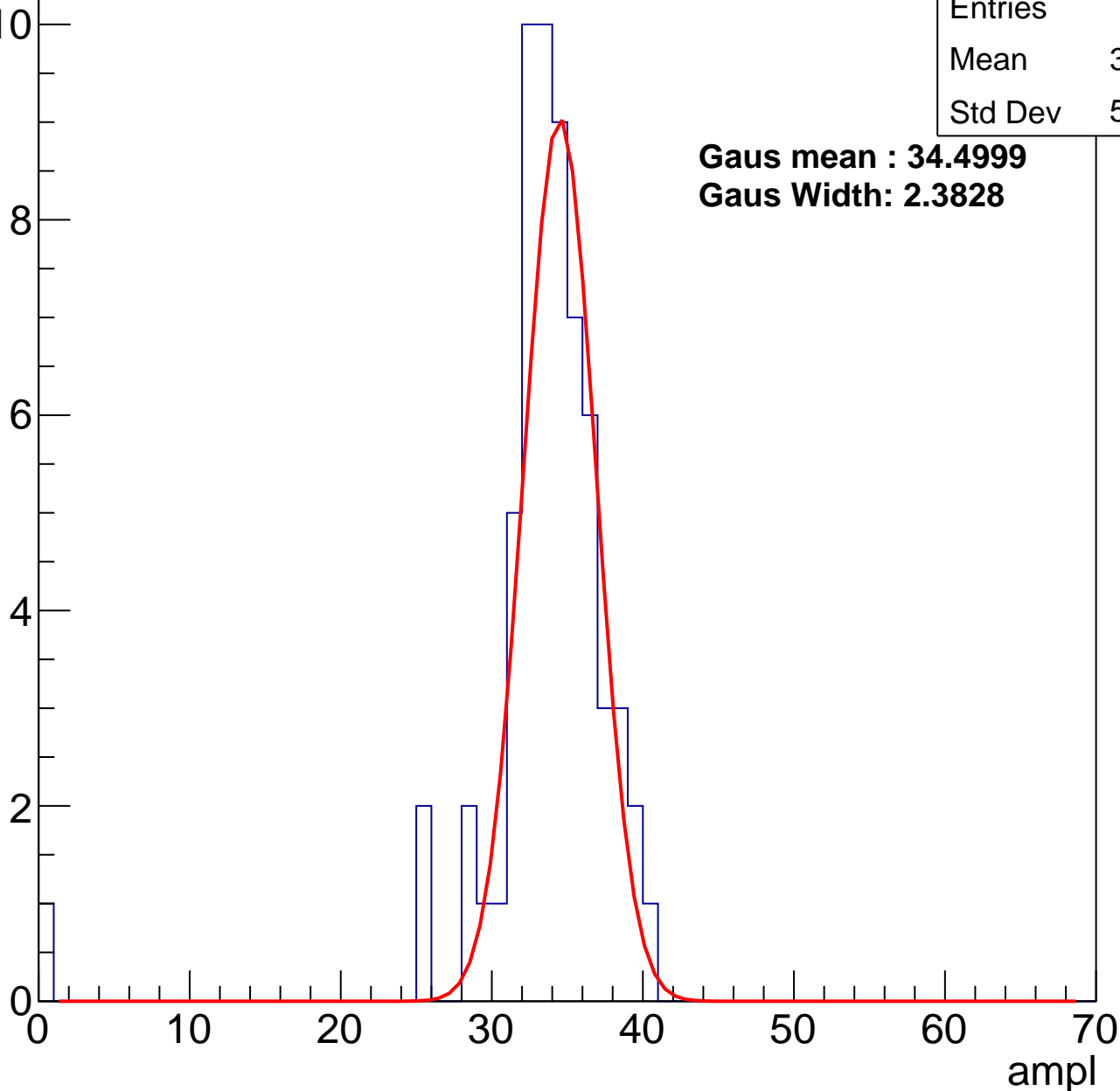
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.02
Std Dev	5.128

Gaus mean : 34.4999

Gaus Width: 2.3828



B1L002S, U7-ch98, adc2

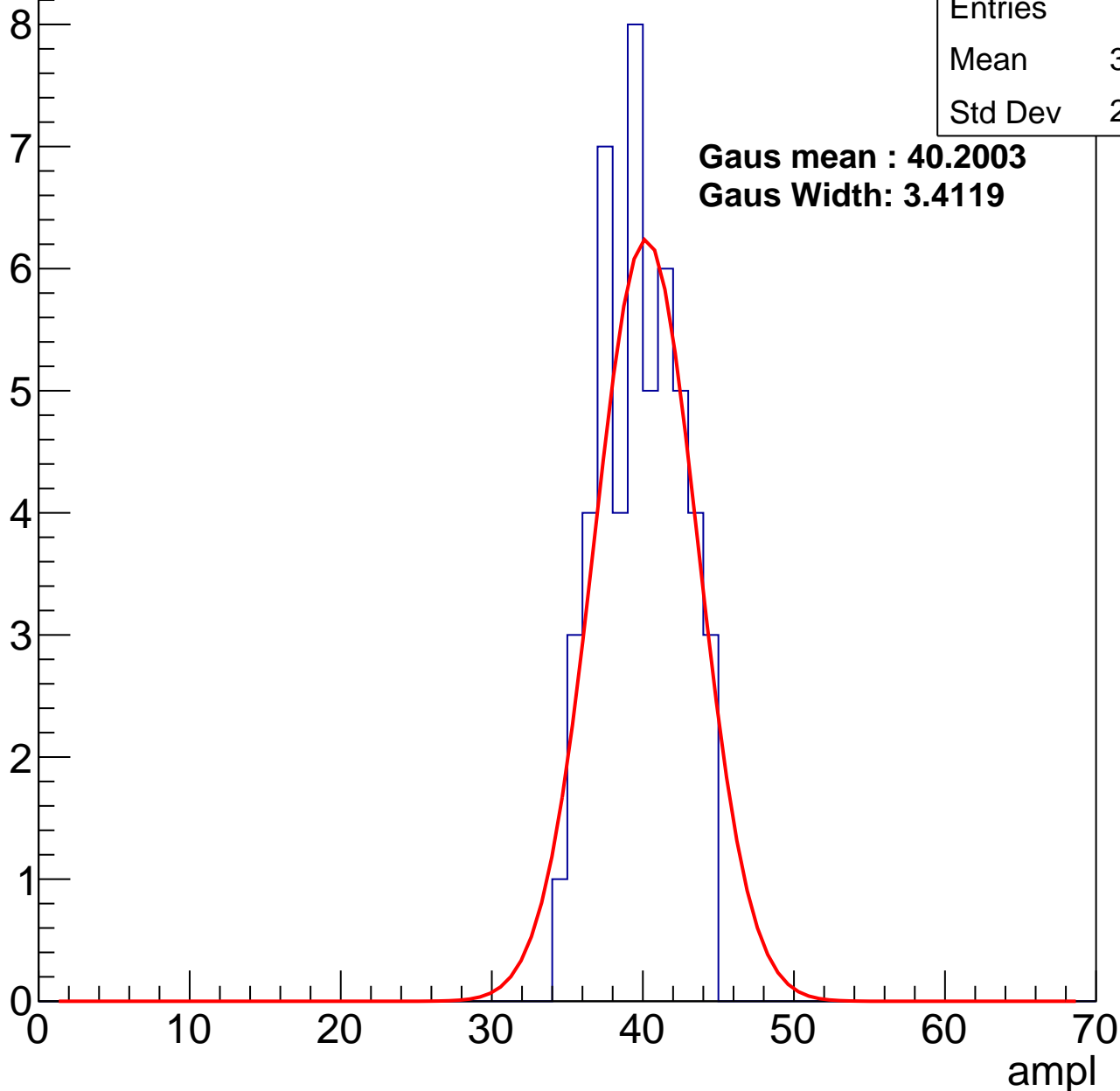
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	39.32
Std Dev	2.642

Gaus mean : 40.2003

Gaus Width: 3.4119



B1L002S, U7-ch98, adc3

calib_packv5_042523_0143.root, FC#10, port B3

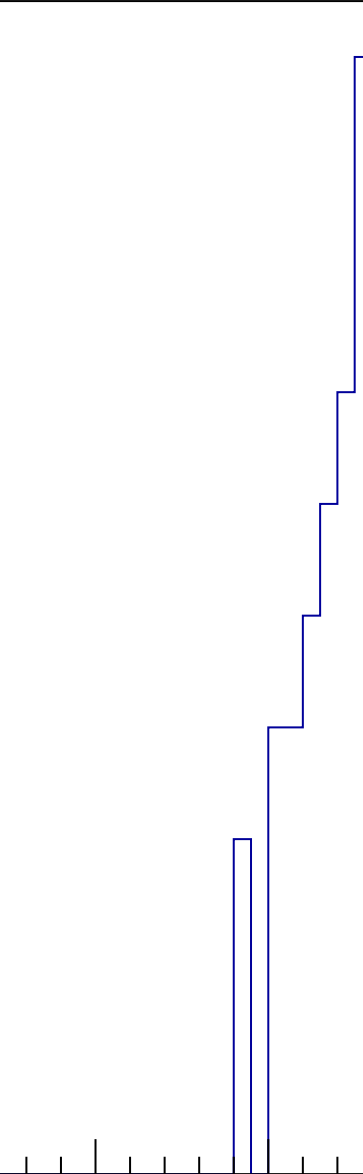
Entries	80
Mean	45.51
Std Dev	3.402

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

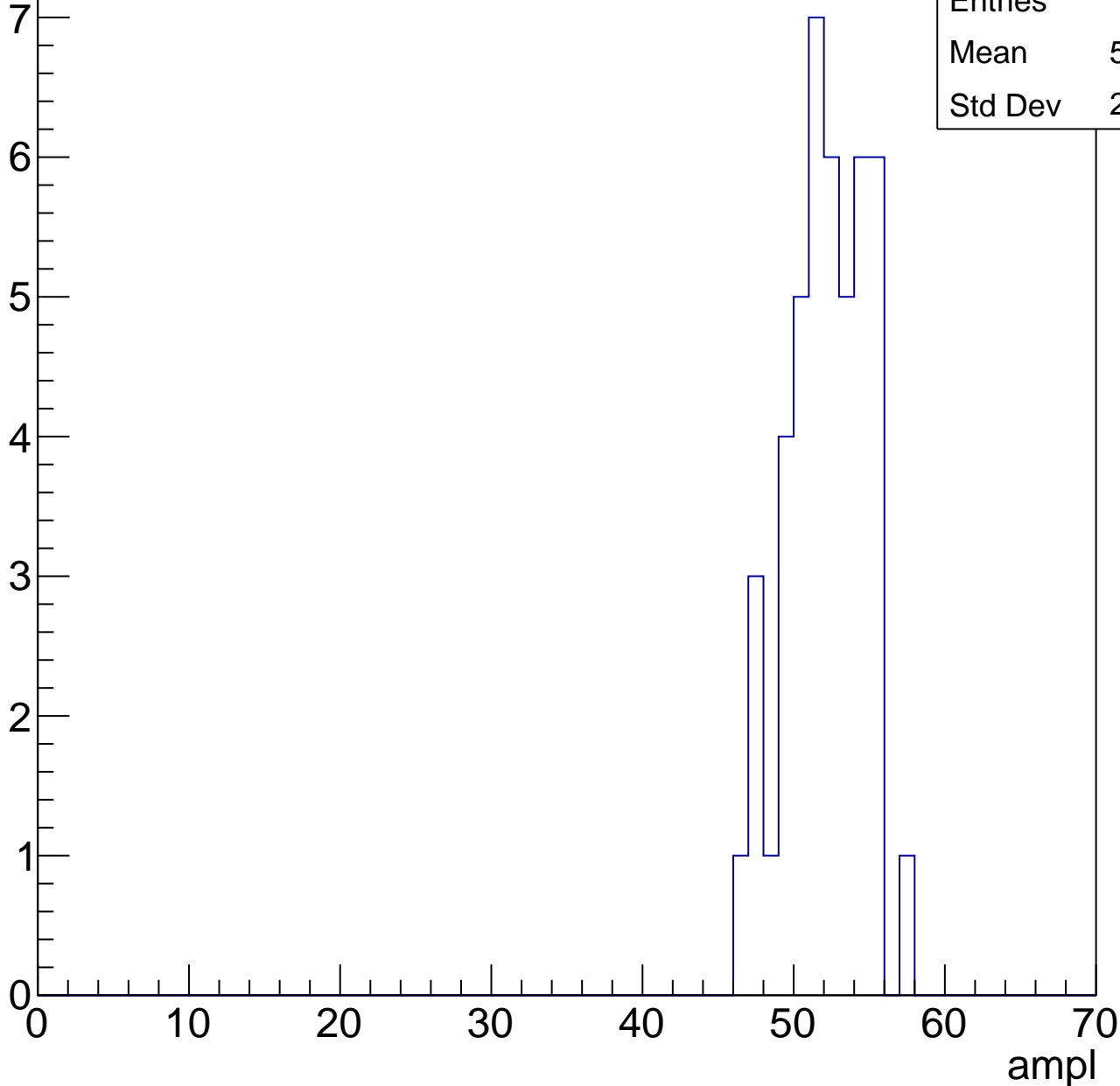


B1L002S, U7-ch98, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	51.69
Std Dev	2.554



B1L002S, U7-ch98, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	57.65
Std Dev	2.89

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

2

4

6

8

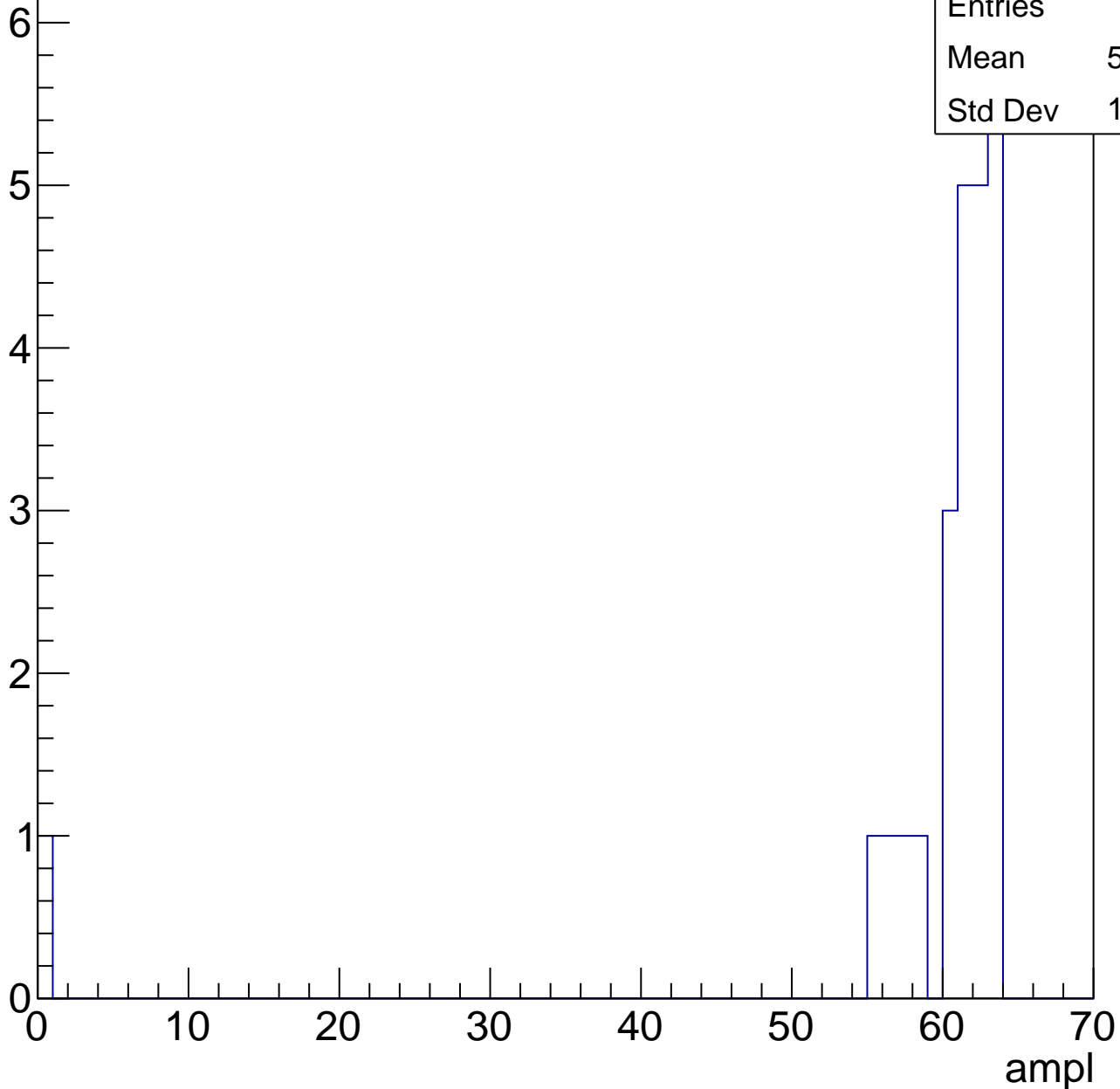
10

B1L002S, U7-ch98, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

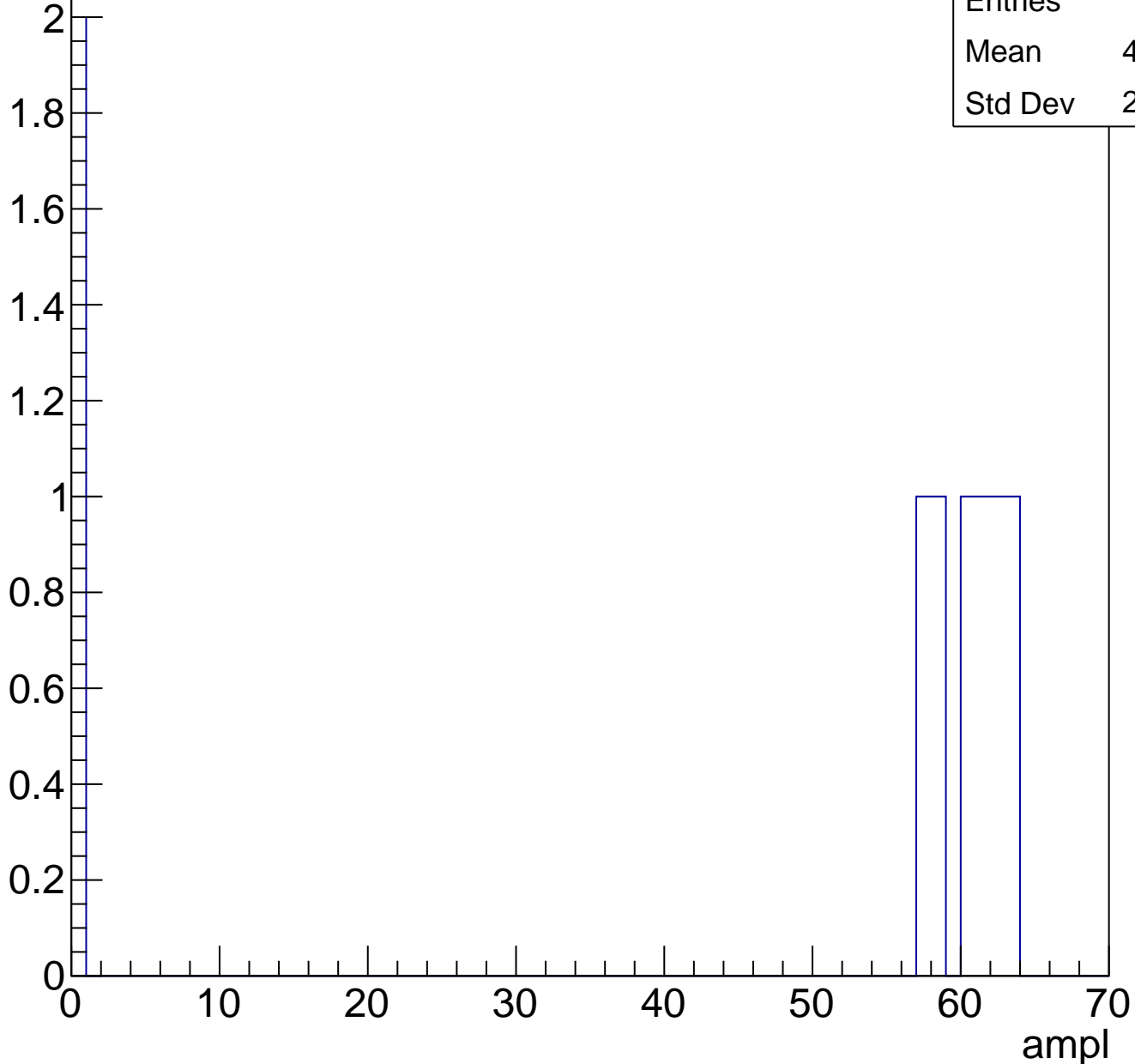
Entries	24
Mean	58.29
Std Dev	12.35



B1L002S, U7-ch98, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	8
Mean	45.12
Std Dev	26.12

B1L002S, U7-ch99, adc0

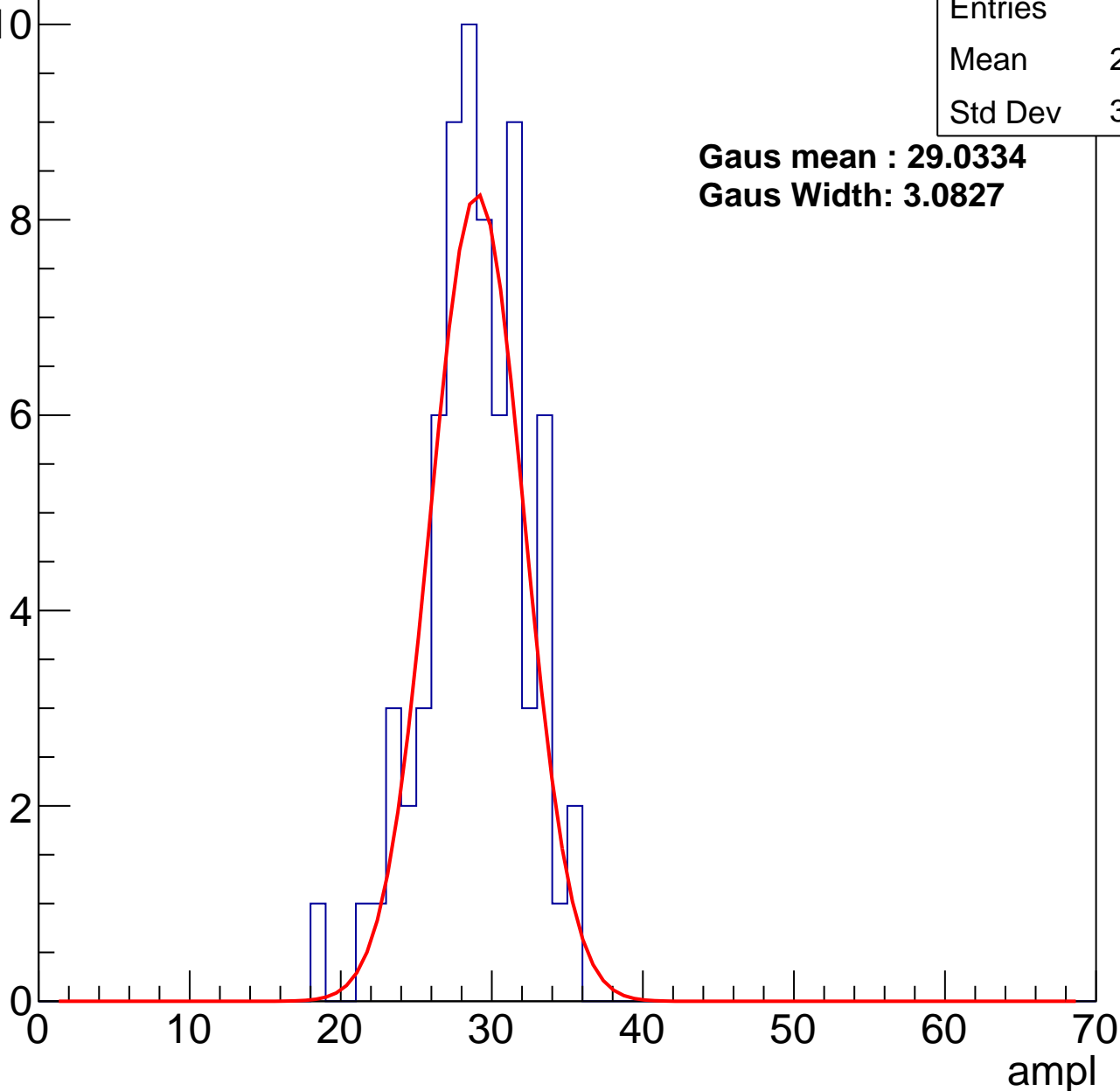
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.46
Std Dev	3.318

Gaus mean : 29.0334

Gaus Width: 3.0827



B1L002S, U7-ch99, adc1

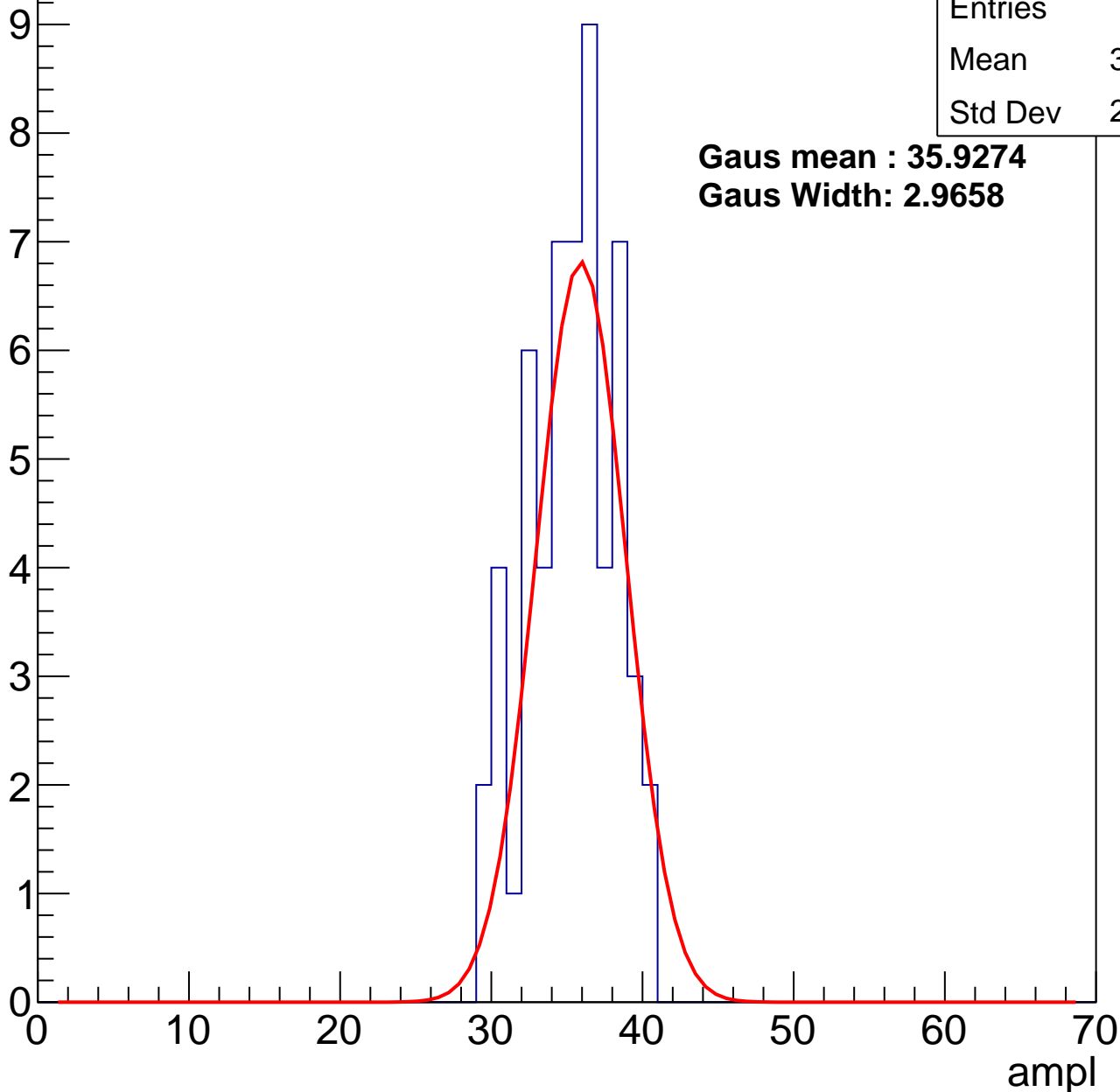
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.84
Std Dev	2.833

Gaus mean : 35.9274

Gaus Width: 2.9658



B1L002S, U7-ch99, adc2

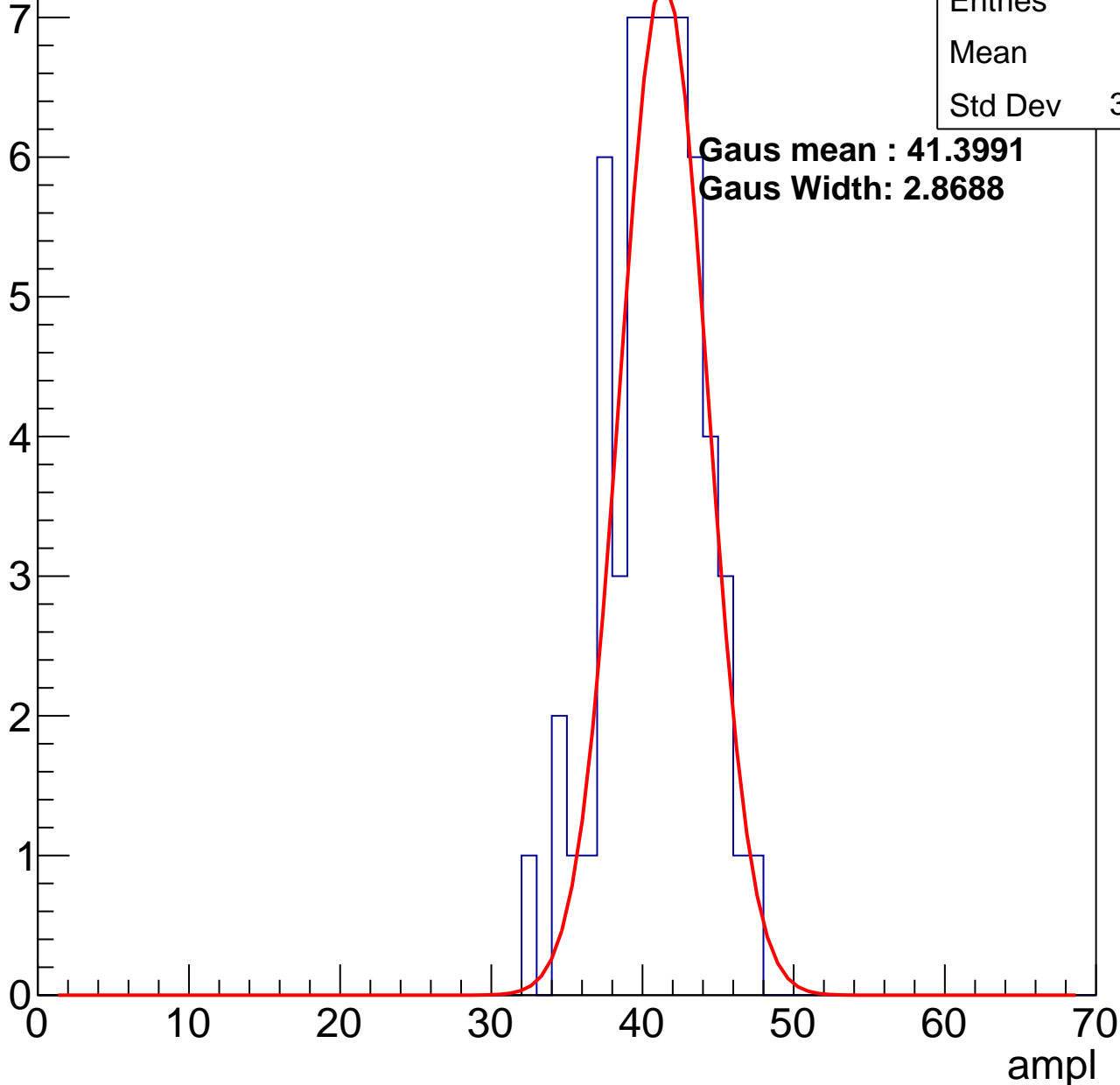
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.4
Std Dev	3.117

Gaus mean : 41.3991

Gaus Width: 2.8688

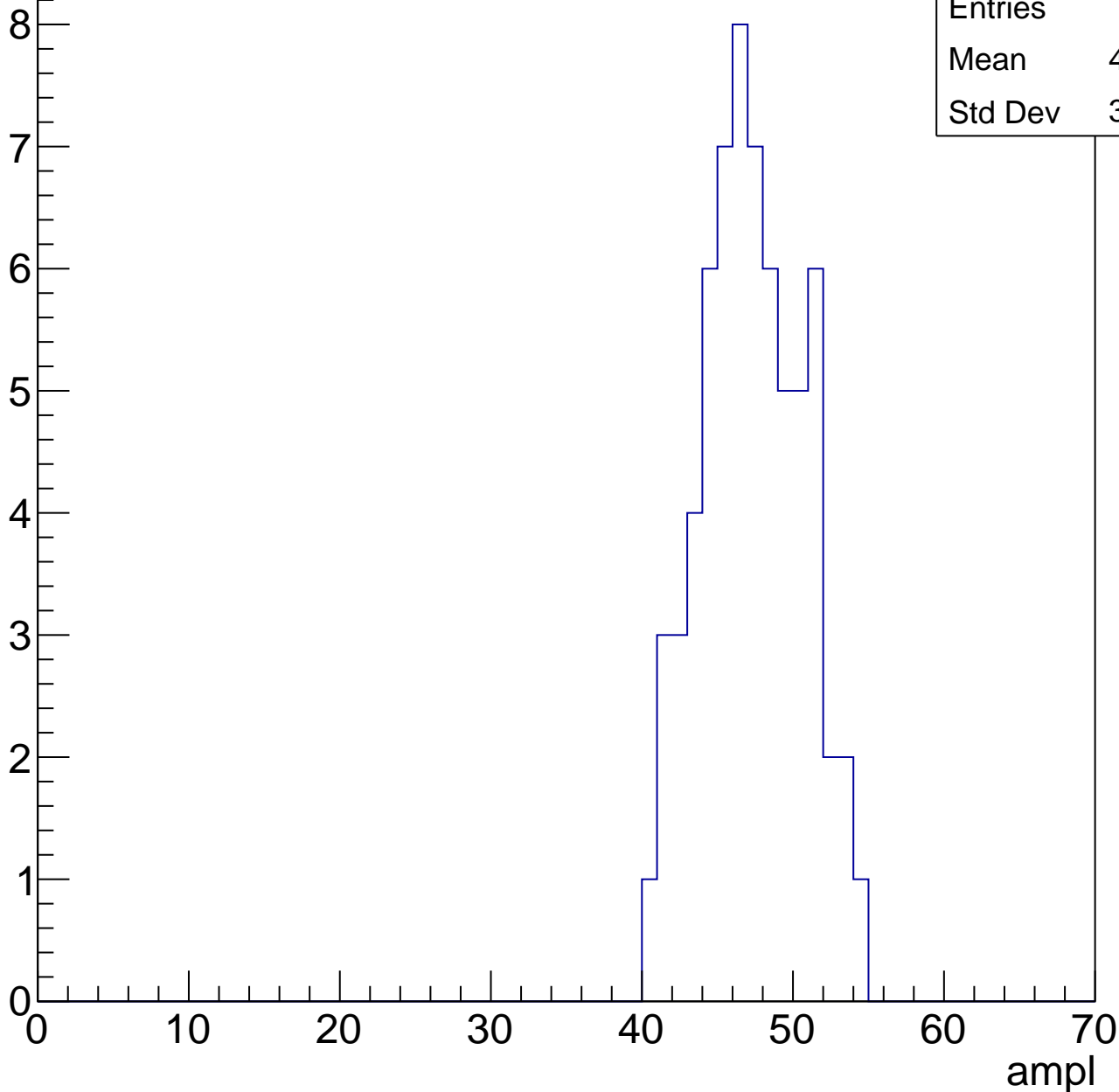


B1L002S, U7-ch99, adc3

calib_packv5_042523_0143.root, FC#10, port B3

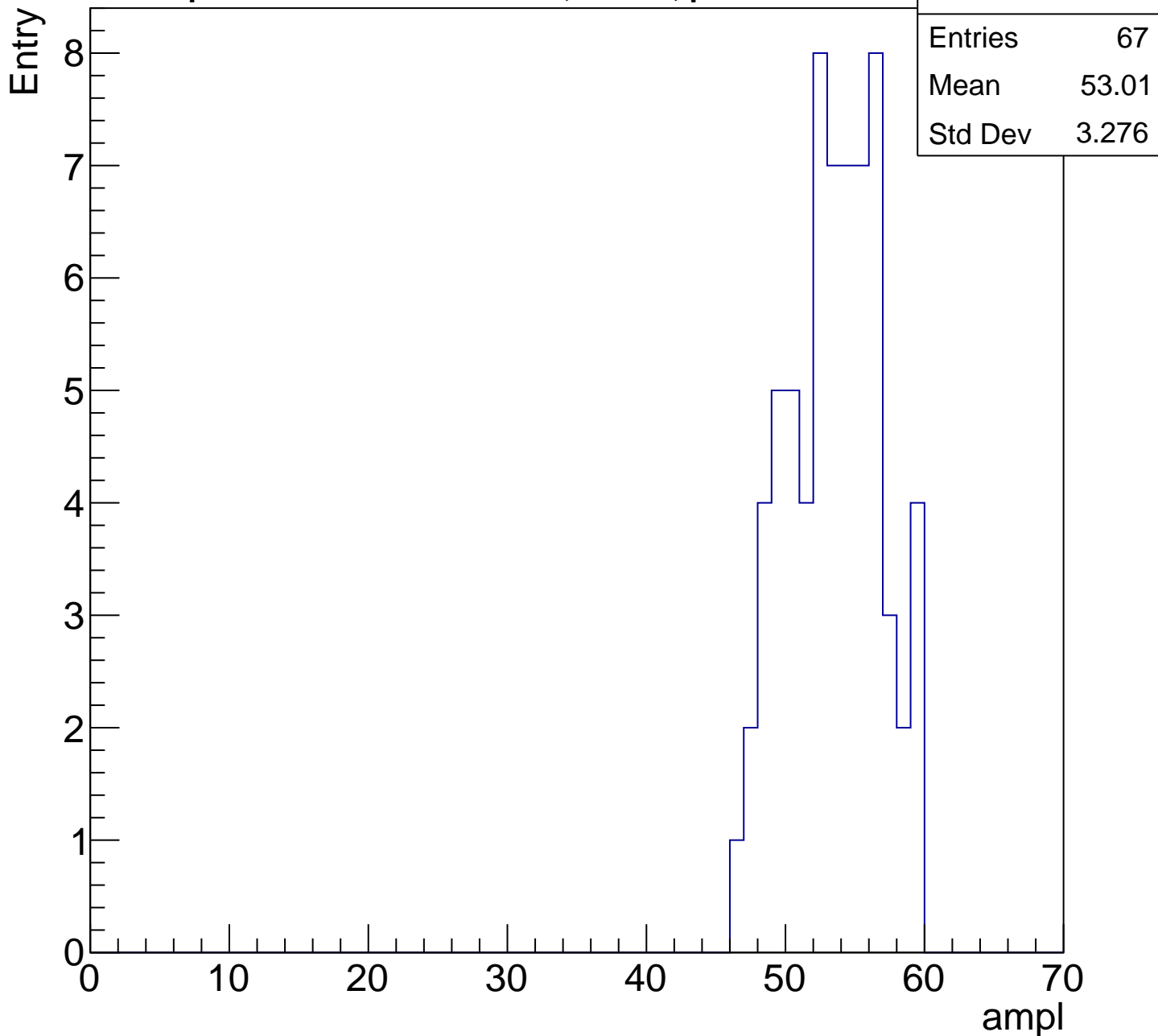
Entry

Entries	66
Mean	46.82
Std Dev	3.307



B1L002S, U7-ch99, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch99, adc5

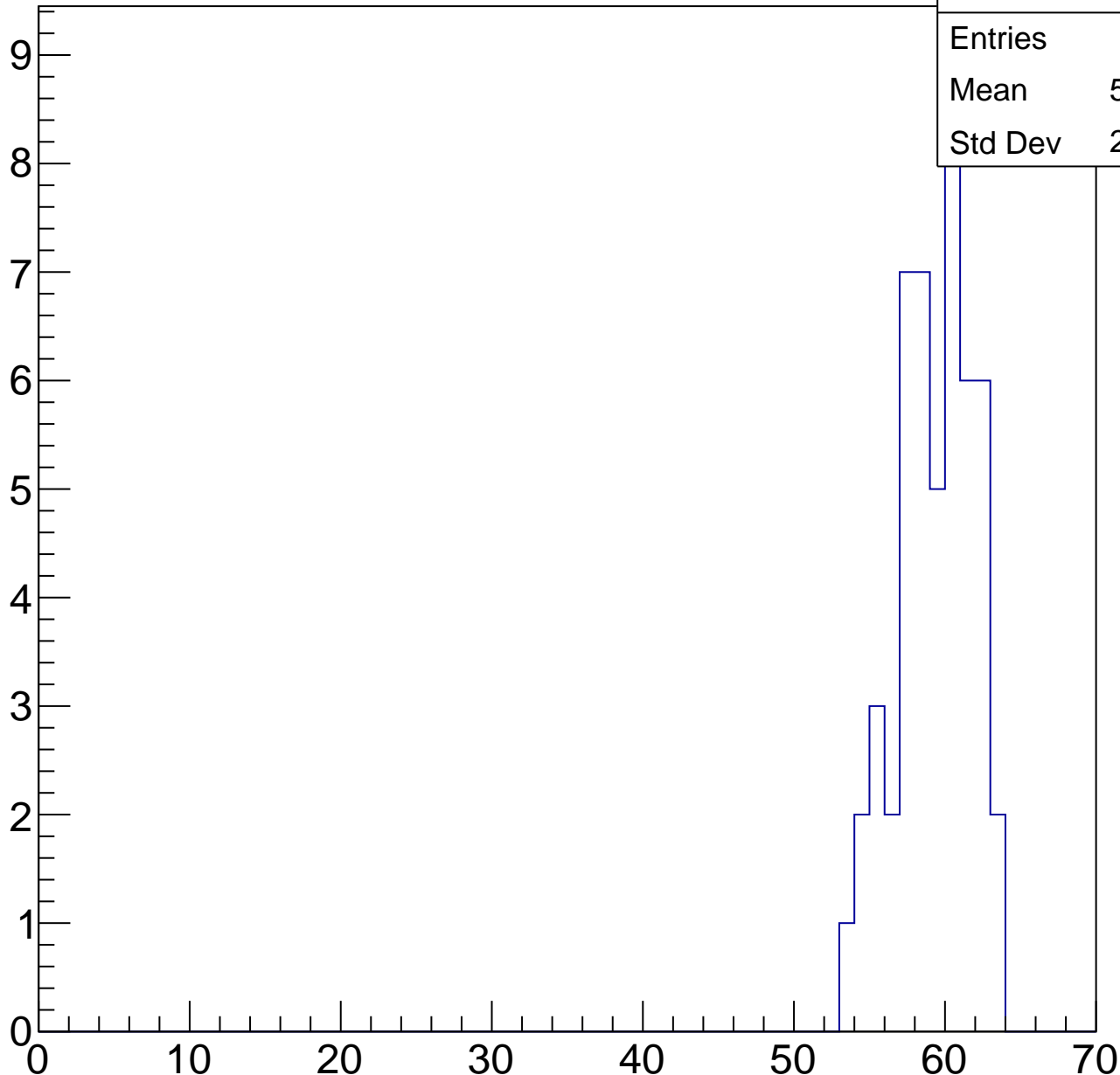
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.84
Std Dev	2.469

ampl

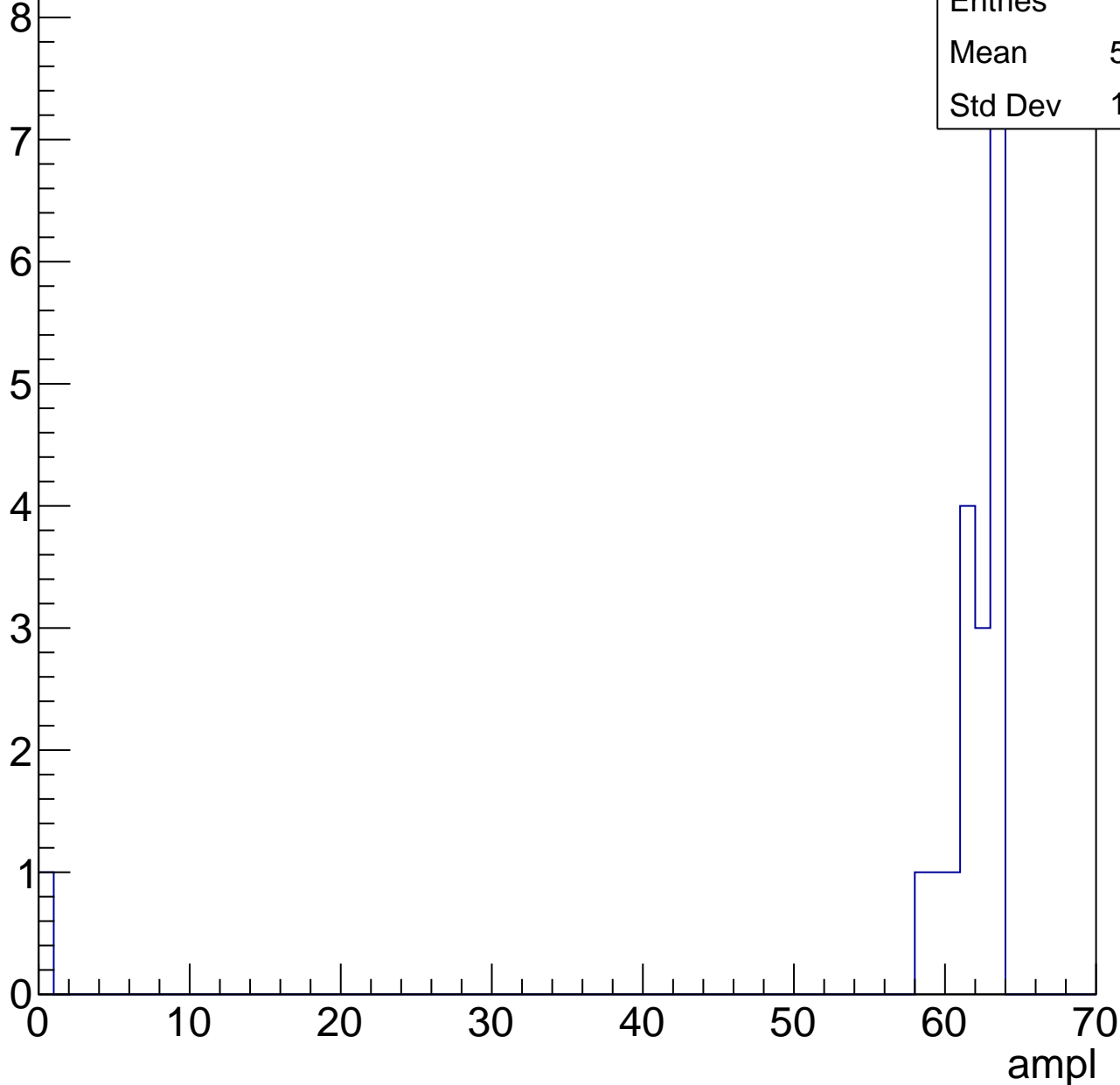


B1L002S, U7-ch99, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.47
Std Dev	13.86



B1L002S, U7-ch99, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch100, adc0

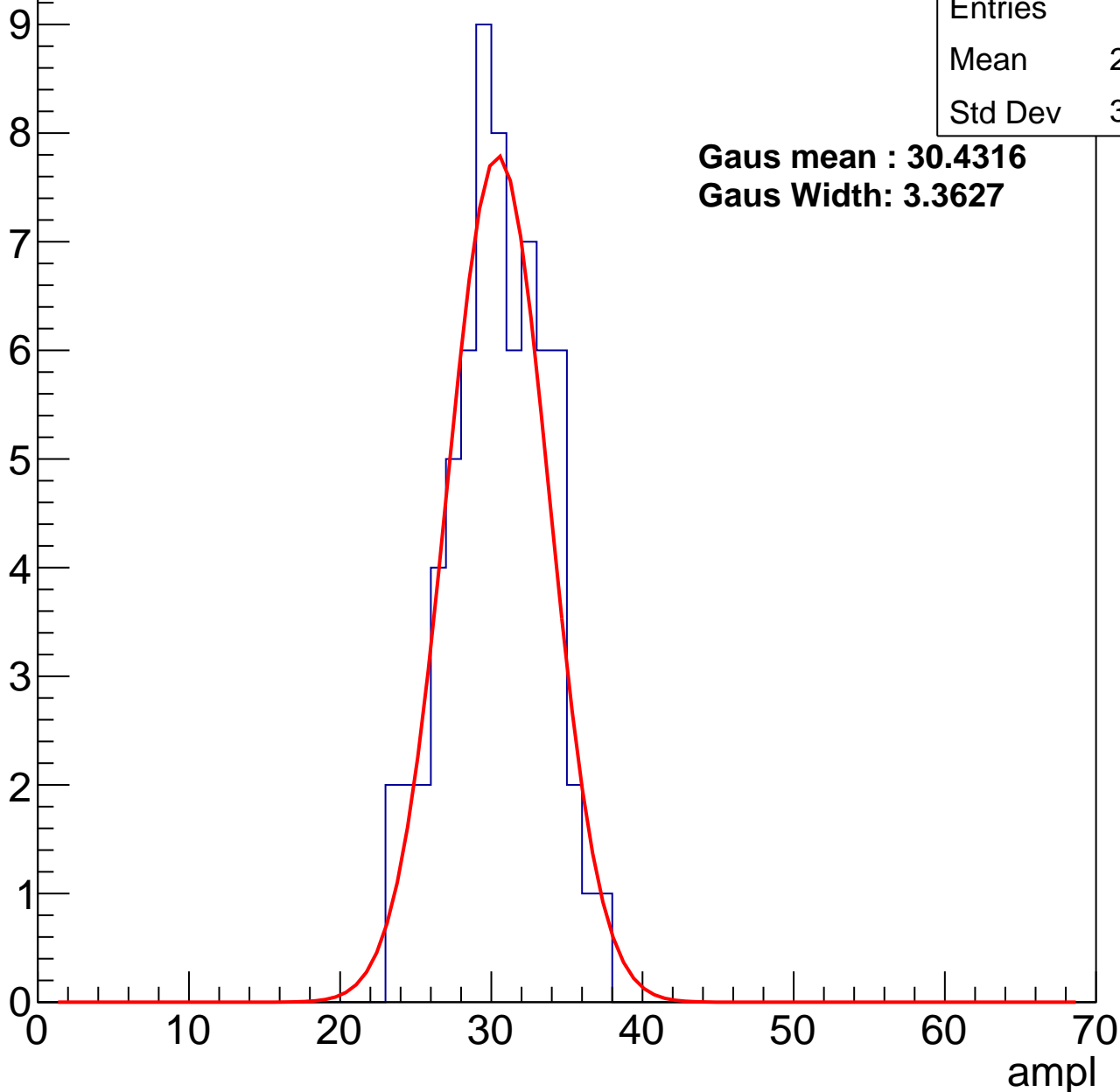
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.96
Std Dev	3.188

Gaus mean : 30.4316

Gaus Width: 3.3627



B1L002S, U7-ch100, adc1

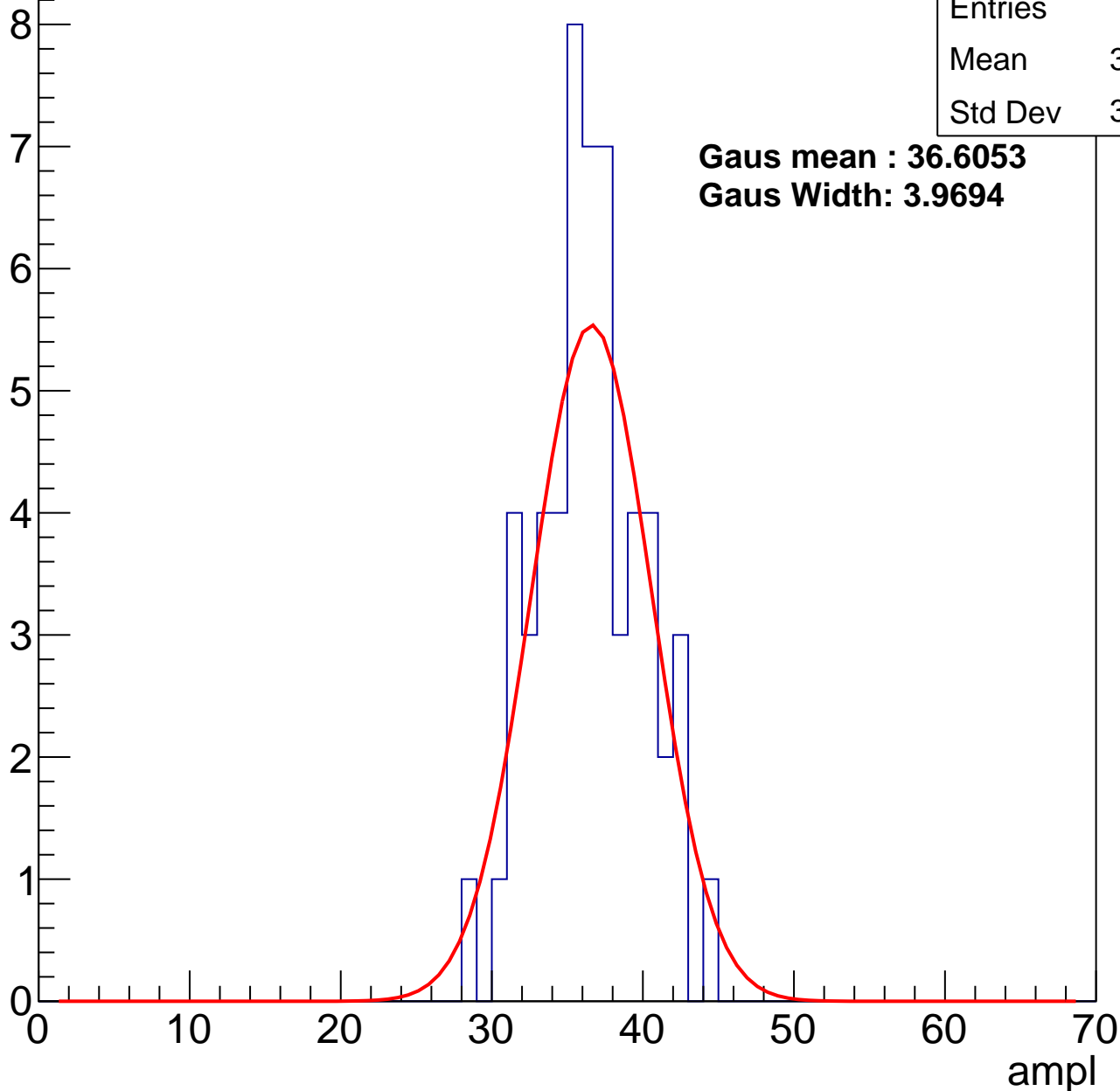
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	36.05
Std Dev	3.404

Gaus mean : 36.6053

Gaus Width: 3.9694



B1L002S, U7-ch100, adc2

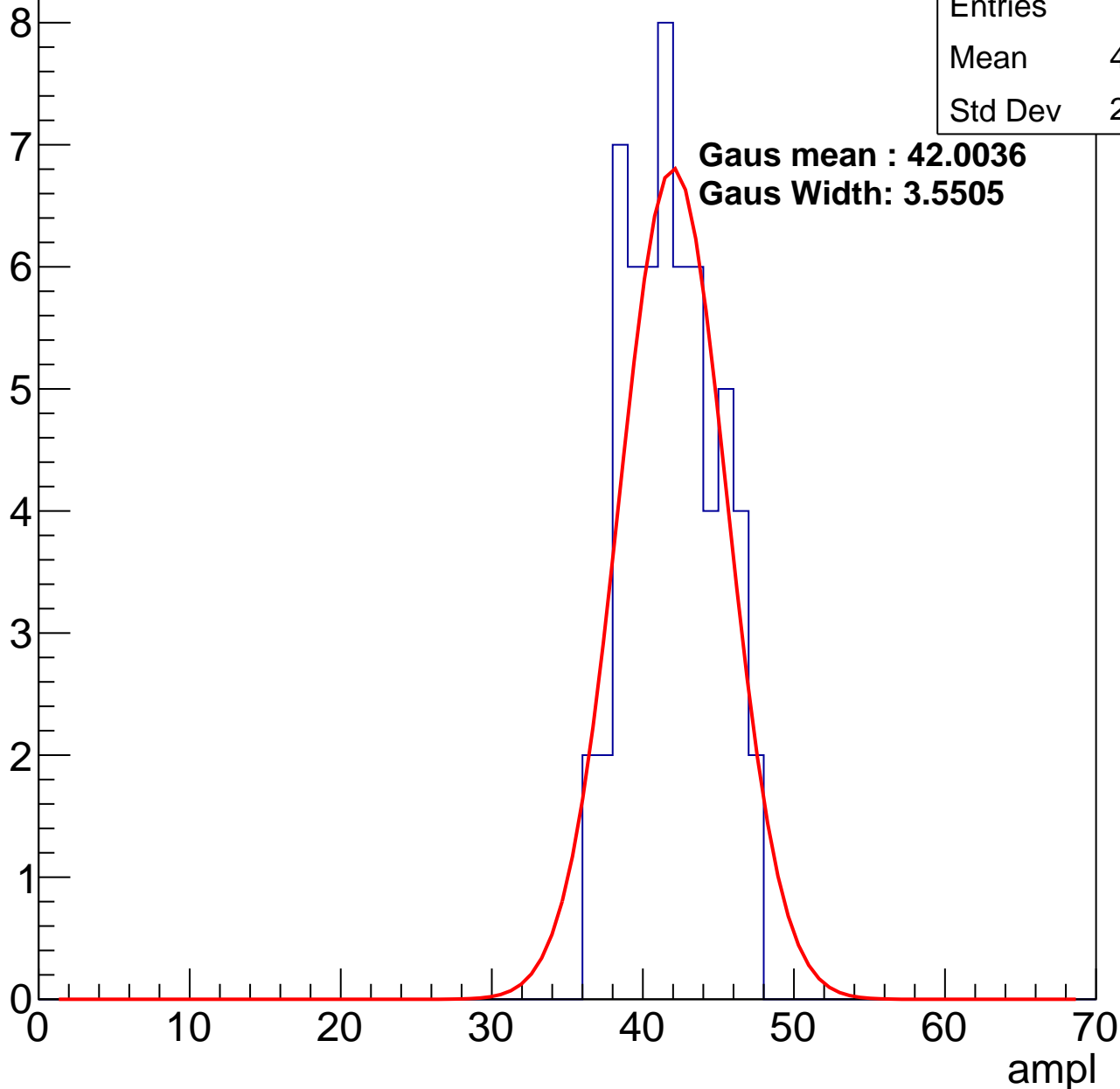
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.43
Std Dev	2.883

Gaus mean : 42.0036

Gaus Width: 3.5505

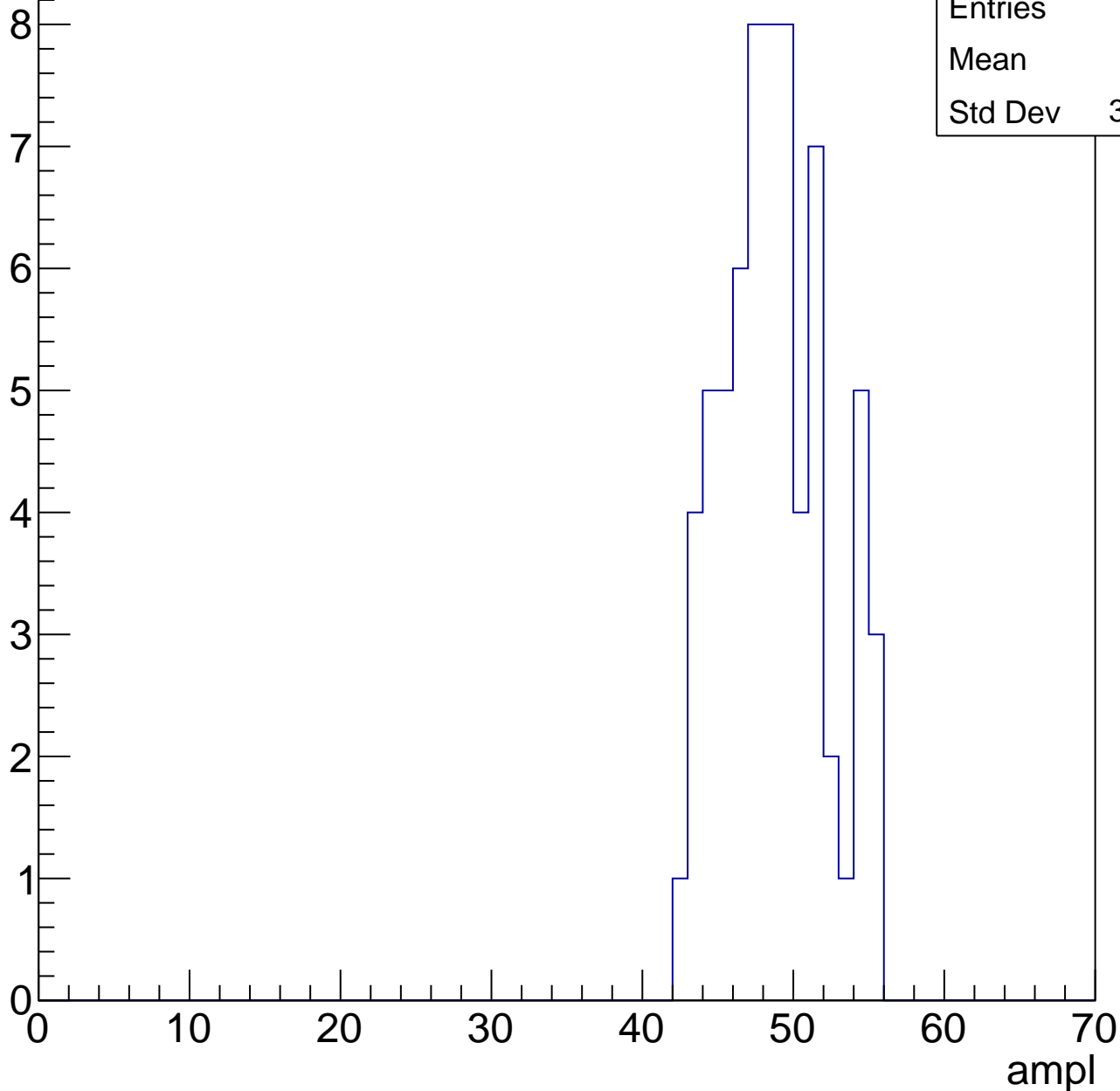


B1L002S, U7-ch100, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.3
Std Dev	3.364

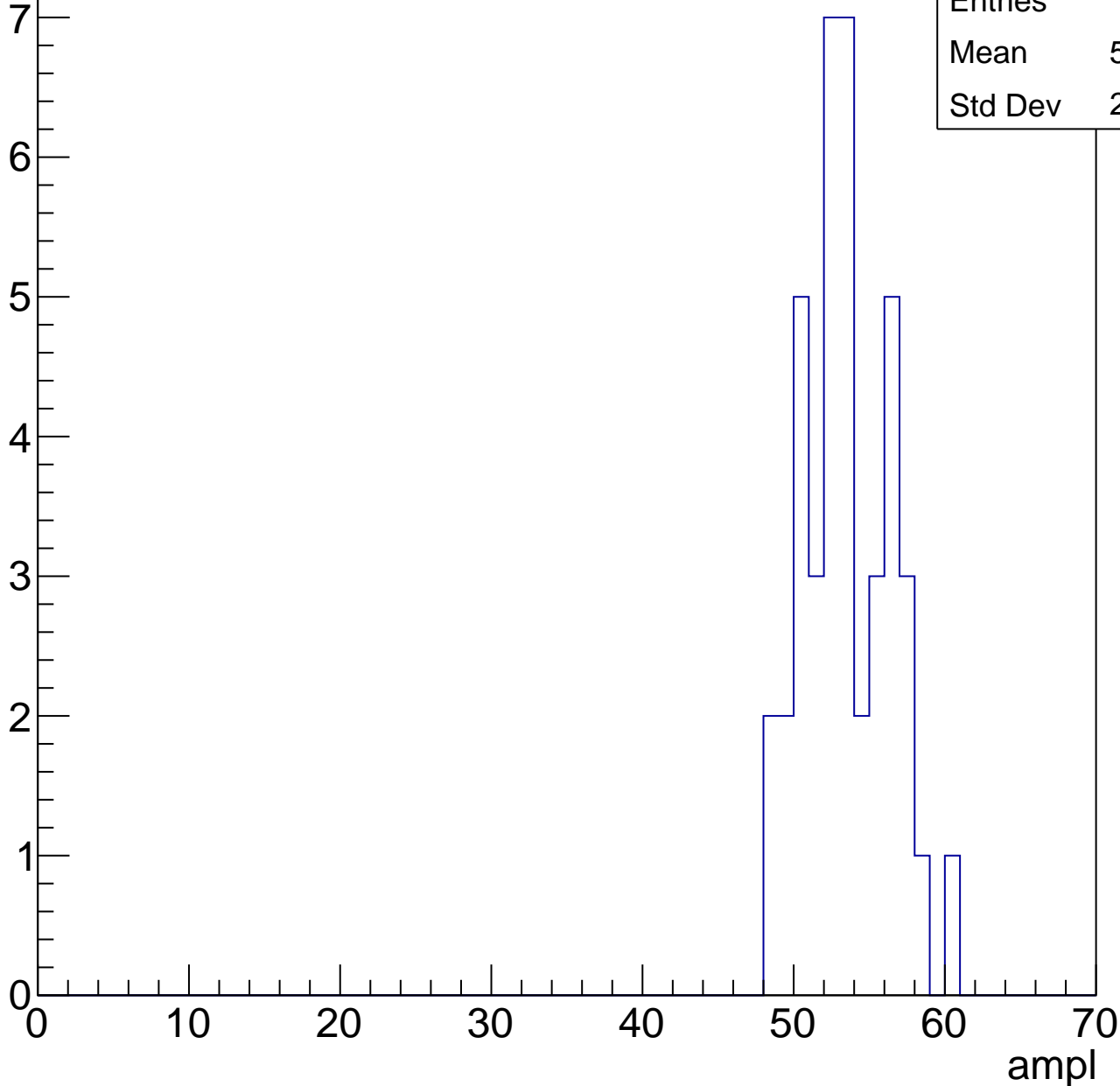


B1L002S, U7-ch100, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	41
Mean	53.02
Std Dev	2.824

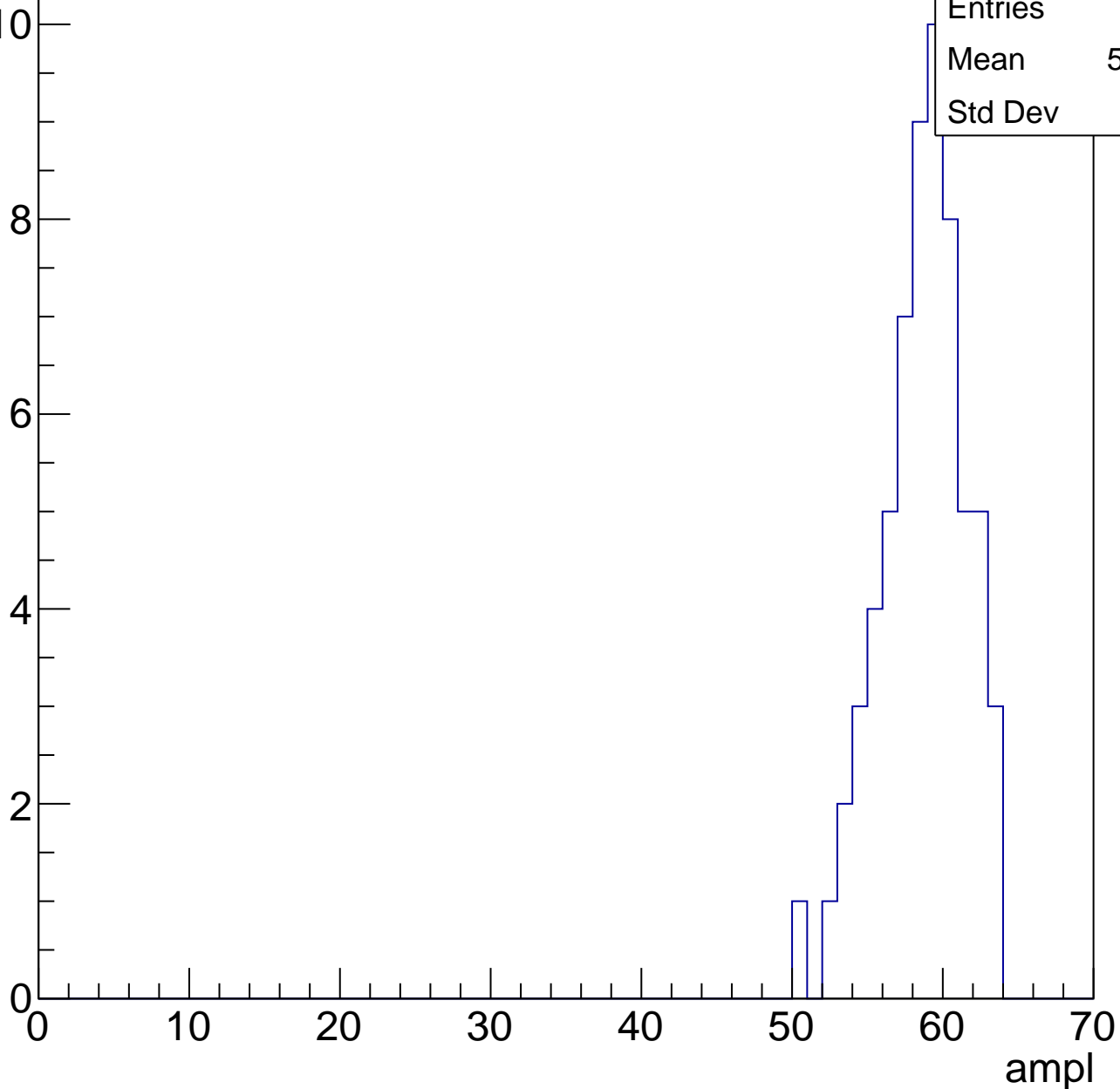


B1L002S, U7-ch100, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	58.17
Std Dev	2.82

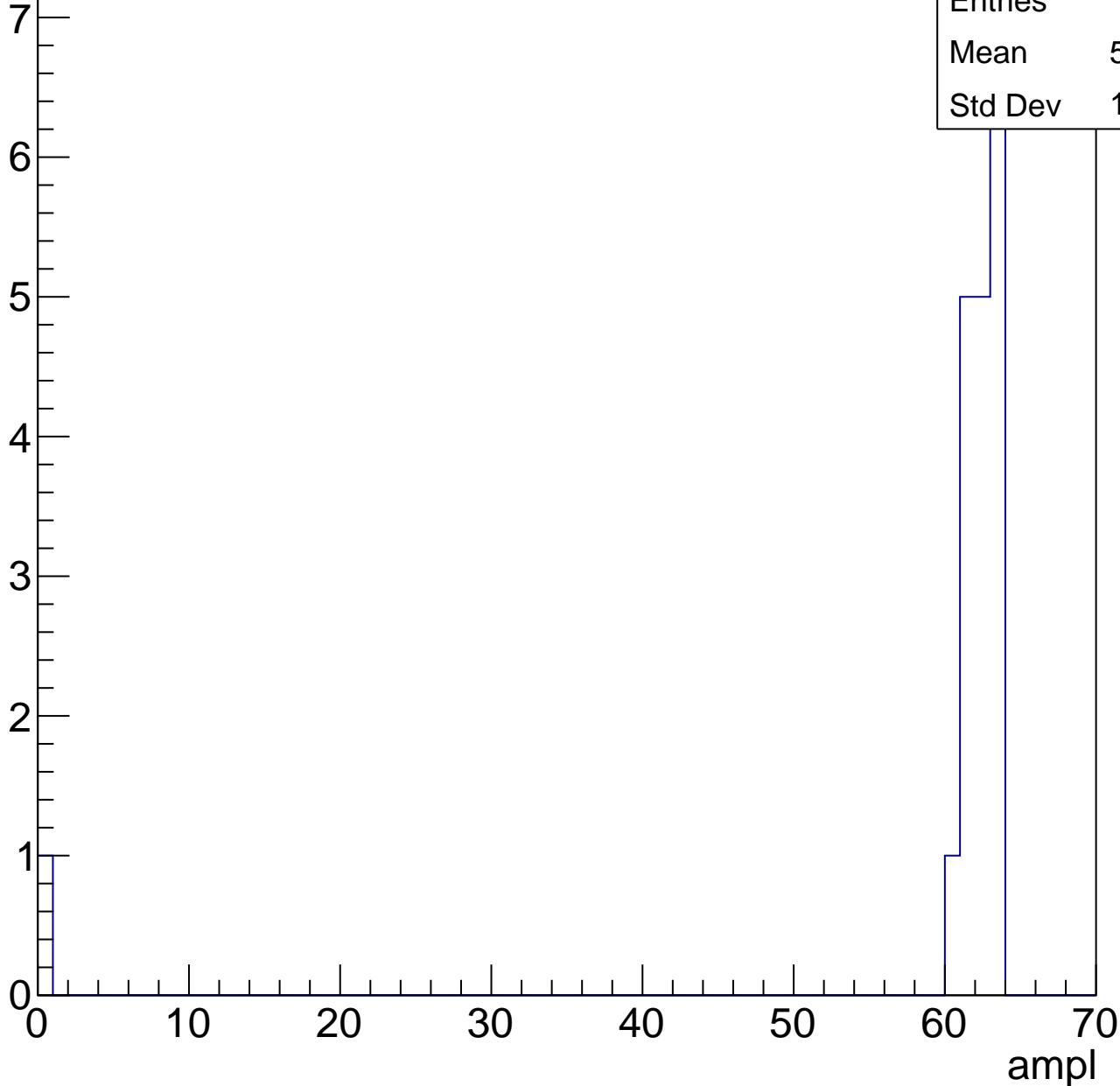


B1L002S, U7-ch100, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.74
Std Dev	13.87



B1L002S, U7-ch100, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch101, adc0

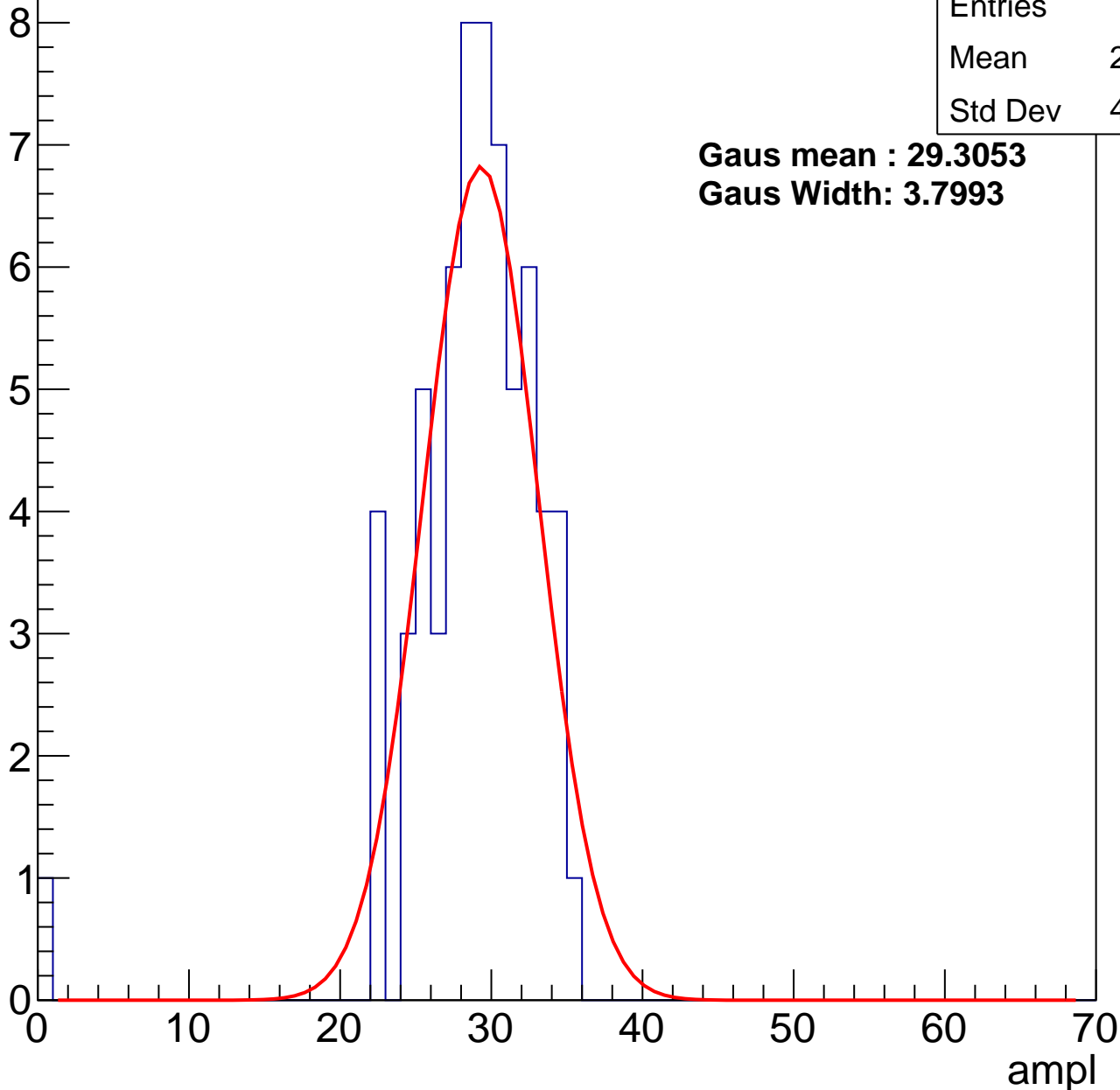
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.32
Std Dev	4.807

Gaus mean : 29.3053

Gaus Width: 3.7993



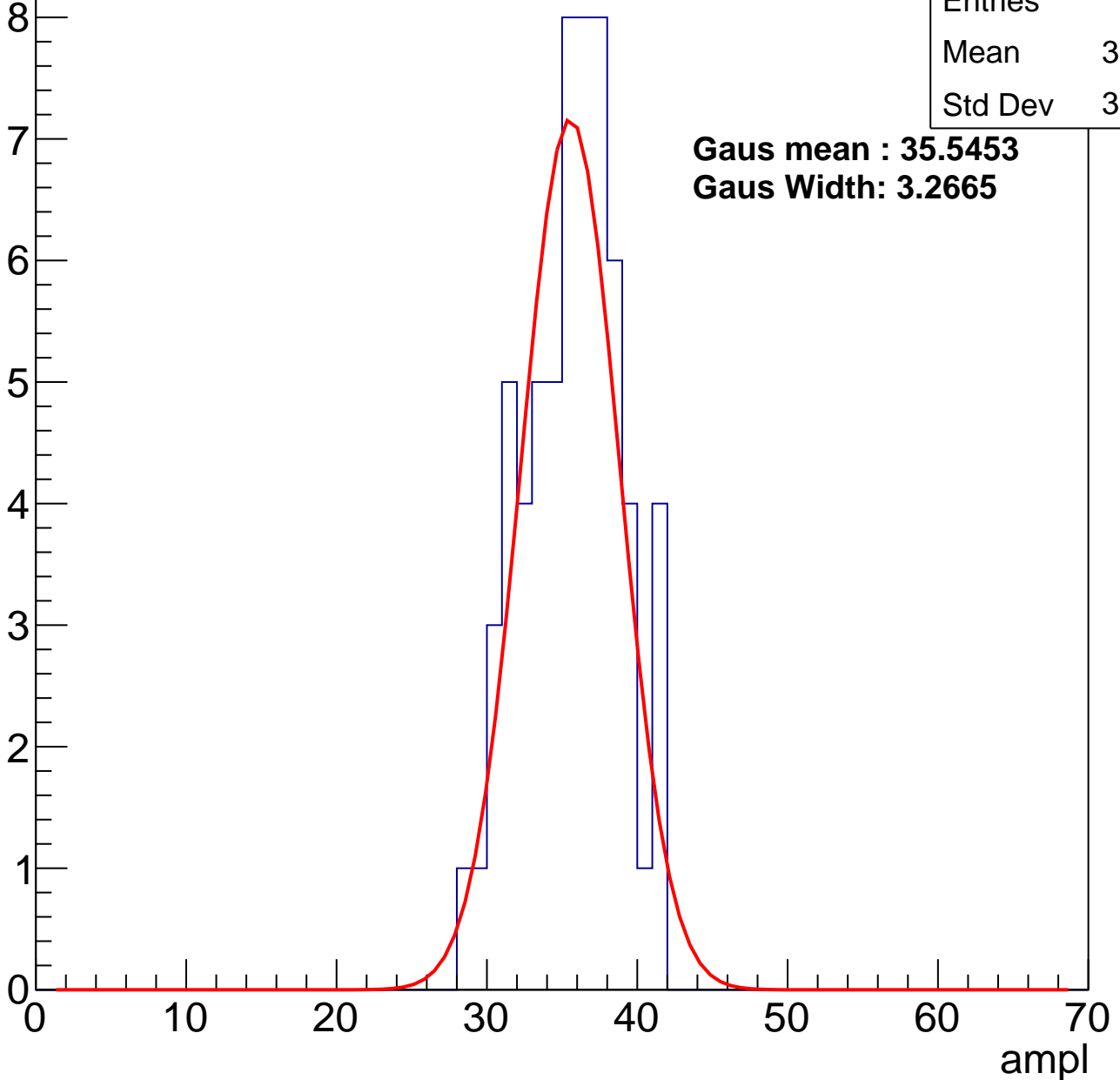
B1L002S, U7-ch101, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.19
Std Dev	3.152

Gaus mean : 35.5453
Gaus Width: 3.2665



B1L002S, U7-ch101, adc2

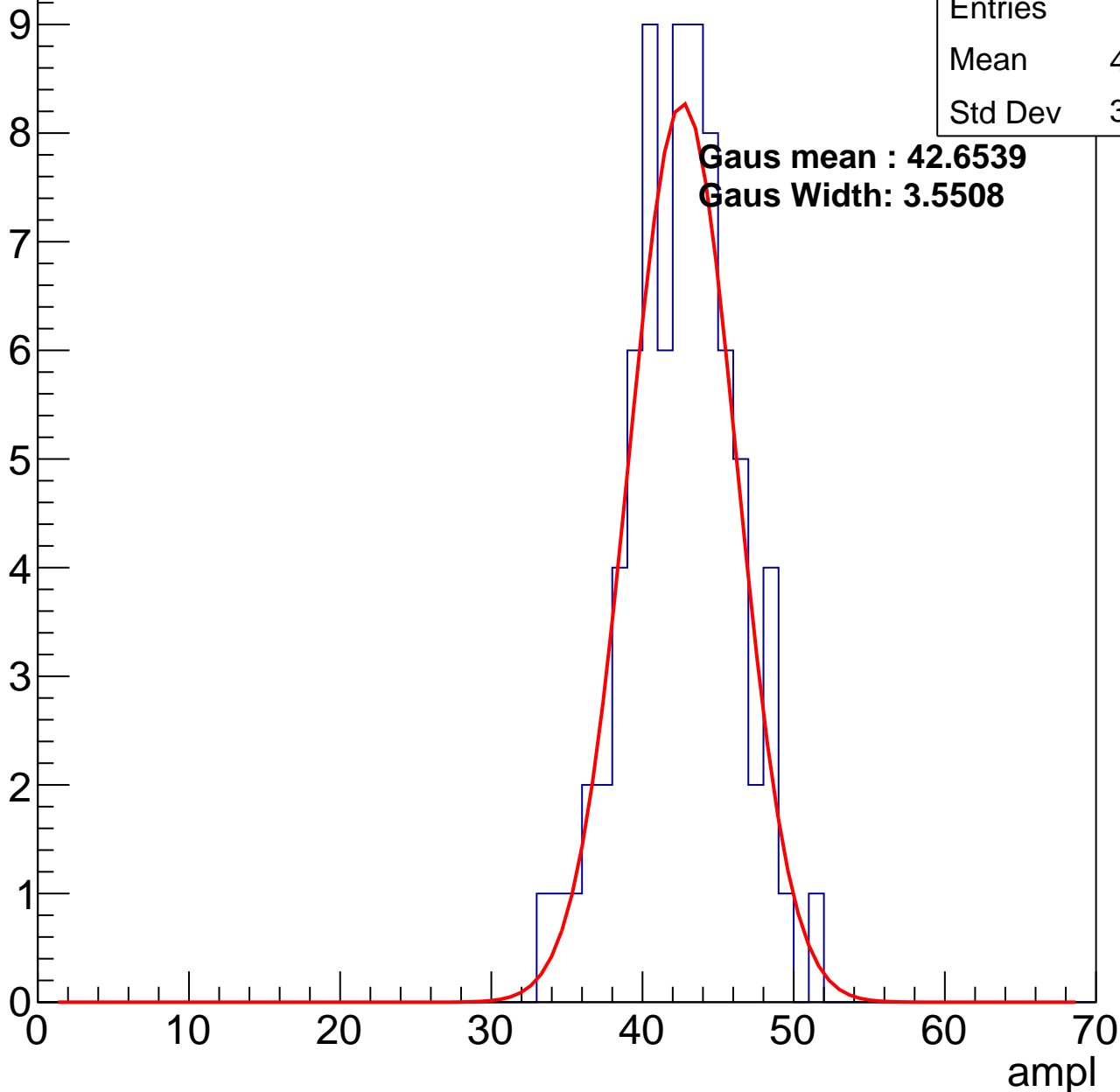
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	42.12
Std Dev	3.556

Gaus mean : 42.6539

Gaus Width: 3.5508

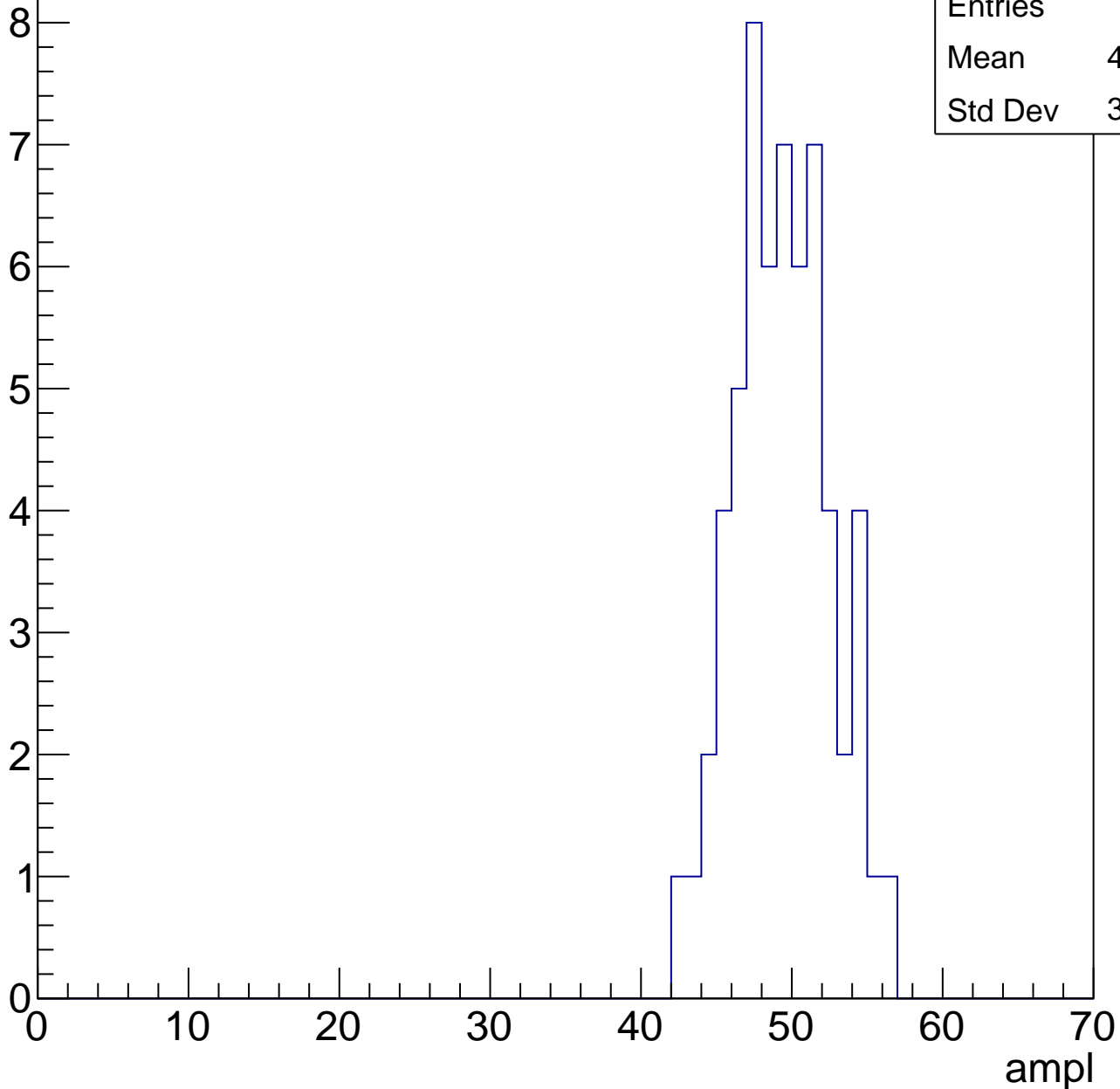


B1L002S, U7-ch101, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	48.95
Std Dev	3.105

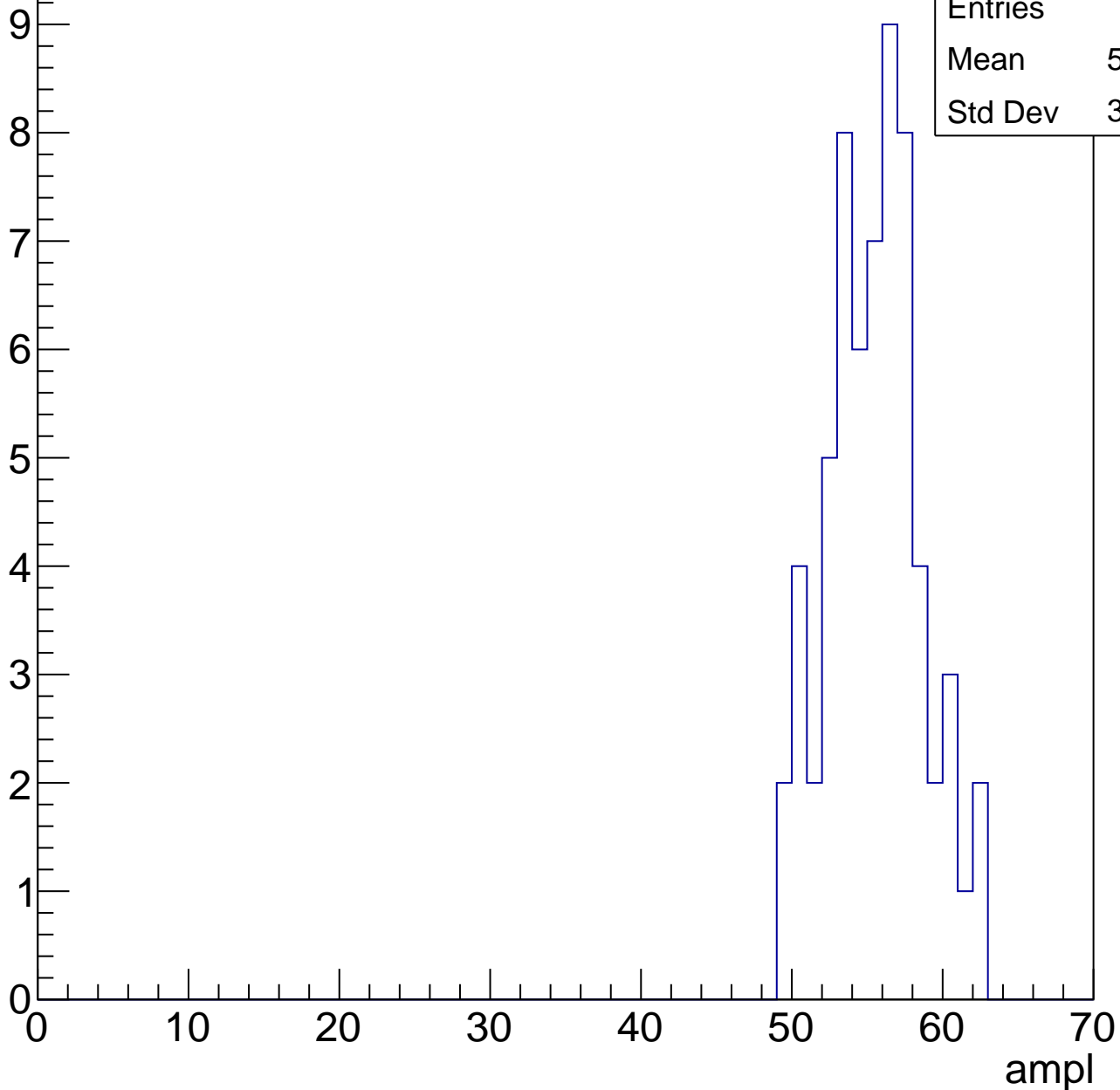


B1L002S, U7-ch101, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	55.05
Std Dev	3.099



B1L002S, U7-ch101, adc5

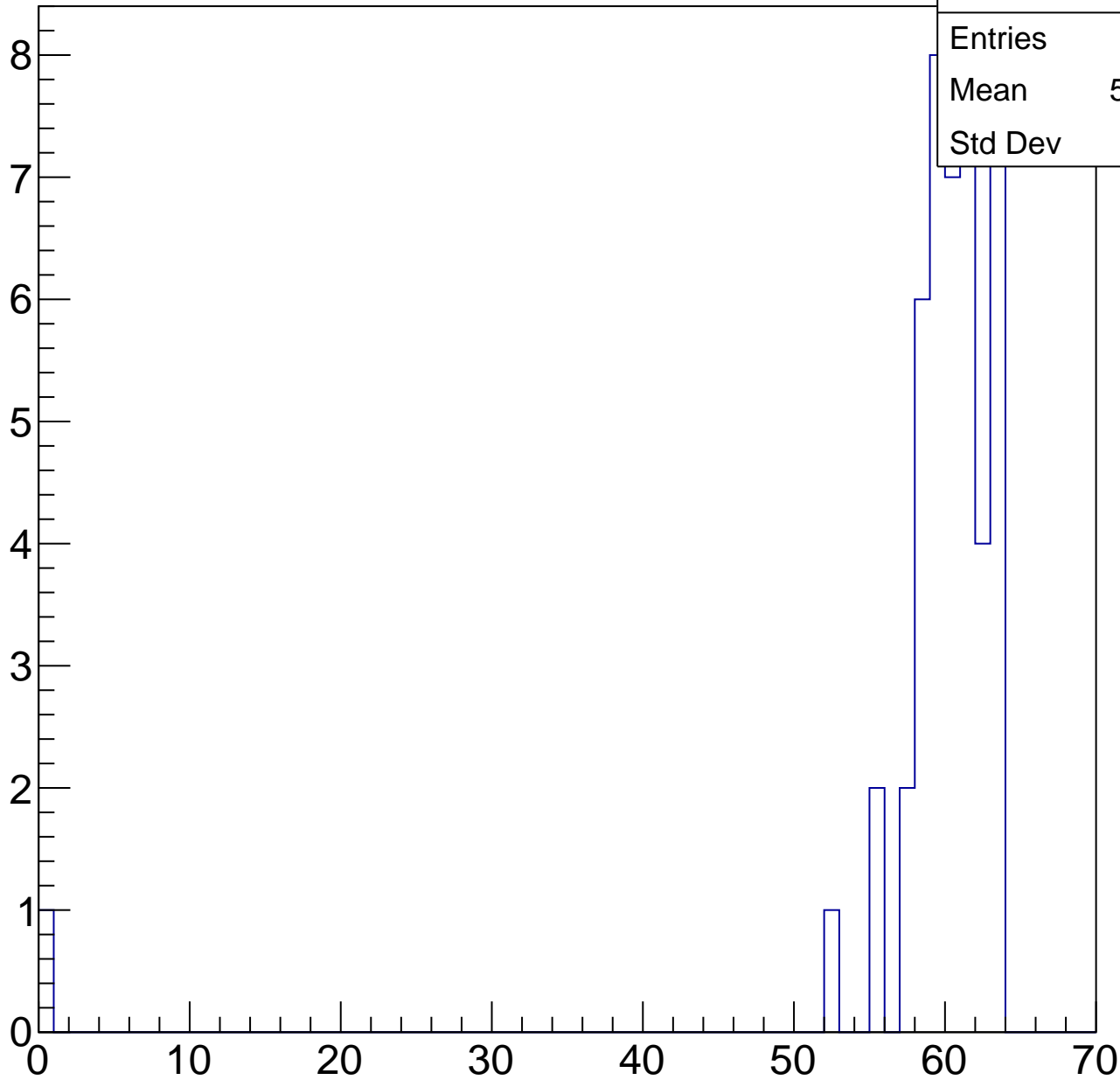
calib_packv5_042523_0143.root, FC#10, port B3

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.64
Std Dev	8.96

ampl



B1L002S, U7-ch101, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch101, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch102, adc0

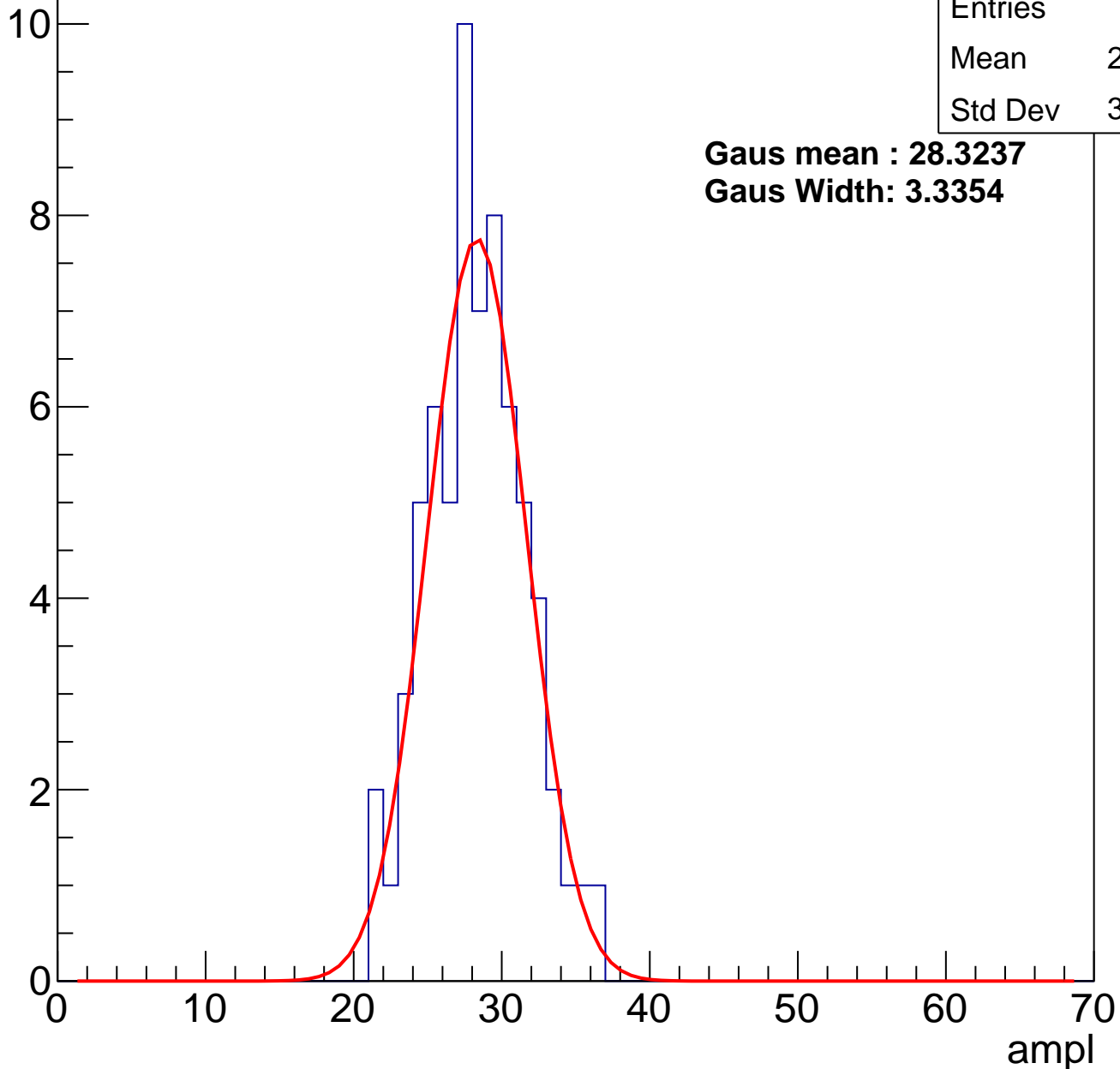
calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	27.84
Std Dev	3.258

Gaus mean : 28.3237

Gaus Width: 3.3354

Entry



B1L002S, U7-ch102, adc1

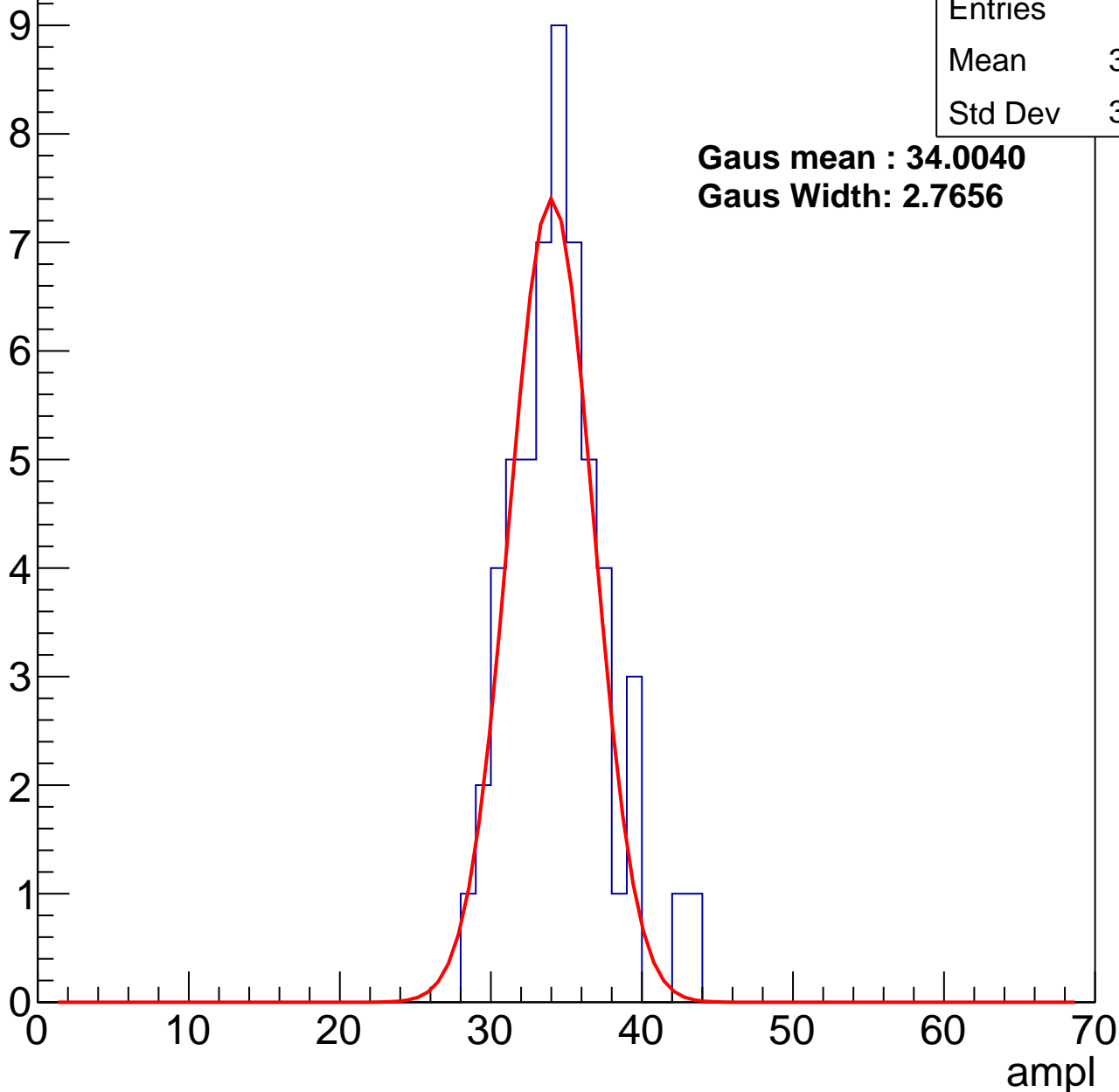
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.02
Std Dev	3.078

Gaus mean : 34.0040

Gaus Width: 2.7656



B1L002S, U7-ch102, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	72
Mean	40.21
Std Dev	3.188

Gaus mean : 40.6822

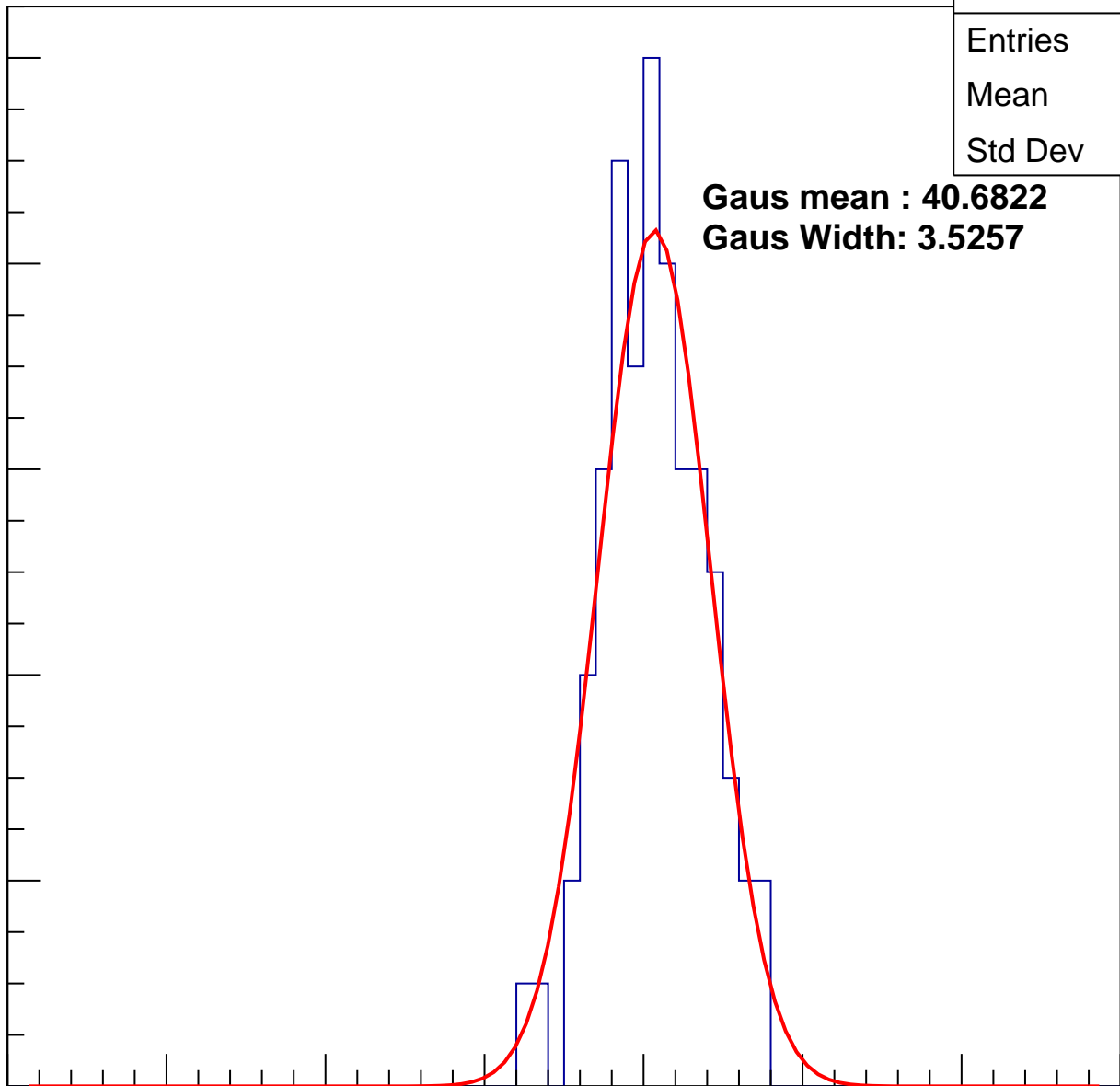
Gaus Width: 3.5257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

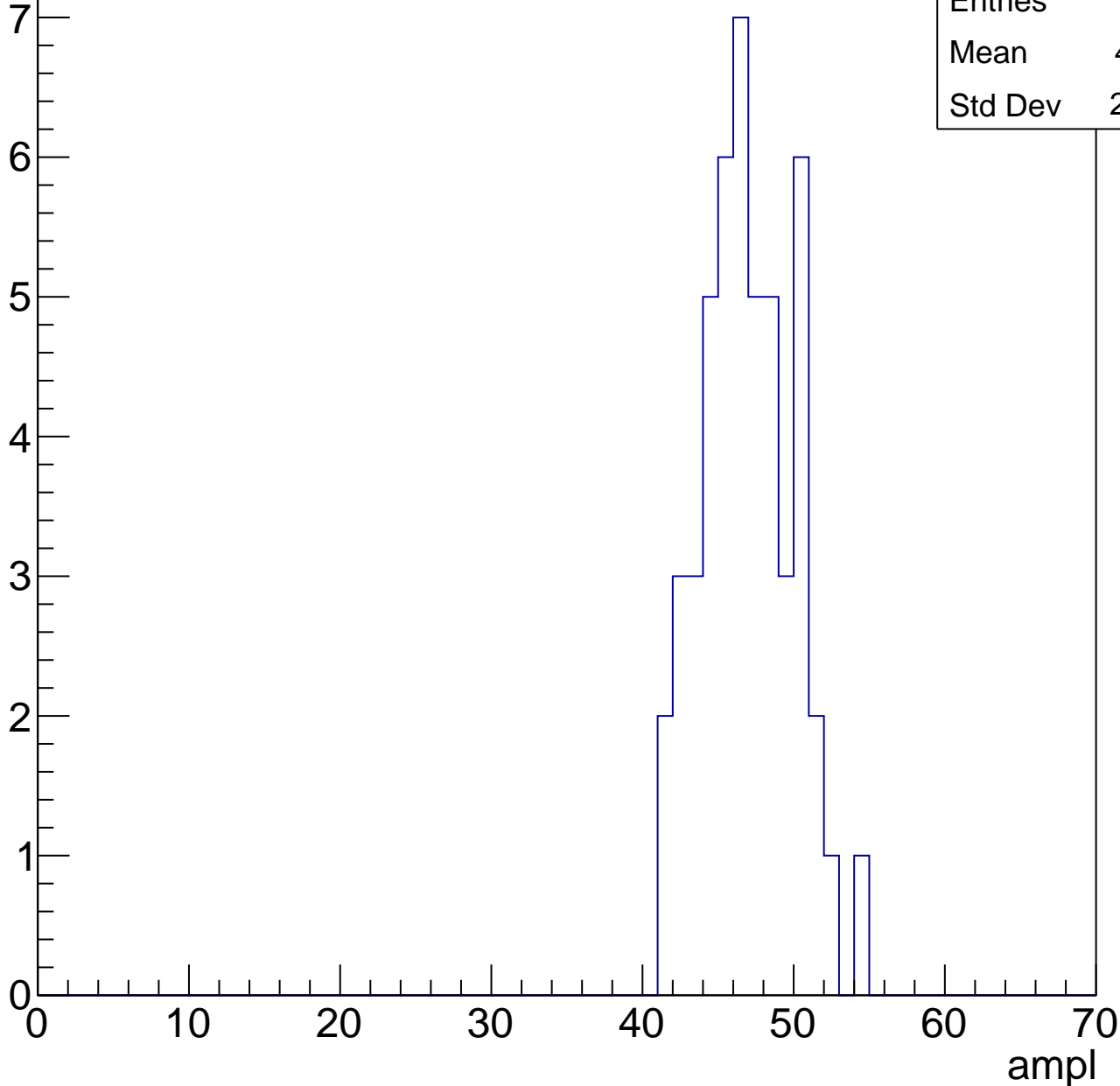


B1L002S, U7-ch102, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

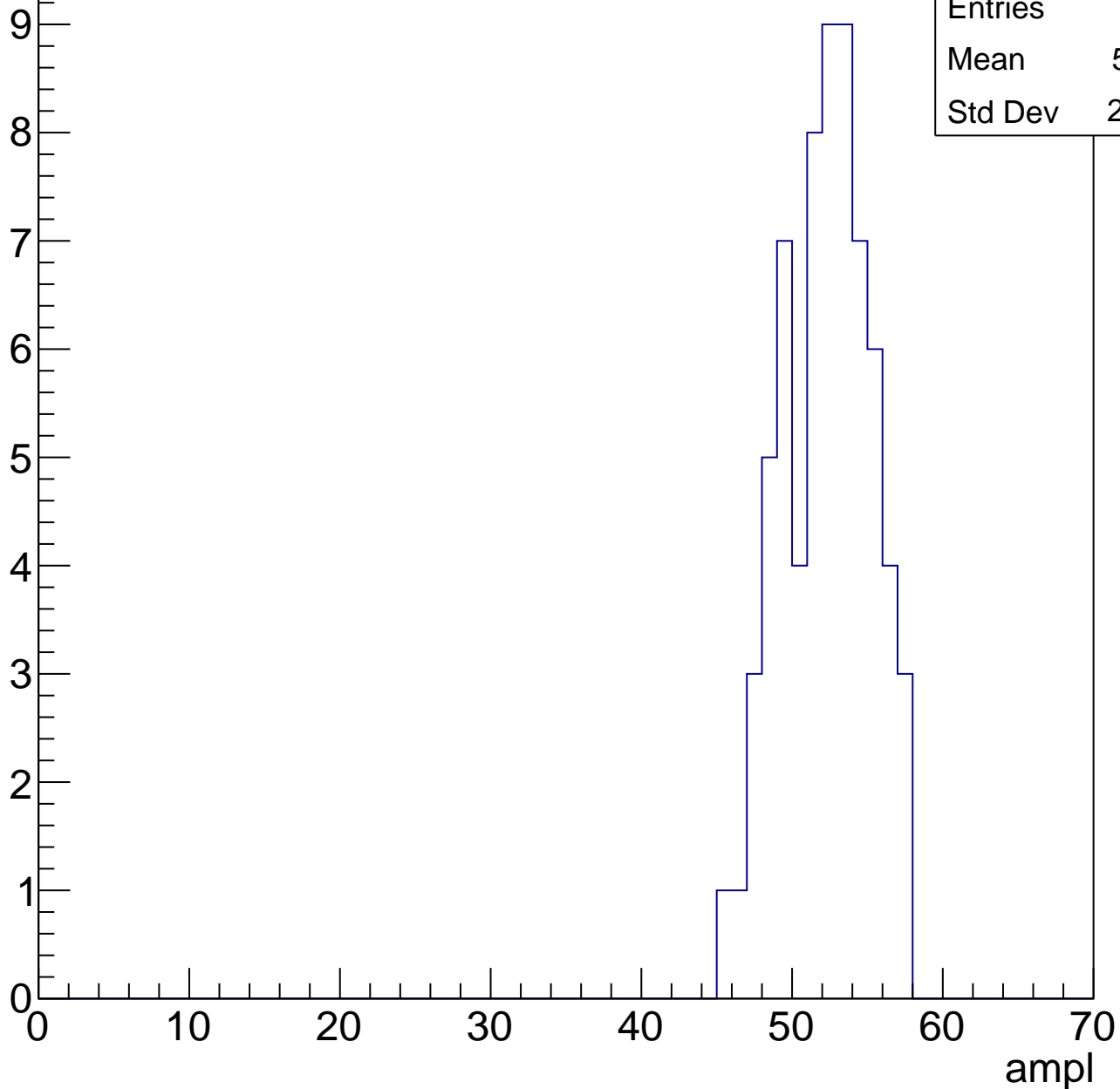
Entries	49
Mean	46.51
Std Dev	2.984



B1L002S, U7-ch102, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

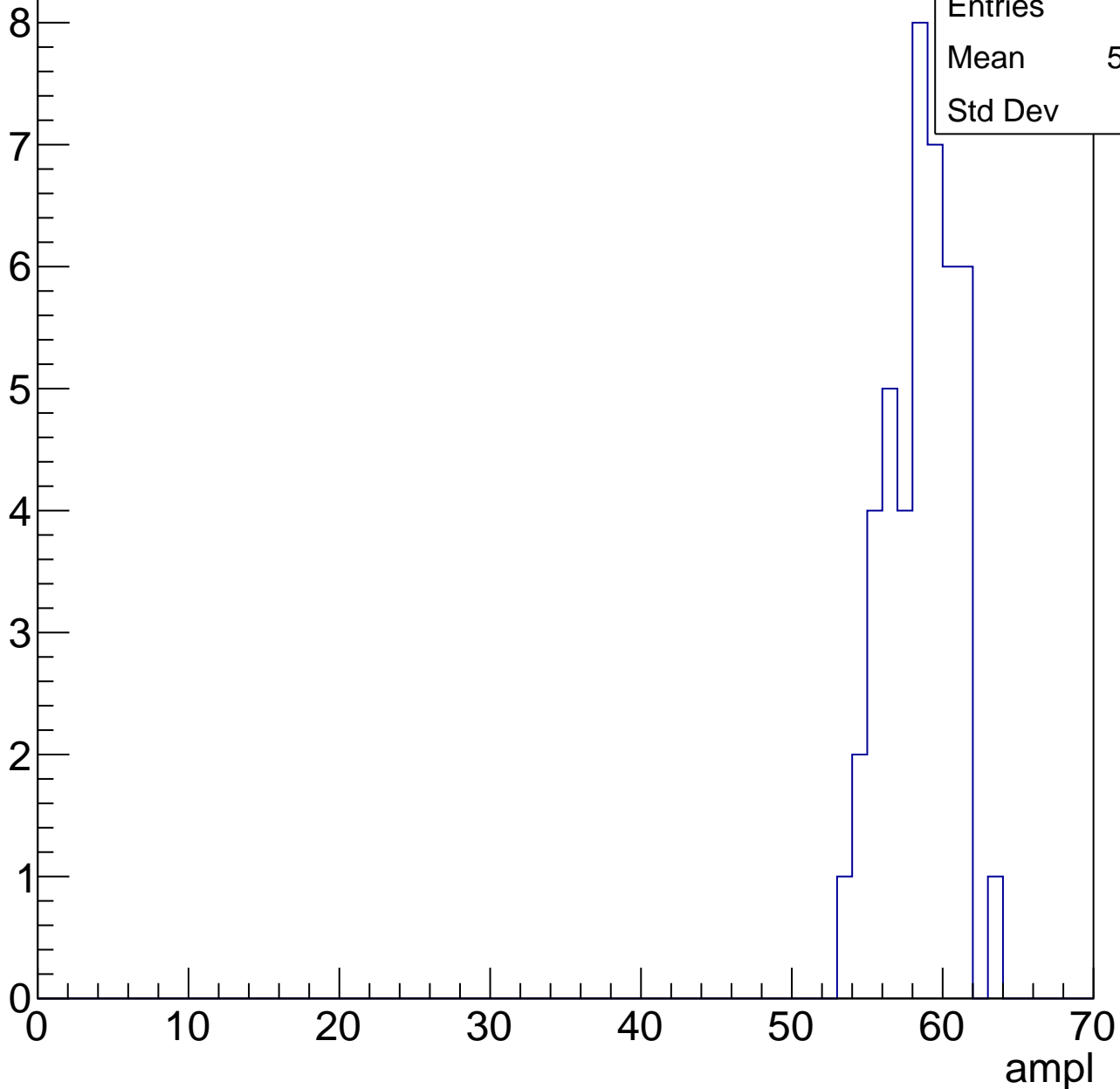


B1L002S, U7-ch102, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.07
Std Dev	2.27

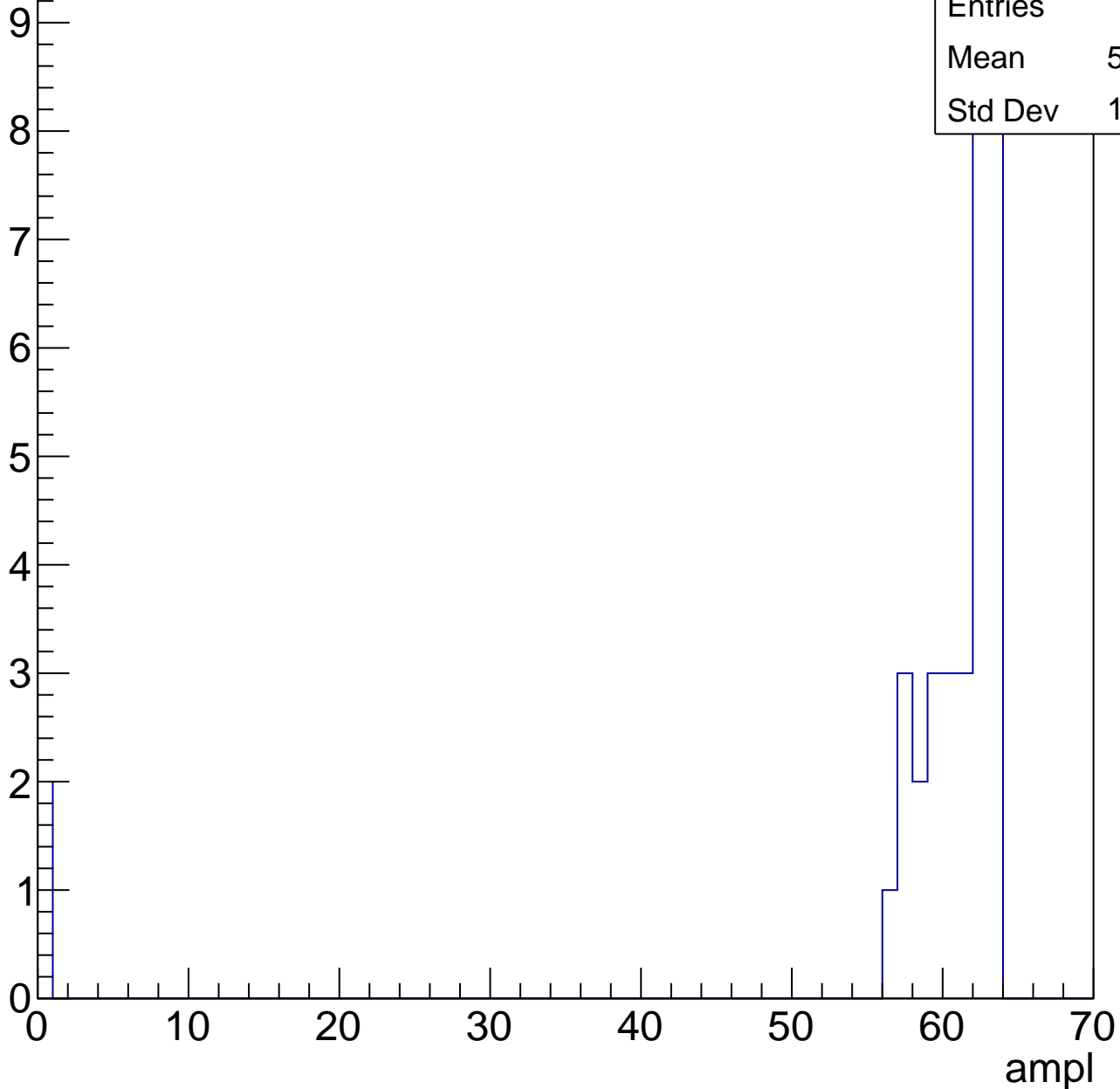


B1L002S, U7-ch102, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

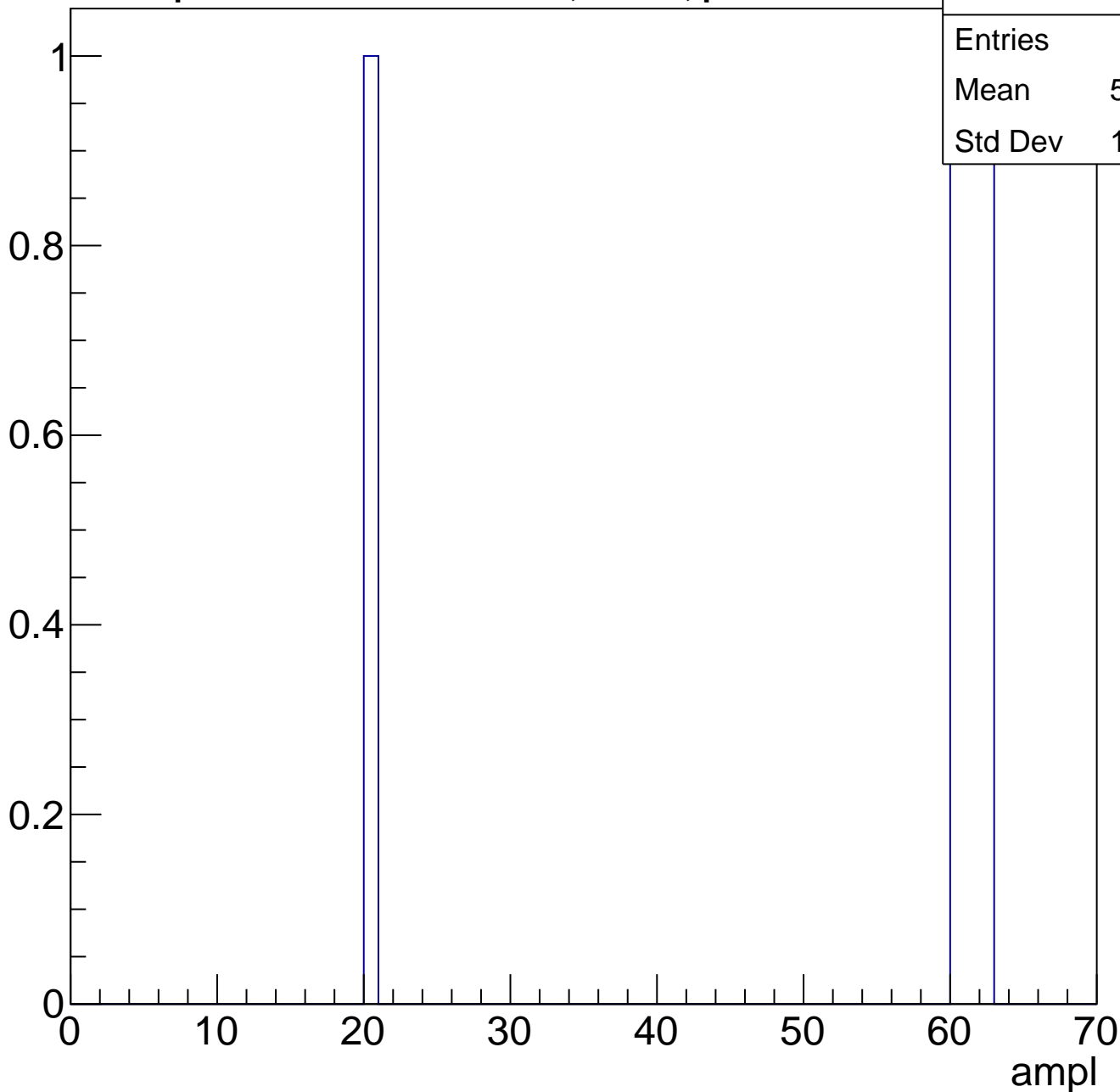
Entries	35
Mean	57.37
Std Dev	14.28



B1L002S, U7-ch102, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	50.75
Std Dev	17.77

B1L002S, U7-ch103, adc0

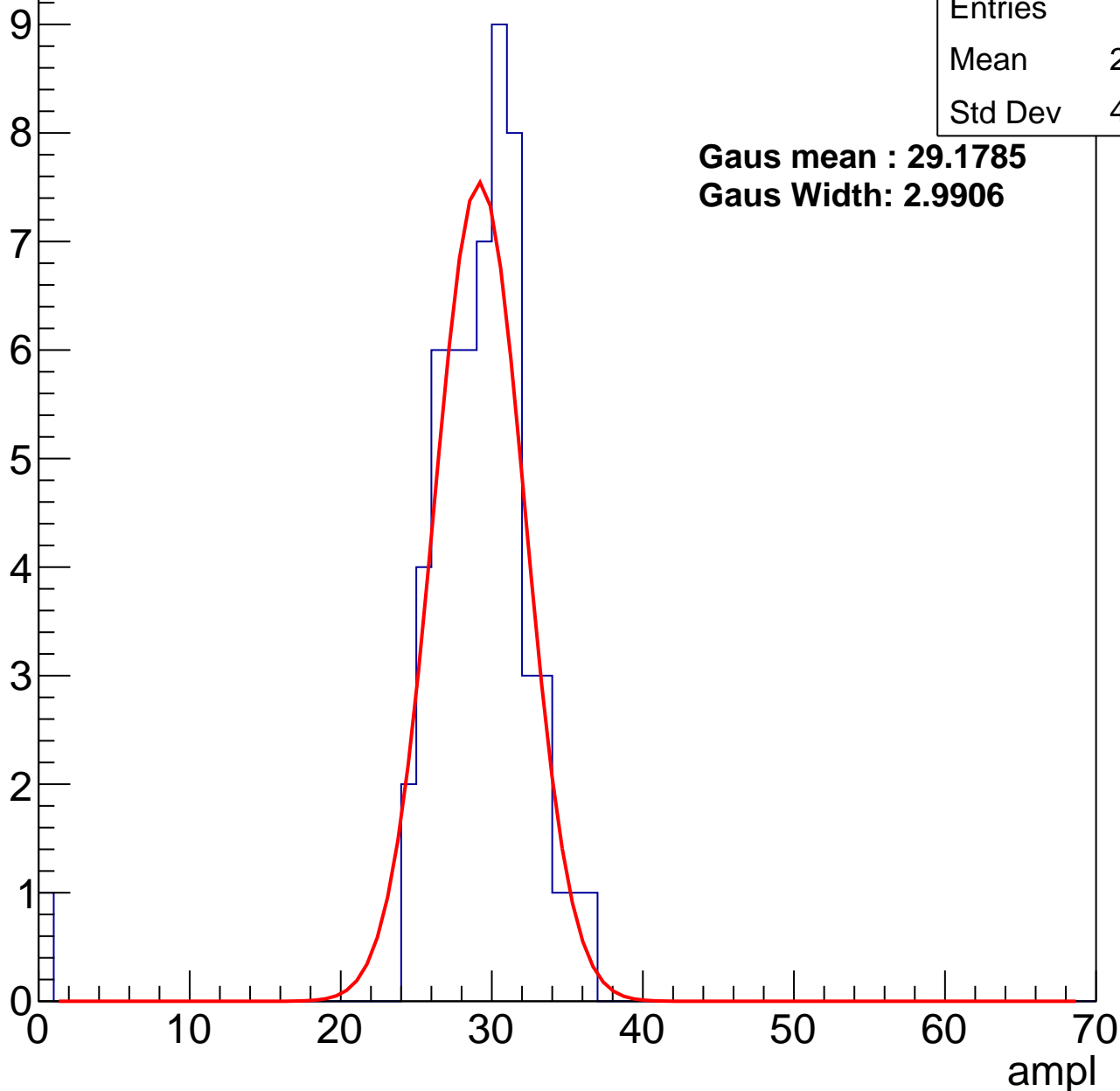
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	28.53
Std Dev	4.647

Gaus mean : 29.1785

Gaus Width: 2.9906



B1L002S, U7-ch103, adc1

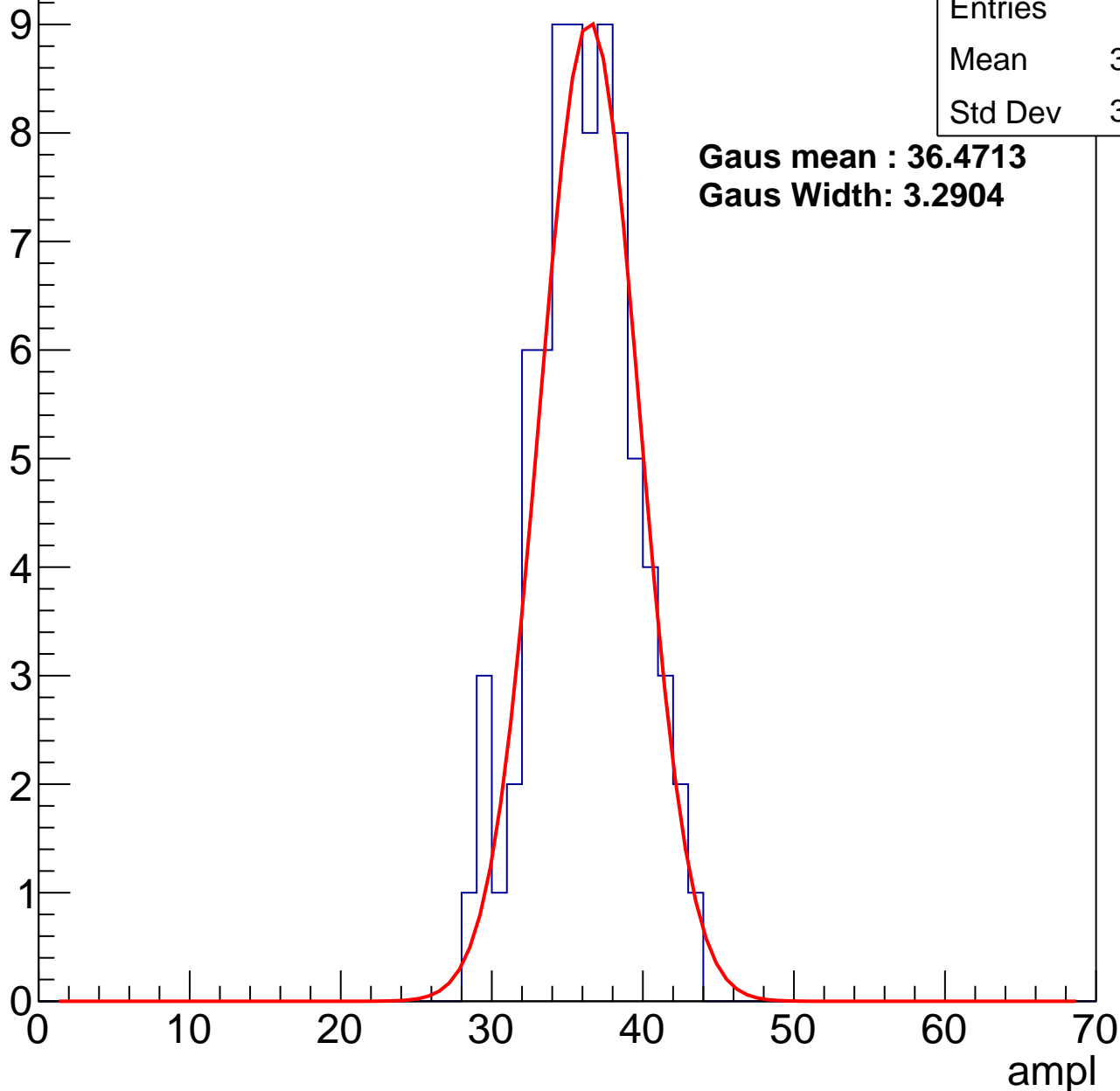
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	35.69
Std Dev	3.284

Gaus mean : 36.4713

Gaus Width: 3.2904



B1L002S, U7-ch103, adc2

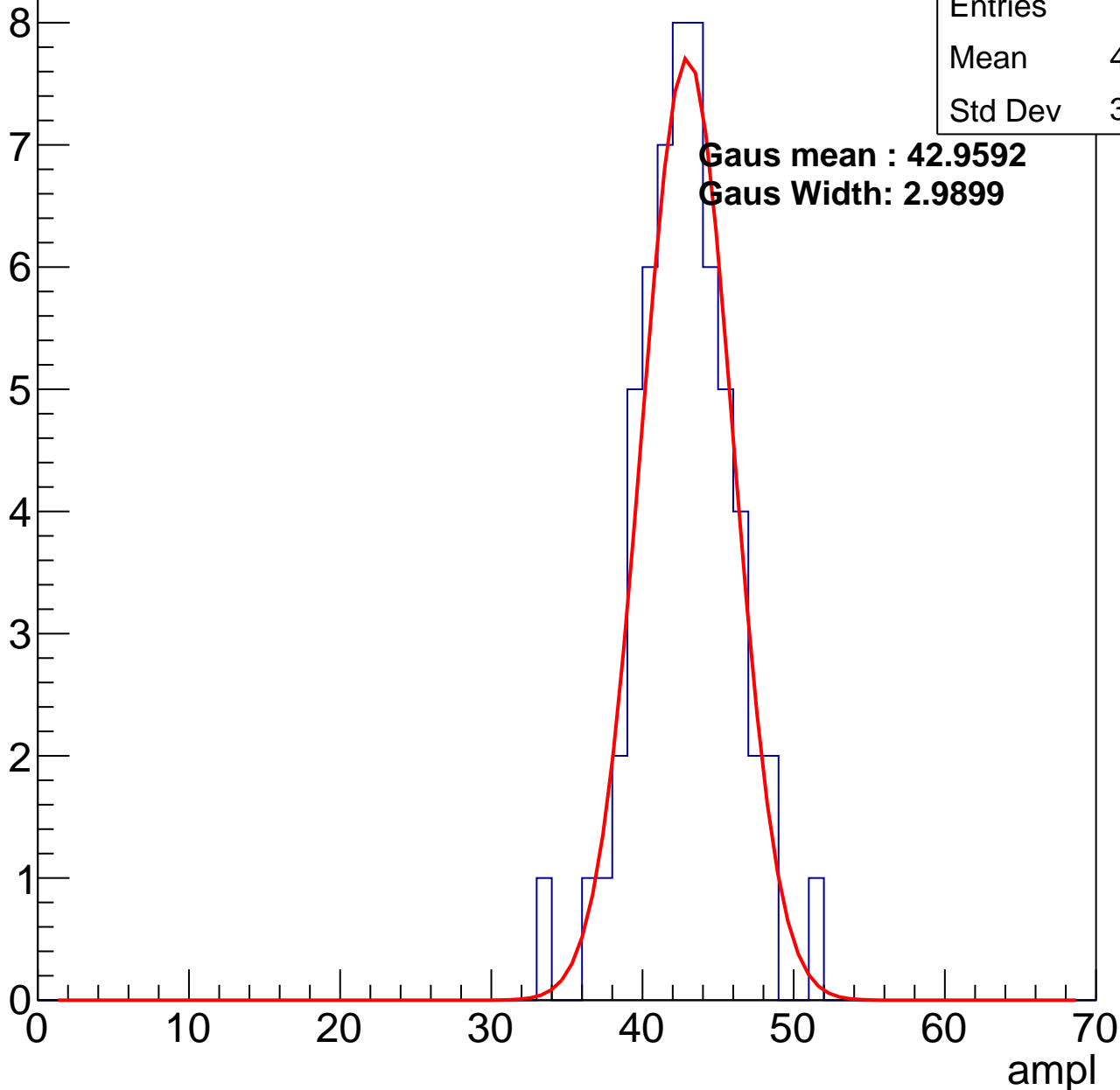
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.34
Std Dev	3.155

Gaus mean : 42.9592

Gaus Width: 2.9899

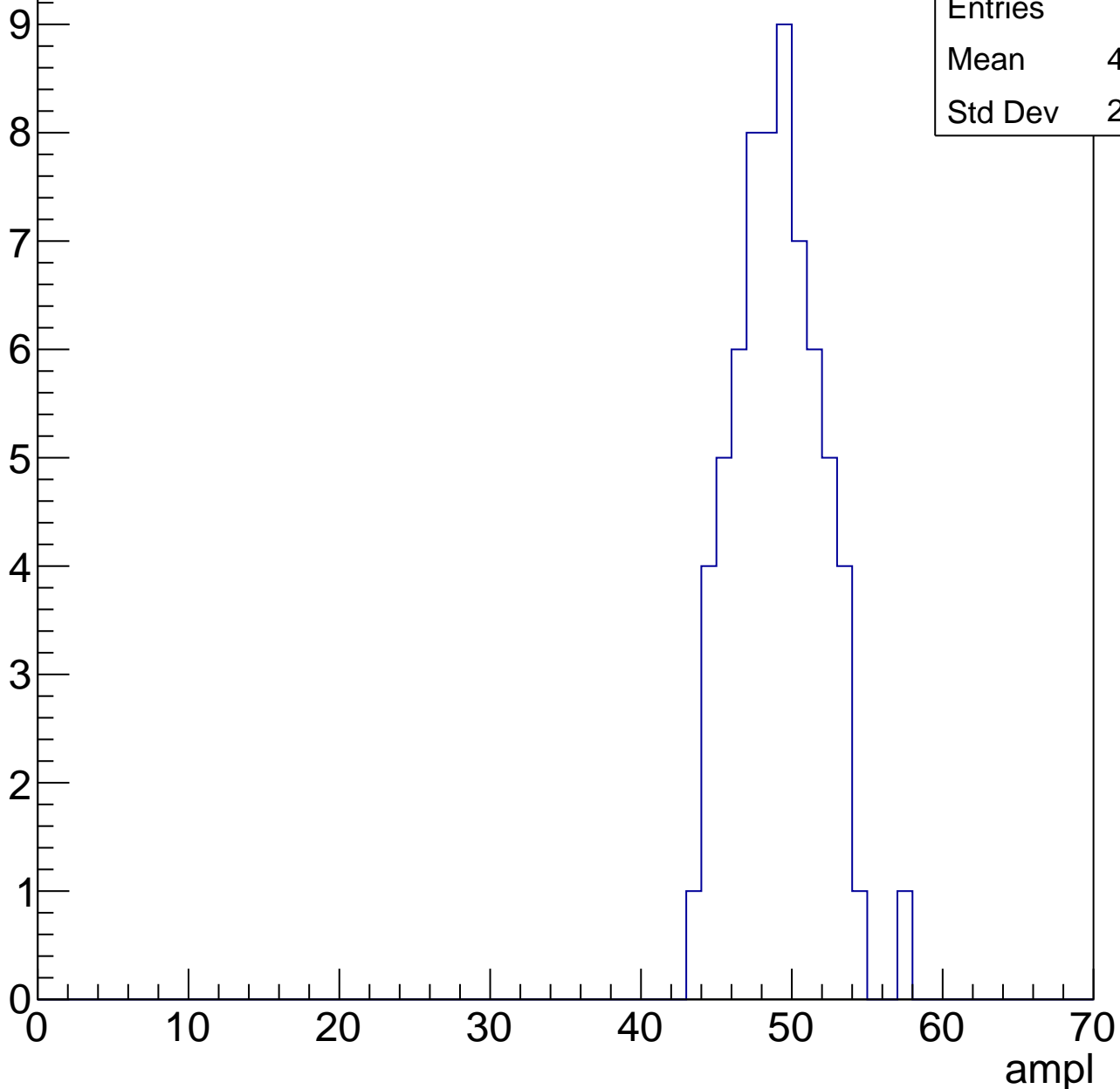


B1L002S, U7-ch103, adc3

calib_packv5_042523_0143.root, FC#10, port B3

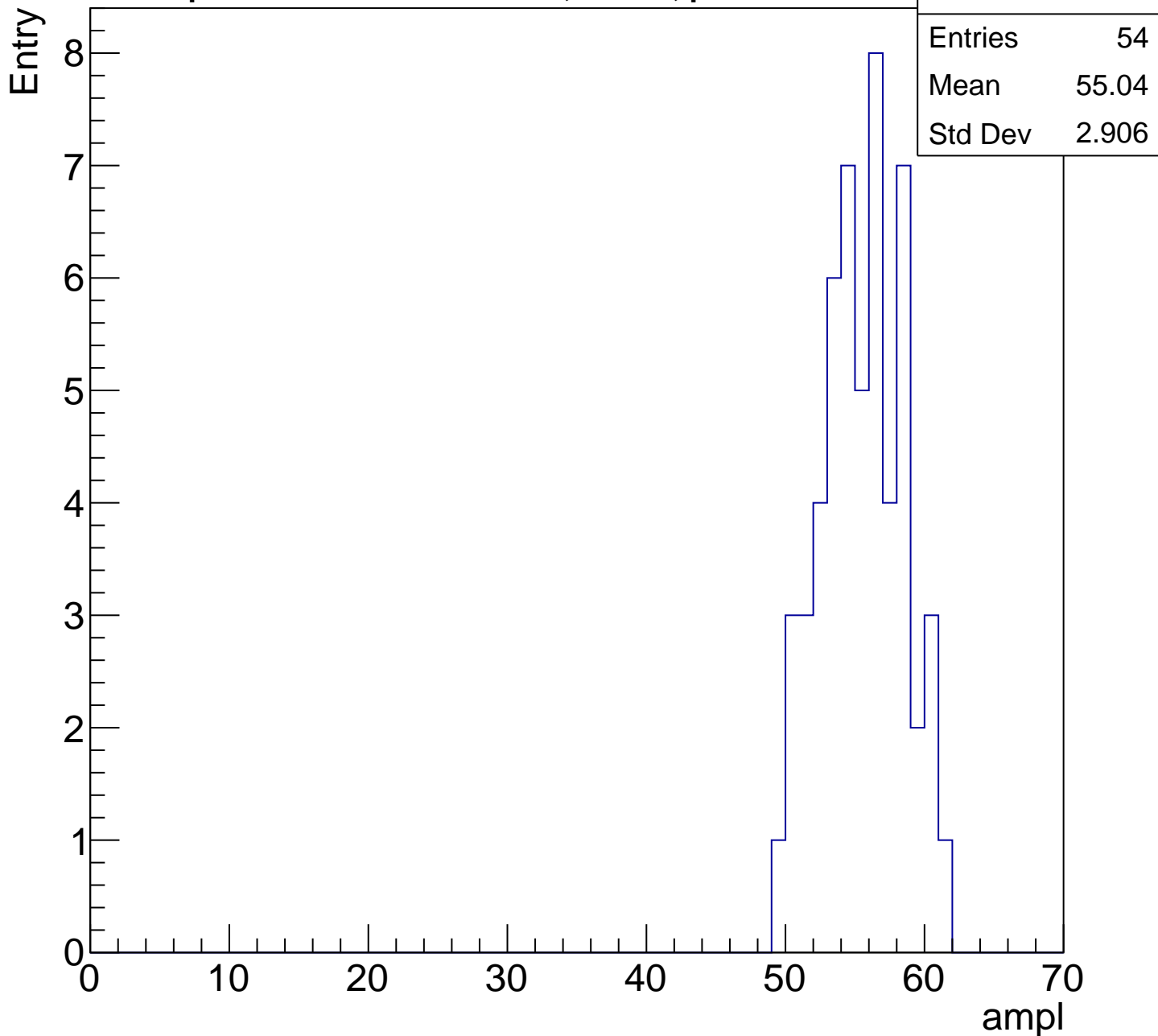
Entry

Entries	65
Mean	48.62
Std Dev	2.854



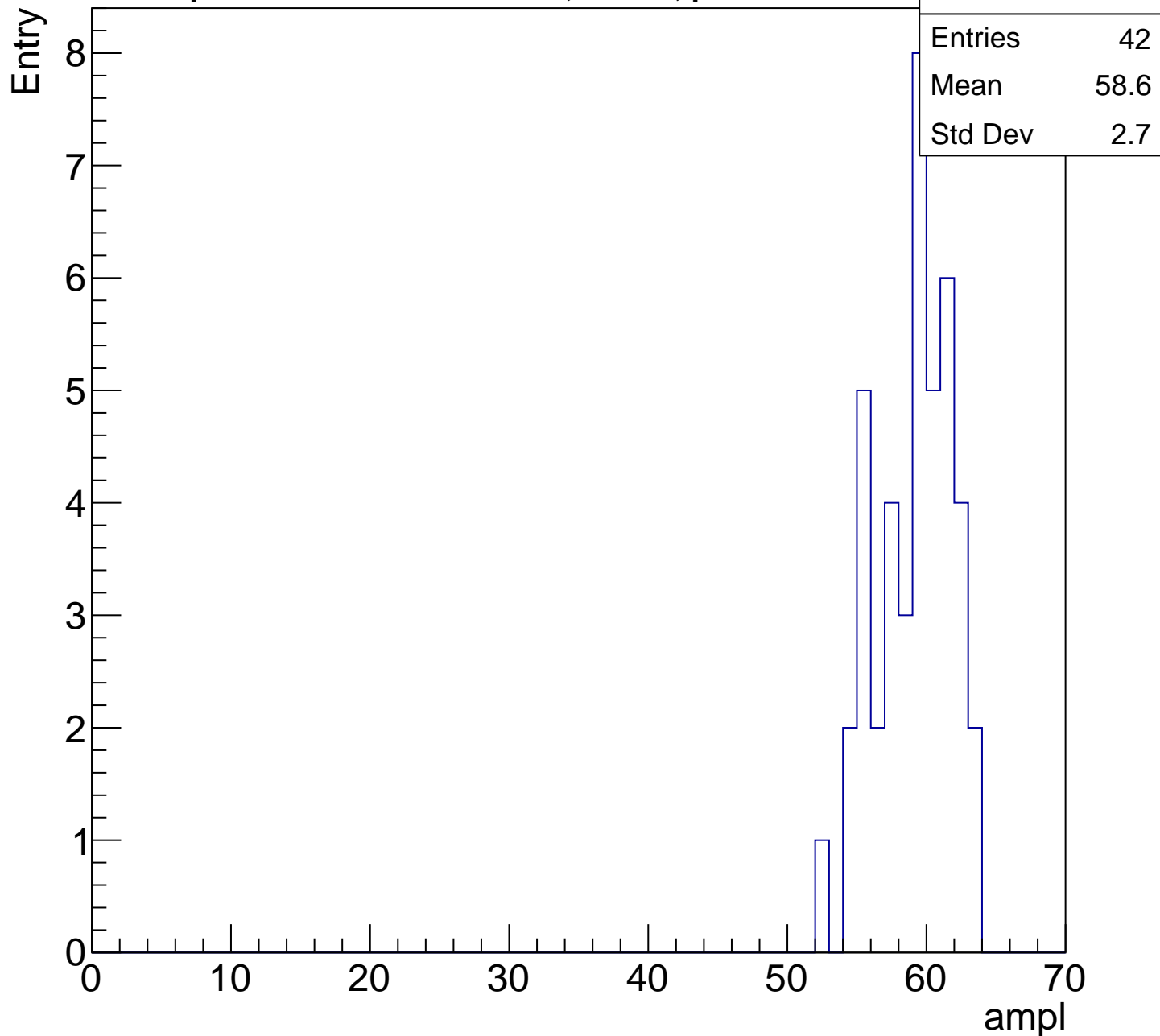
B1L002S, U7-ch103, adc4

calib_packv5_042523_0143.root, FC#10, port B3



B1L002S, U7-ch103, adc5

calib_packv5_042523_0143.root, FC#10, port B3

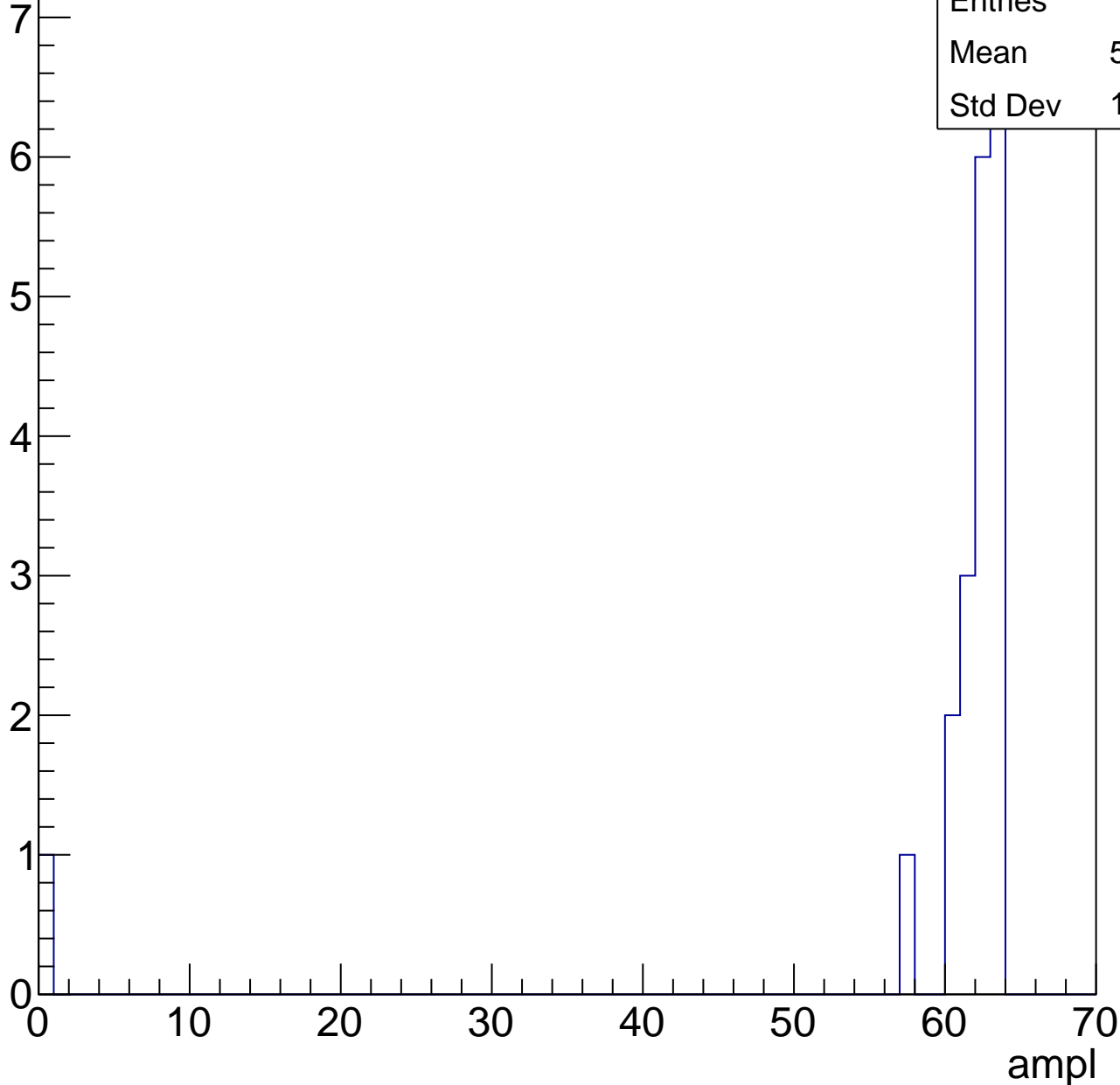


B1L002S, U7-ch103, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.65
Std Dev	13.53



B1L002S, U7-ch103, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch104, adc0

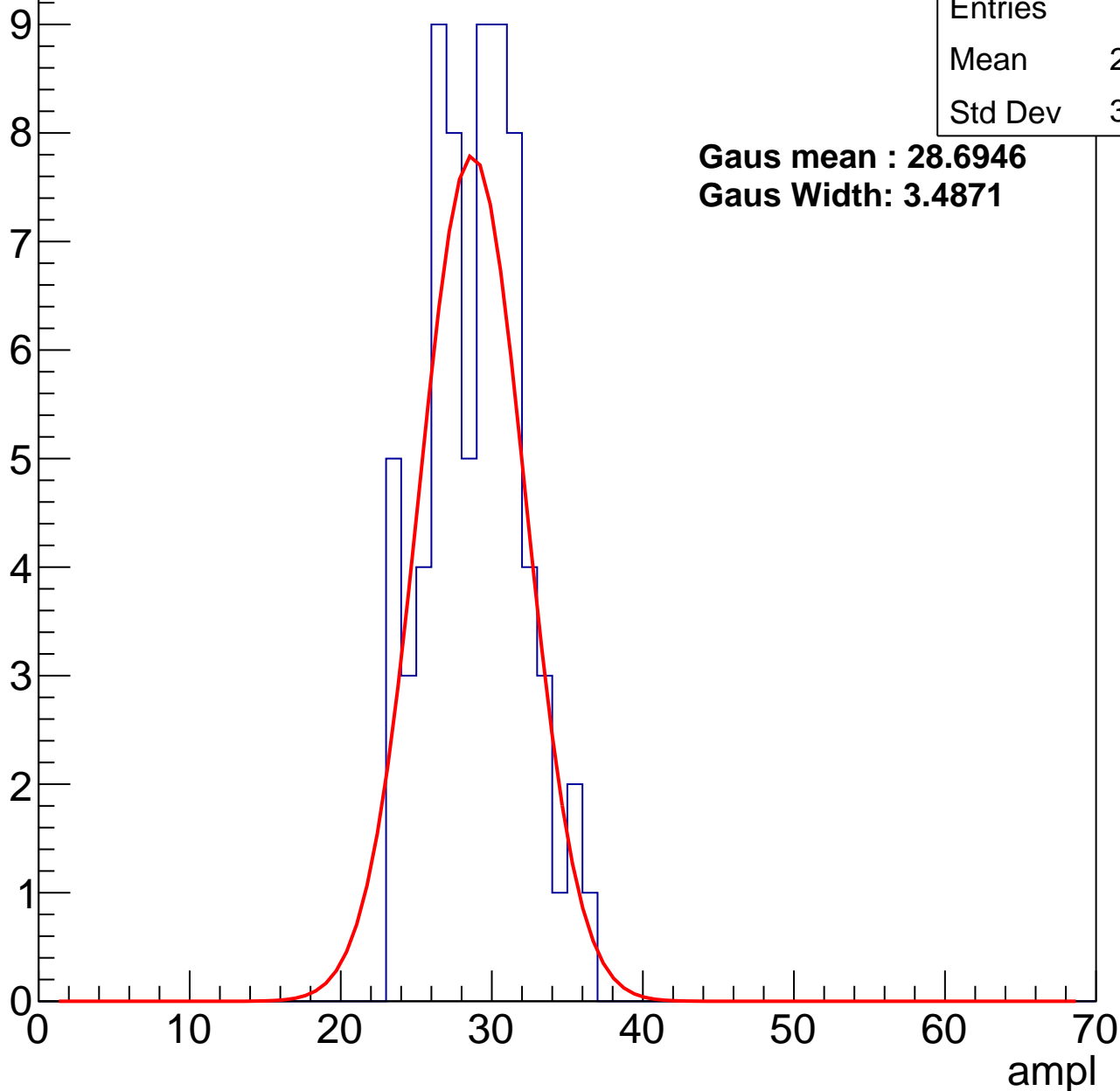
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.49
Std Dev	3.117

Gaus mean : 28.6946

Gaus Width: 3.4871



B1L002S, U7-ch104, adc1

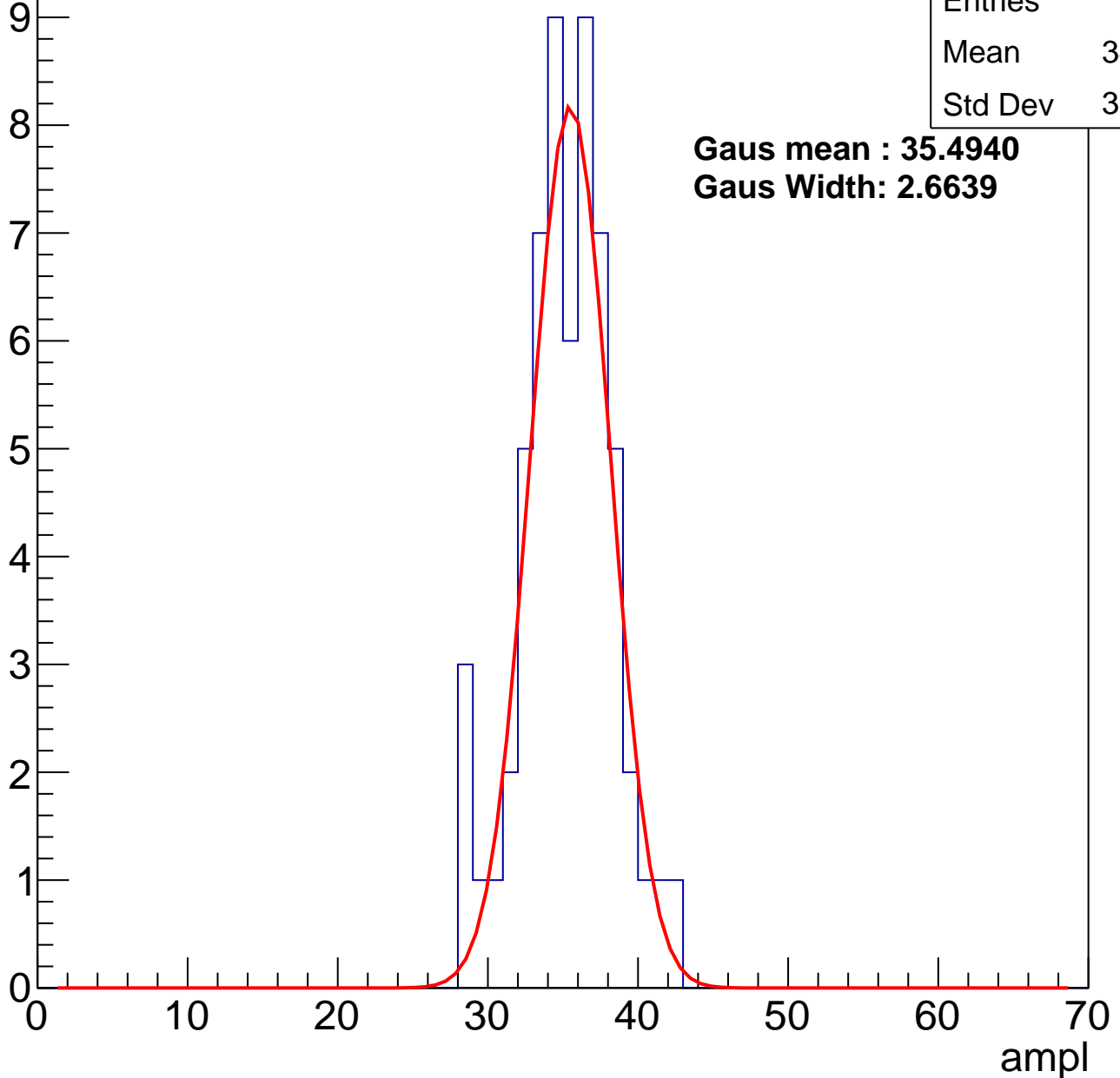
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.77
Std Dev	3.008

Gaus mean : 35.4940

Gaus Width: 2.6639



B1L002S, U7-ch104, adc2

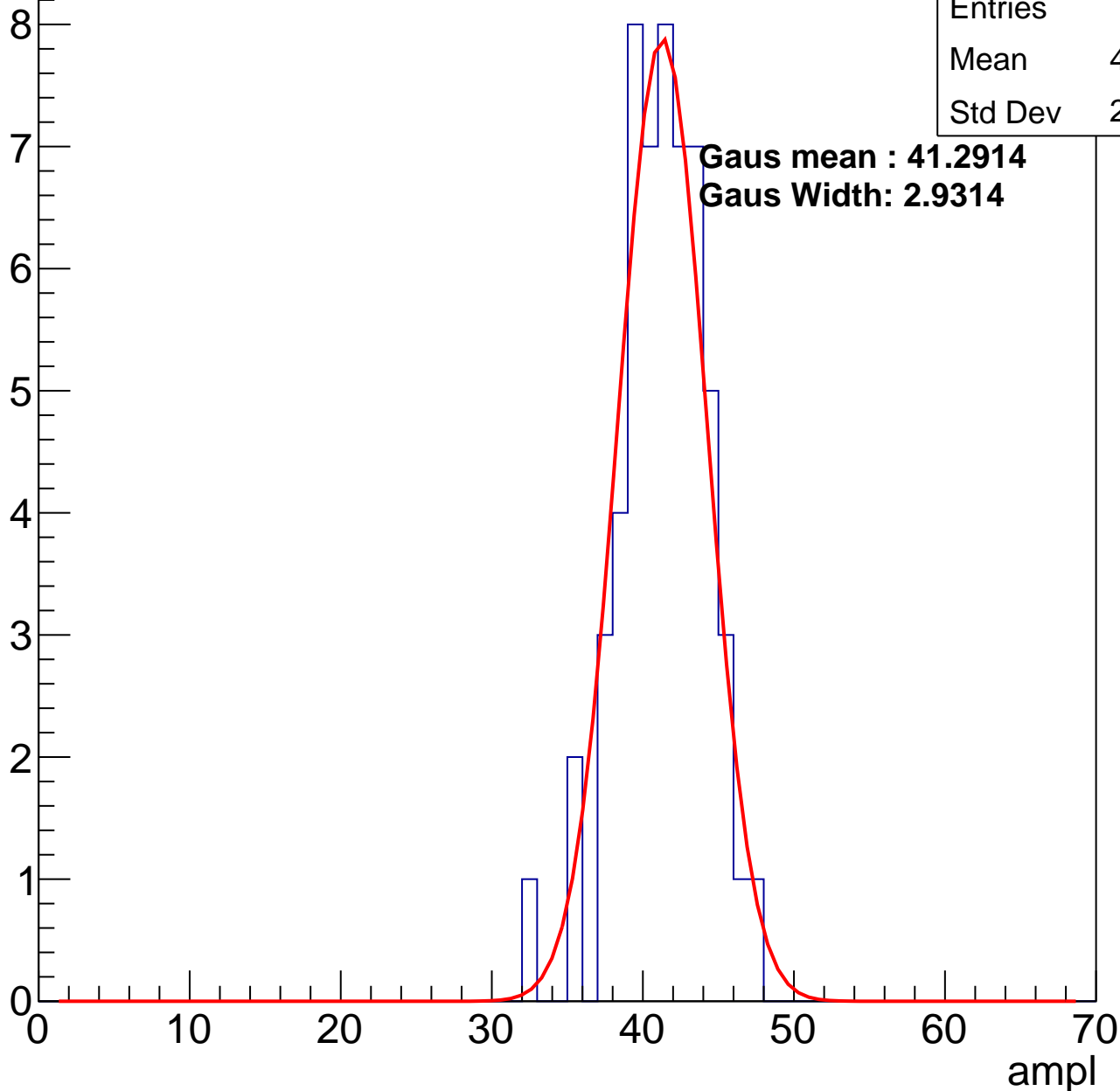
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.84
Std Dev	2.852

Gaus mean : 41.2914

Gaus Width: 2.9314

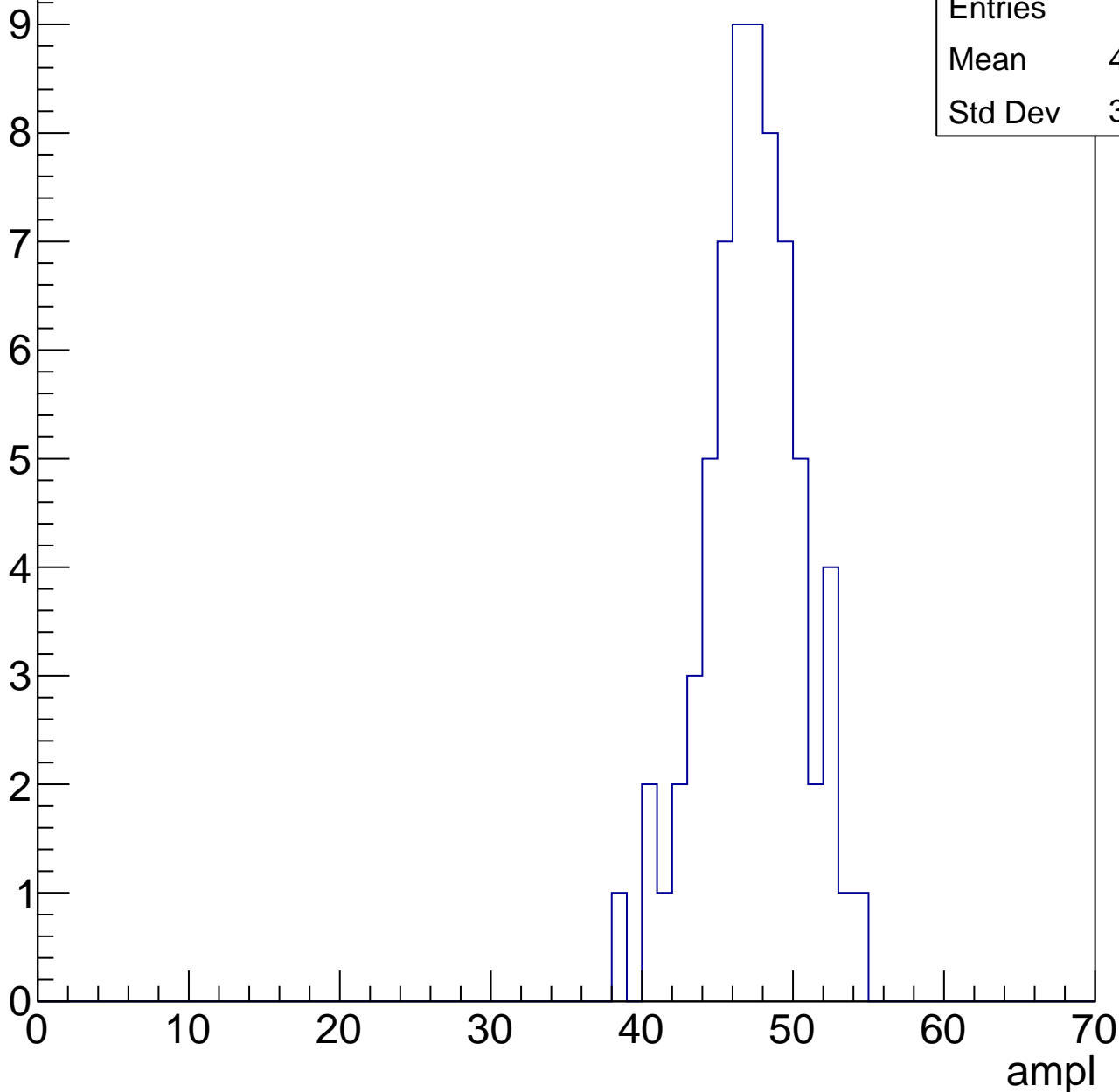


B1L002S, U7-ch104, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	46.84
Std Dev	3.212

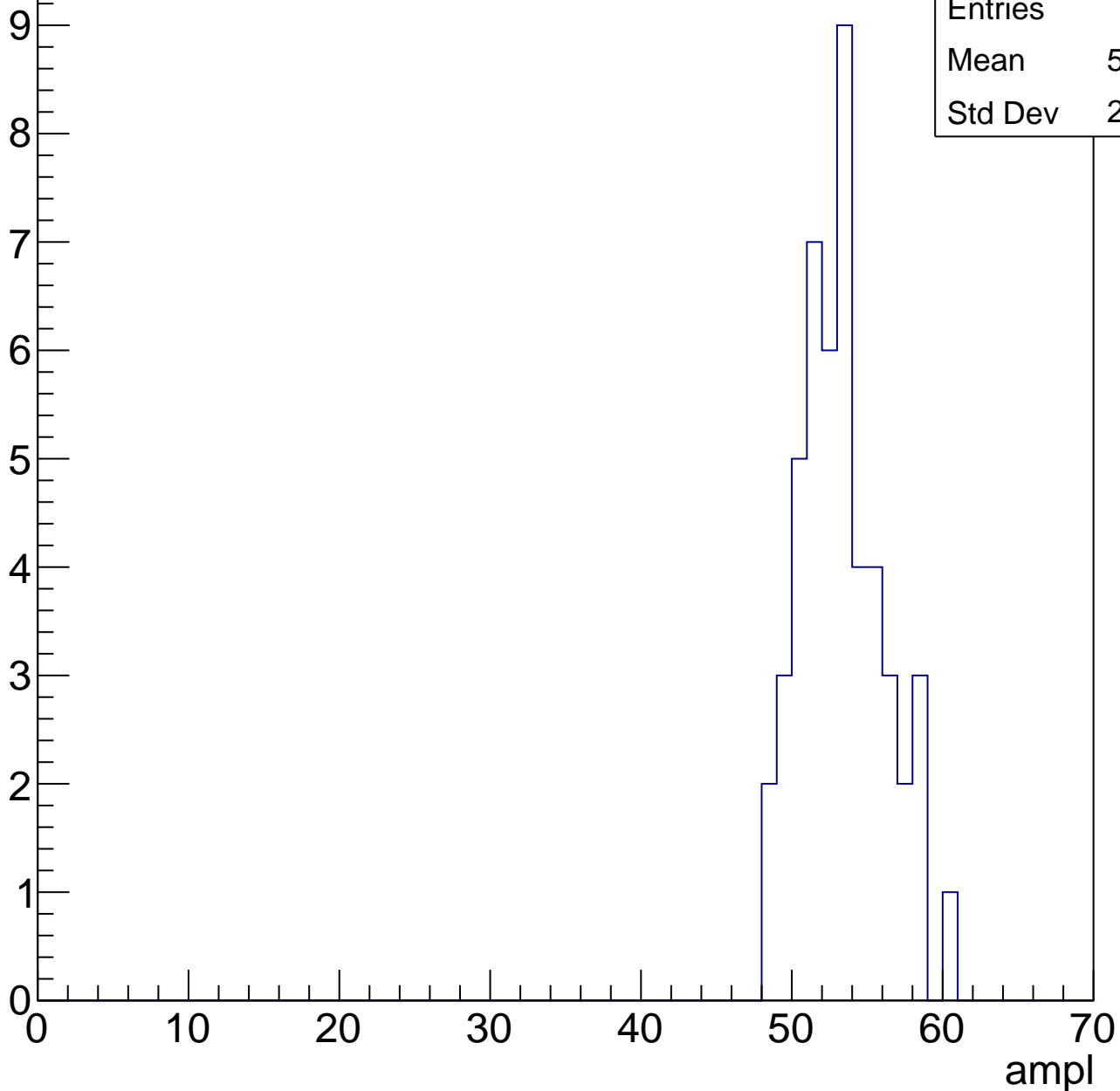


B1L002S, U7-ch104, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.88
Std Dev	2.782

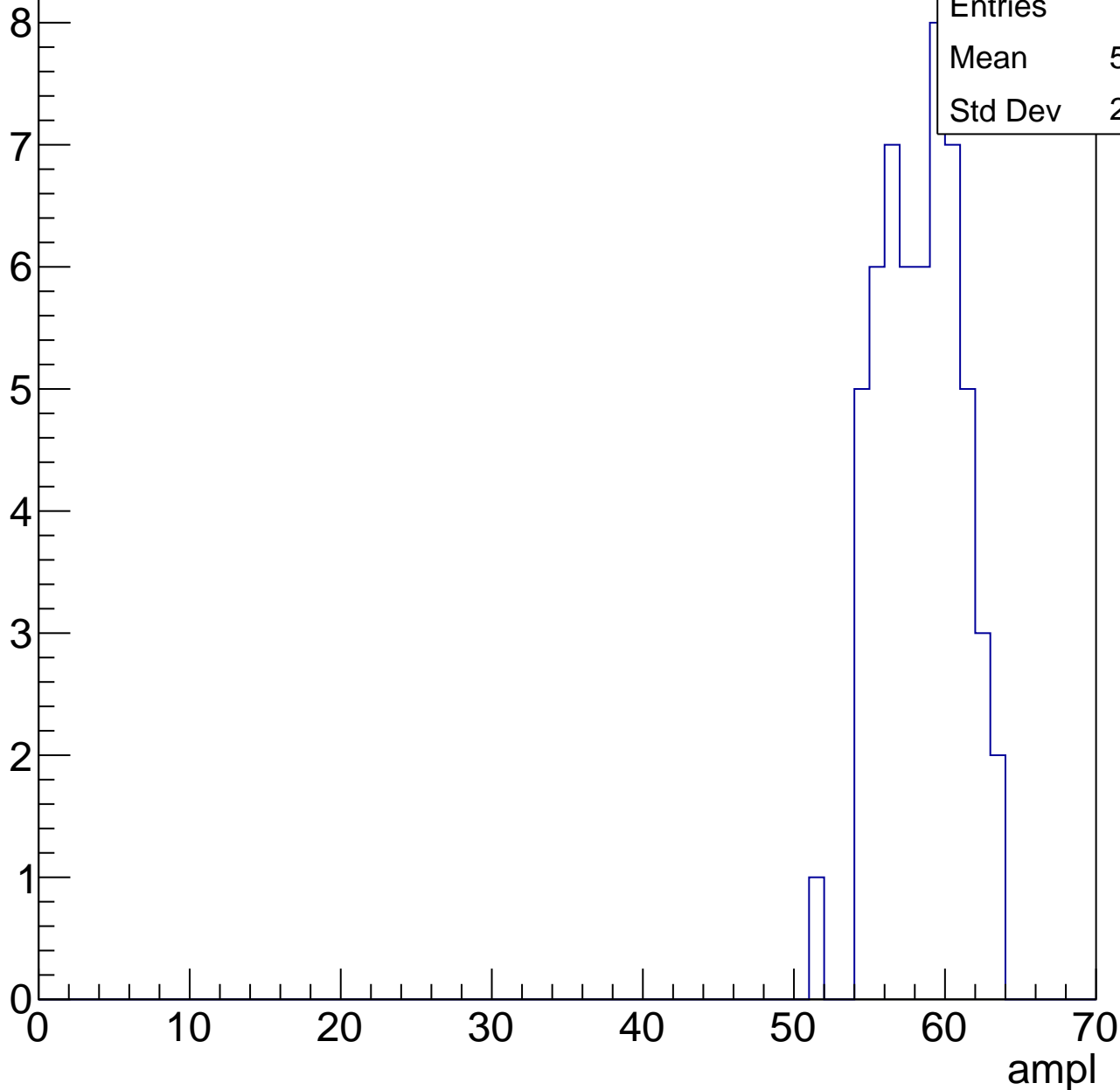


B1L002S, U7-ch104, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.89
Std Dev	2.657

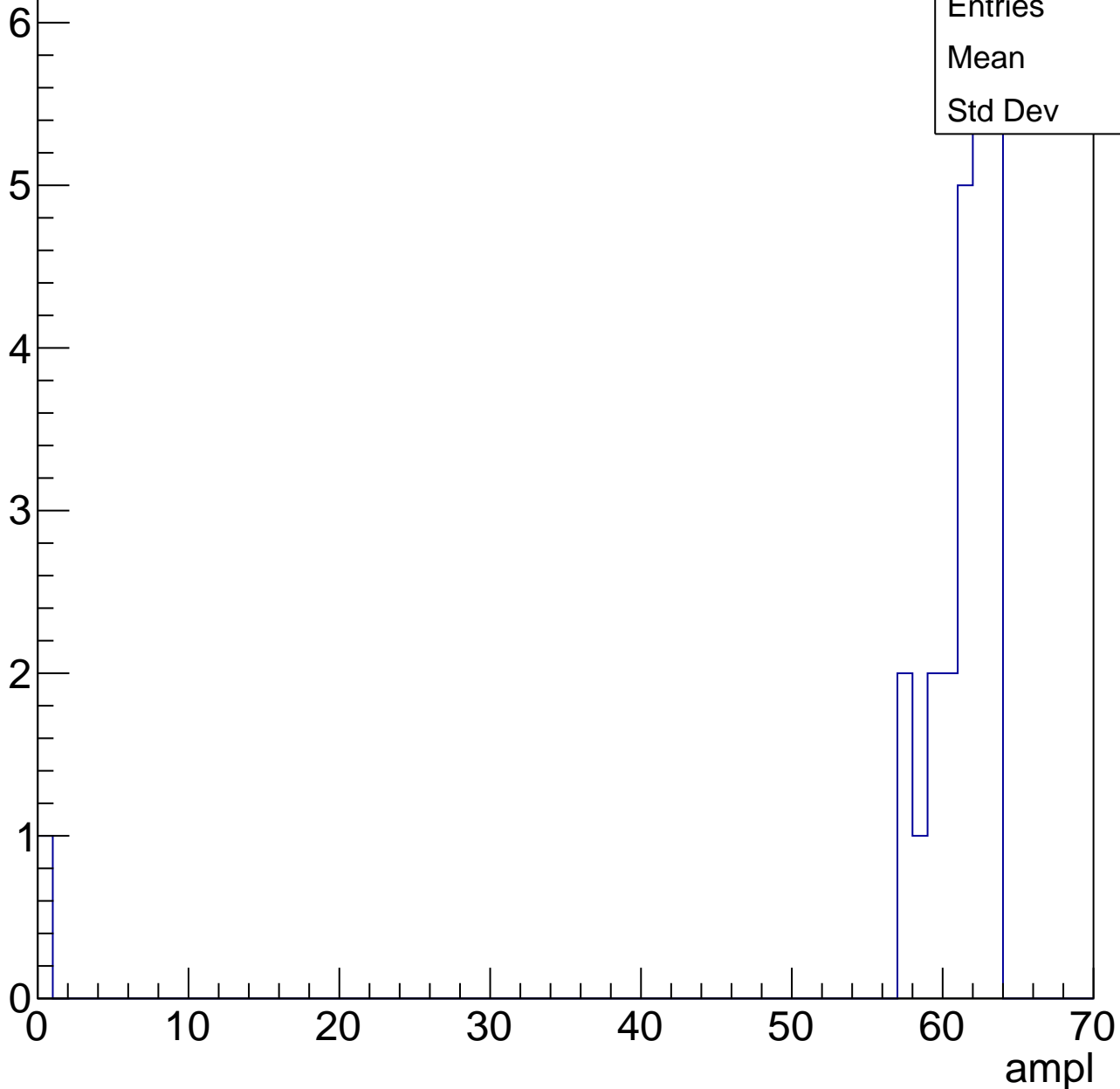


B1L002S, U7-ch104, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

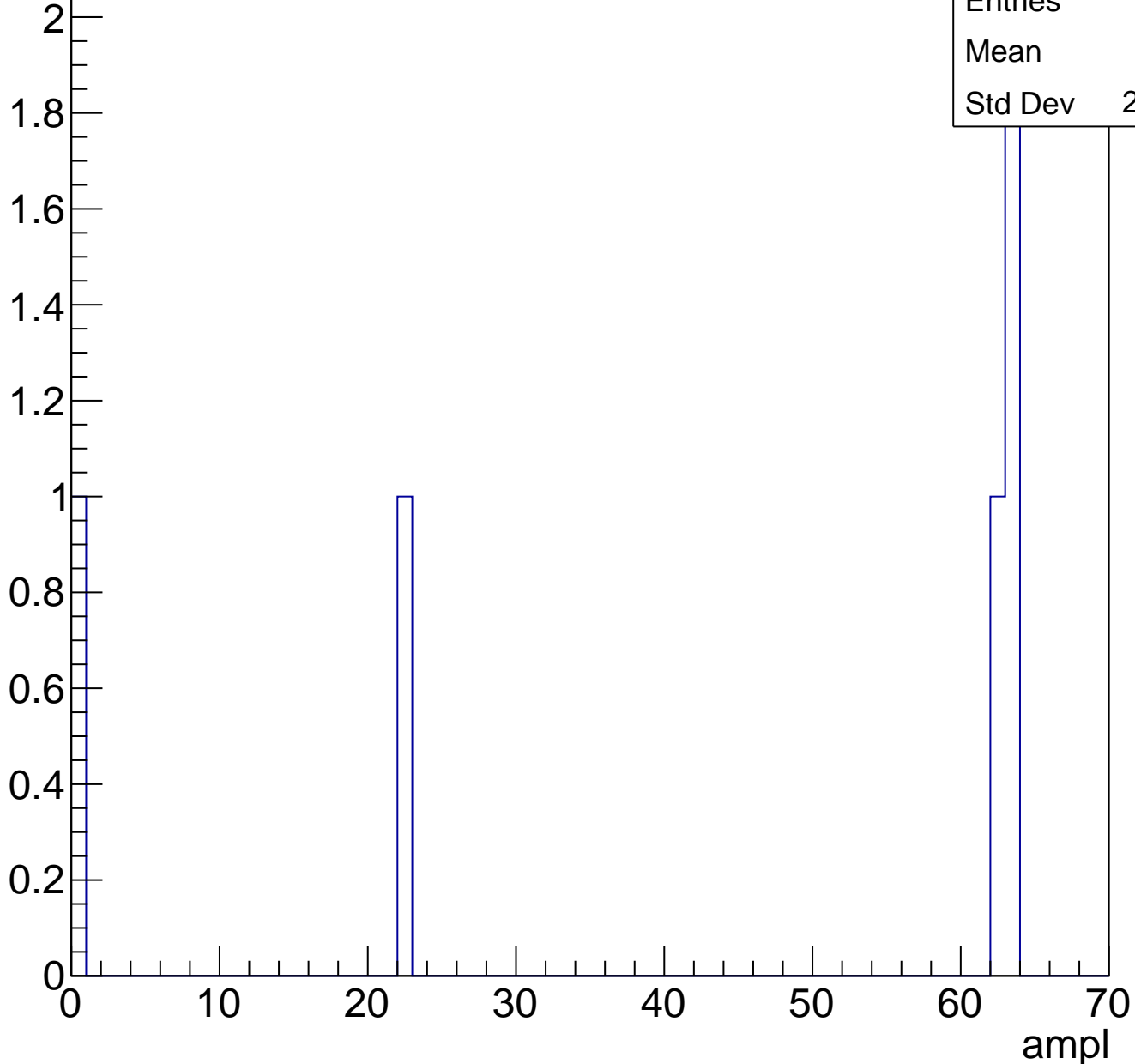
Entries	25
Mean	58.6
Std Dev	12.1



B1L002S, U7-ch104, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	5
Mean	42
Std Dev	26.25

B1L002S, U7-ch105, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	78
Mean	28.95
Std Dev	3.445

Gaus mean : 29.5271

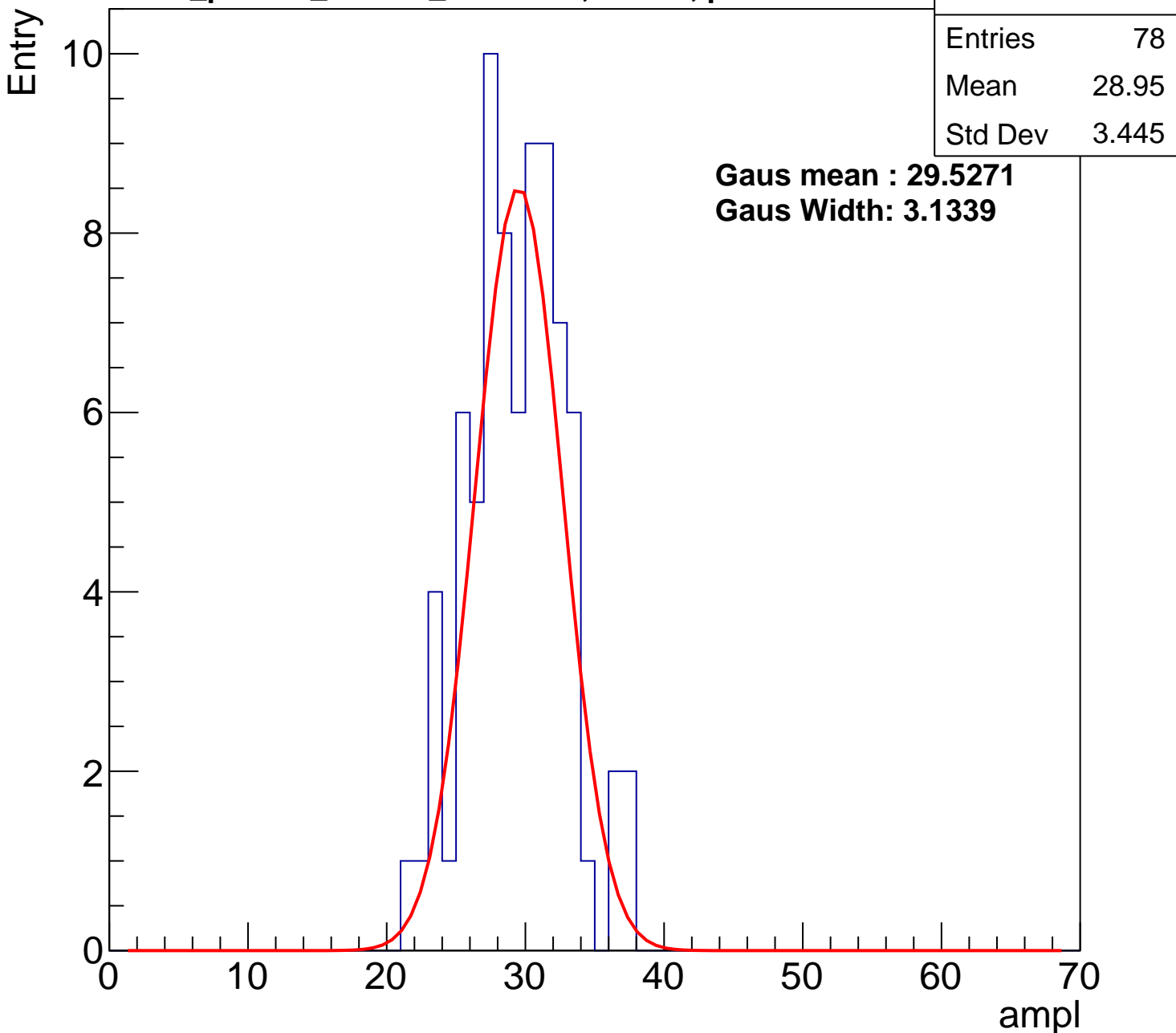
Gaus Width: 3.1339

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch105, adc1

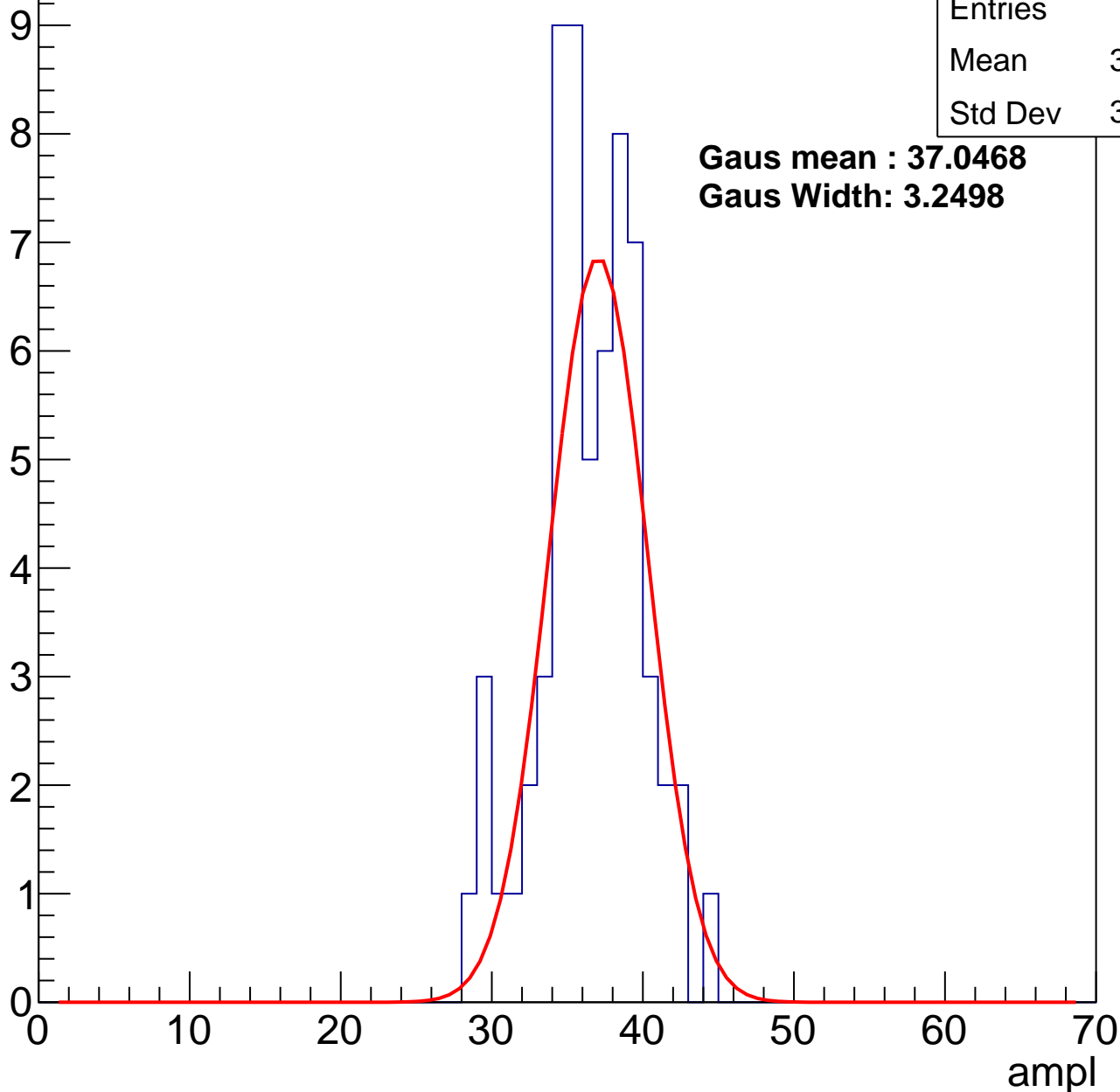
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.02
Std Dev	3.359

Gaus mean : 37.0468

Gaus Width: 3.2498



B1L002S, U7-ch105, adc2

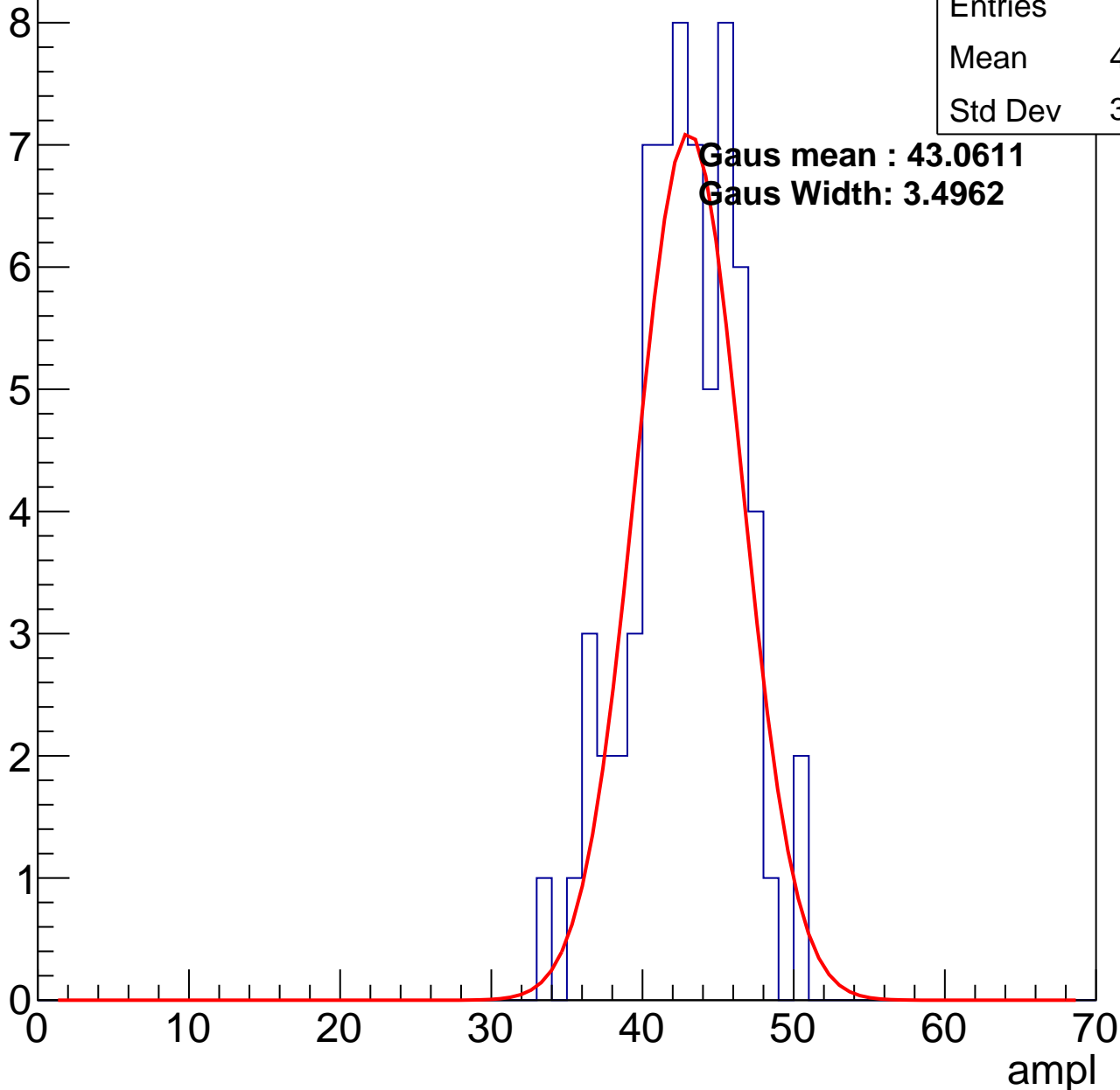
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	42.37
Std Dev	3.523

Gaus mean : 43.0611

Gaus Width: 3.4962

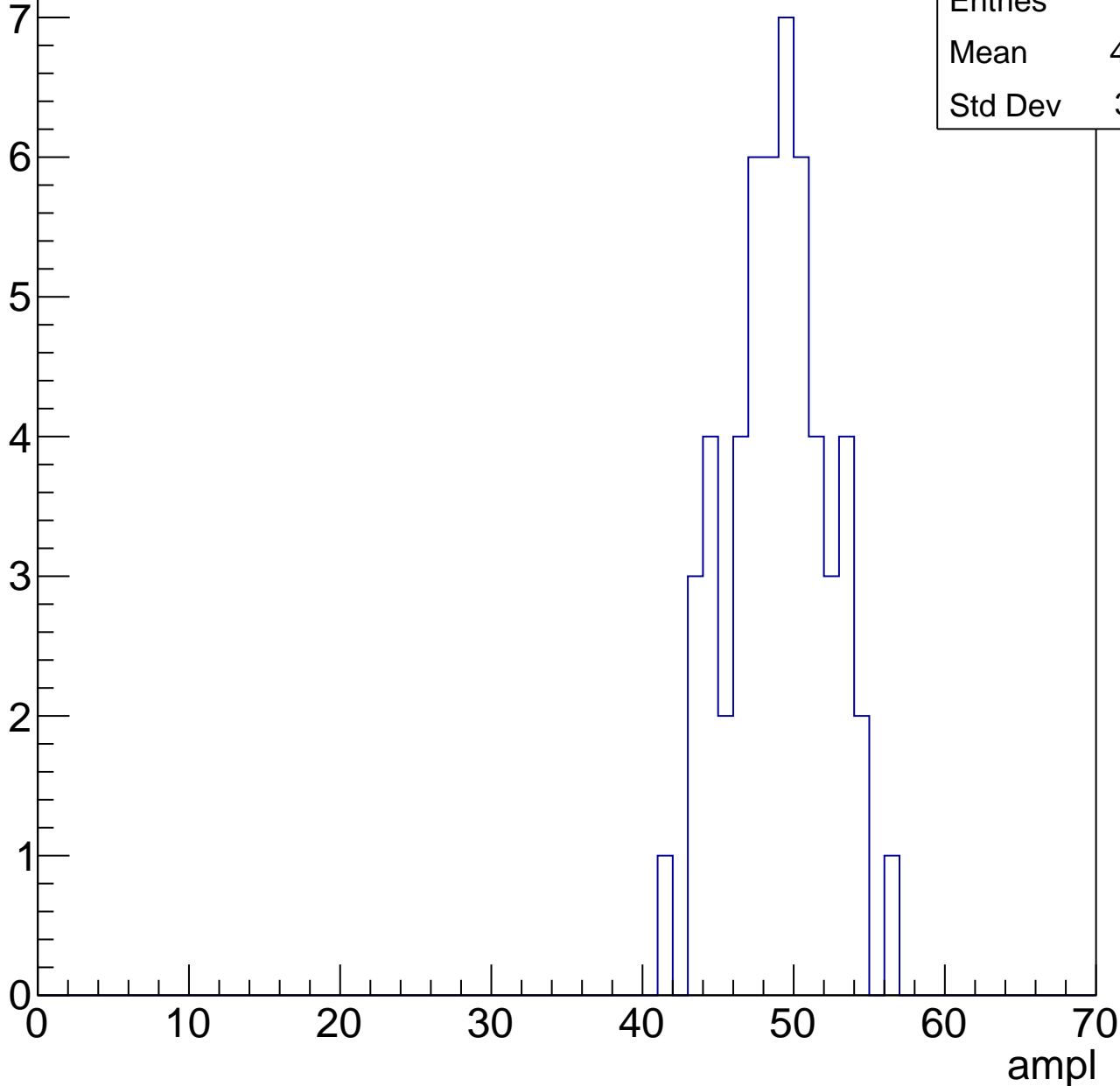


B1L002S, U7-ch105, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	48.47
Std Dev	3.271

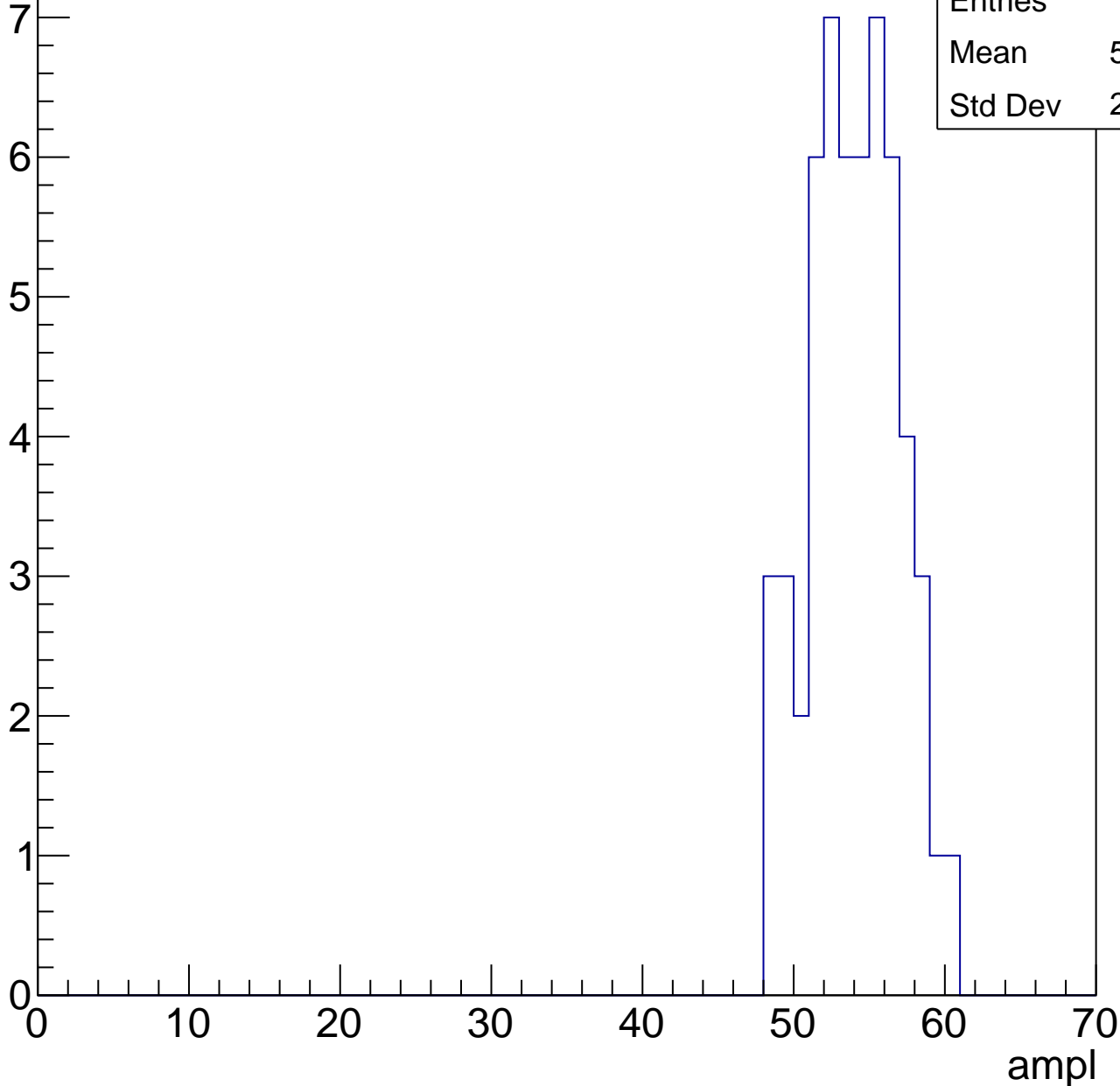


B1L002S, U7-ch105, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.55
Std Dev	2.916

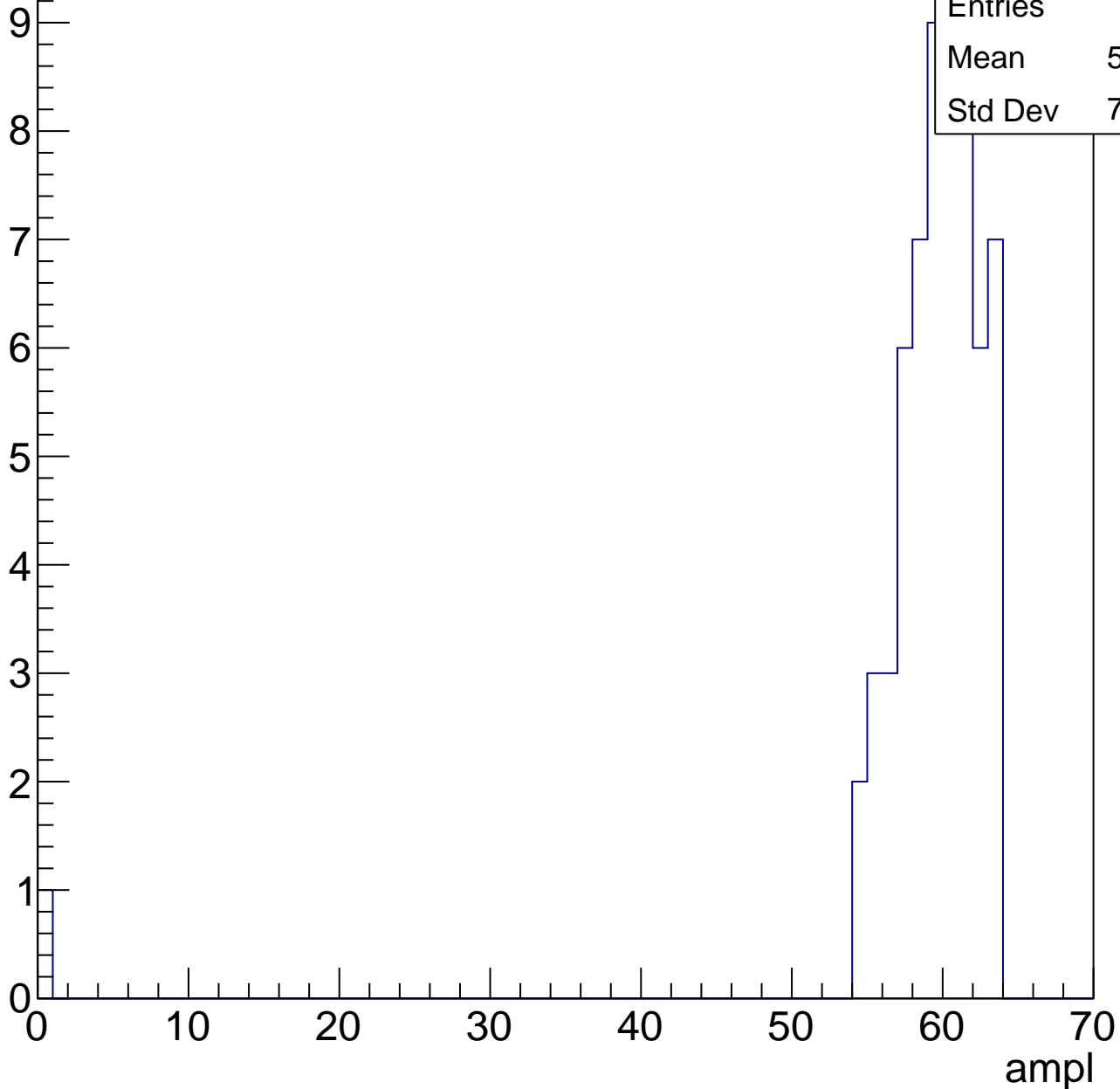


B1L002S, U7-ch105, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.42
Std Dev	7.857



B1L002S, U7-ch105, adc6

calib_packv5_042523_0143.root, FC#10, port B3

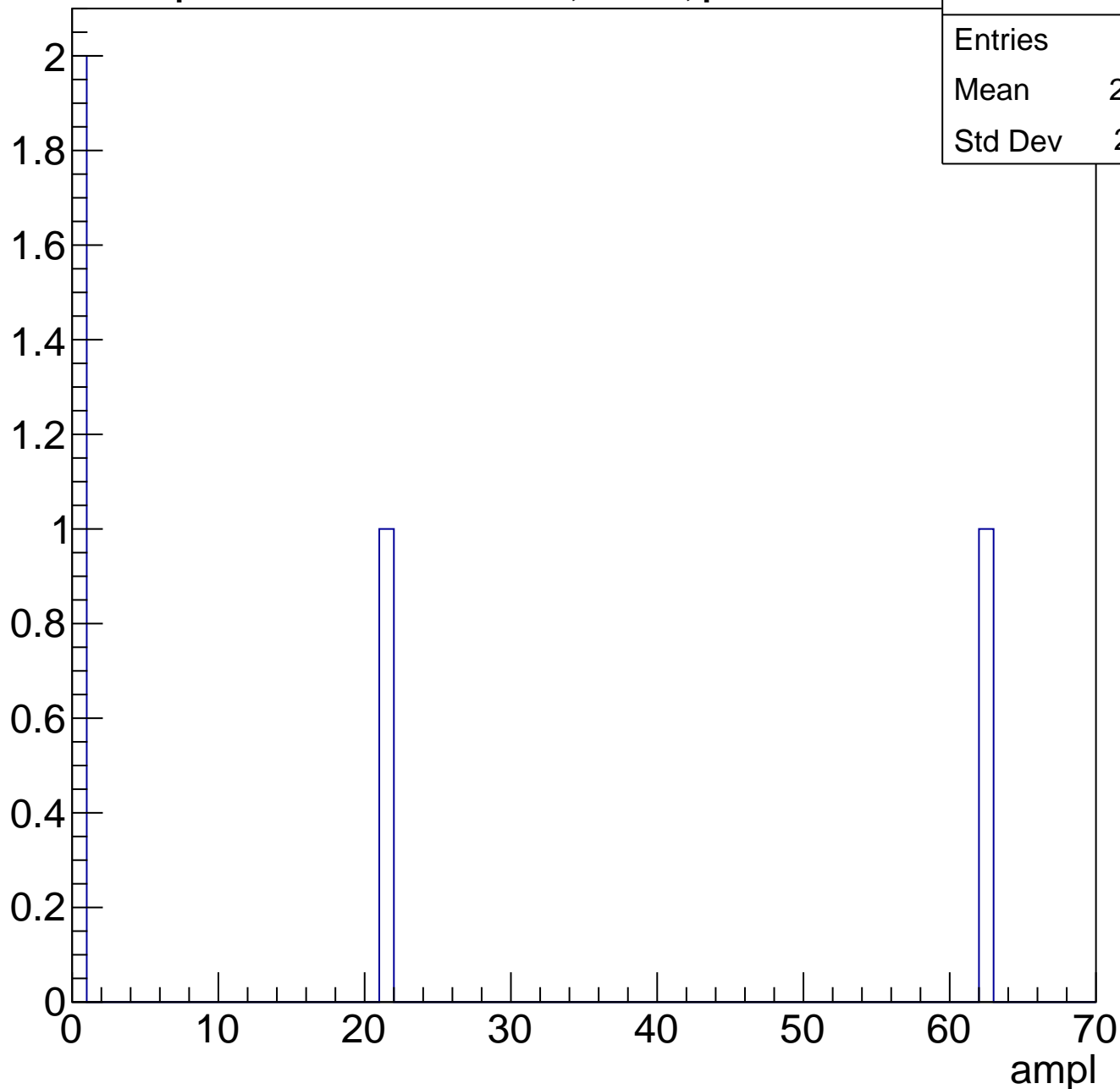
Entry



B1L002S, U7-ch105, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.75
Std Dev	25.31

B1L002S, U7-ch106, adc0

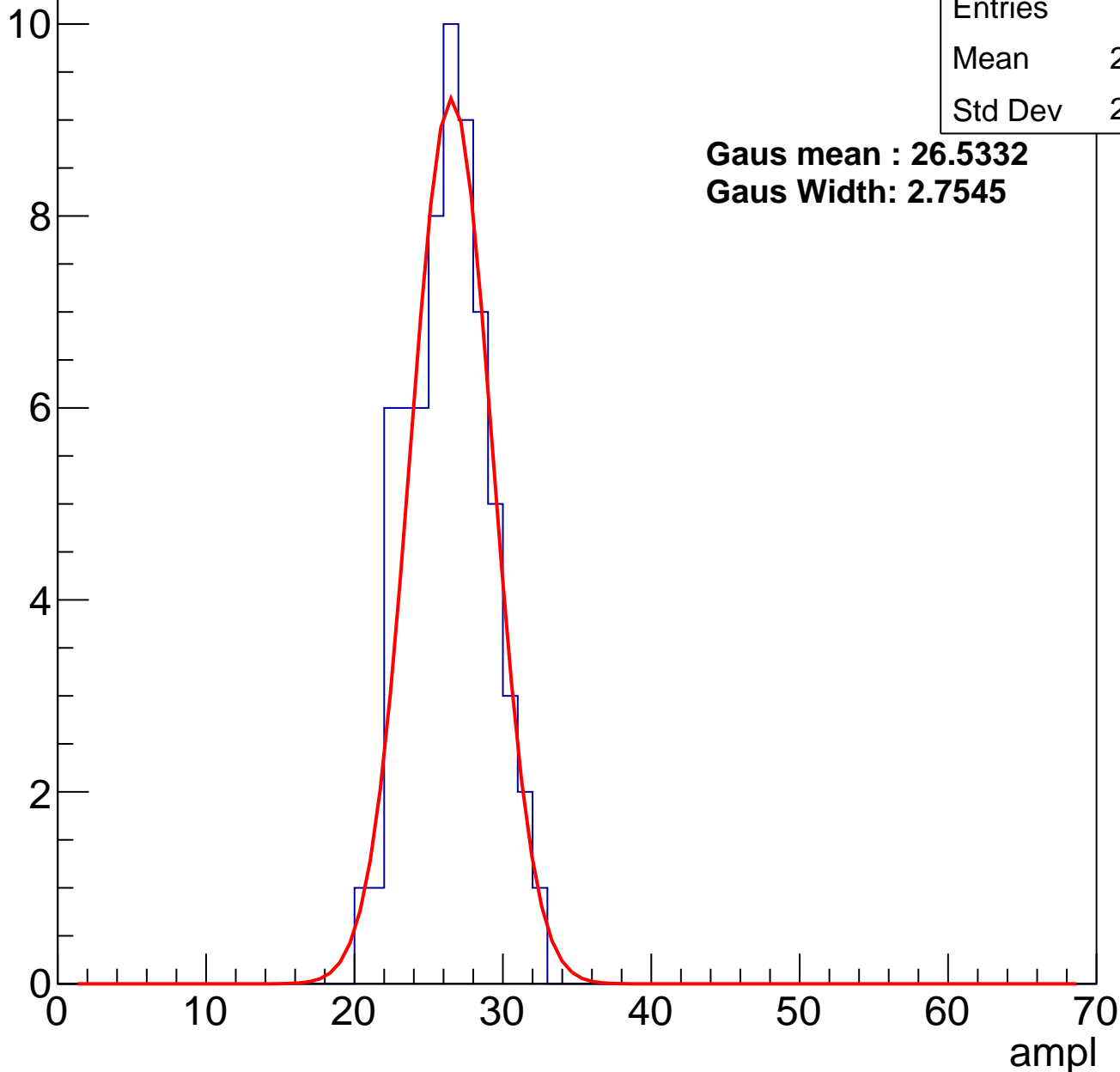
calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	25.89
Std Dev	2.655

Gaus mean : 26.5332

Gaus Width: 2.7545

Entry



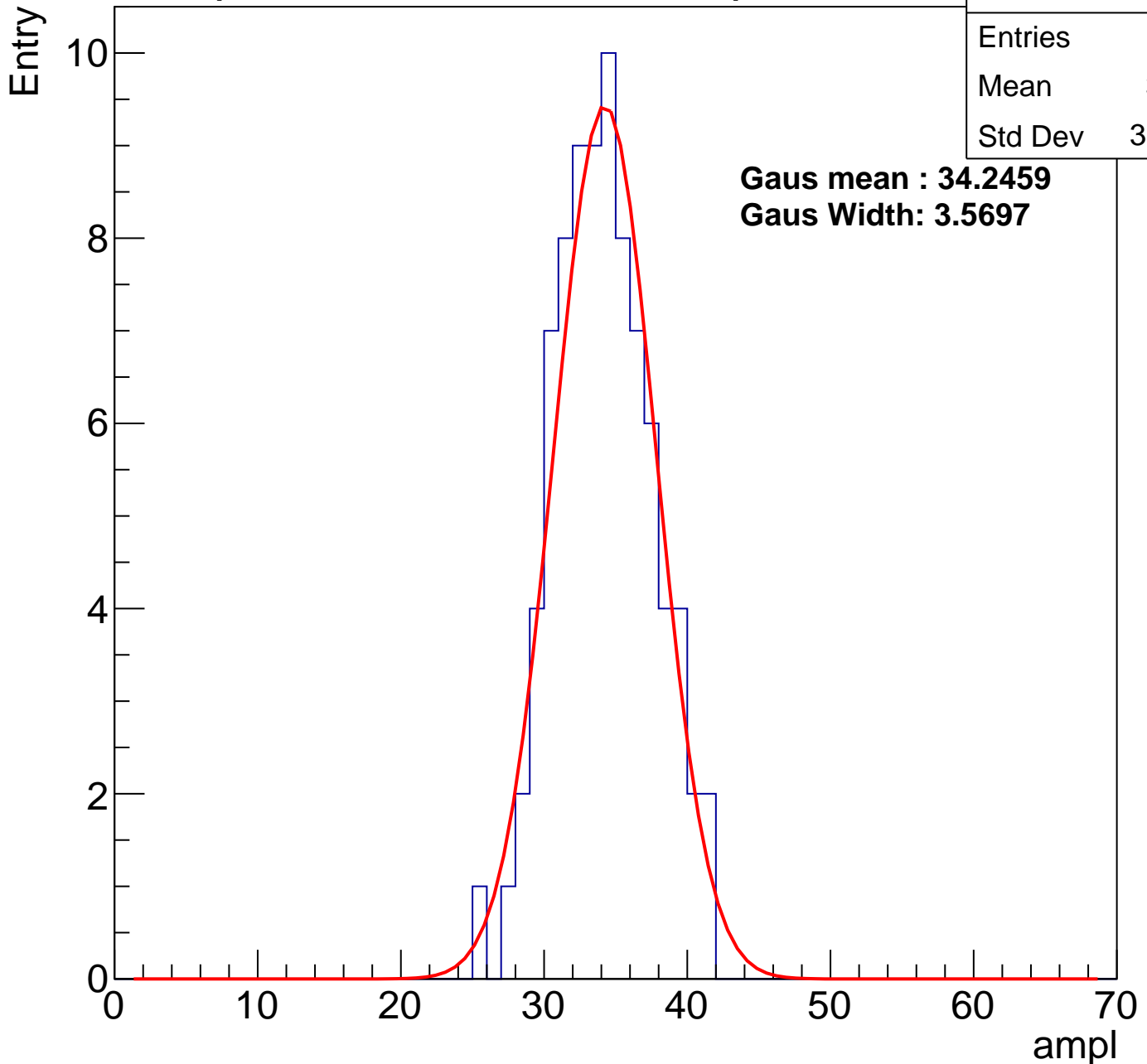
B1L002S, U7-ch106, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	84
Mean	33.7
Std Dev	3.359

Gaus mean : 34.2459

Gaus Width: 3.5697



B1L002S, U7-ch106, adc2

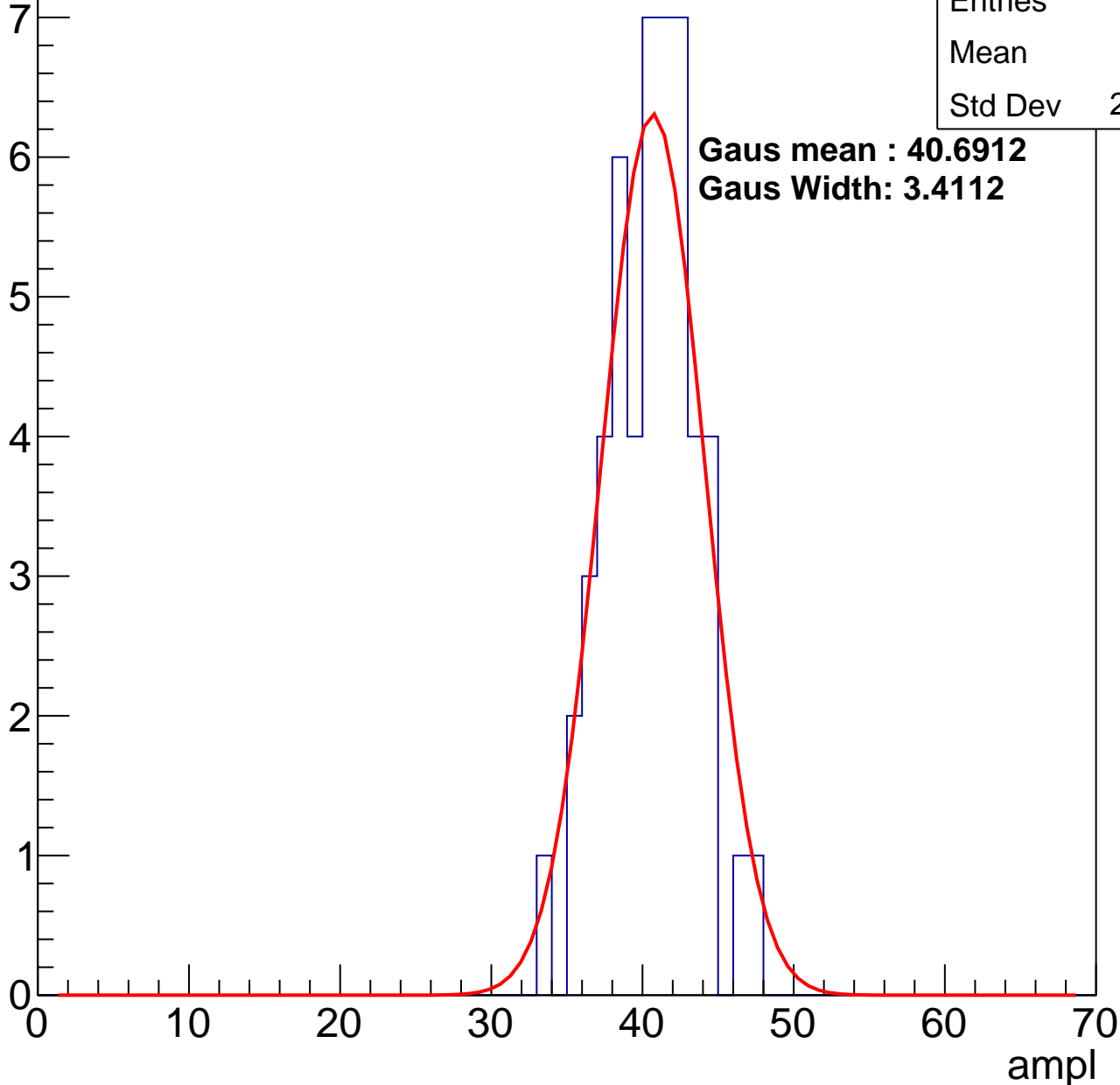
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.1
Std Dev	2.905

Gaus mean : 40.6912

Gaus Width: 3.4112

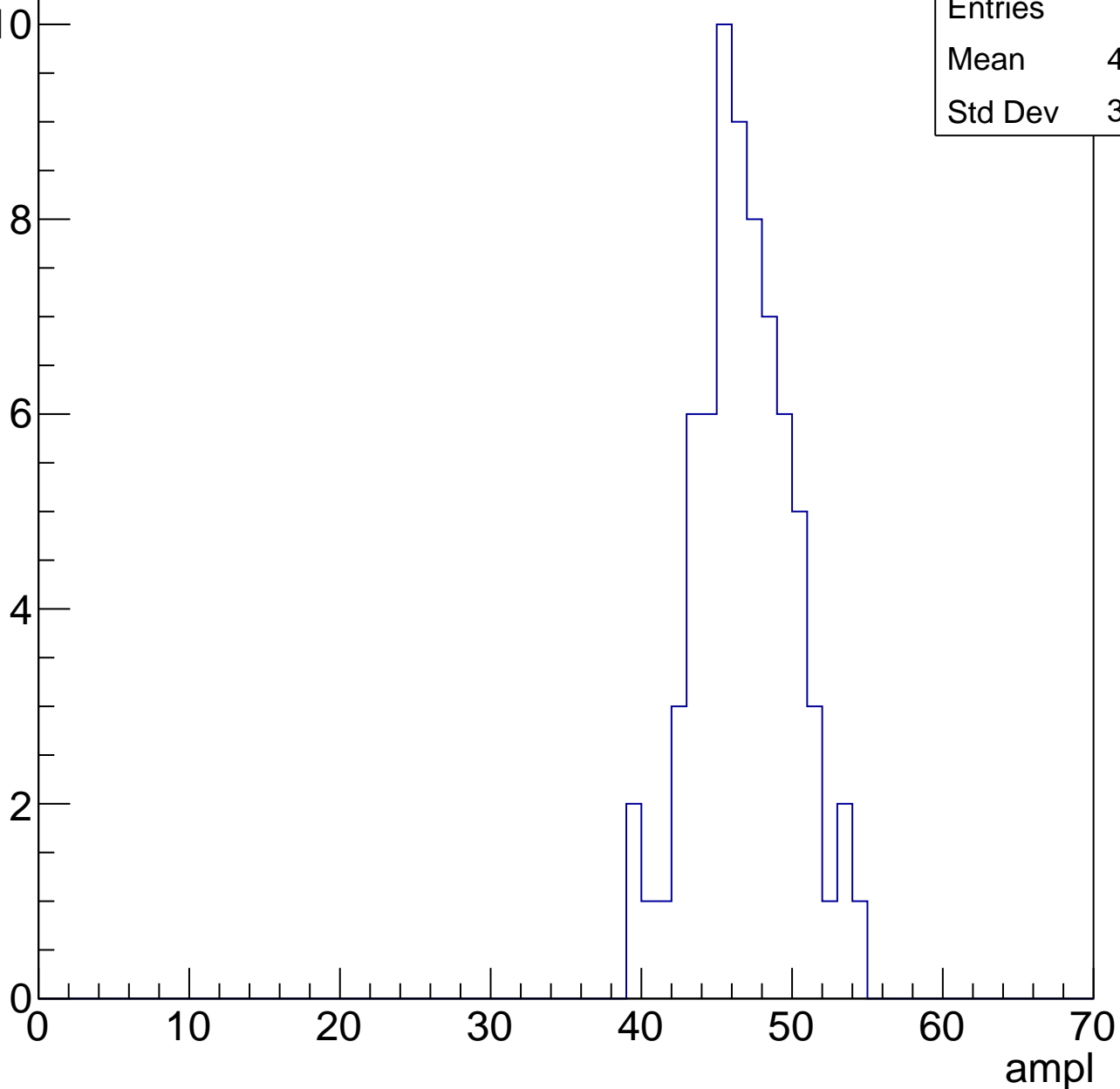


B1L002S, U7-ch106, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	46.37
Std Dev	3.203

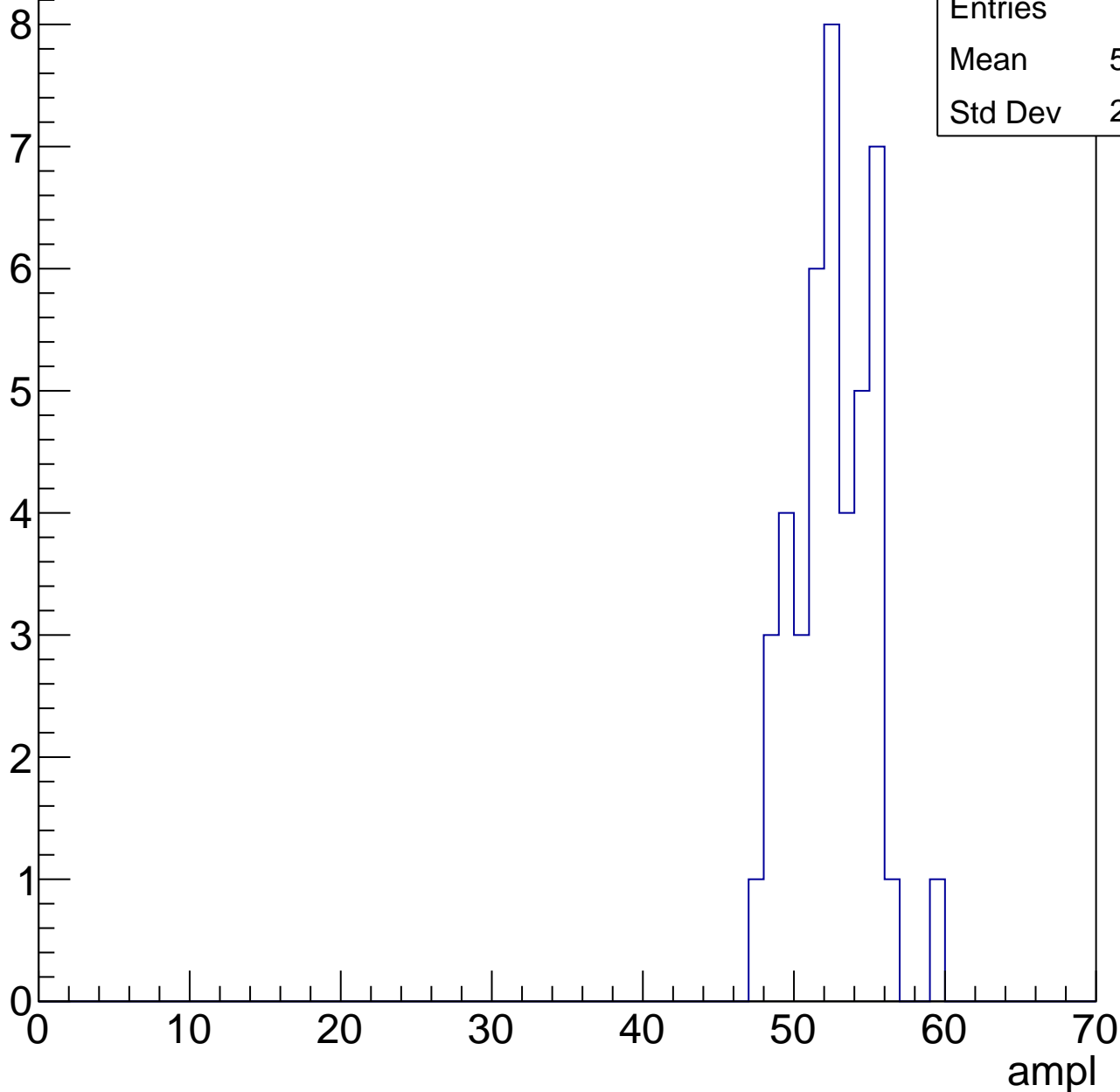


B1L002S, U7-ch106, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	43
Mean	52.12
Std Dev	2.545

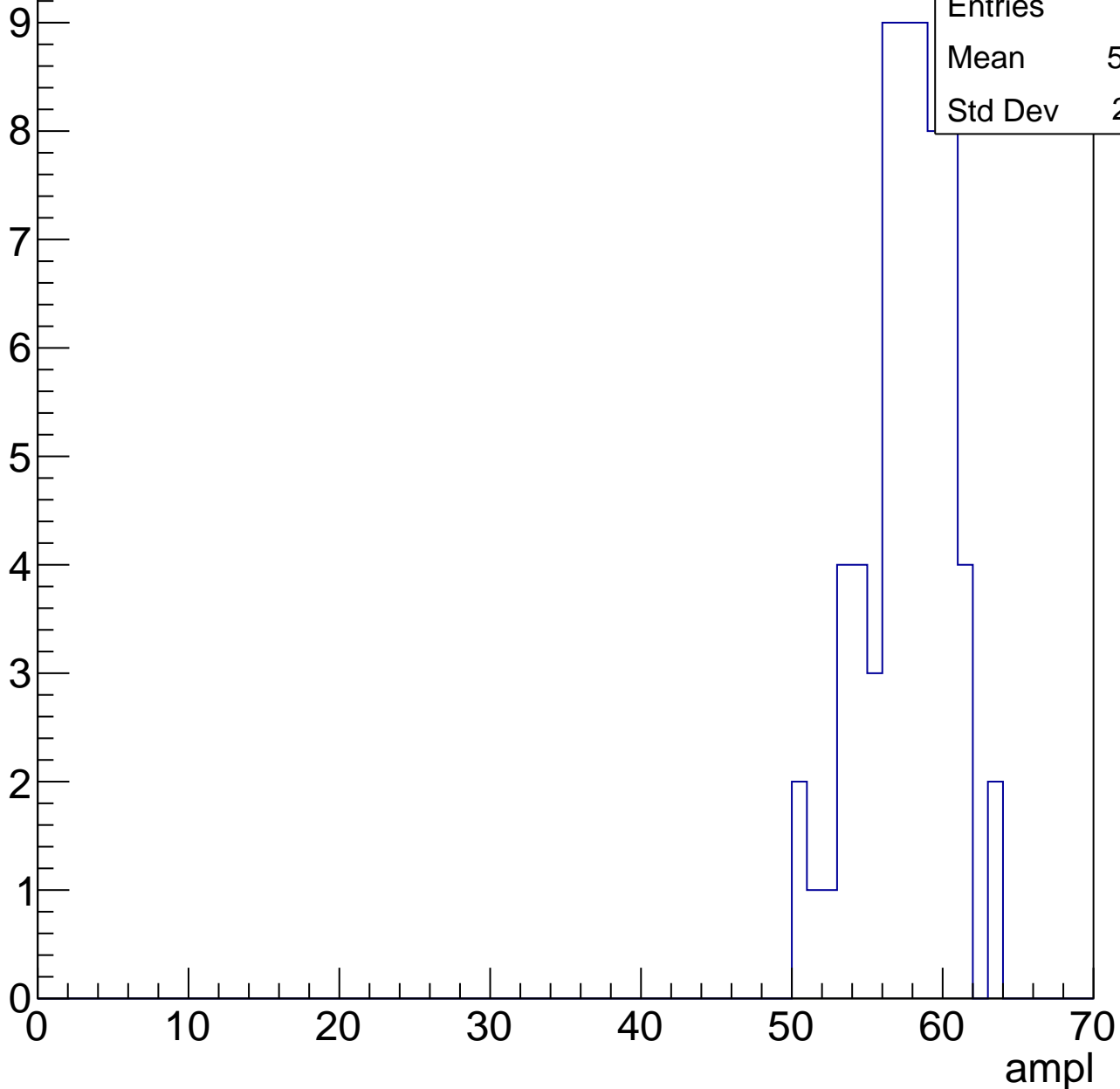


B1L002S, U7-ch106, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	57.18
Std Dev	2.871

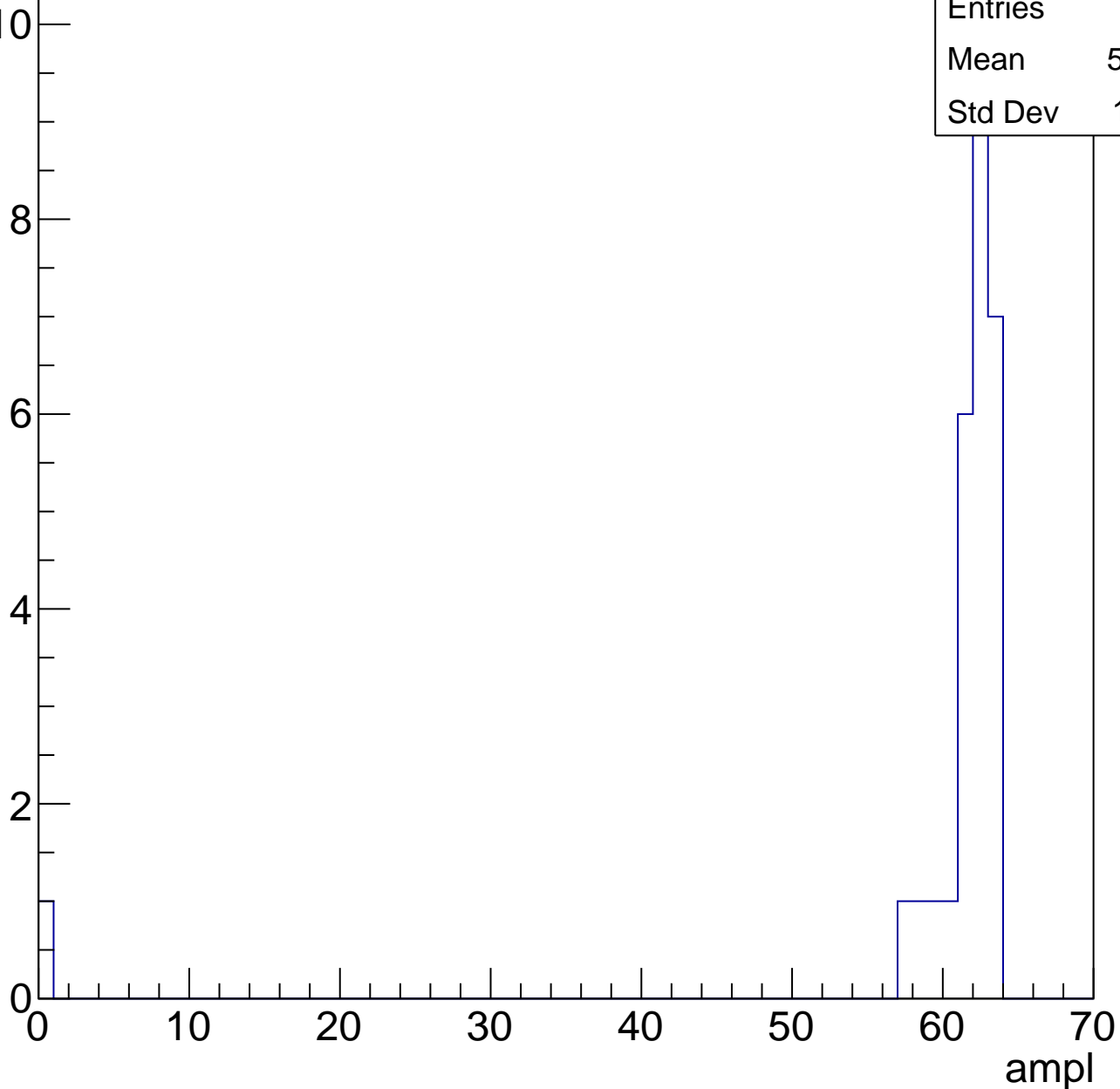


B1L002S, U7-ch106, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59.32
Std Dev	11.51



B1L002S, U7-ch106, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L002S, U7-ch107, adc0

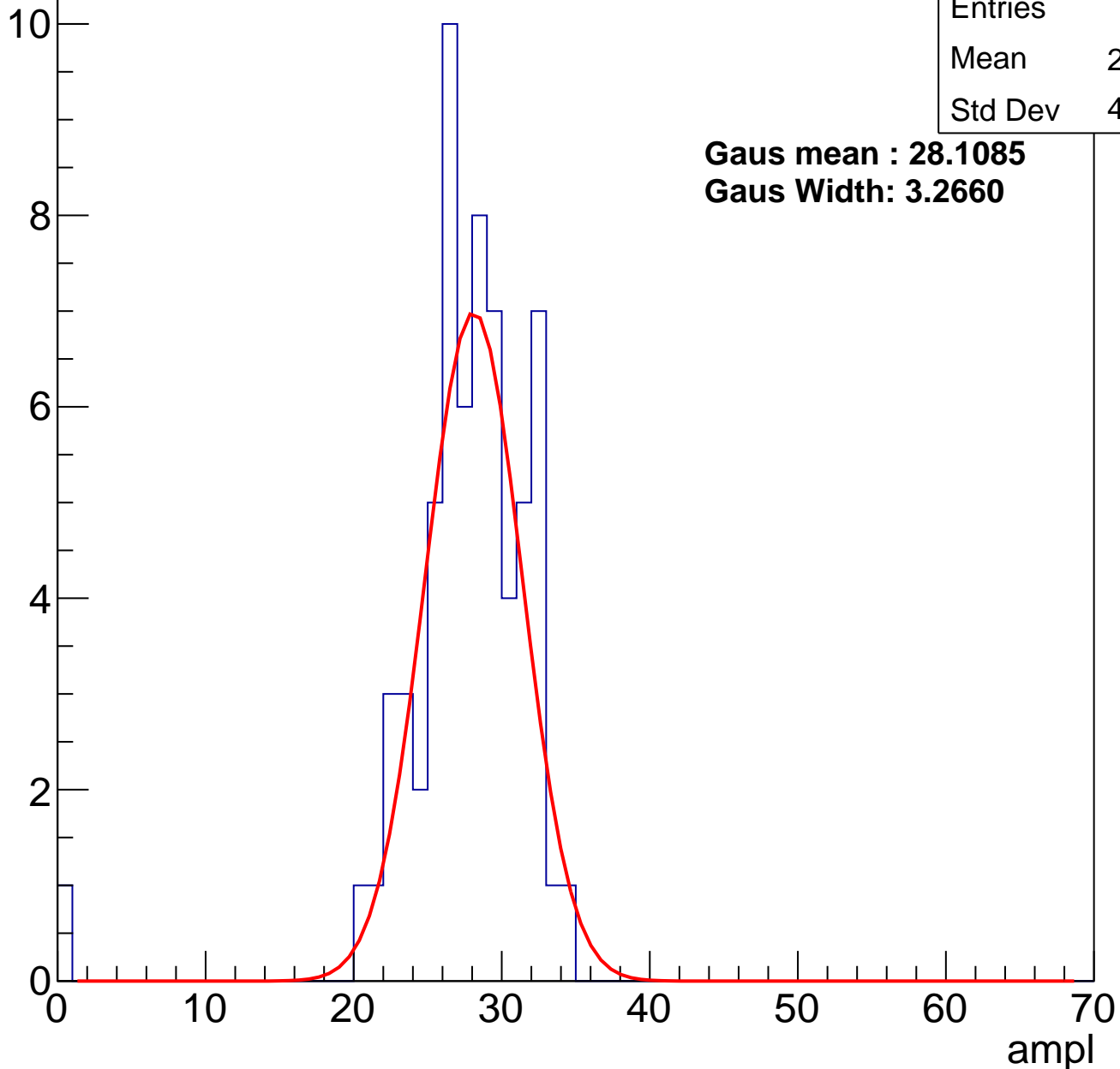
calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	27.14
Std Dev	4.634

Gaus mean : 28.1085

Gaus Width: 3.2660

Entry



B1L002S, U7-ch107, adc1

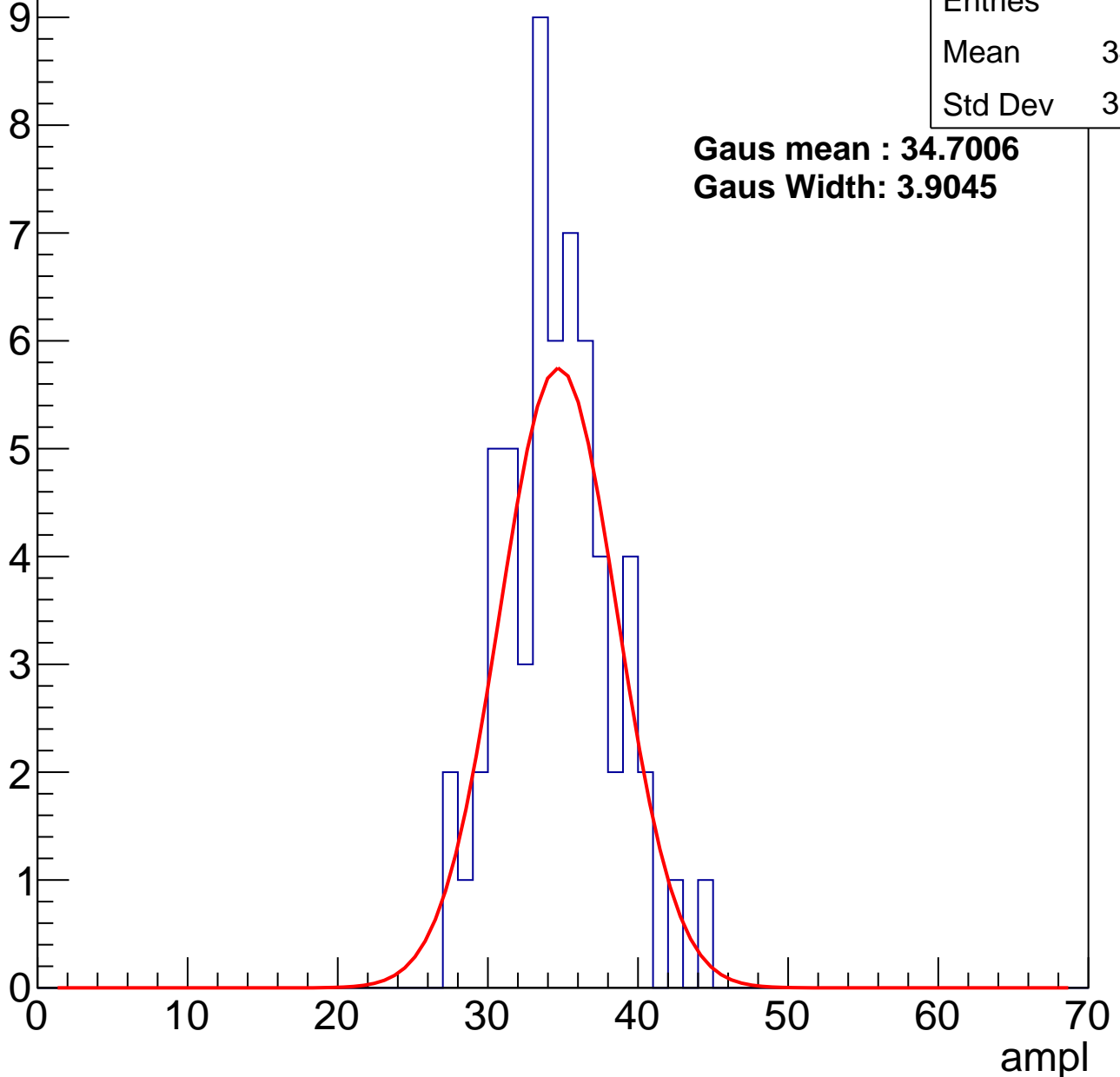
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.15
Std Dev	3.568

Gaus mean : 34.7006

Gaus Width: 3.9045



B1L002S, U7-ch107, adc2

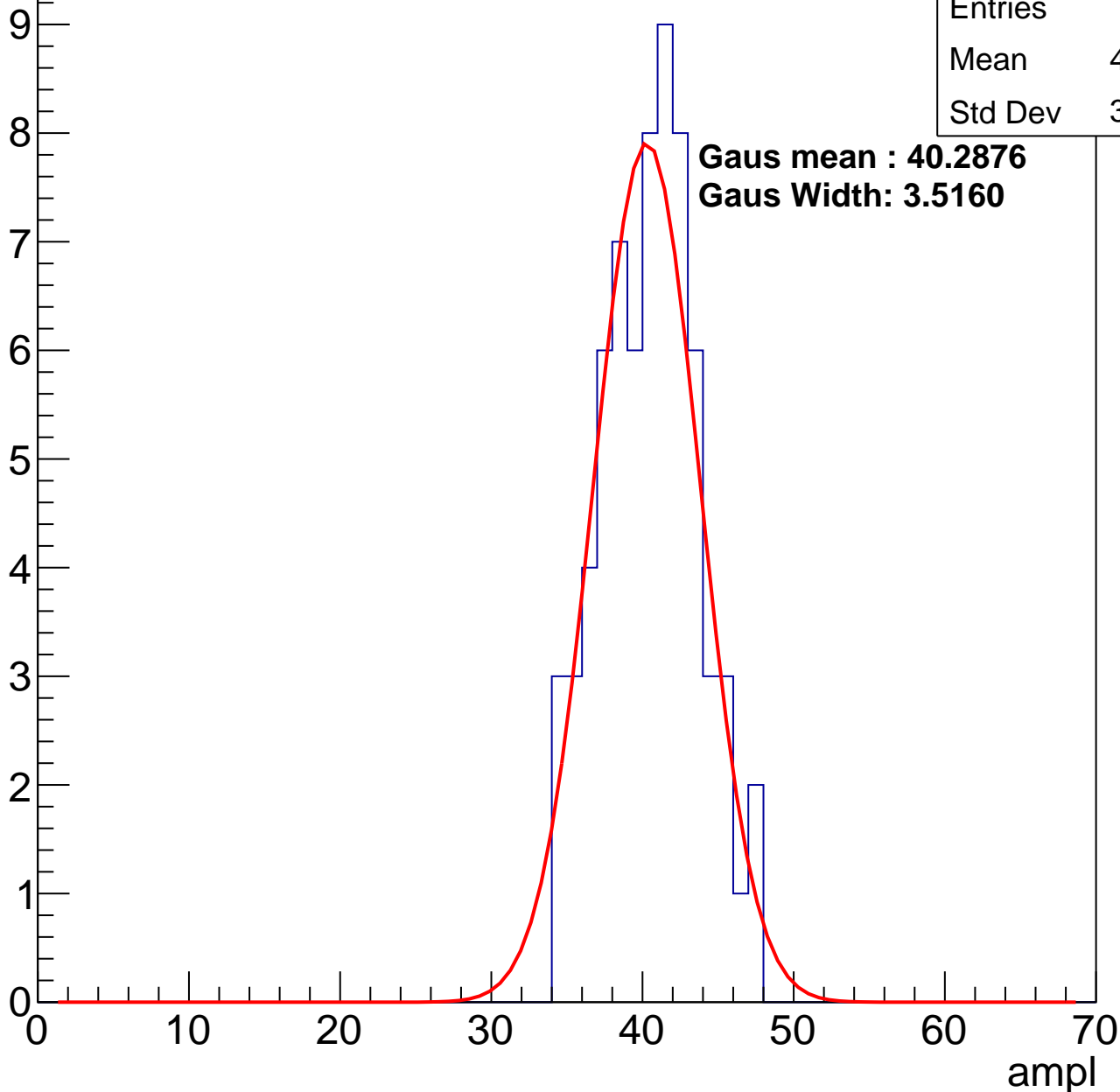
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.04
Std Dev	3.155

Gaus mean : 40.2876

Gaus Width: 3.5160



B1L002S, U7-ch107, adc3

calib_packv5_042523_0143.root, FC#10, port B3

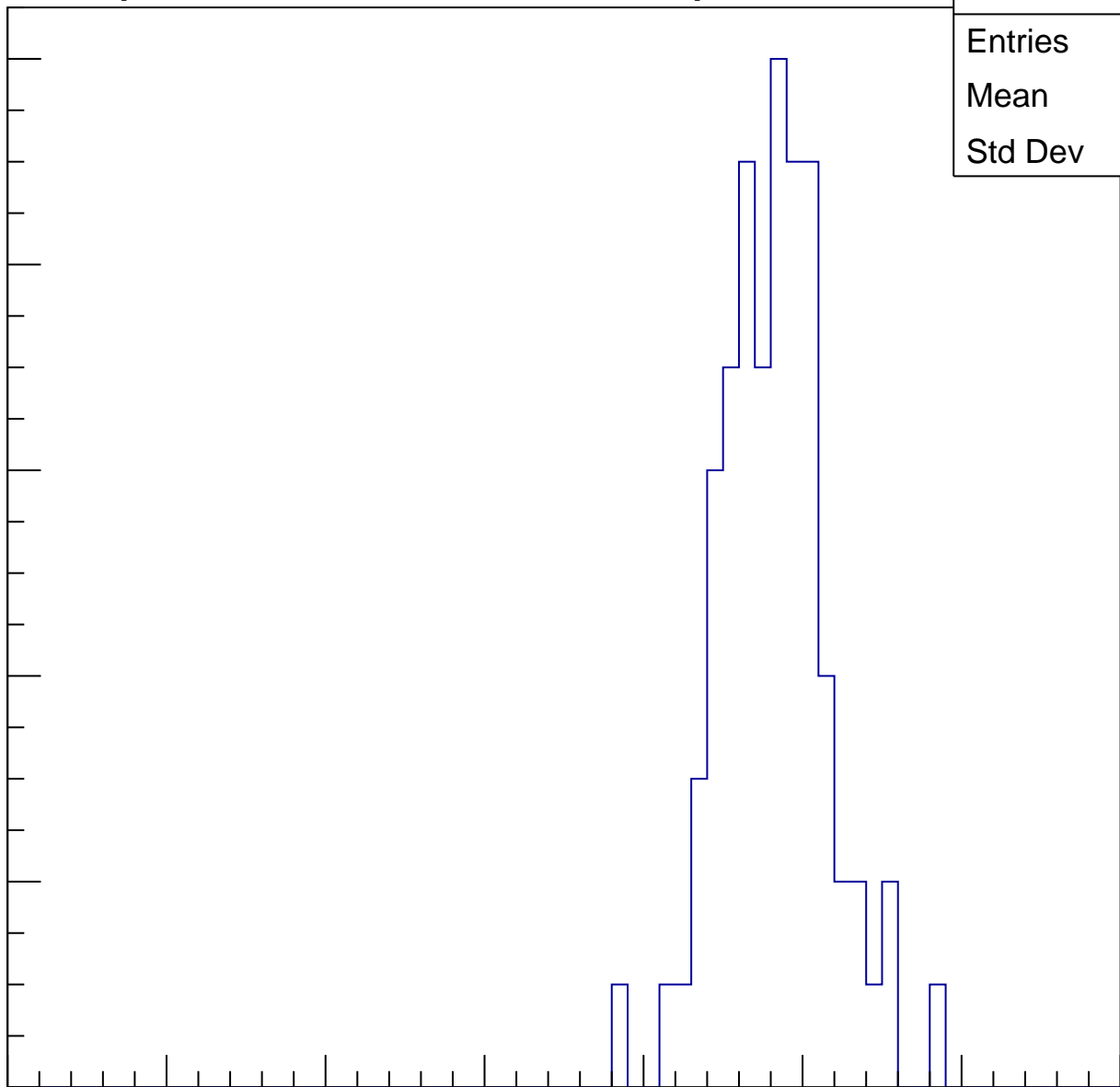
Entries	75
Mean	47.72
Std Dev	3.373

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

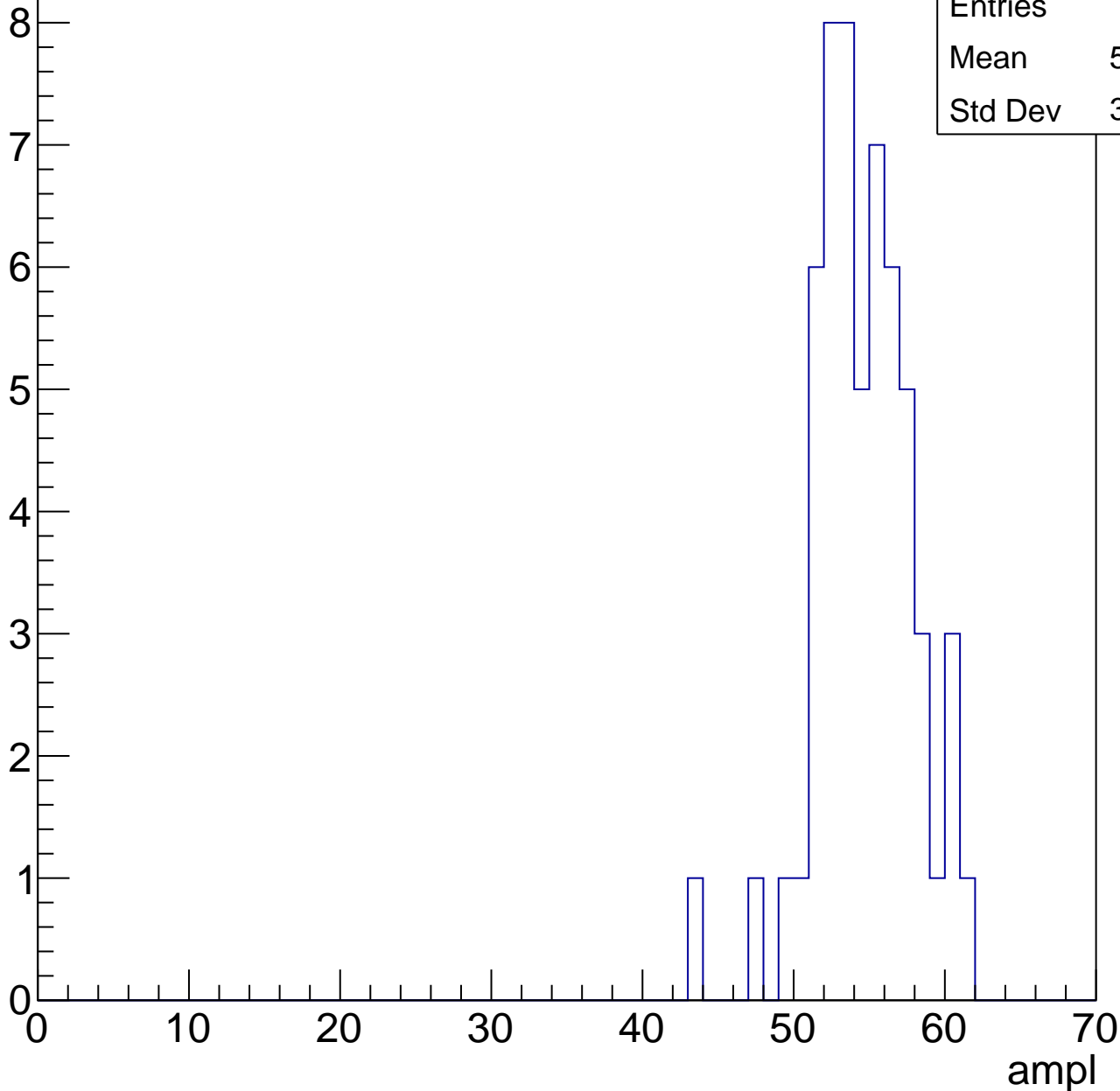


B1L002S, U7-ch107, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

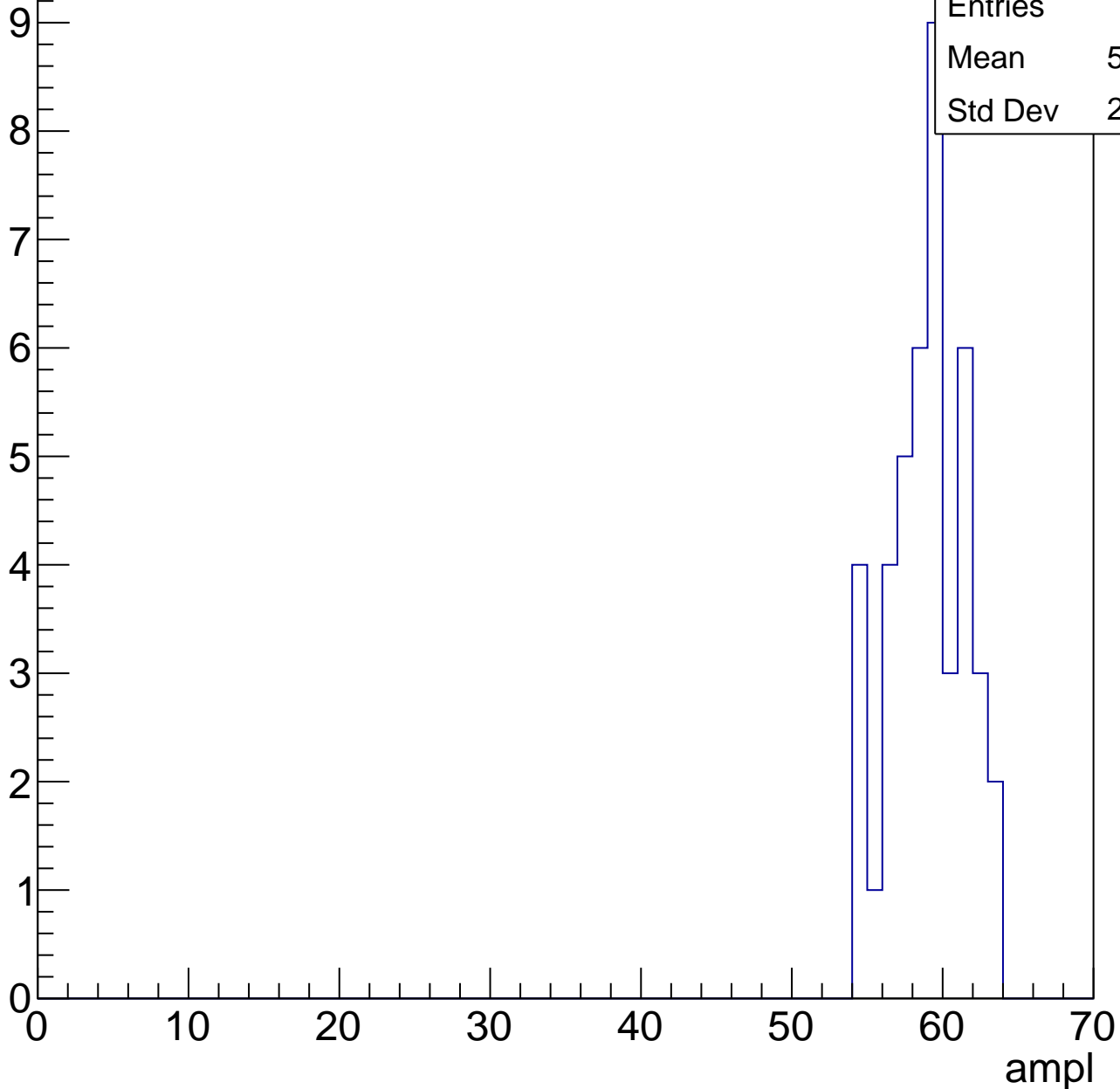
Entries	57
Mean	54.12
Std Dev	3.272



B1L002S, U7-ch107, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



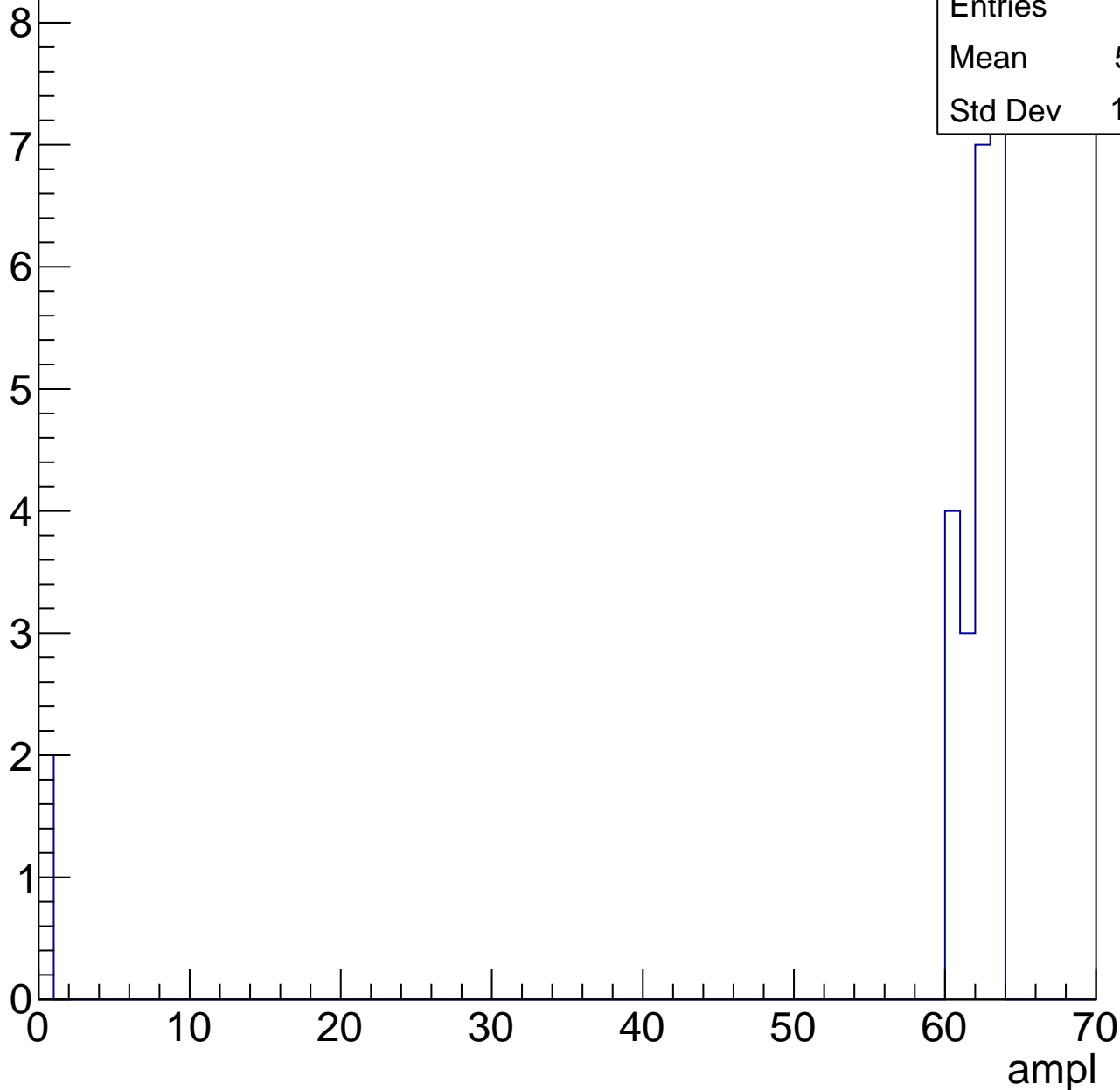
Entries	43
Mean	58.53
Std Dev	2.434

B1L002S, U7-ch107, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	24
Mean	56.71
Std Dev	17.13



B1L002S, U7-ch107, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

B1L002S, U7-ch108, adc0

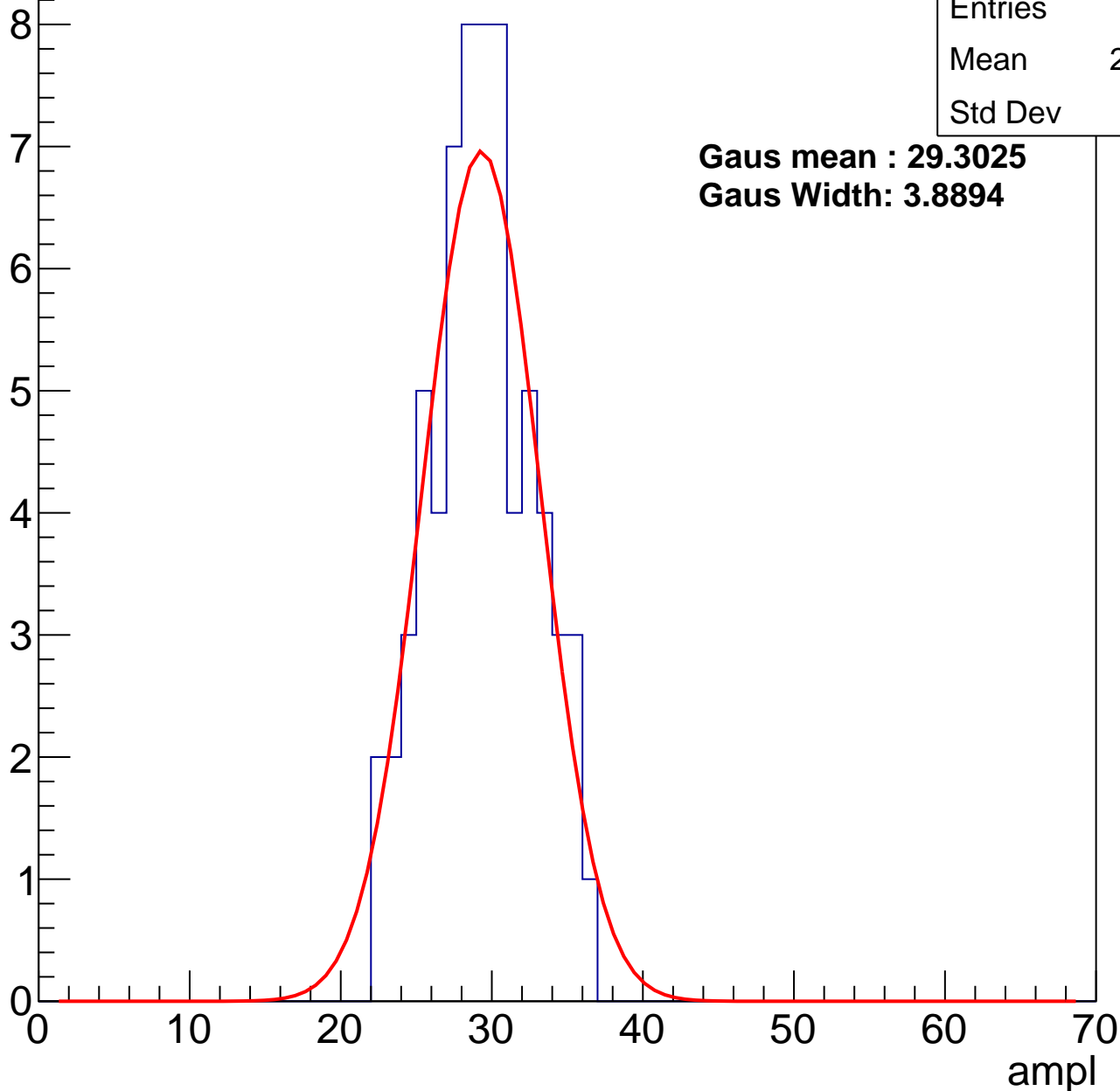
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.88
Std Dev	3.37

Gaus mean : 29.3025

Gaus Width: 3.8894



B1L002S, U7-ch108, adc1

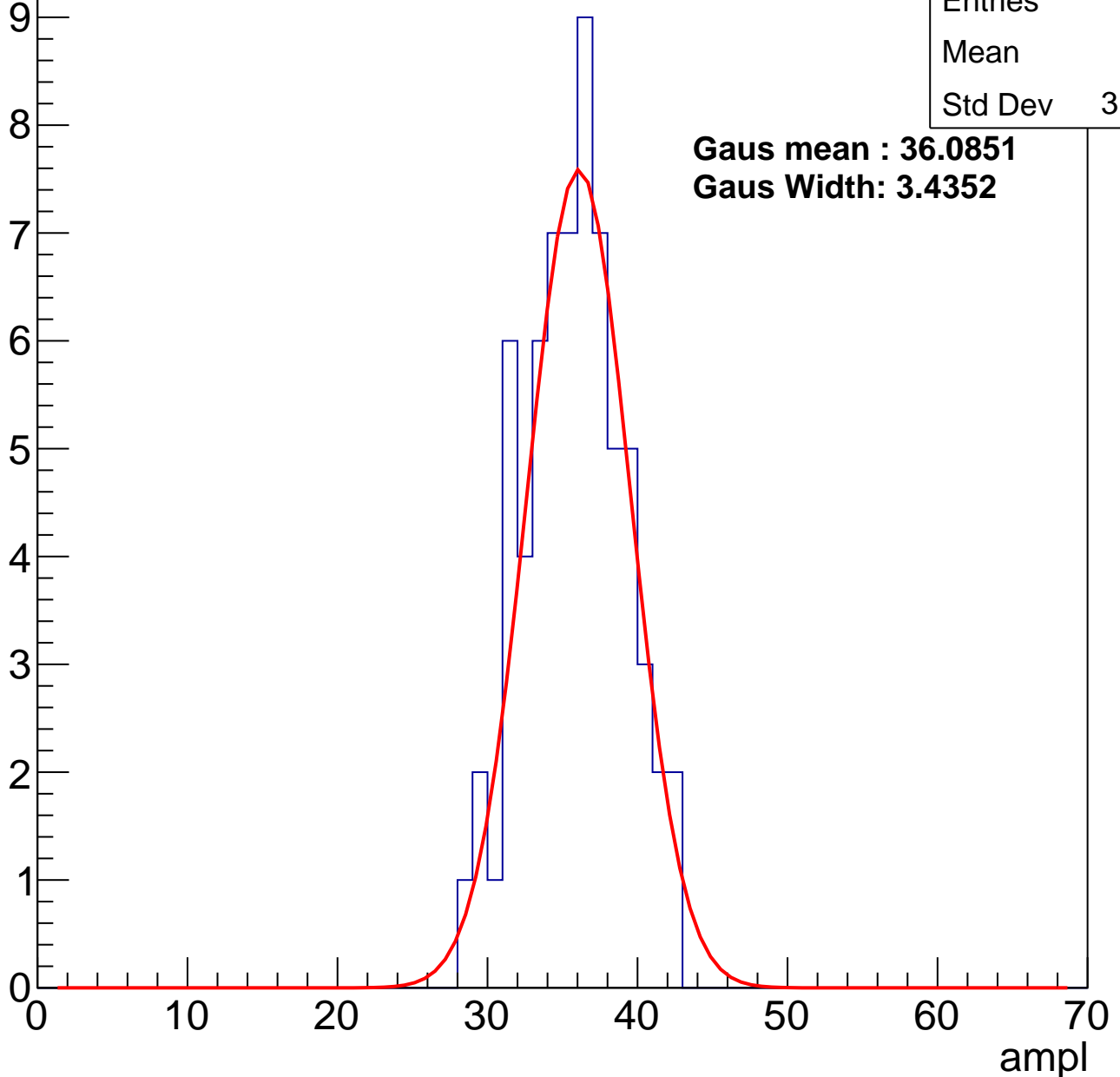
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	35.3
Std Dev	3.255

Gaus mean : 36.0851

Gaus Width: 3.4352



B1L002S, U7-ch108, adc2

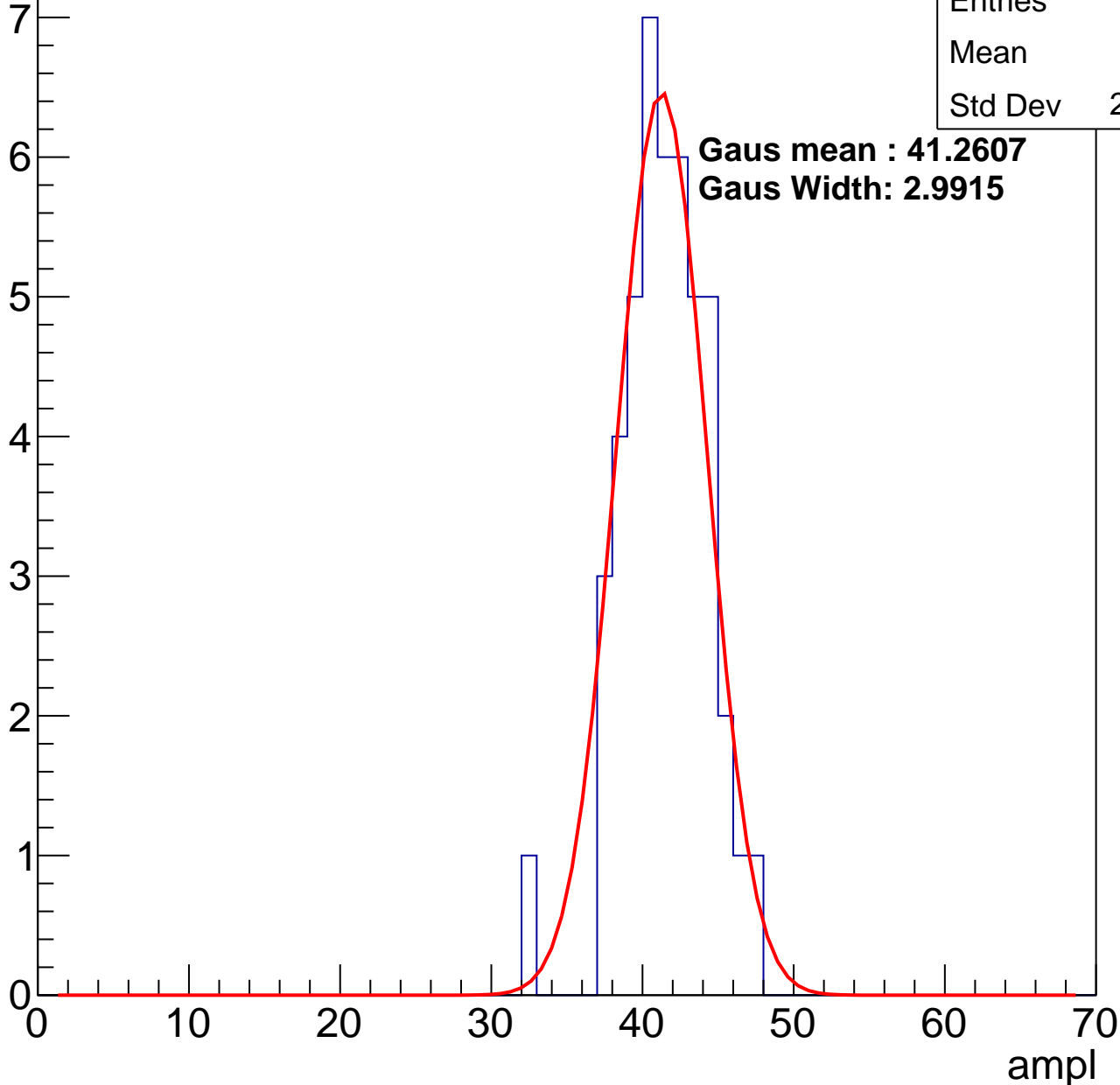
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	41
Std Dev	2.782

Gaus mean : 41.2607

Gaus Width: 2.9915

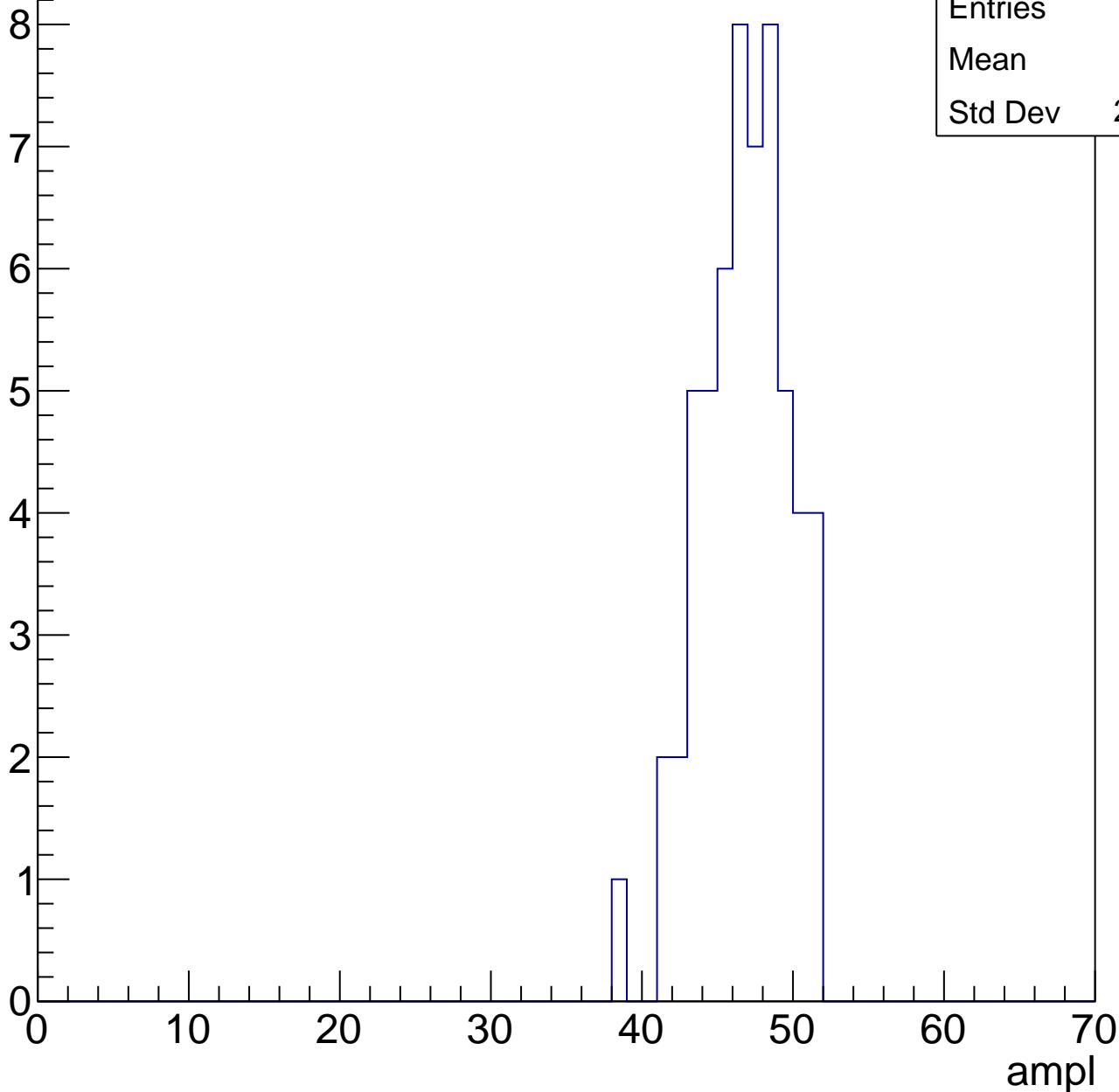


B1L002S, U7-ch108, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.3
Std Dev	2.841

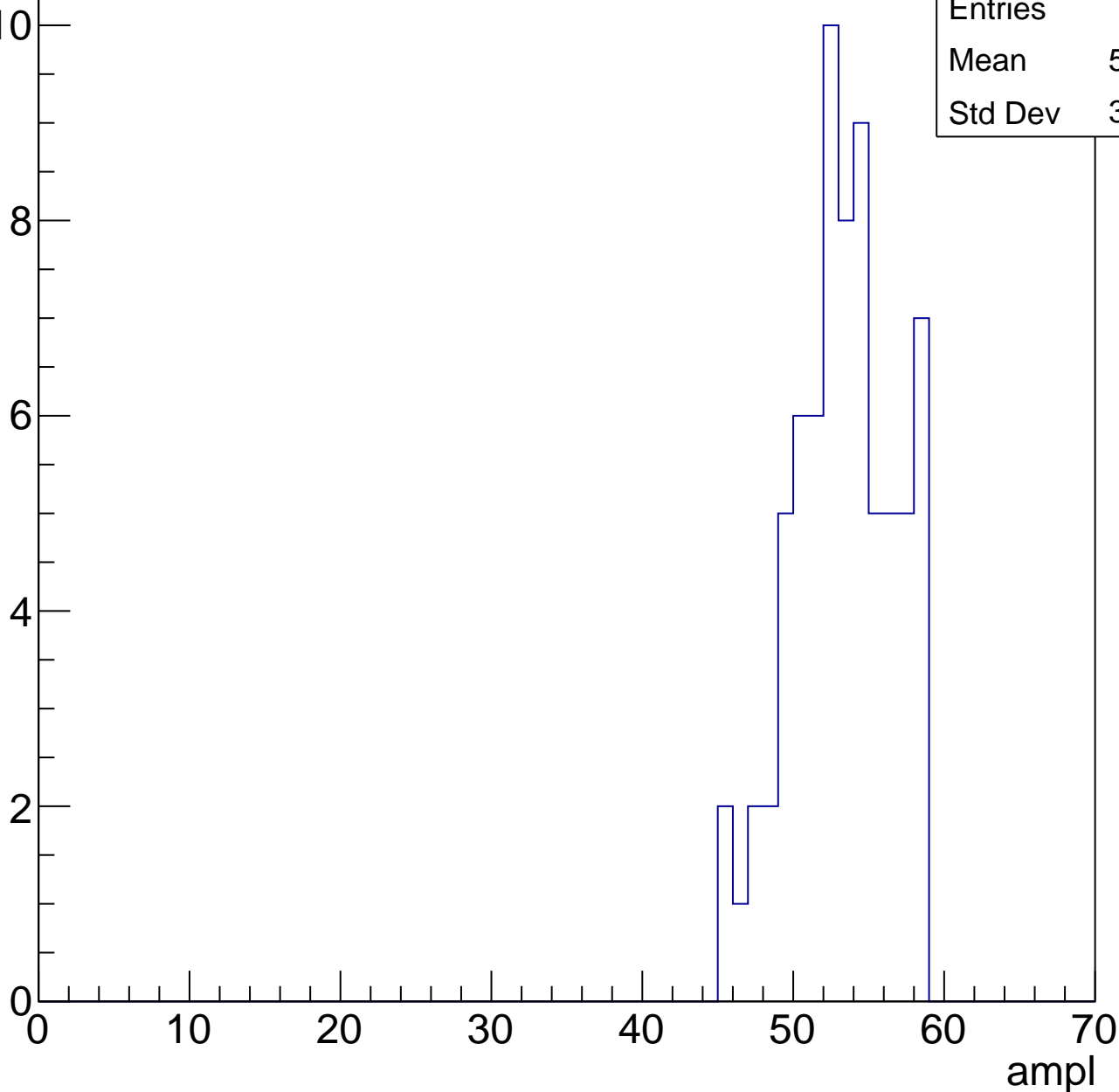


B1L002S, U7-ch108, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

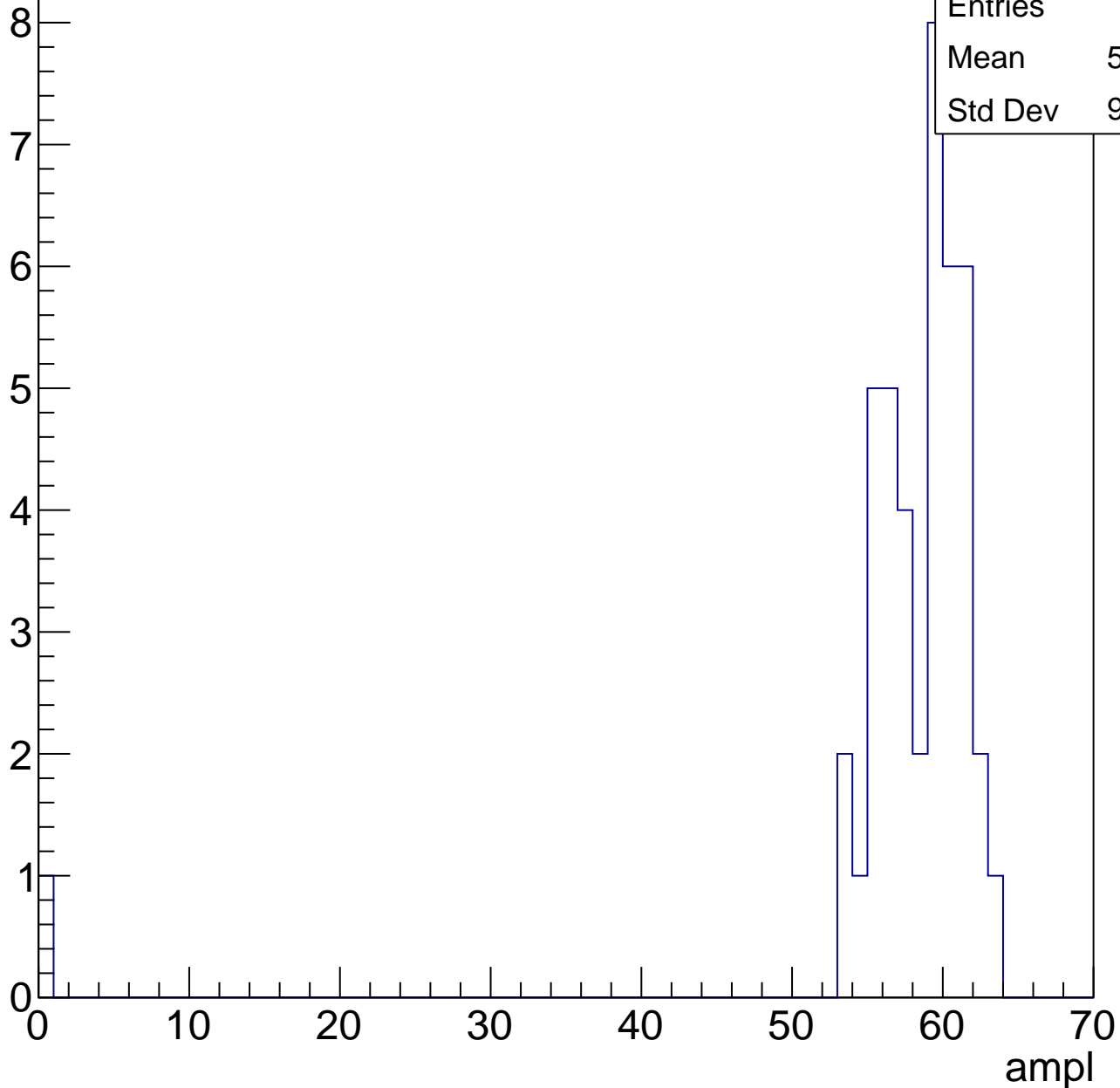
Entries	73
Mean	52.78
Std Dev	3.295



B1L002S, U7-ch108, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch108, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

8
7
6
5
4
3
2
1
0

Entries	26
Mean	61.27
Std Dev	1.677

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

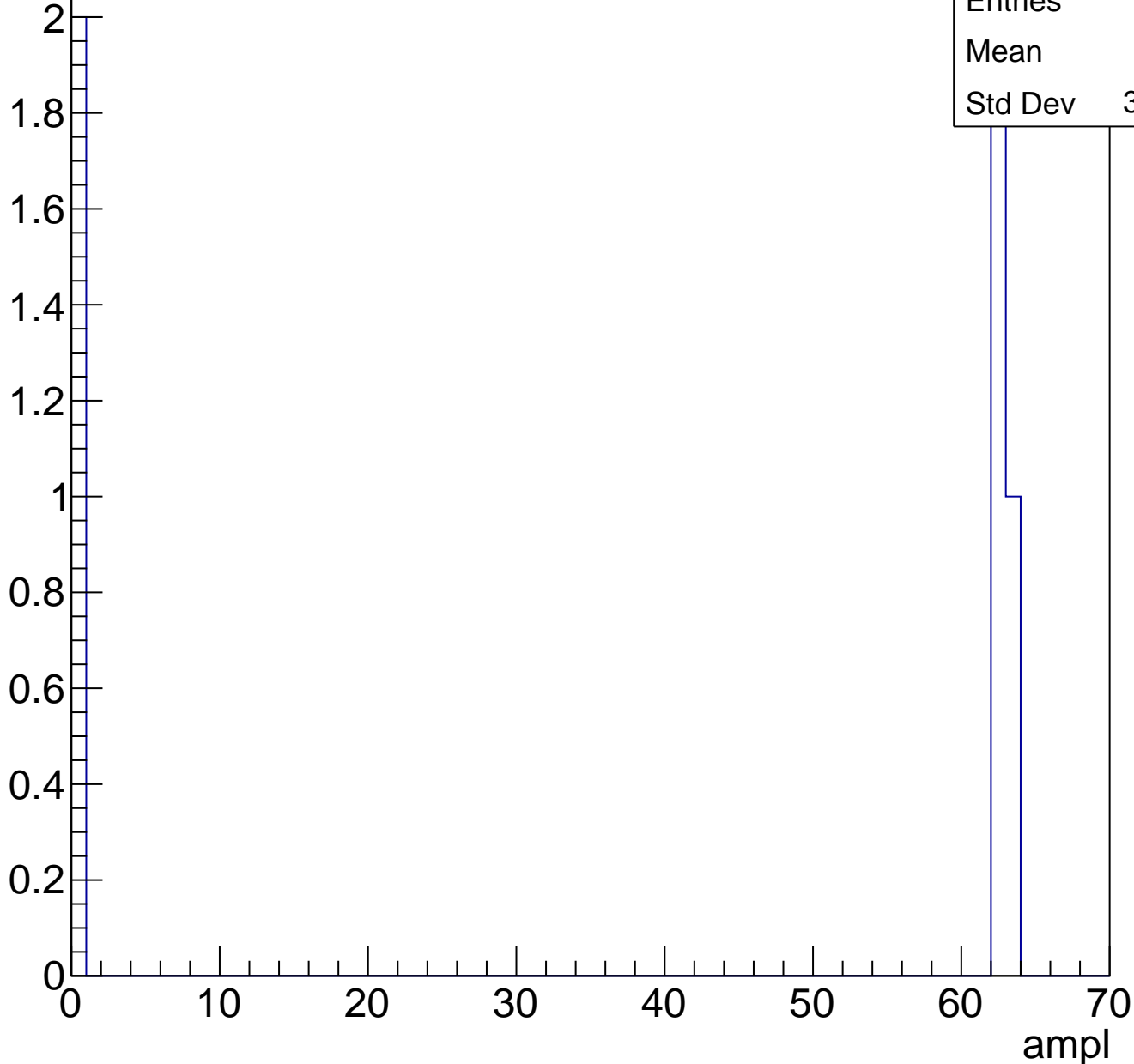
7

8

B1L002S, U7-ch108, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch109, adc0

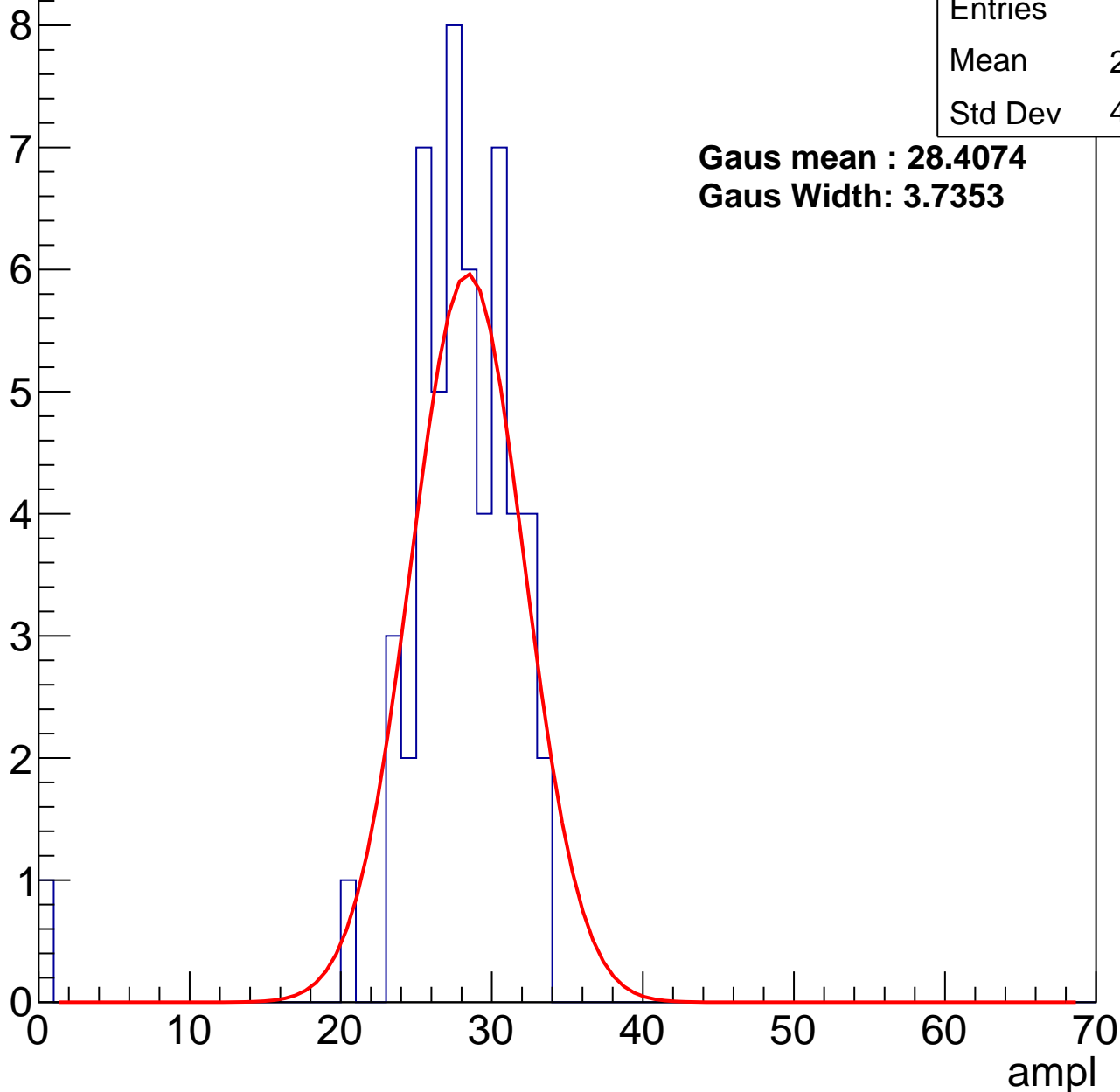
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	27.22
Std Dev	4.705

Gaus mean : 28.4074

Gaus Width: 3.7353



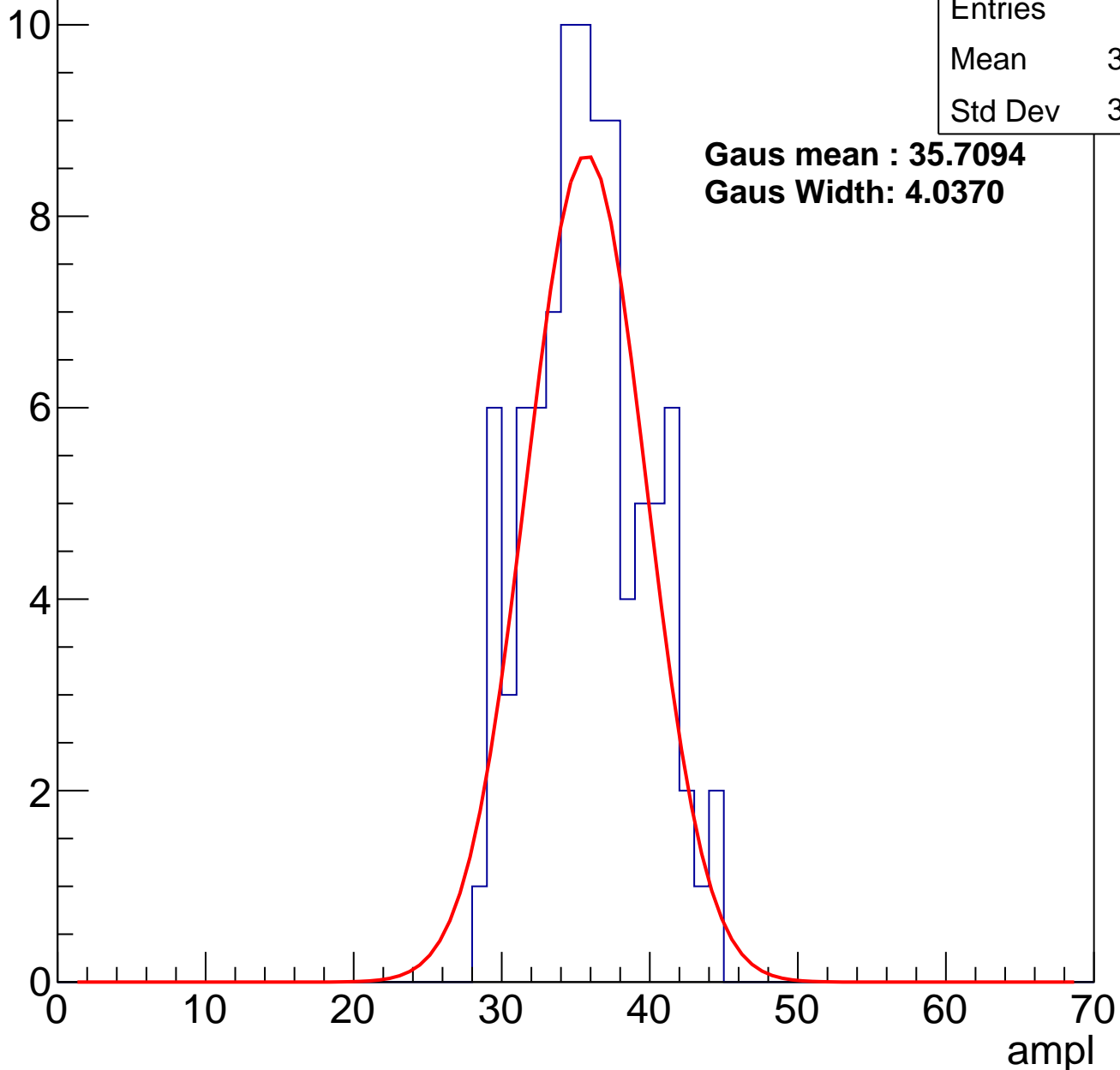
B1L002S, U7-ch109, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	92
Mean	35.39
Std Dev	3.816

Gaus mean : 35.7094
Gaus Width: 4.0370

Entry



B1L002S, U7-ch109, adc2

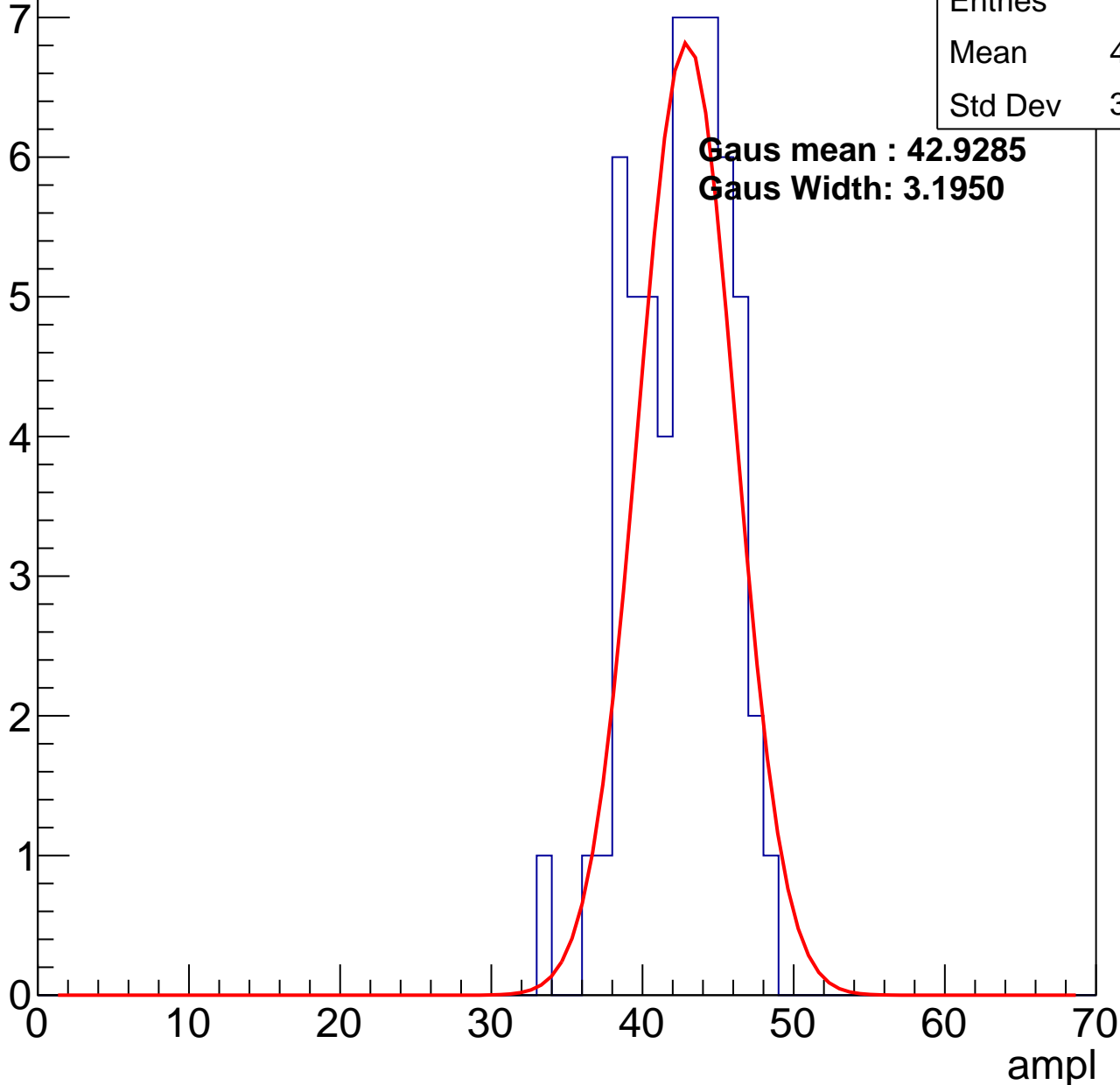
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	42.03
Std Dev	3.113

Gaus mean : 42.9285

Gaus Width: 3.1950

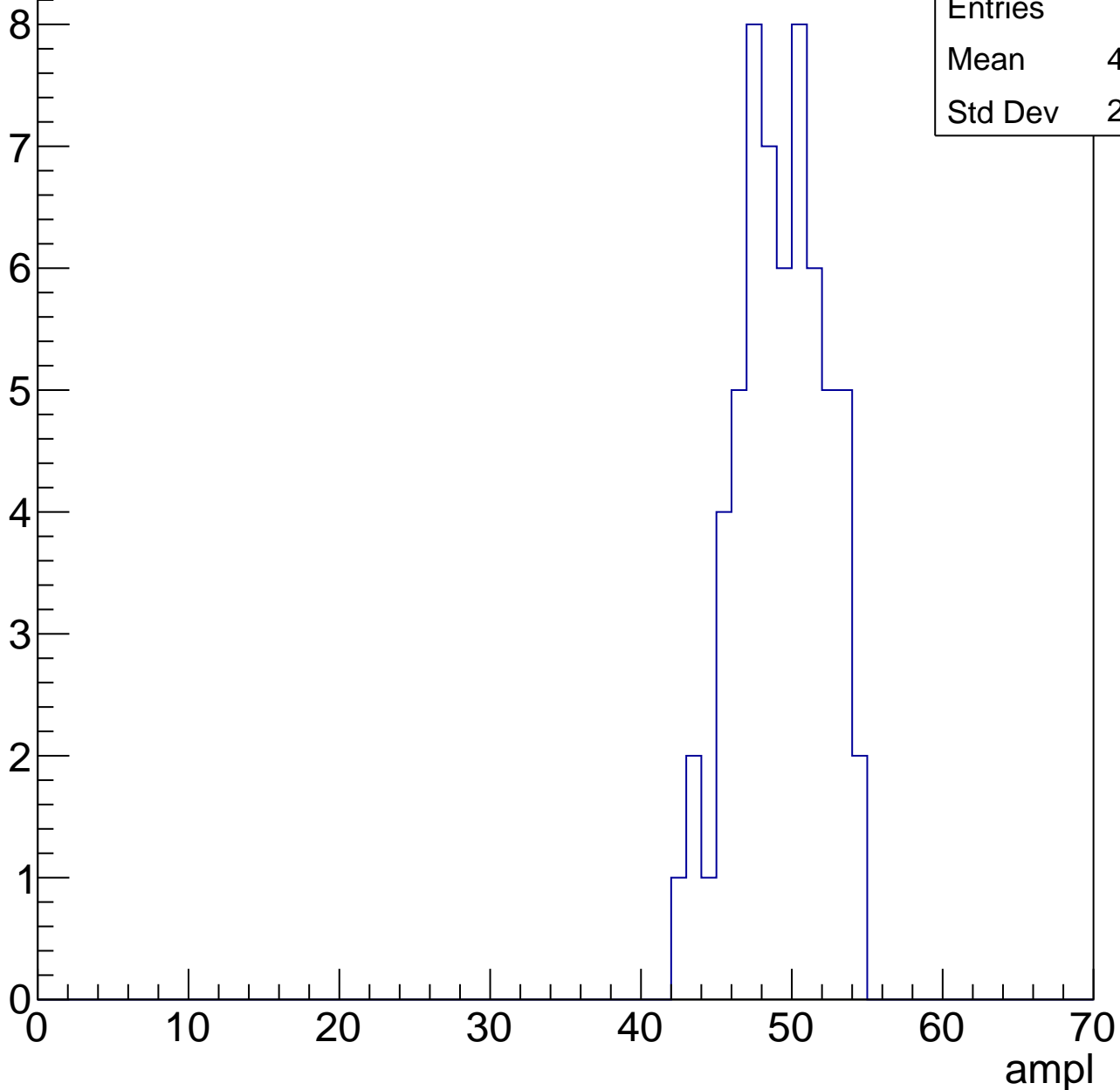


B1L002S, U7-ch109, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.78
Std Dev	2.882

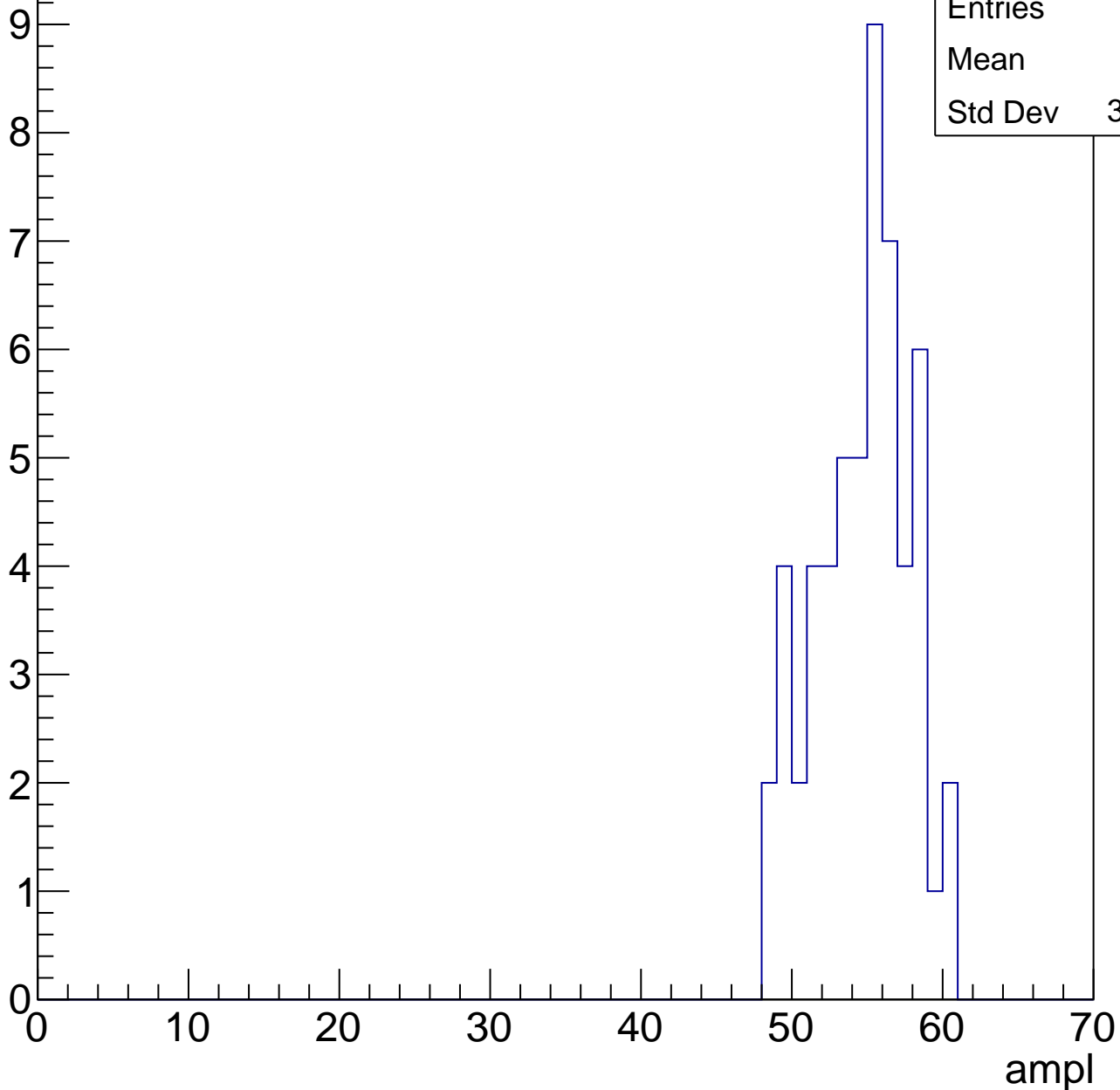


B1L002S, U7-ch109, adc4

calib_packv5_042523_0143.root, FC#10, port B3

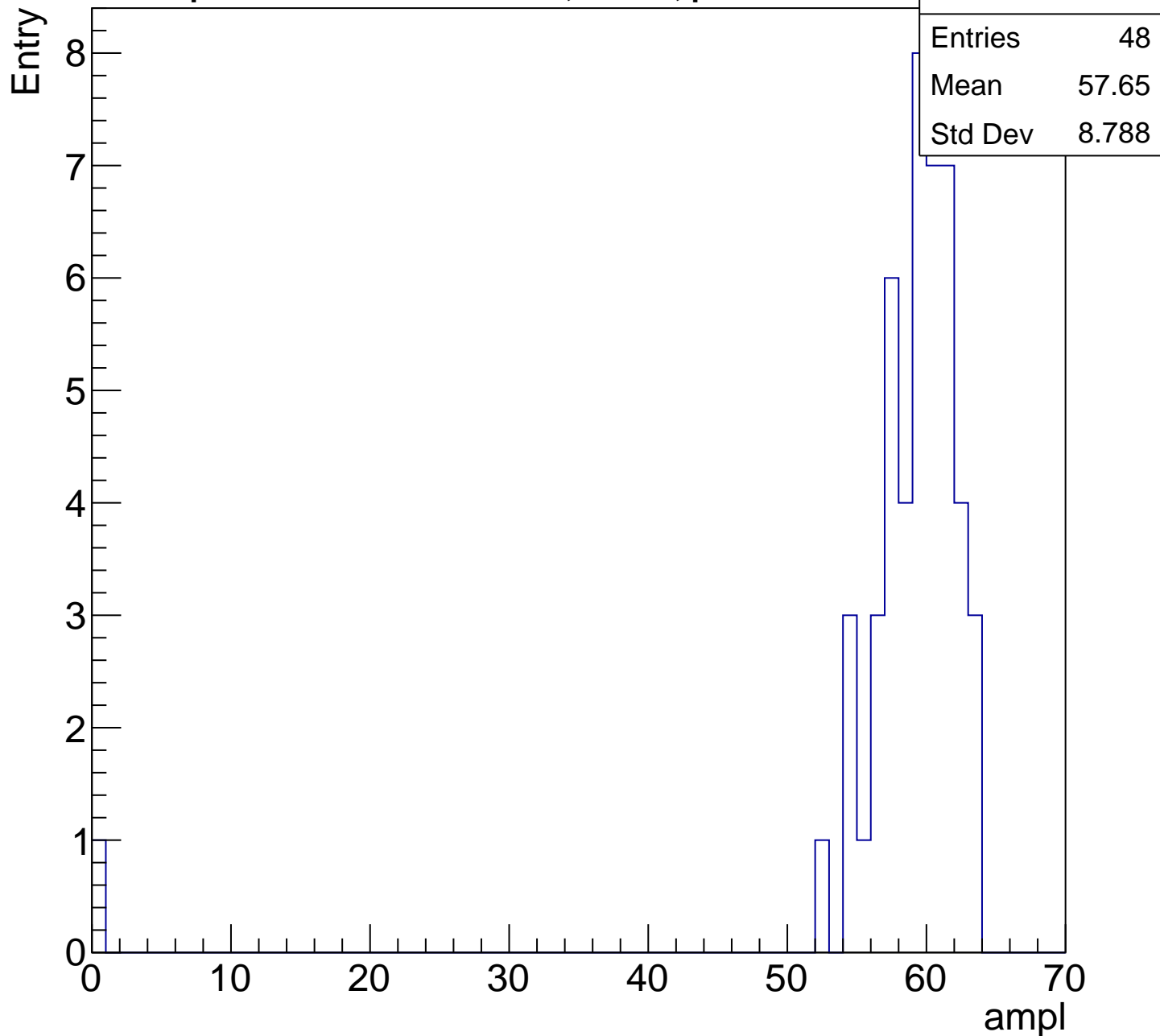
Entry

Entries	55
Mean	54.2
Std Dev	3.089



B1L002S, U7-ch109, adc5

calib_packv5_042523_0143.root, FC#10, port B3

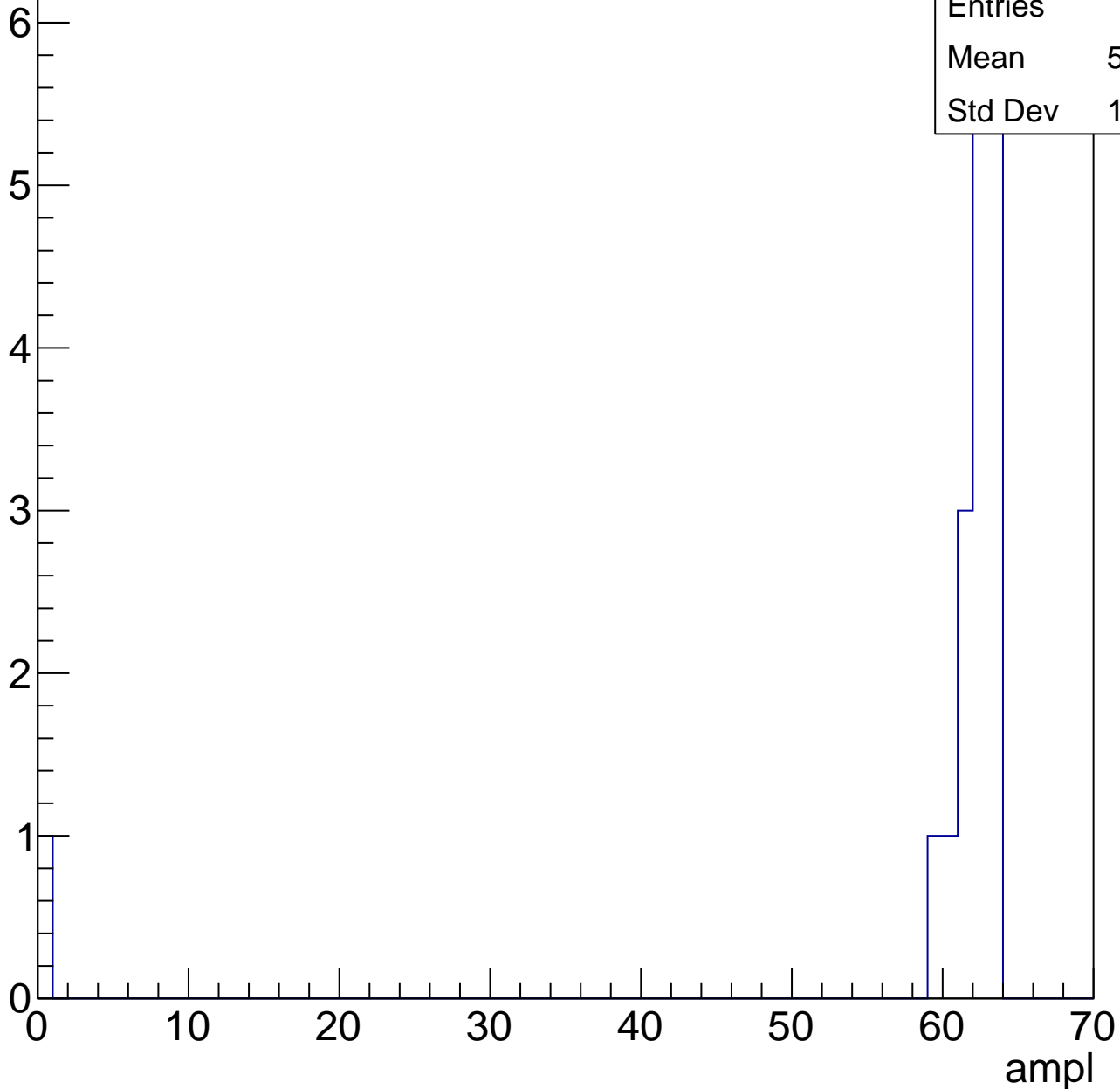


B1L002S, U7-ch109, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.44
Std Dev	14.22



B1L002S, U7-ch109, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	63
Std Dev	0

B1L002S, U7-ch110, adc0

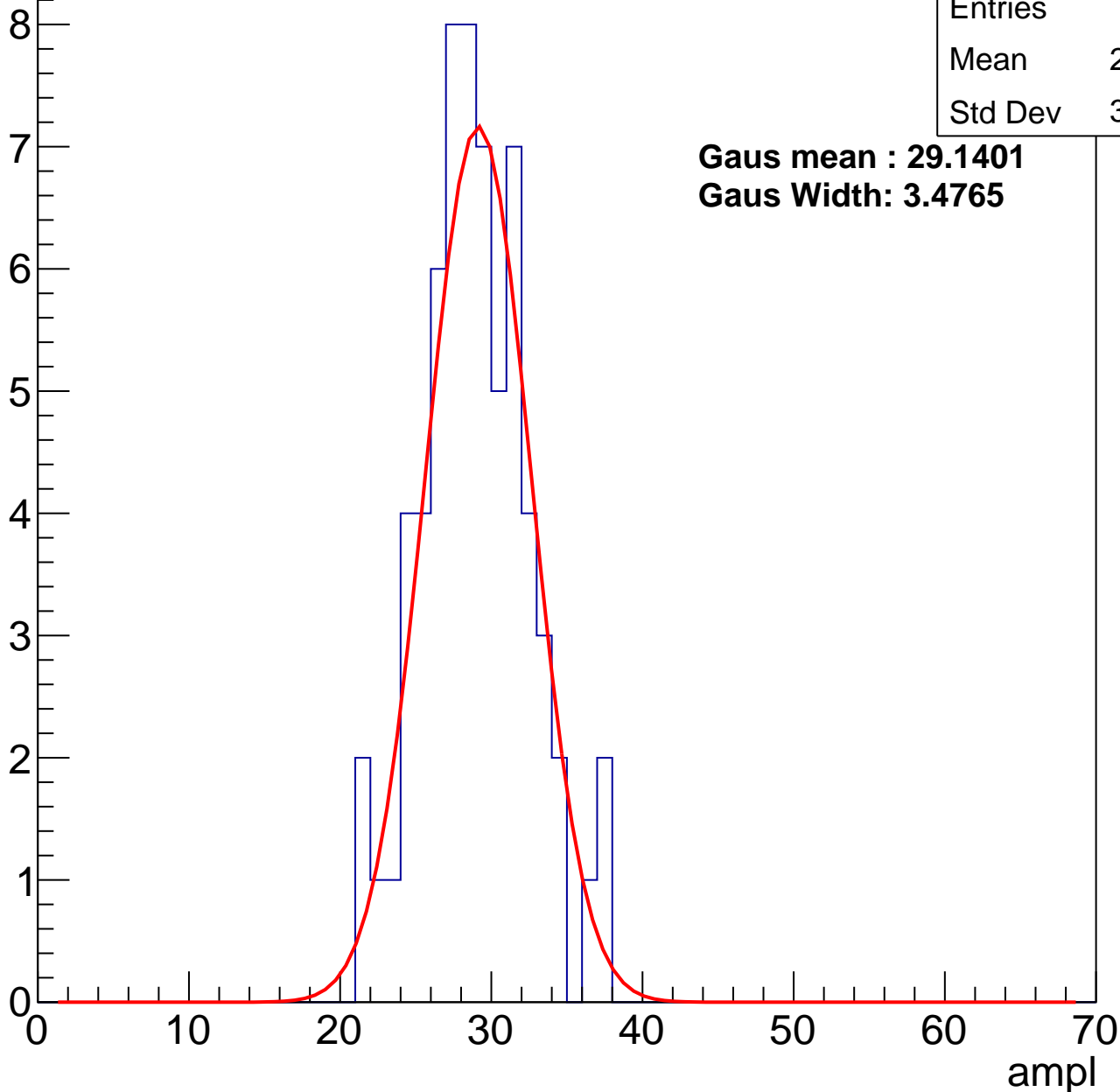
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.52
Std Dev	3.509

Gaus mean : 29.1401

Gaus Width: 3.4765



B1L002S, U7-ch110, adc1

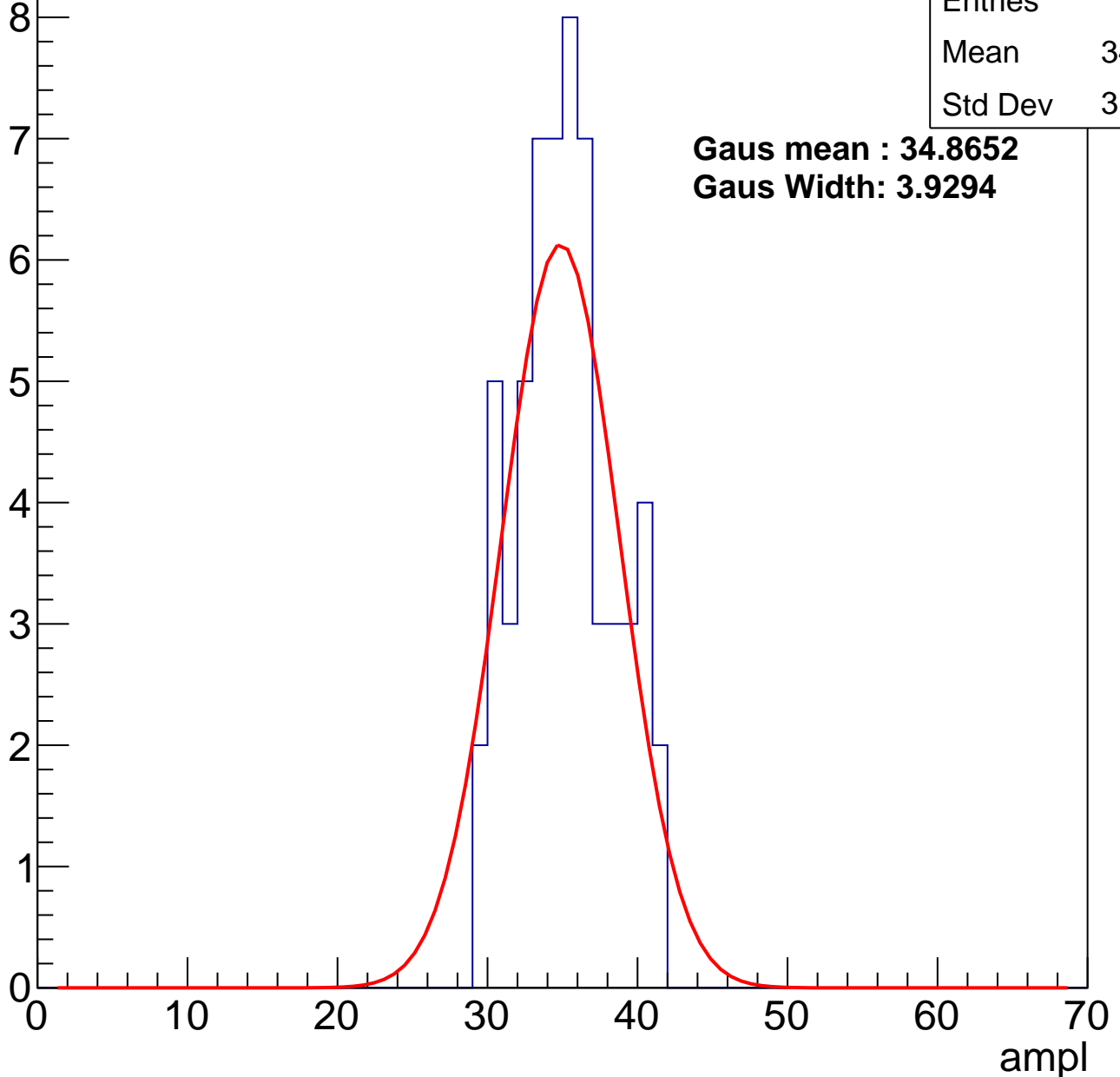
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.68
Std Dev	3.149

Gaus mean : 34.8652

Gaus Width: 3.9294



B1L002S, U7-ch110, adc2

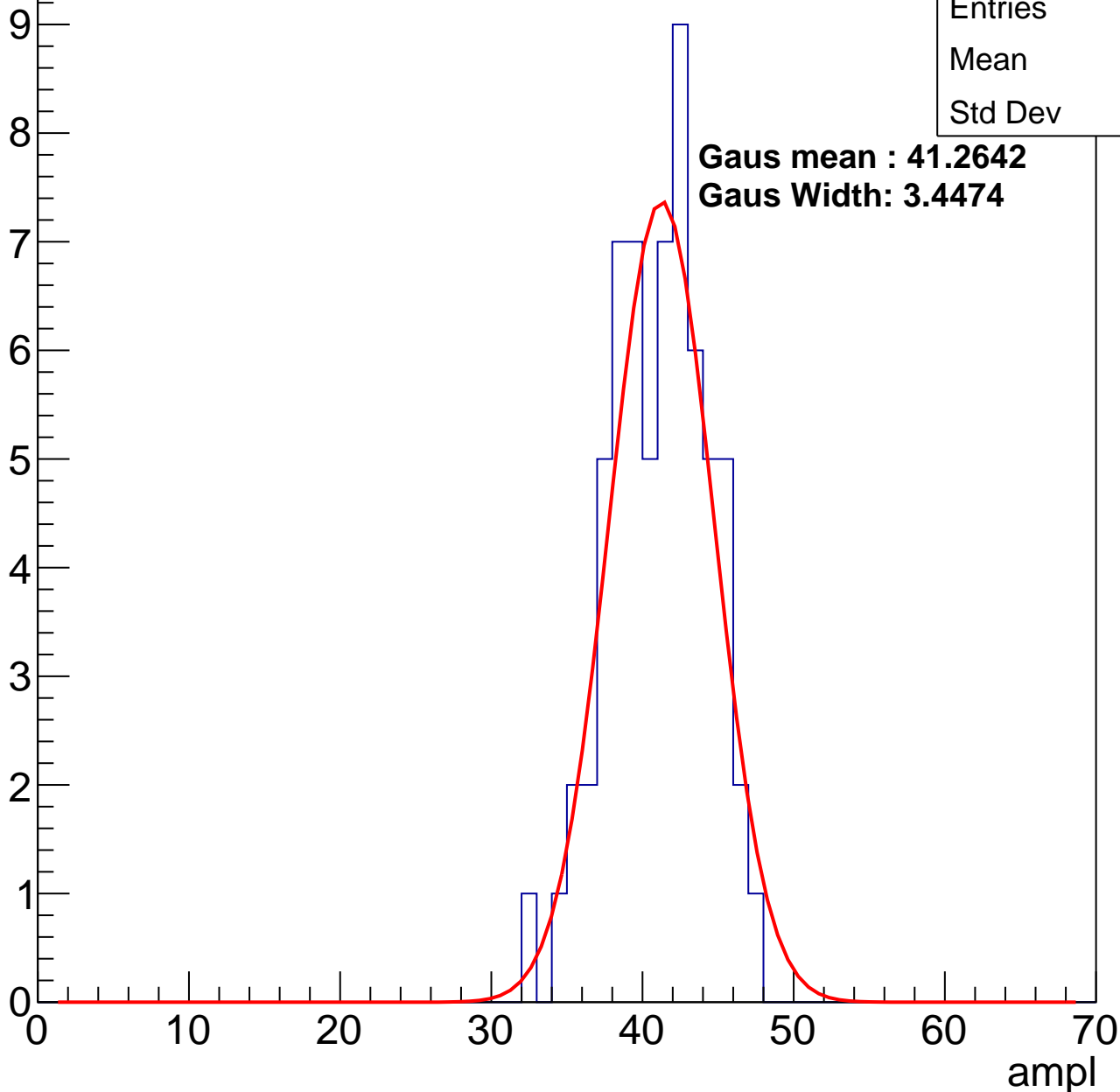
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.6
Std Dev	3.19

Gaus mean : 41.2642

Gaus Width: 3.4474

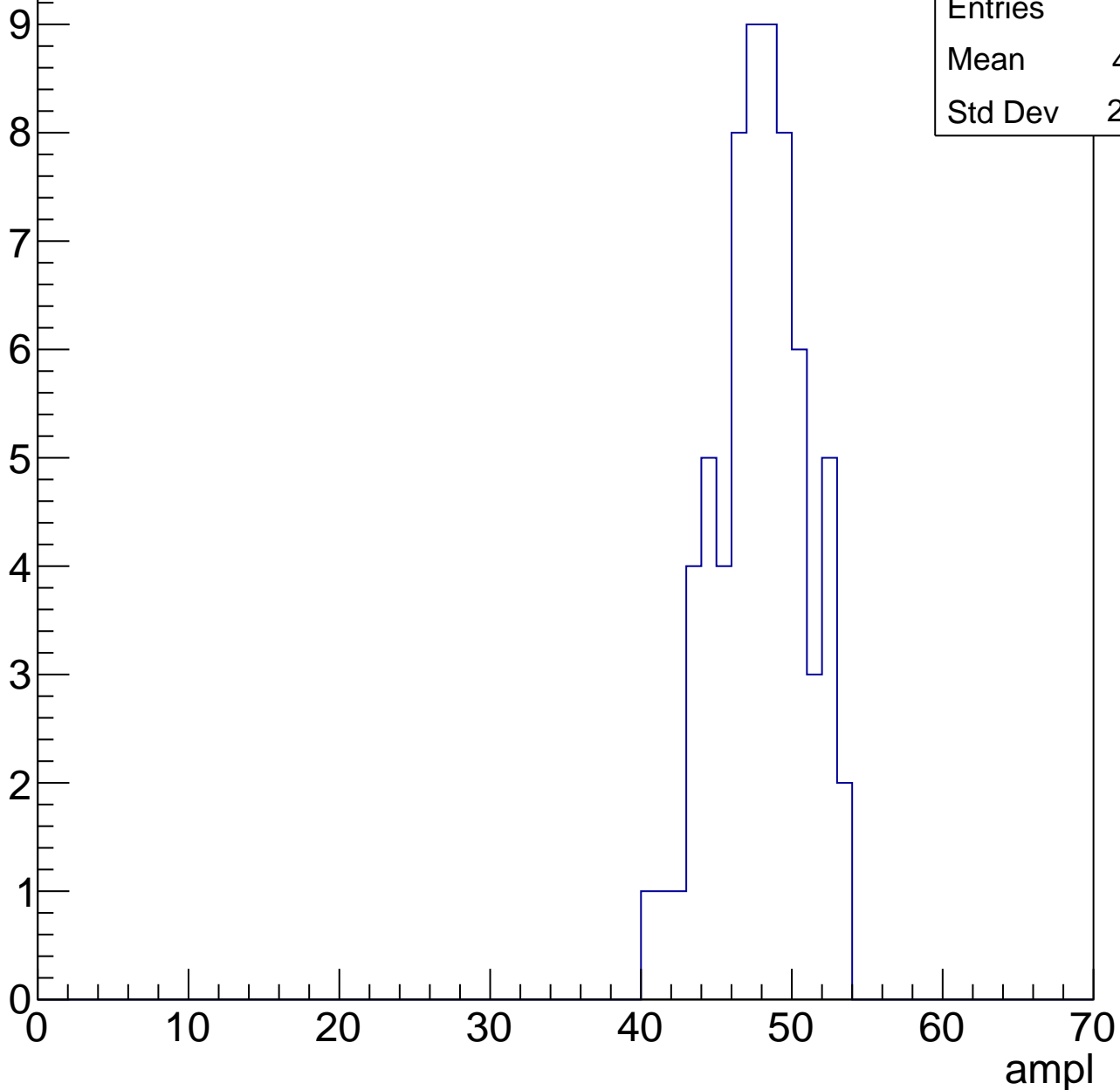


B1L002S, U7-ch110, adc3

calib_packv5_042523_0143.root, FC#10, port B3

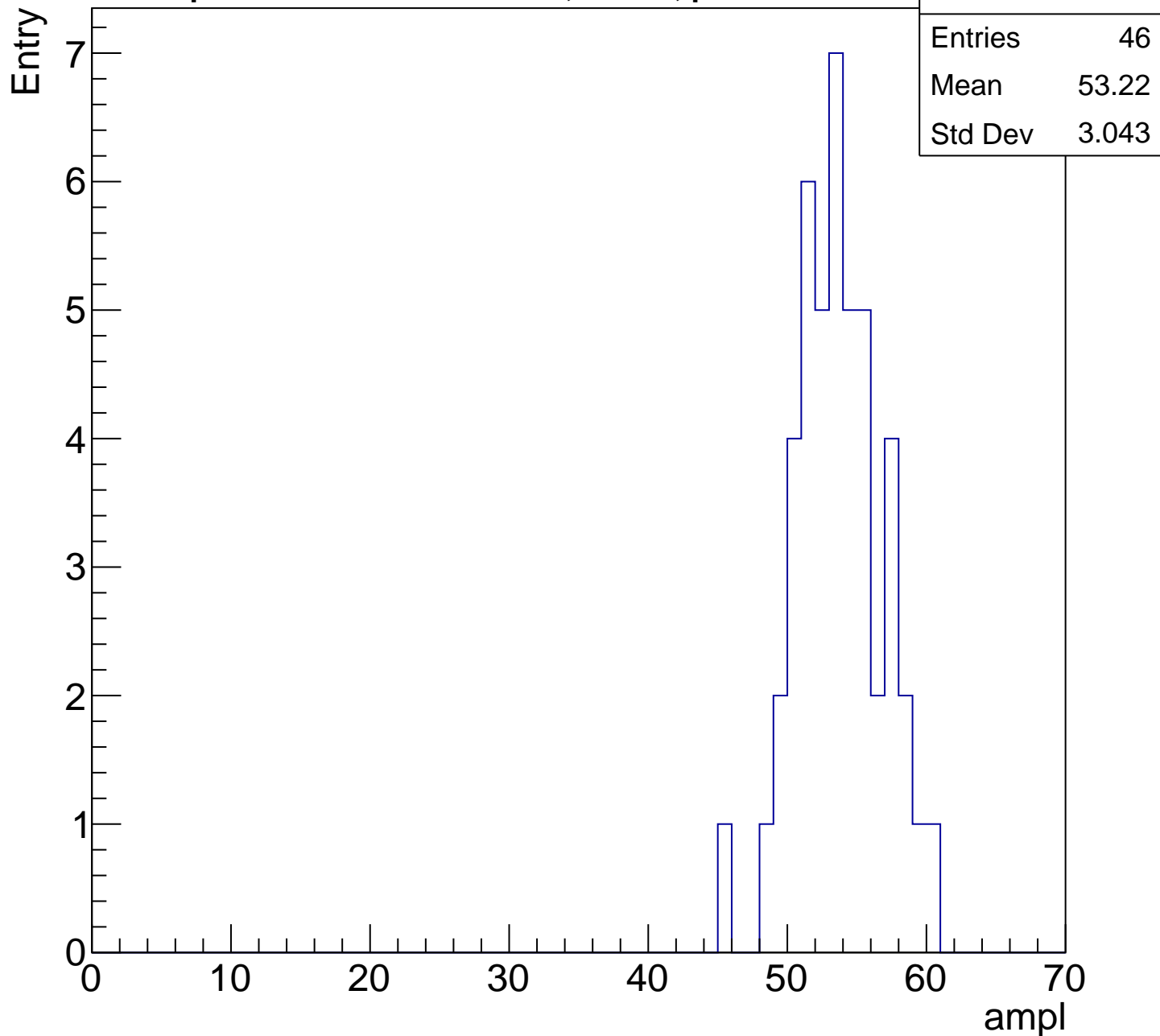
Entry

Entries	66
Mean	47.41
Std Dev	2.944



B1L002S, U7-ch110, adc4

calib_packv5_042523_0143.root, FC#10, port B3

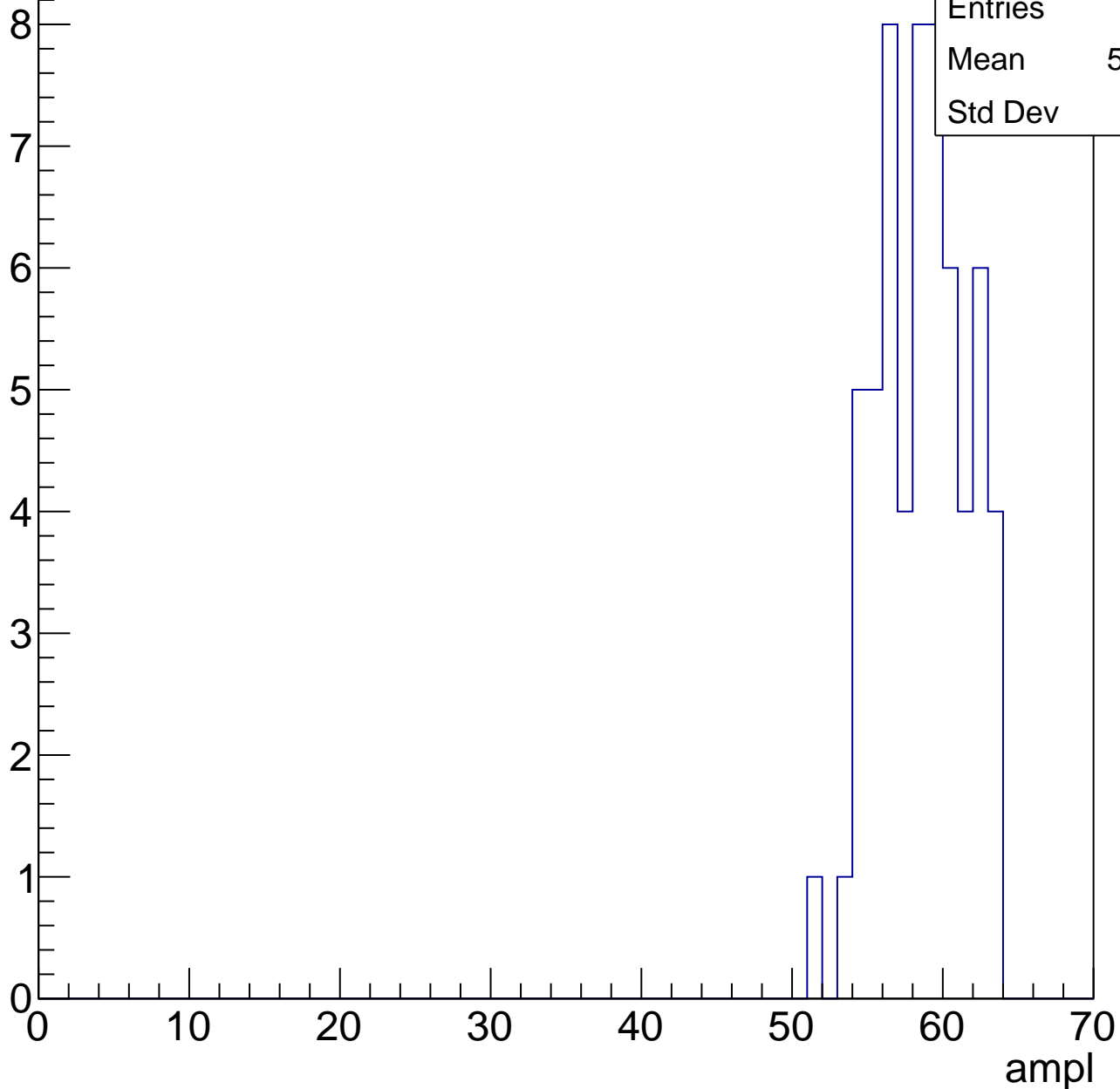


B1L002S, U7-ch110, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.15
Std Dev	2.88

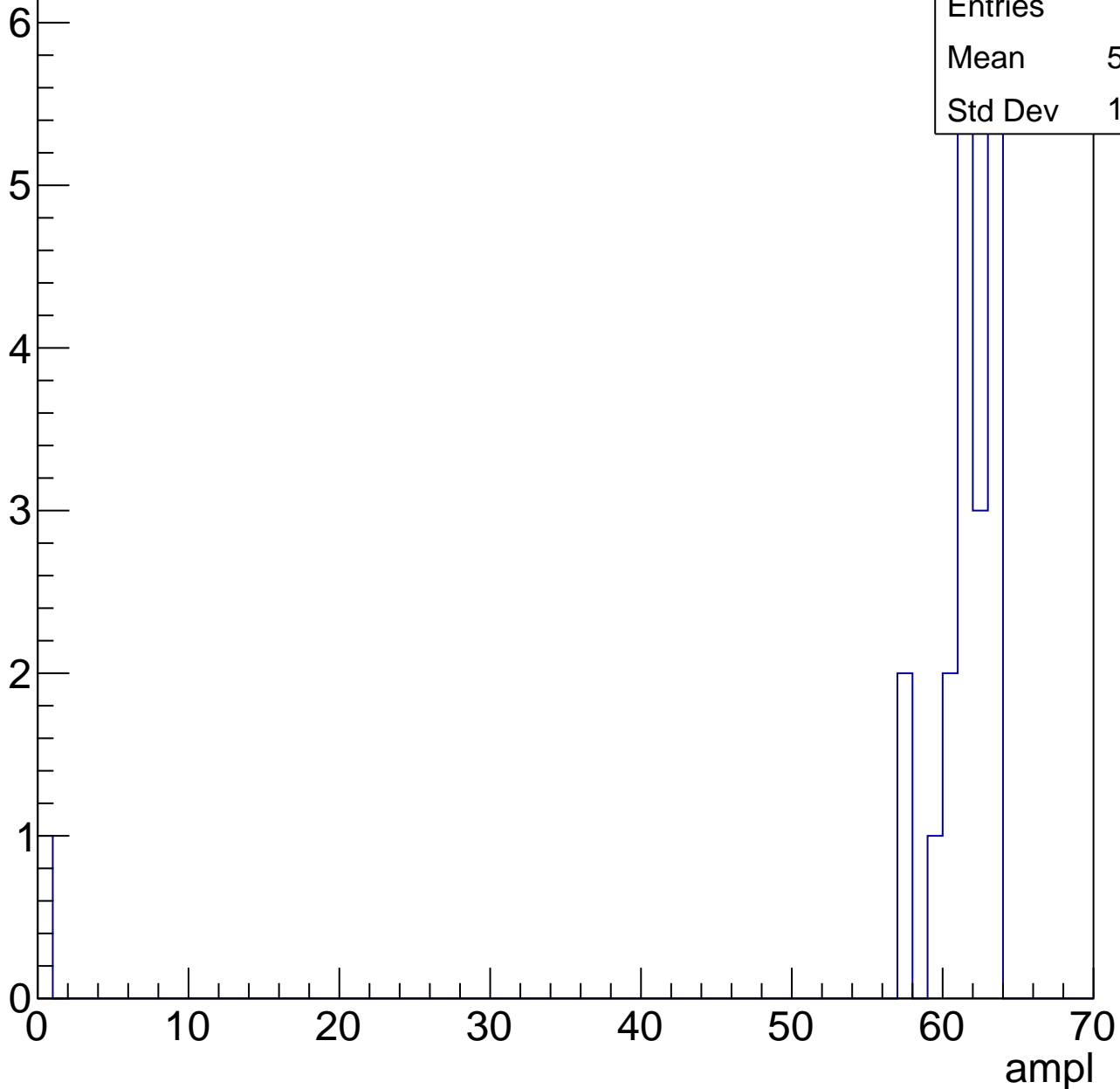


B1L002S, U7-ch110, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

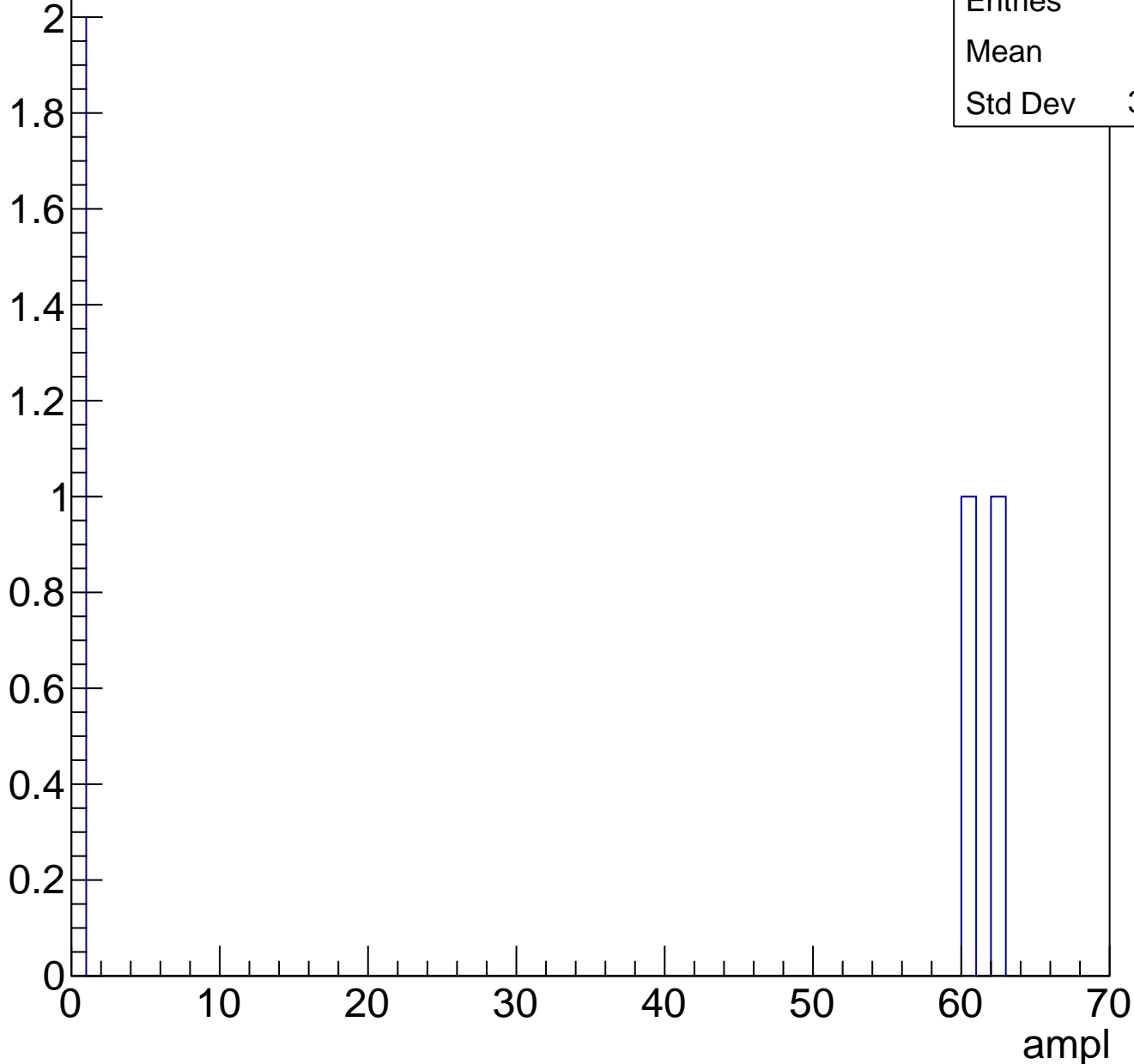
Entries	21
Mean	58.24
Std Dev	13.14



B1L002S, U7-ch110, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch111, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	65
Mean	27.42
Std Dev	4.506

Gaus mean : 28.0445

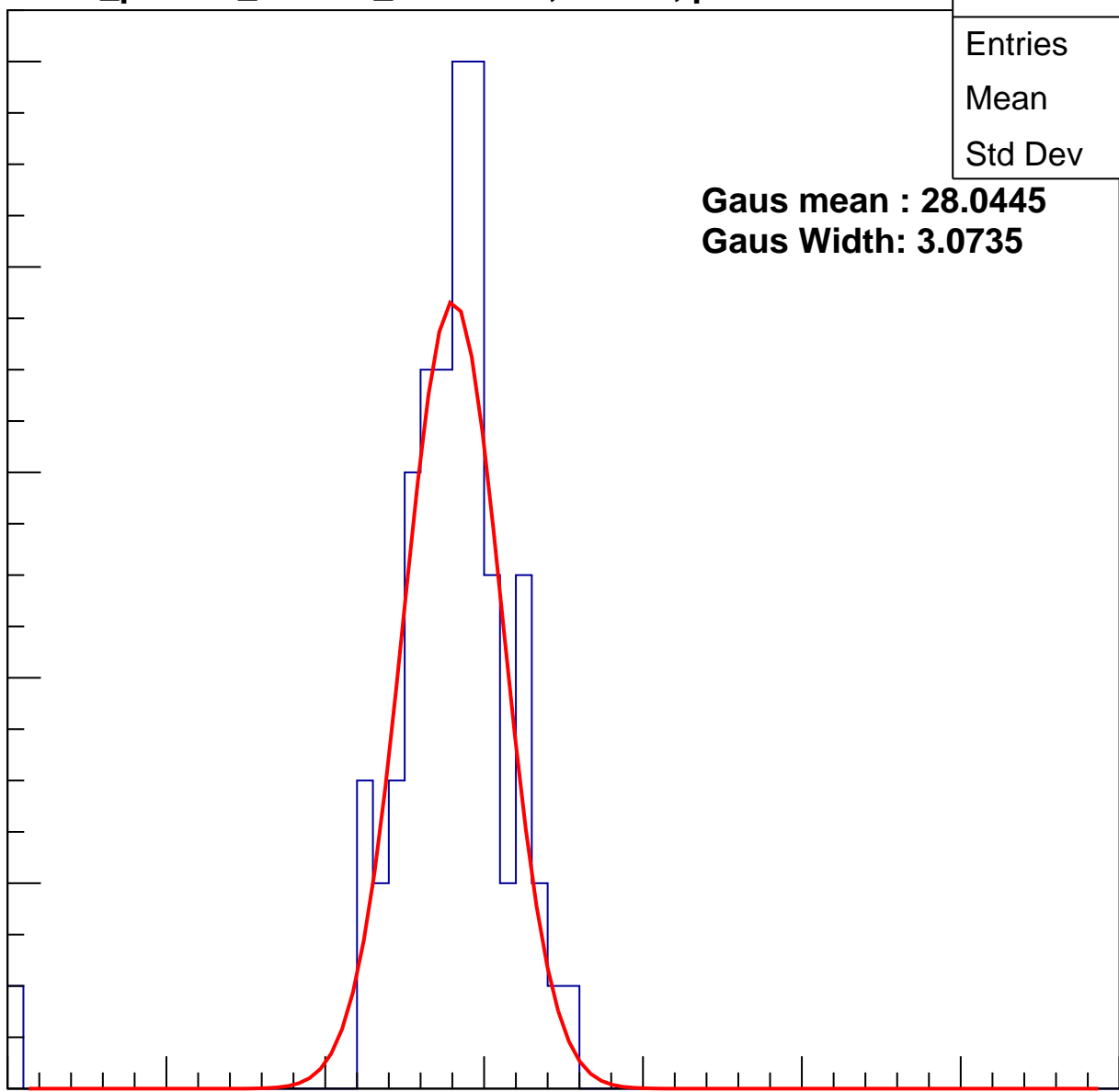
Gaus Width: 3.0735

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch111, adc1

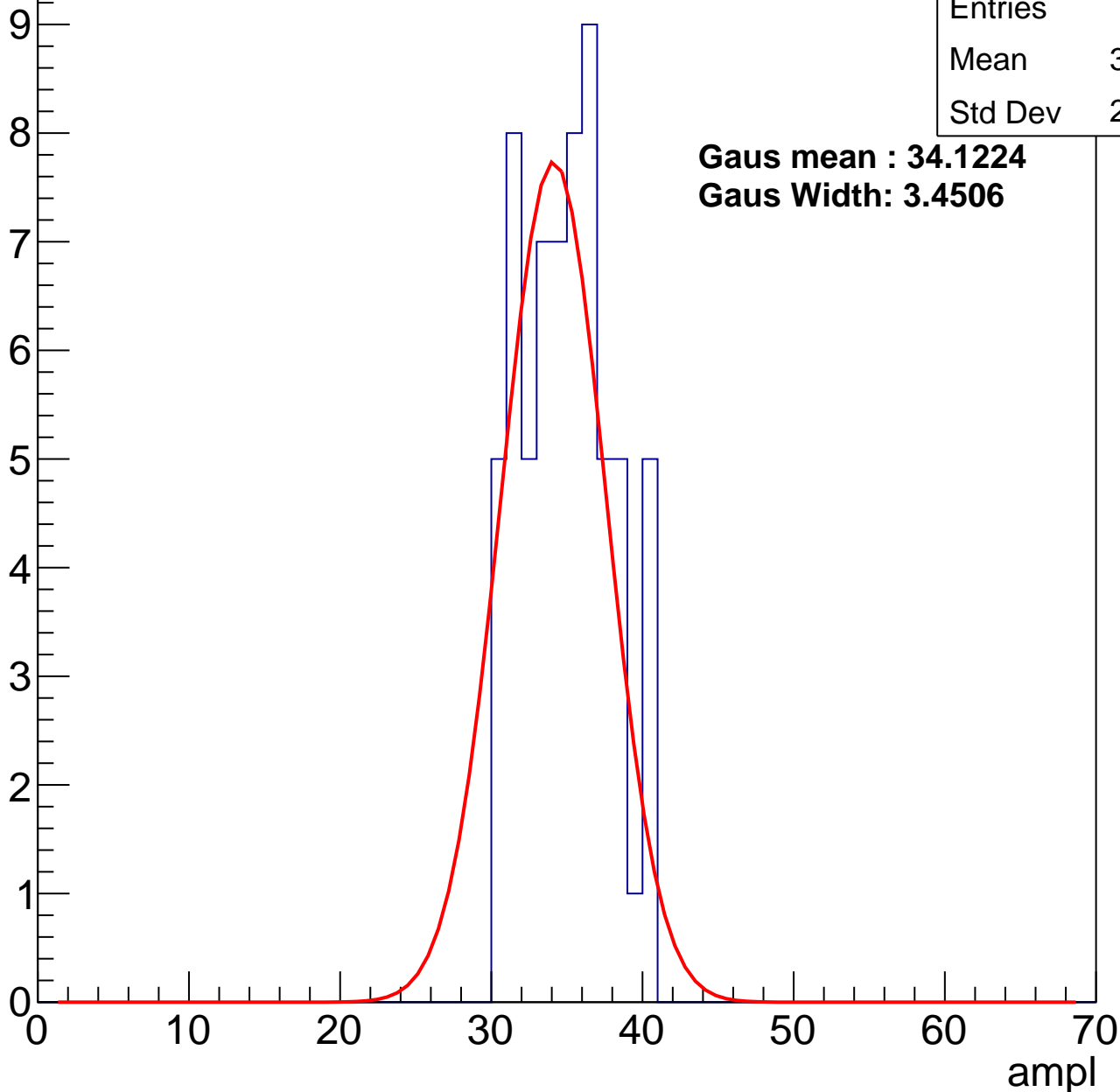
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.54
Std Dev	2.867

Gaus mean : 34.1224

Gaus Width: 3.4506



B1L002S, U7-ch111, adc2

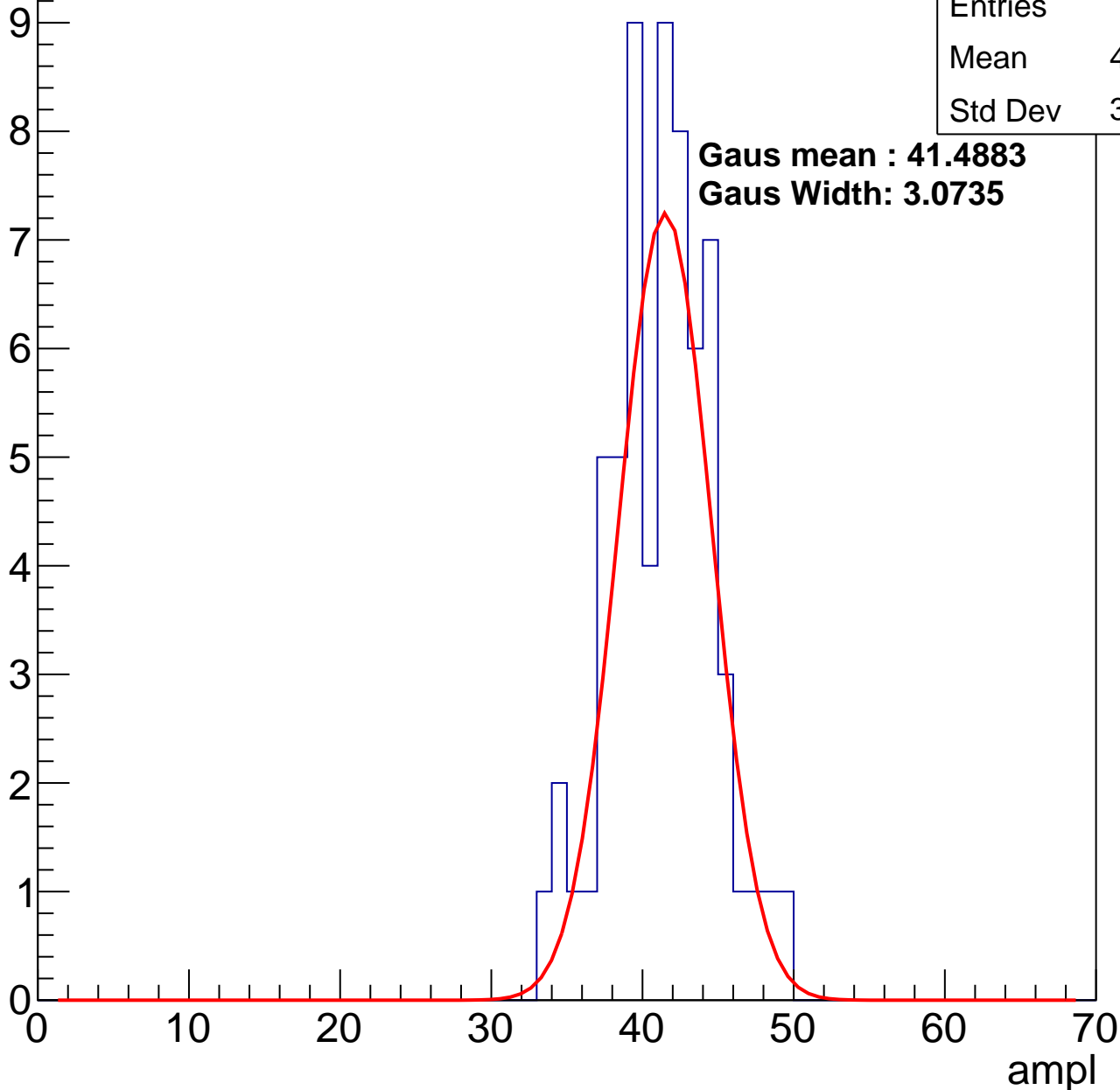
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.83
Std Dev	3.289

Gaus mean : 41.4883

Gaus Width: 3.0735

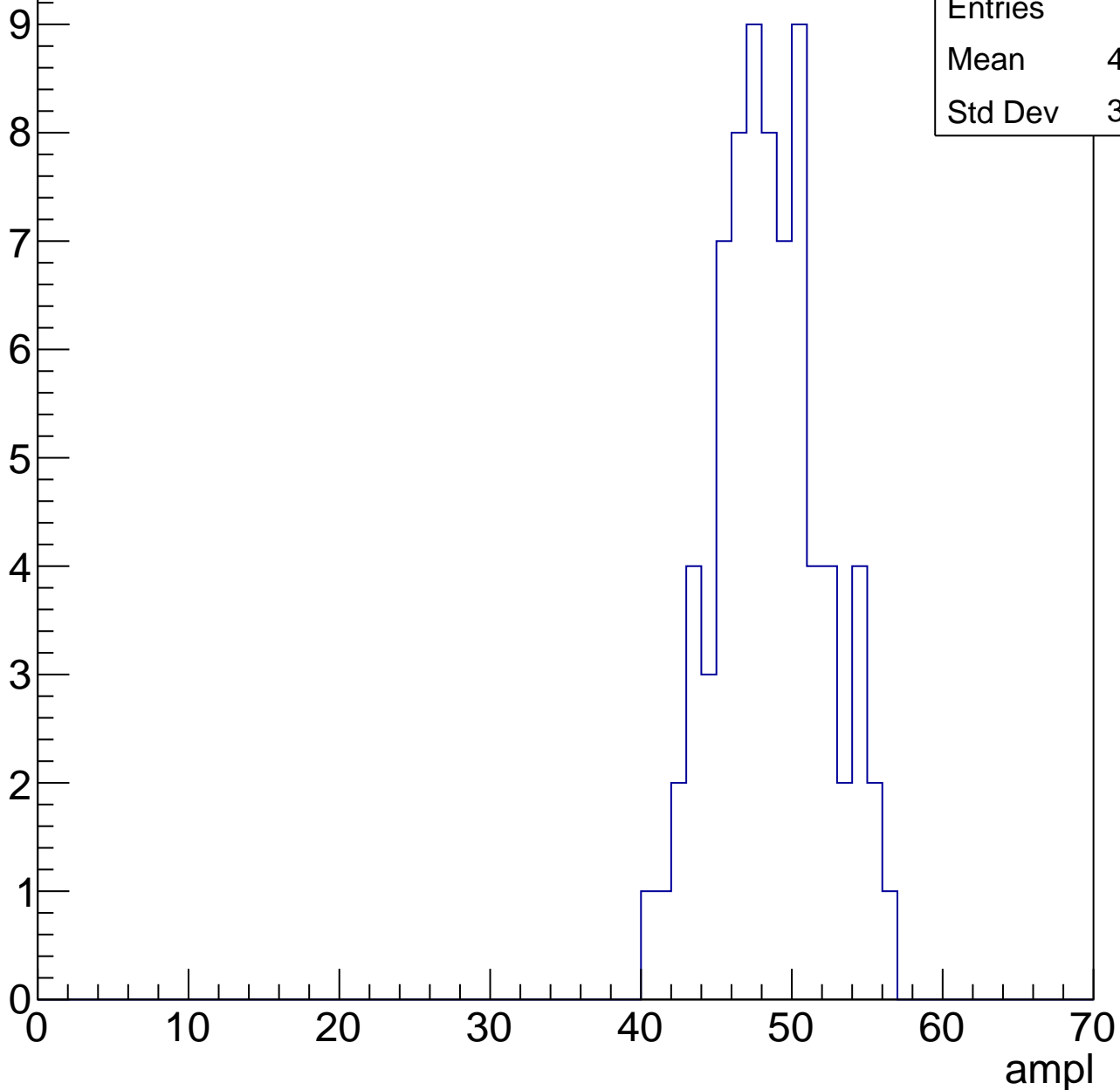


B1L002S, U7-ch111, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	76
Mean	48.05
Std Dev	3.509

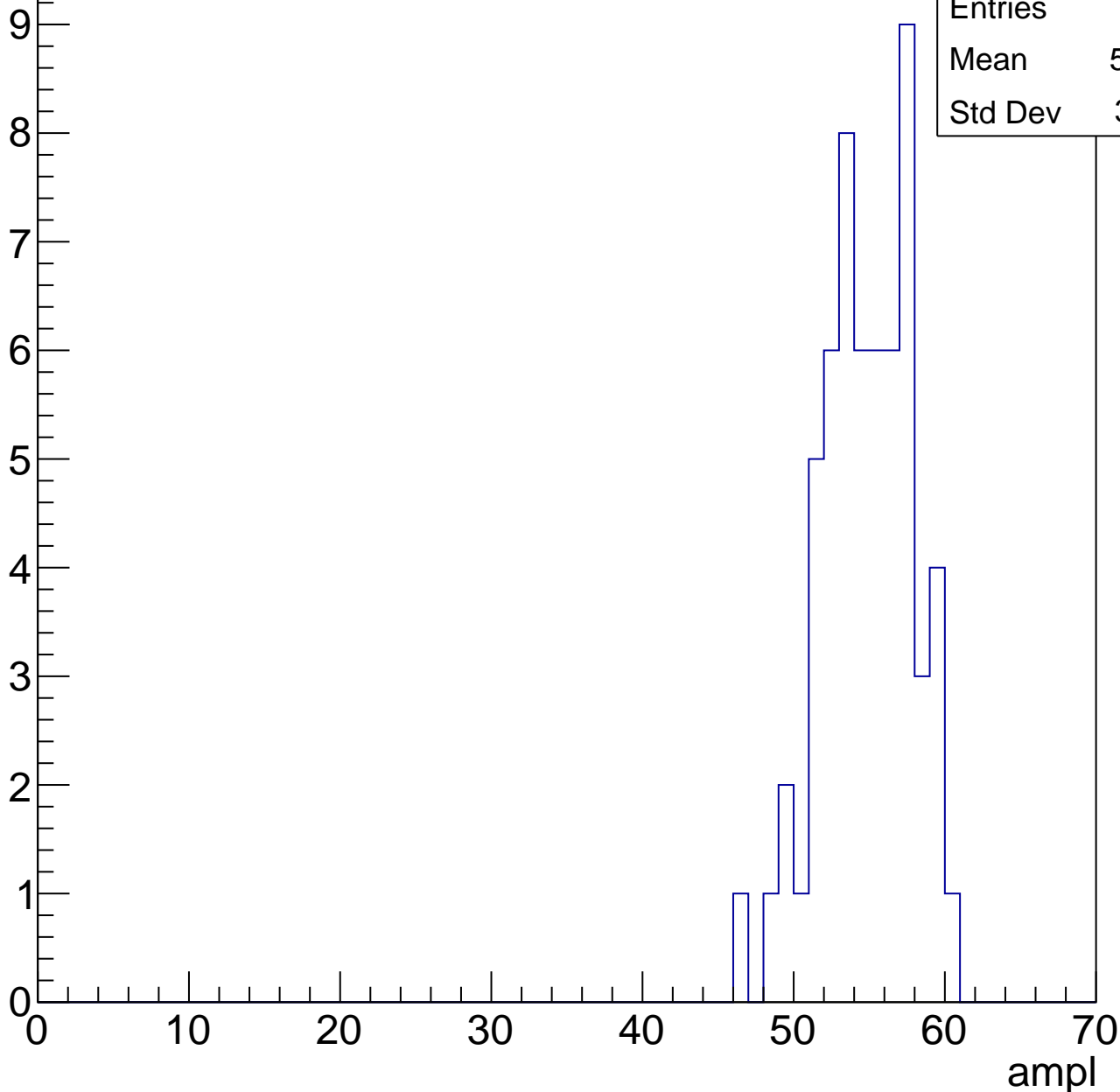


B1L002S, U7-ch111, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	54.34
Std Dev	3.001

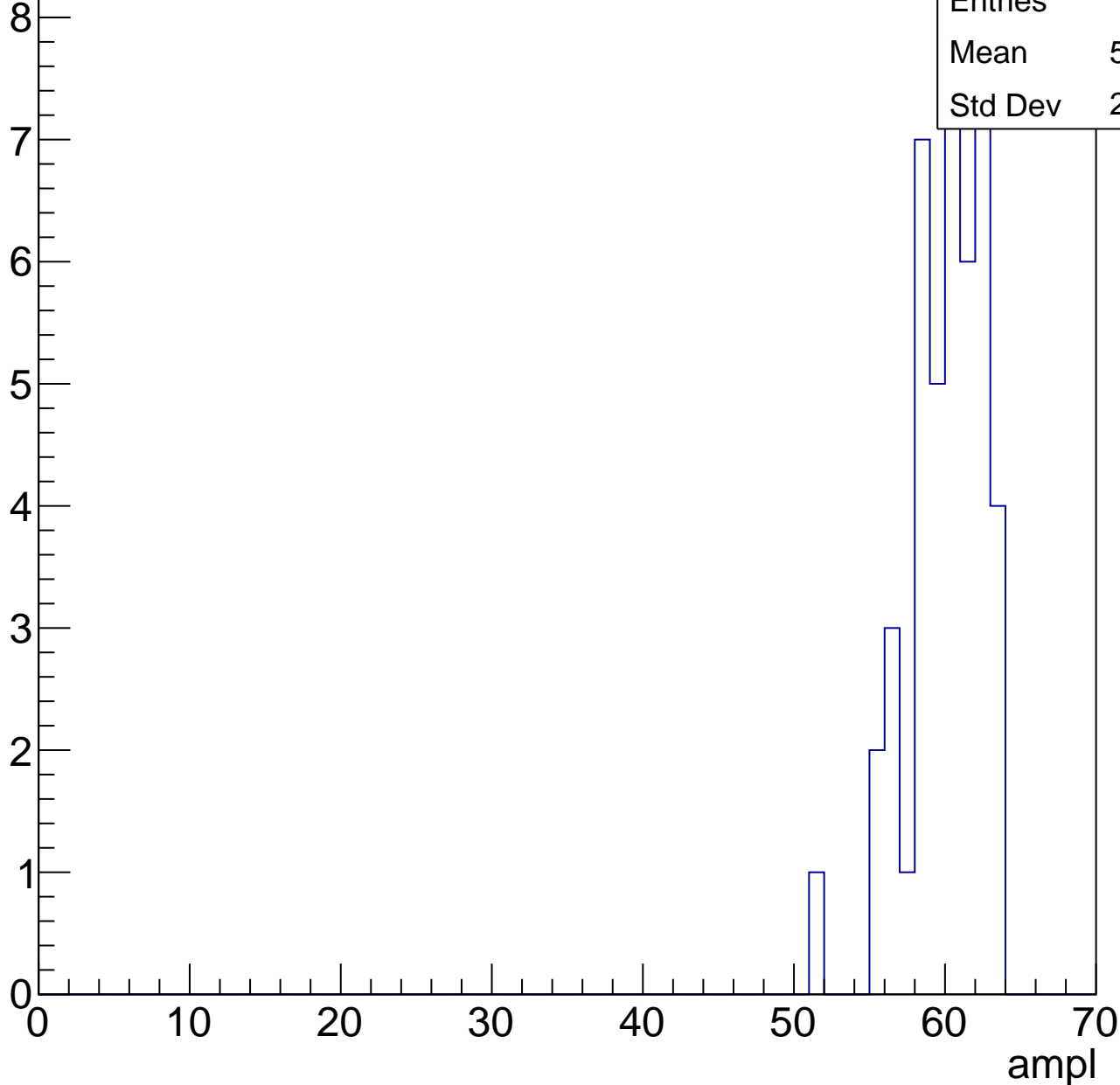


B1L002S, U7-ch111, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	59.58
Std Dev	2.525

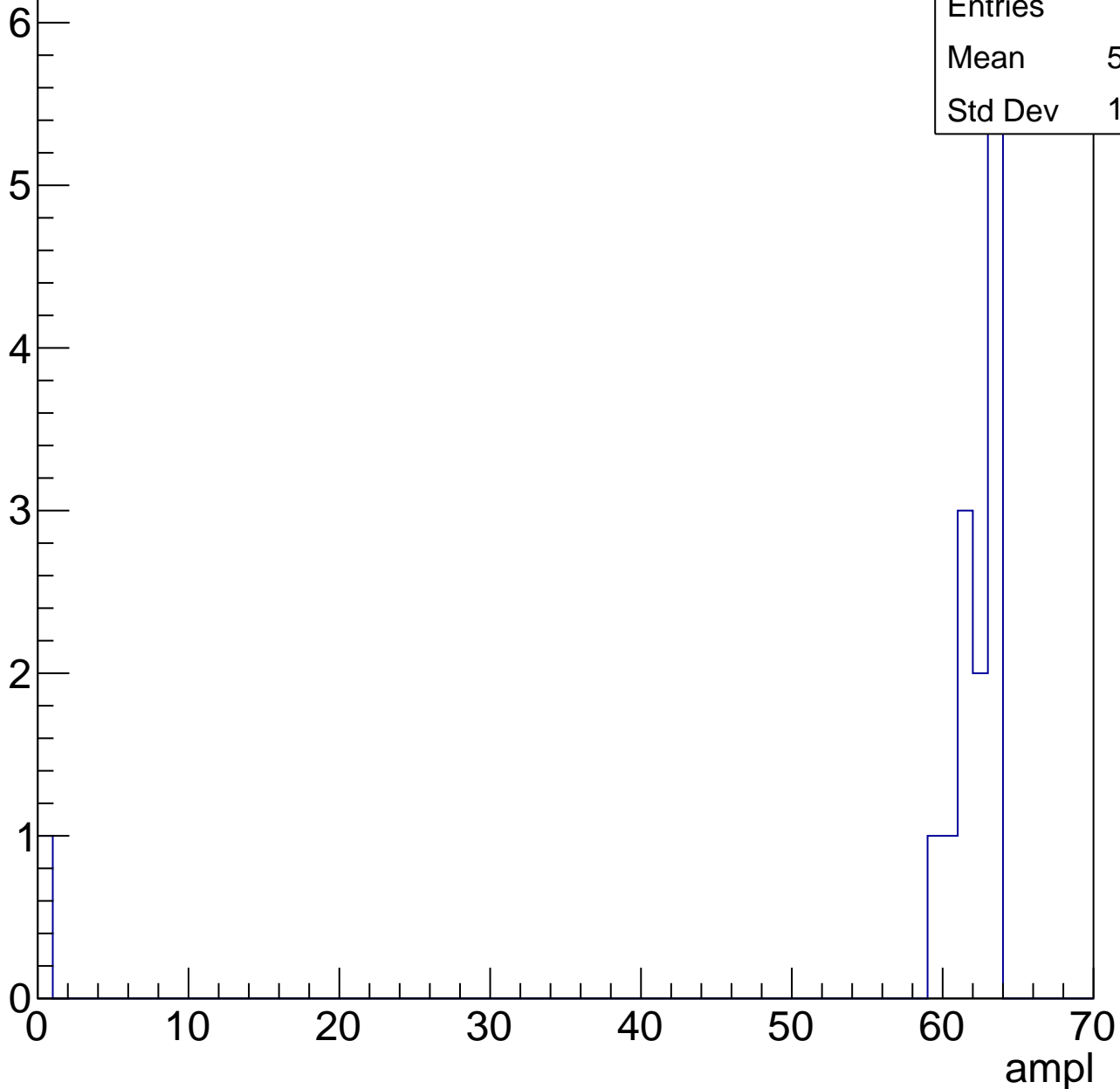


B1L002S, U7-ch111, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.43
Std Dev	15.98



B1L002S, U7-ch111, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch112, adc0

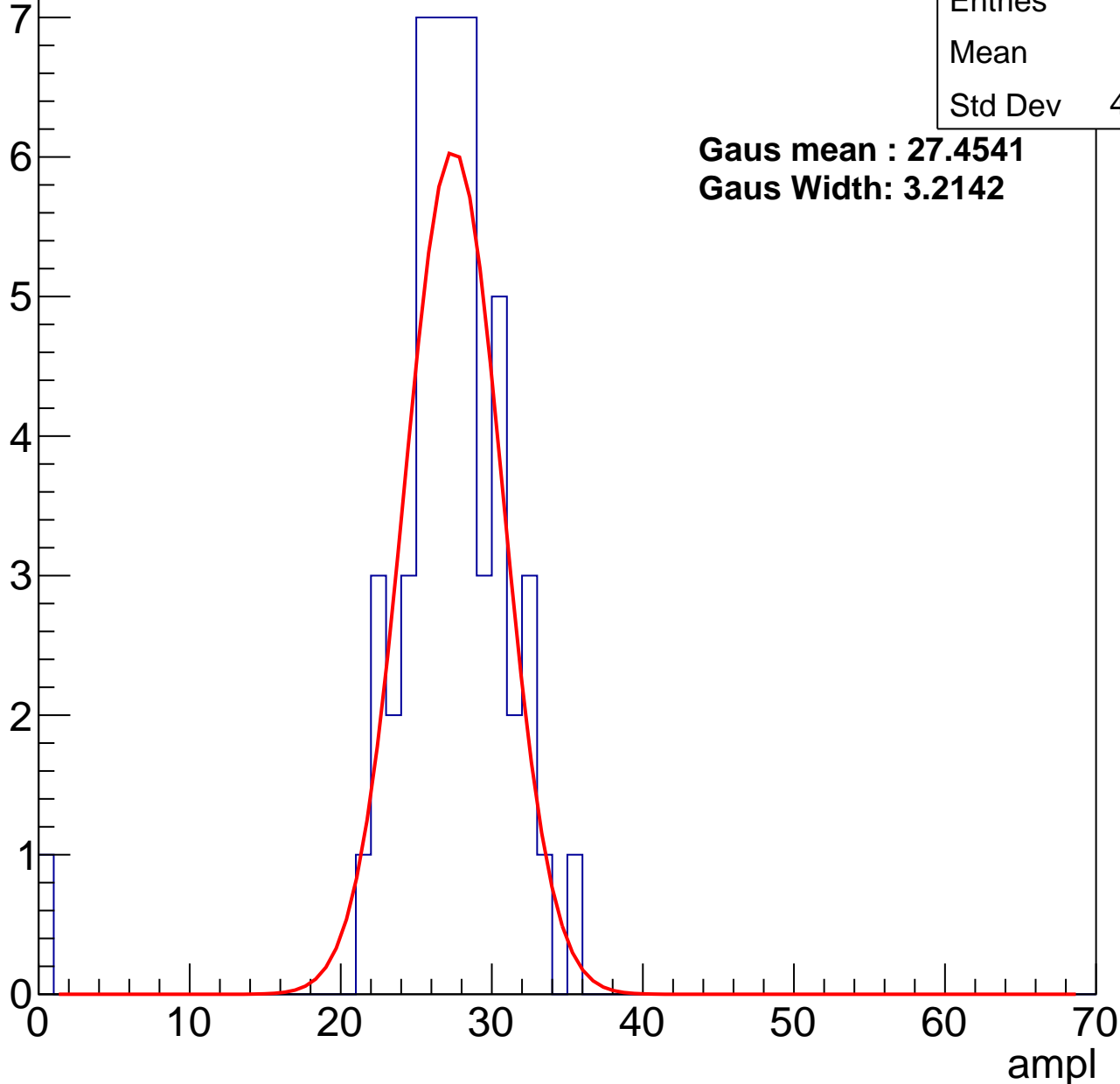
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	53
Mean	26.6
Std Dev	4.752

Gaus mean : 27.4541

Gaus Width: 3.2142



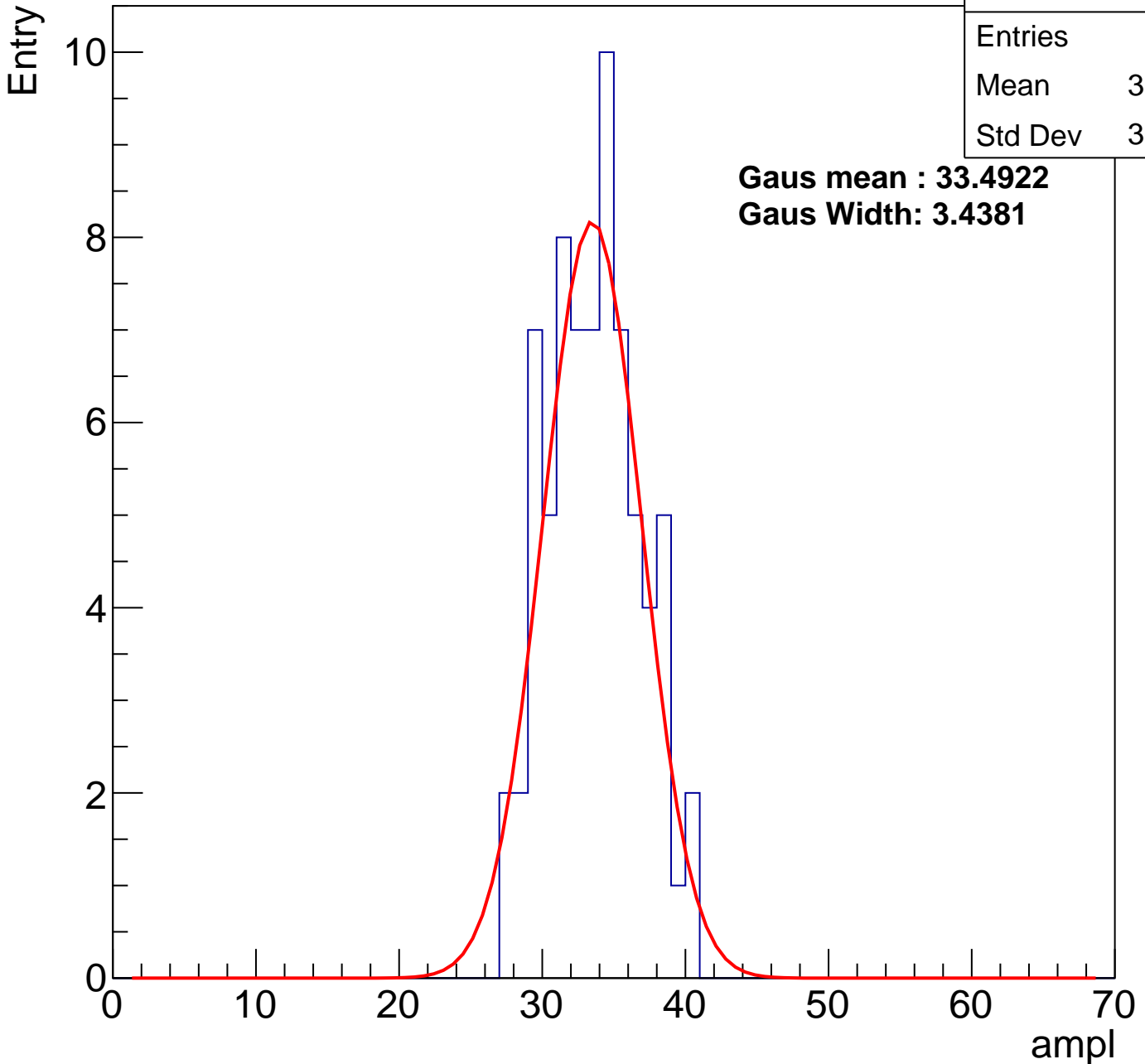
B1L002S, U7-ch112, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	72
Mean	33.17
Std Dev	3.167

Gaus mean : 33.4922

Gaus Width: 3.4381



B1L002S, U7-ch112, adc2

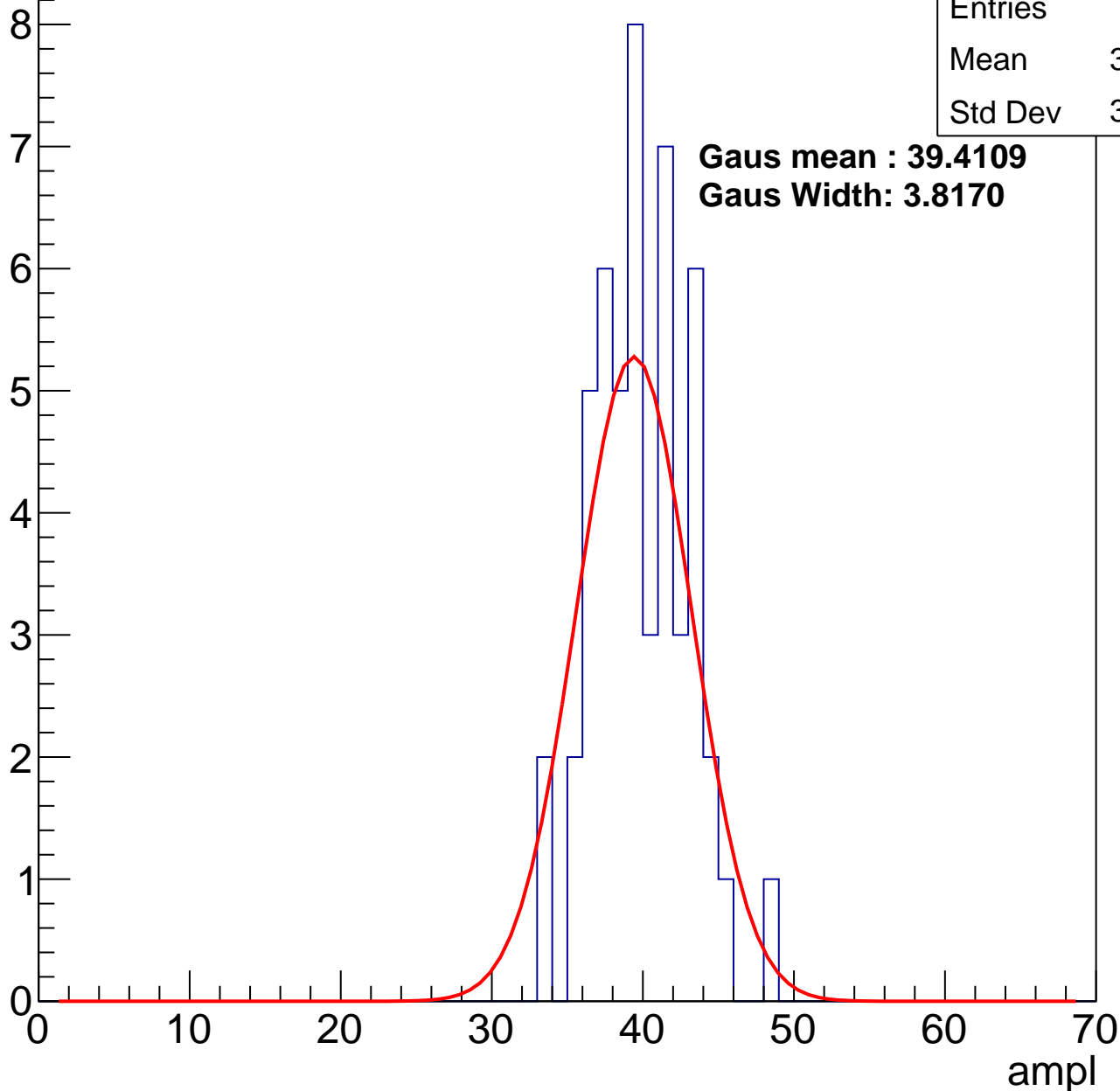
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	39.45
Std Dev	3.095

Gaus mean : 39.4109

Gaus Width: 3.8170

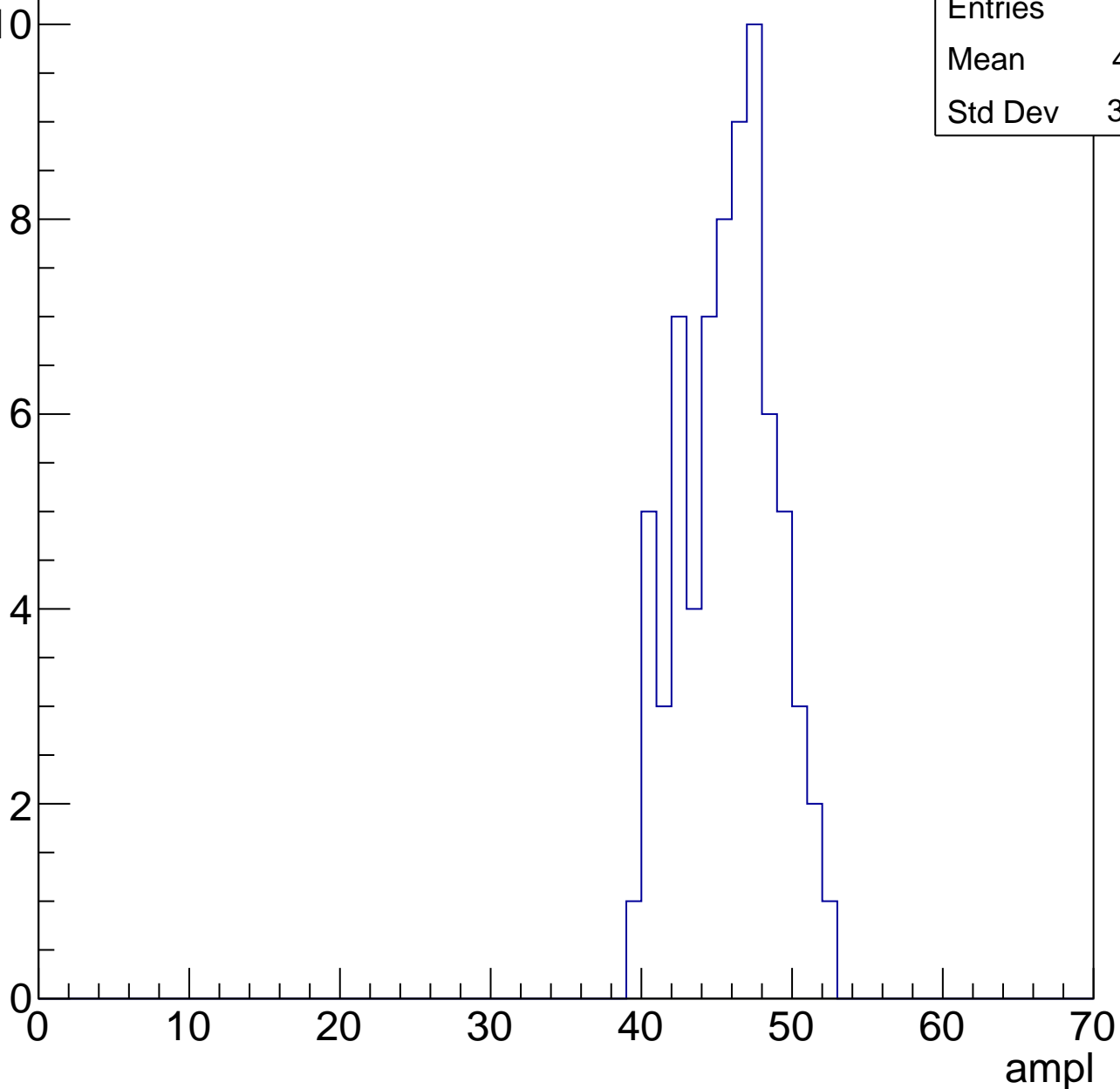


B1L002S, U7-ch112, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	45.31
Std Dev	3.065

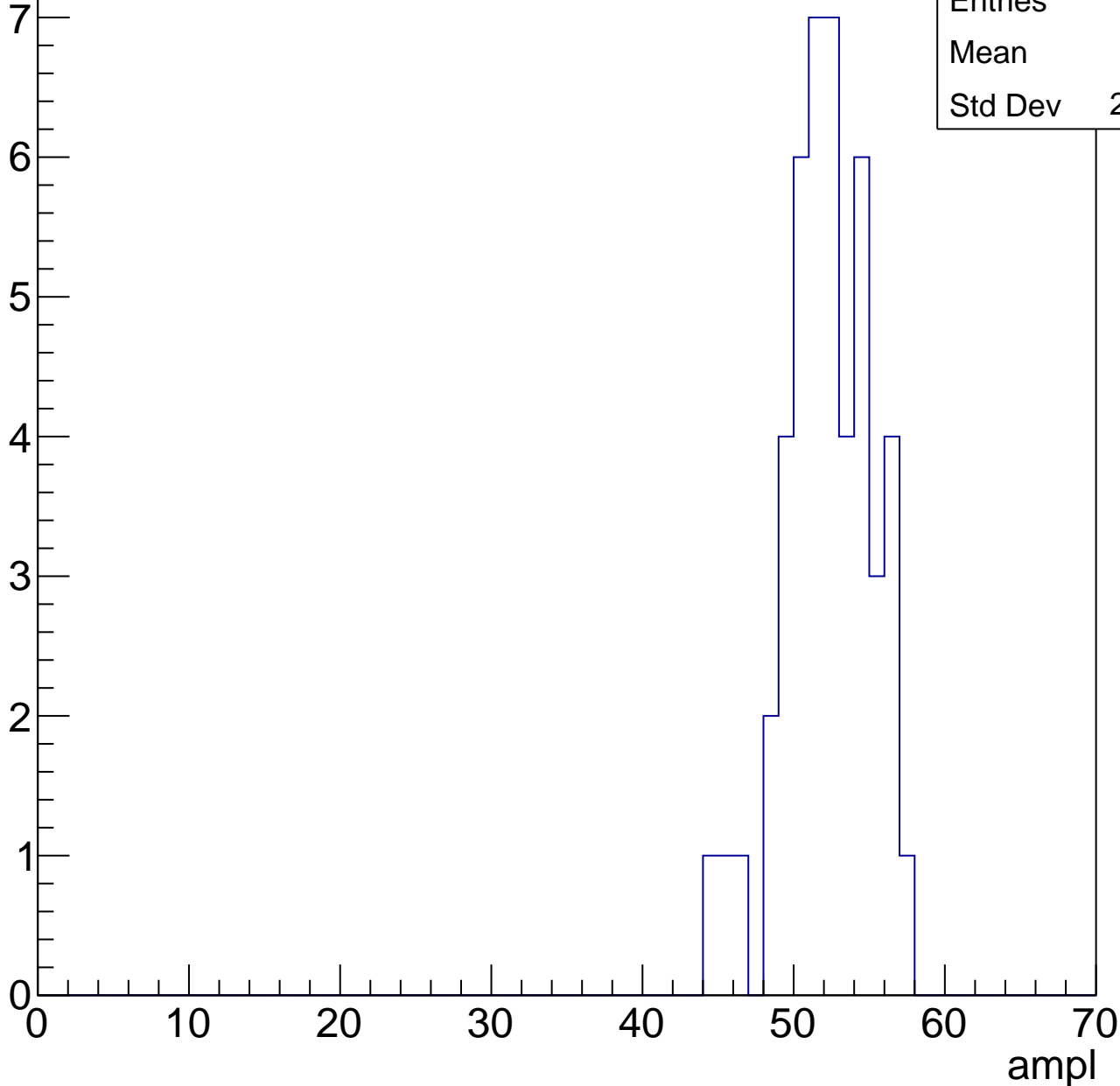


B1L002S, U7-ch112, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	51.7
Std Dev	2.873



B1L002S, U7-ch112, adc5

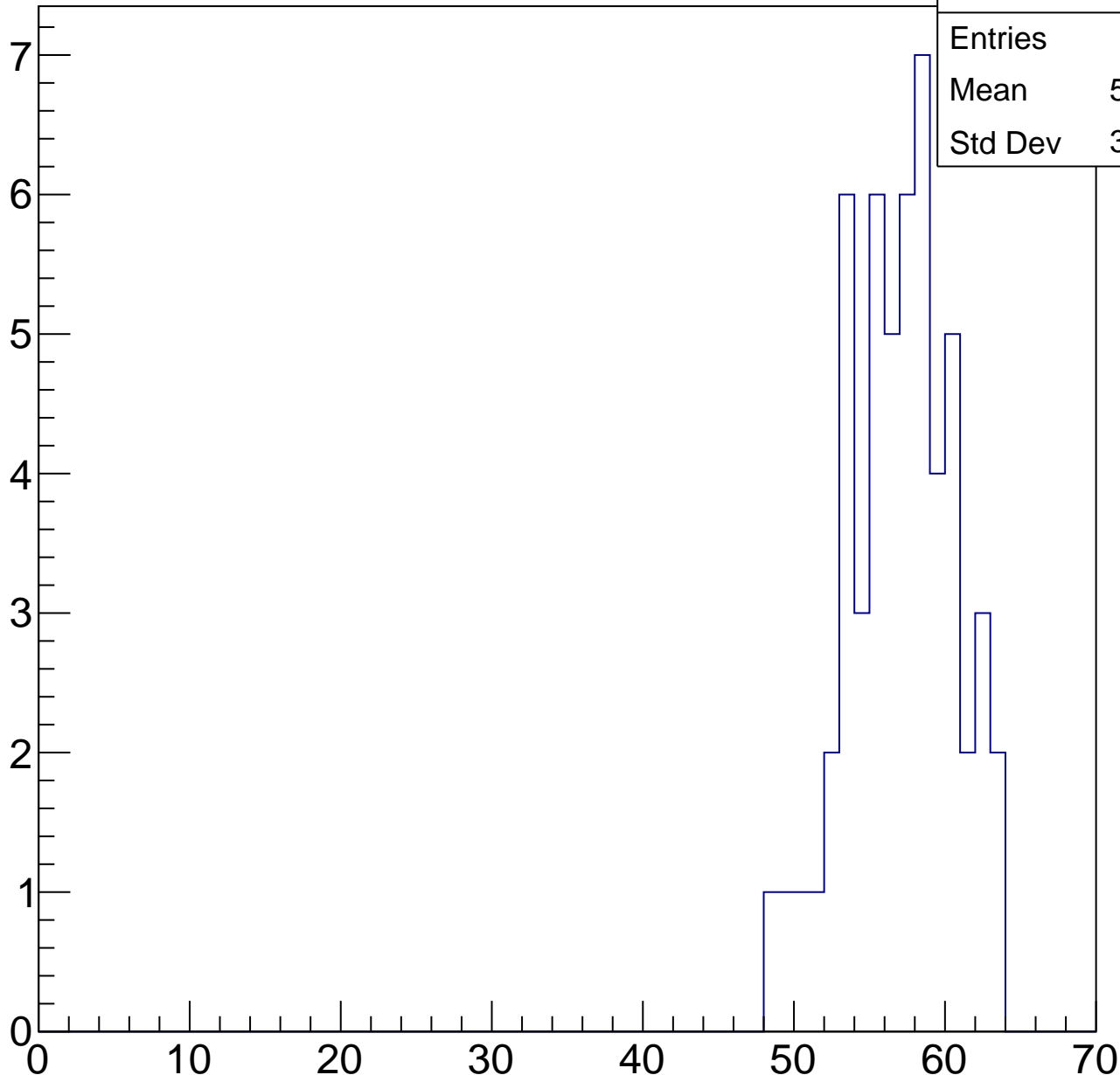
calib_packv5_042523_0143.root, FC#10, port B3

Entry

7
6
5
4
3
2
1
0

Entries	55
Mean	56.55
Std Dev	3.484

ampl

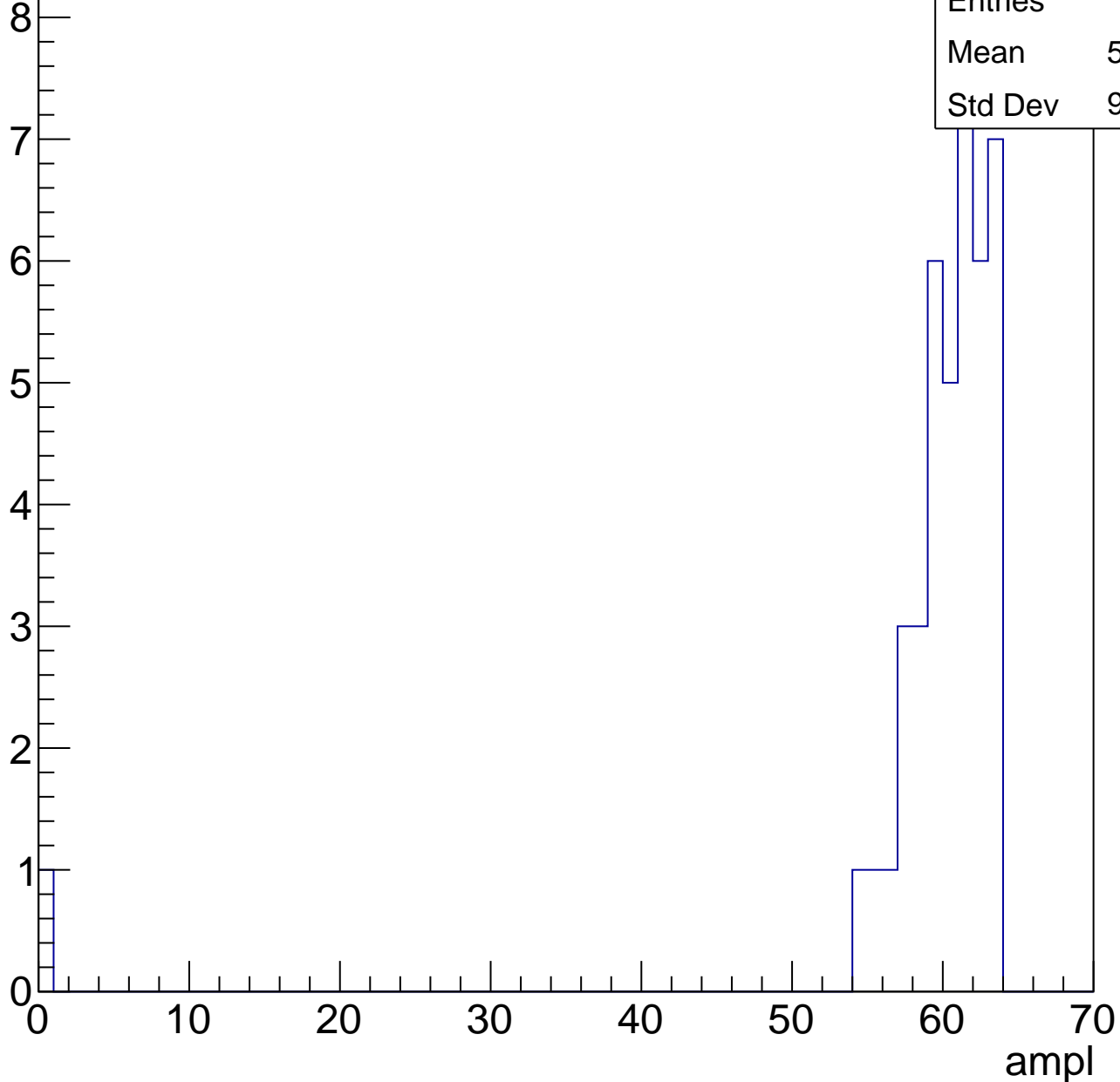


B1L002S, U7-ch112, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

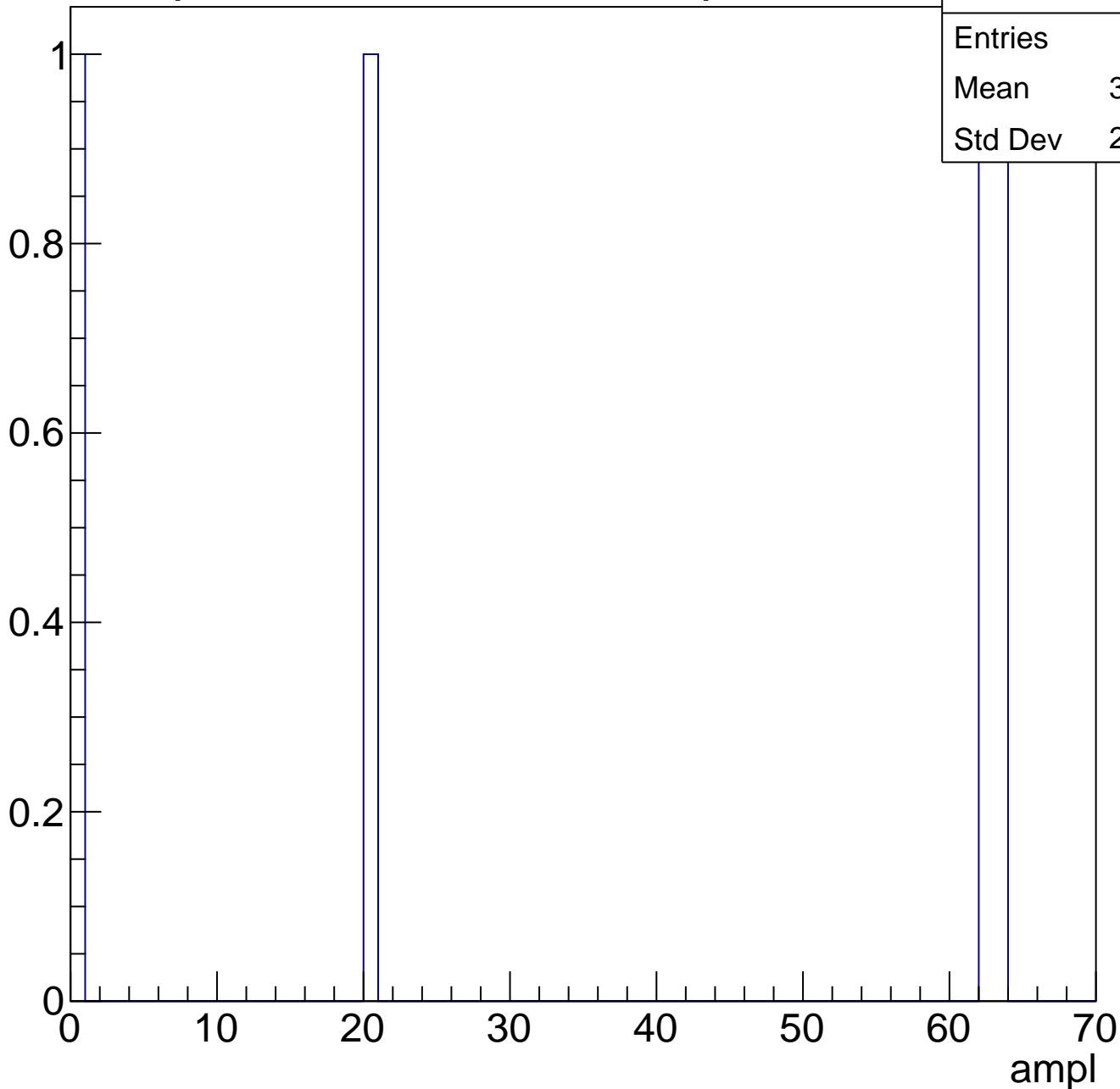
Entries	42
Mean	58.69
Std Dev	9.443



B1L002S, U7-ch112, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch113, adc0

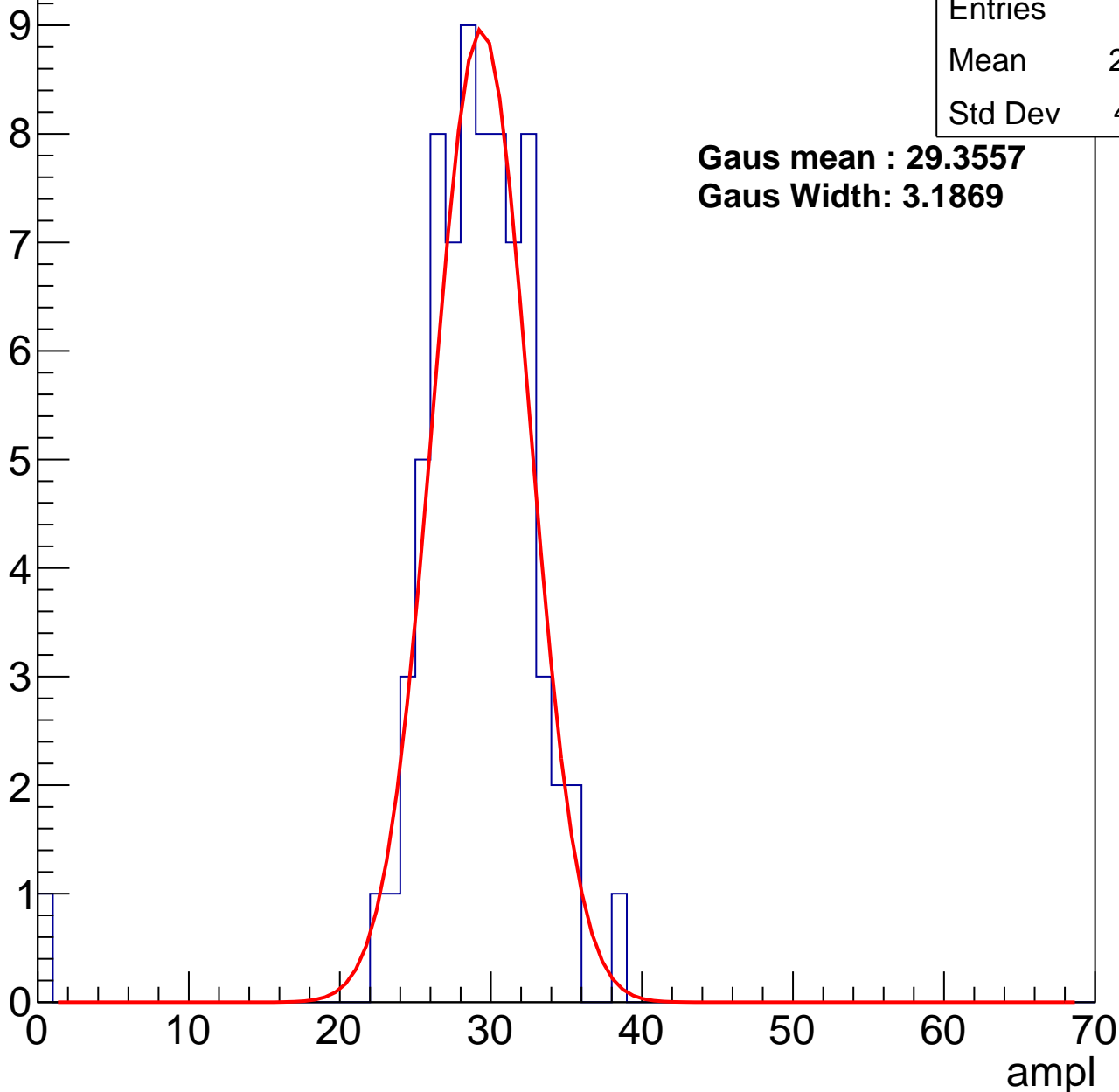
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.53
Std Dev	4.551

Gaus mean : 29.3557

Gaus Width: 3.1869



B1L002S, U7-ch113, adc1

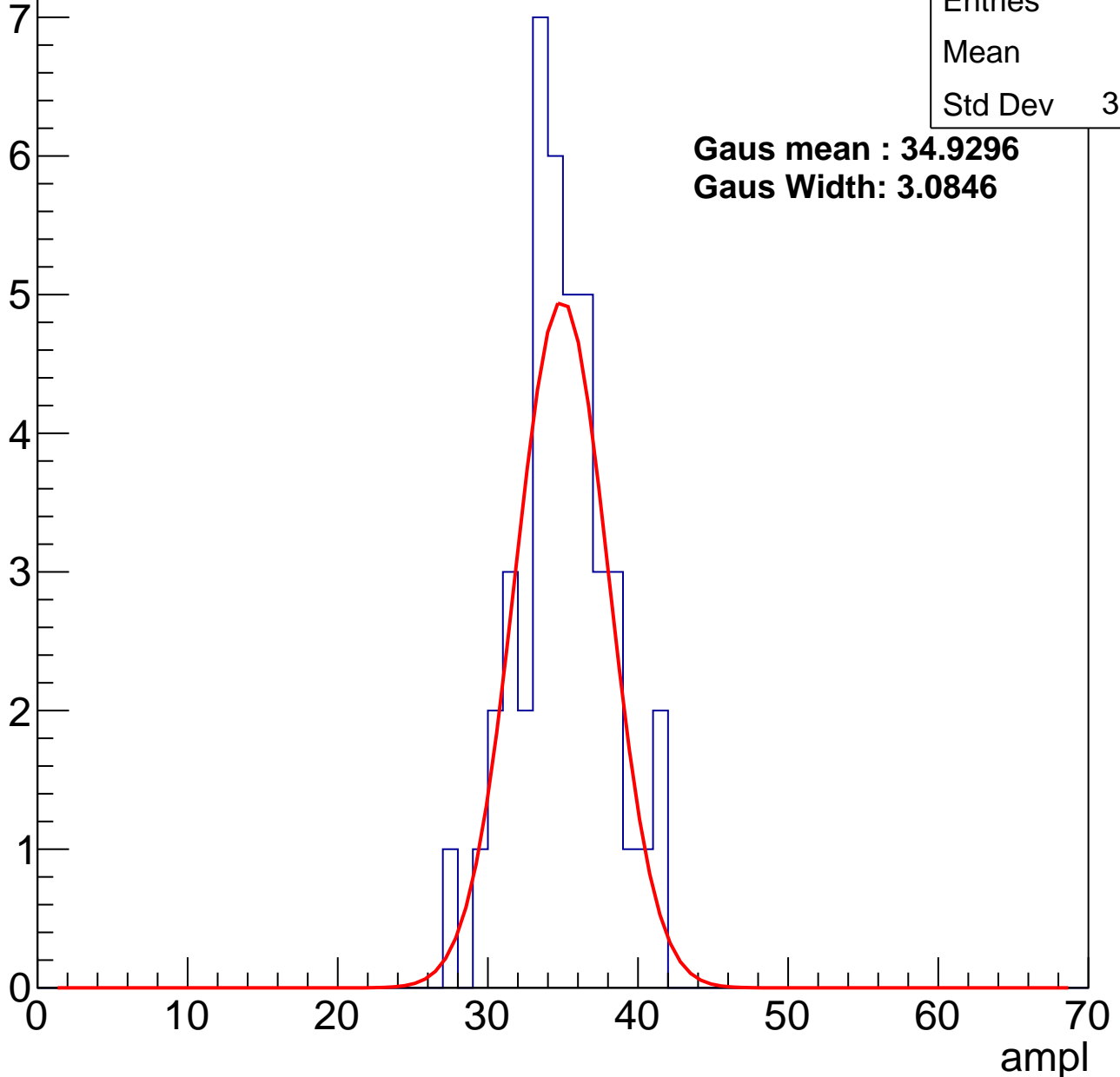
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	42
Mean	34.5
Std Dev	3.073

Gaus mean : 34.9296

Gaus Width: 3.0846



B1L002S, U7-ch113, adc2

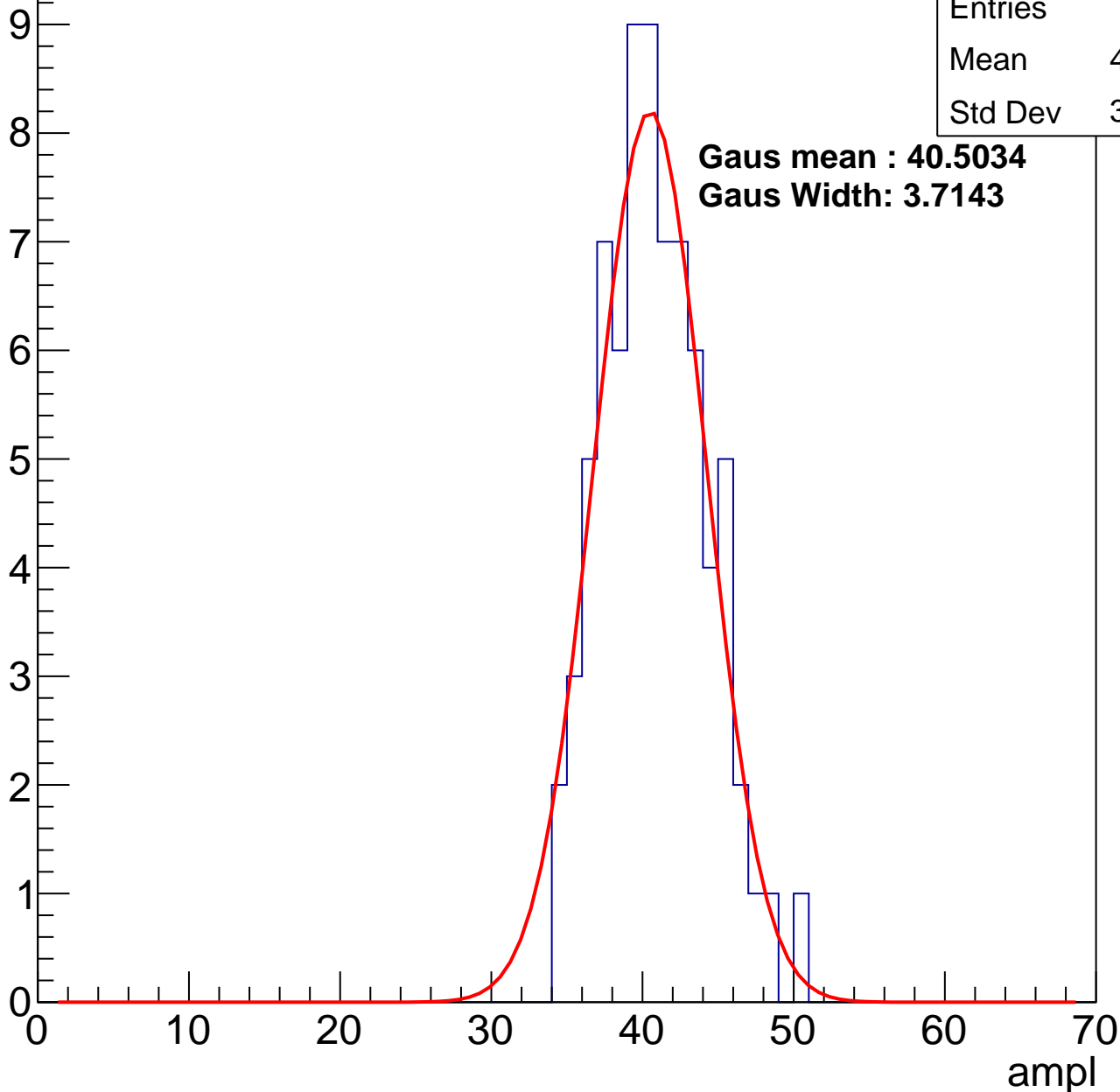
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	40.37
Std Dev	3.417

Gaus mean : 40.5034

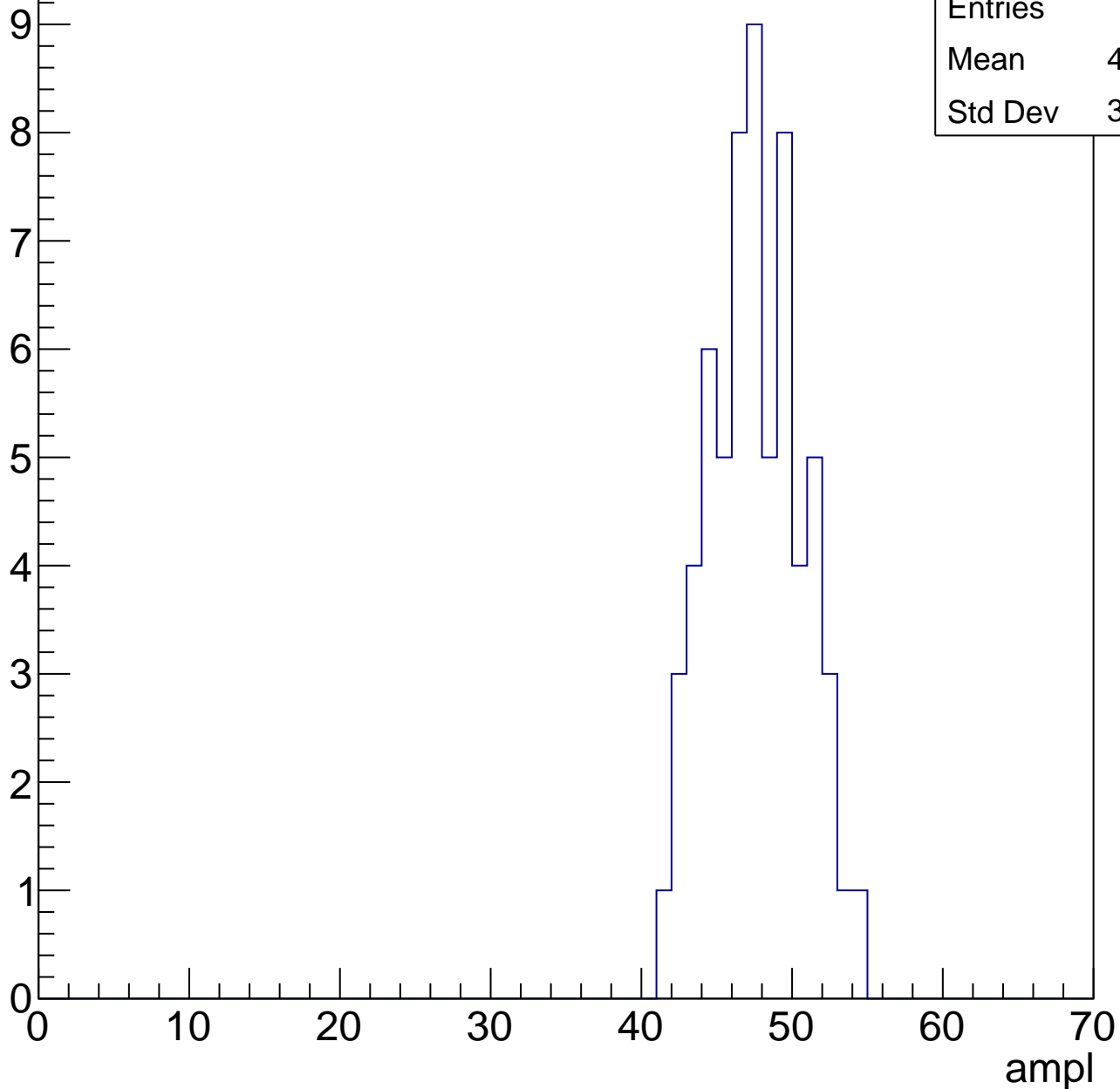
Gaus Width: 3.7143



B1L002S, U7-ch113, adc3

calib_packv5_042523_0143.root, FC#10, port B3

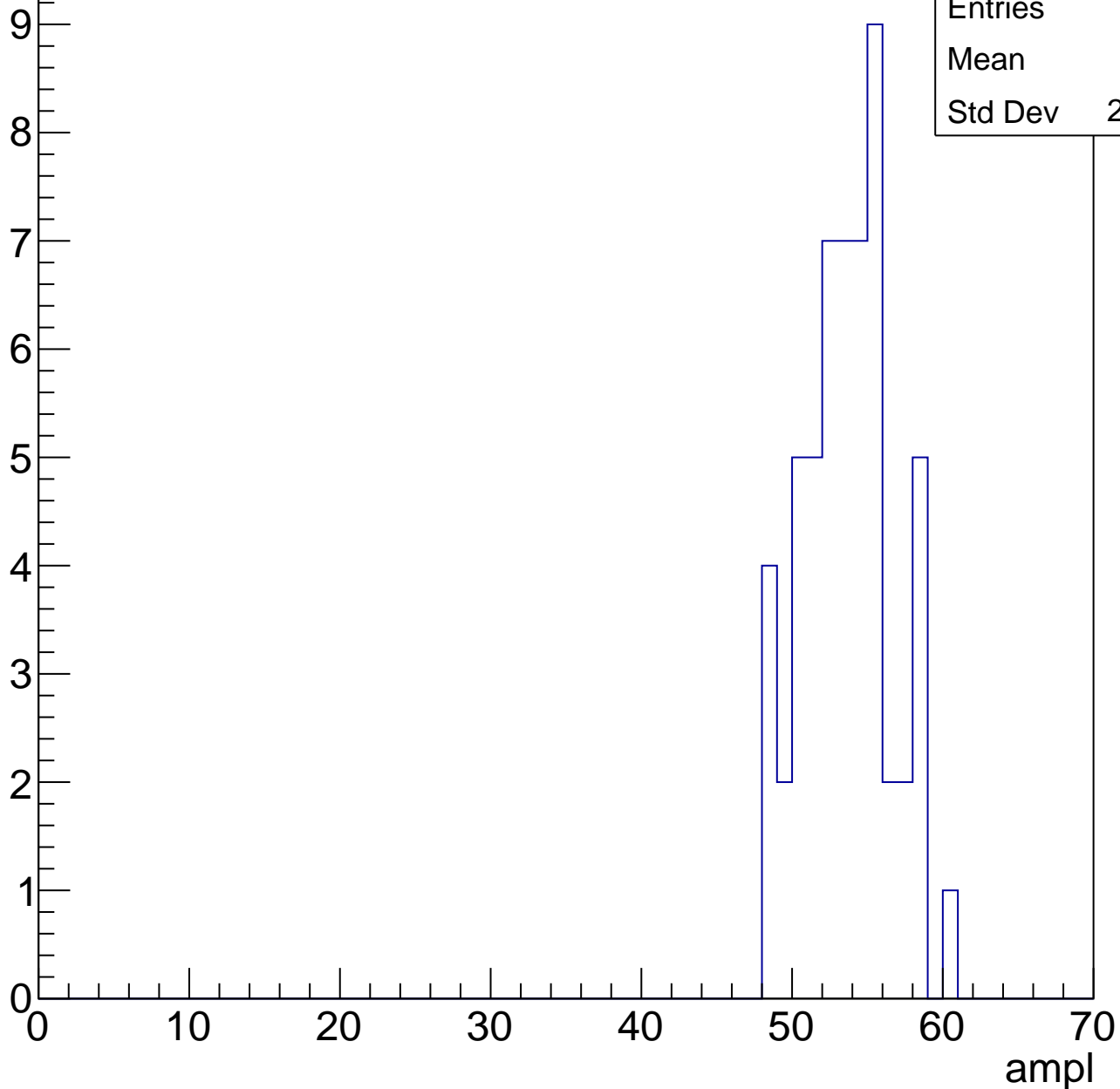
Entry



B1L002S, U7-ch113, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

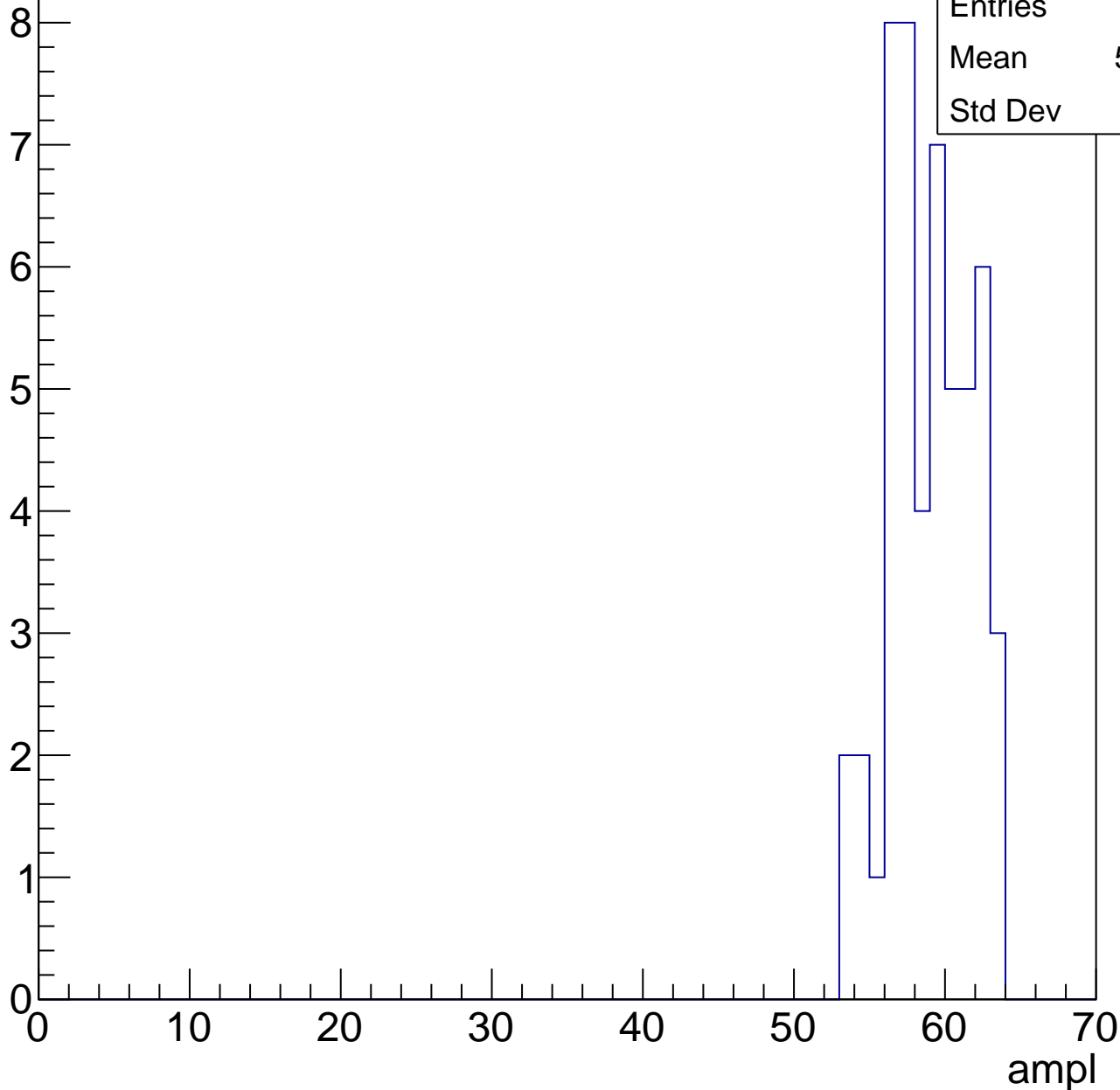


Entries	56
Mean	53.2
Std Dev	2.893

B1L002S, U7-ch113, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry



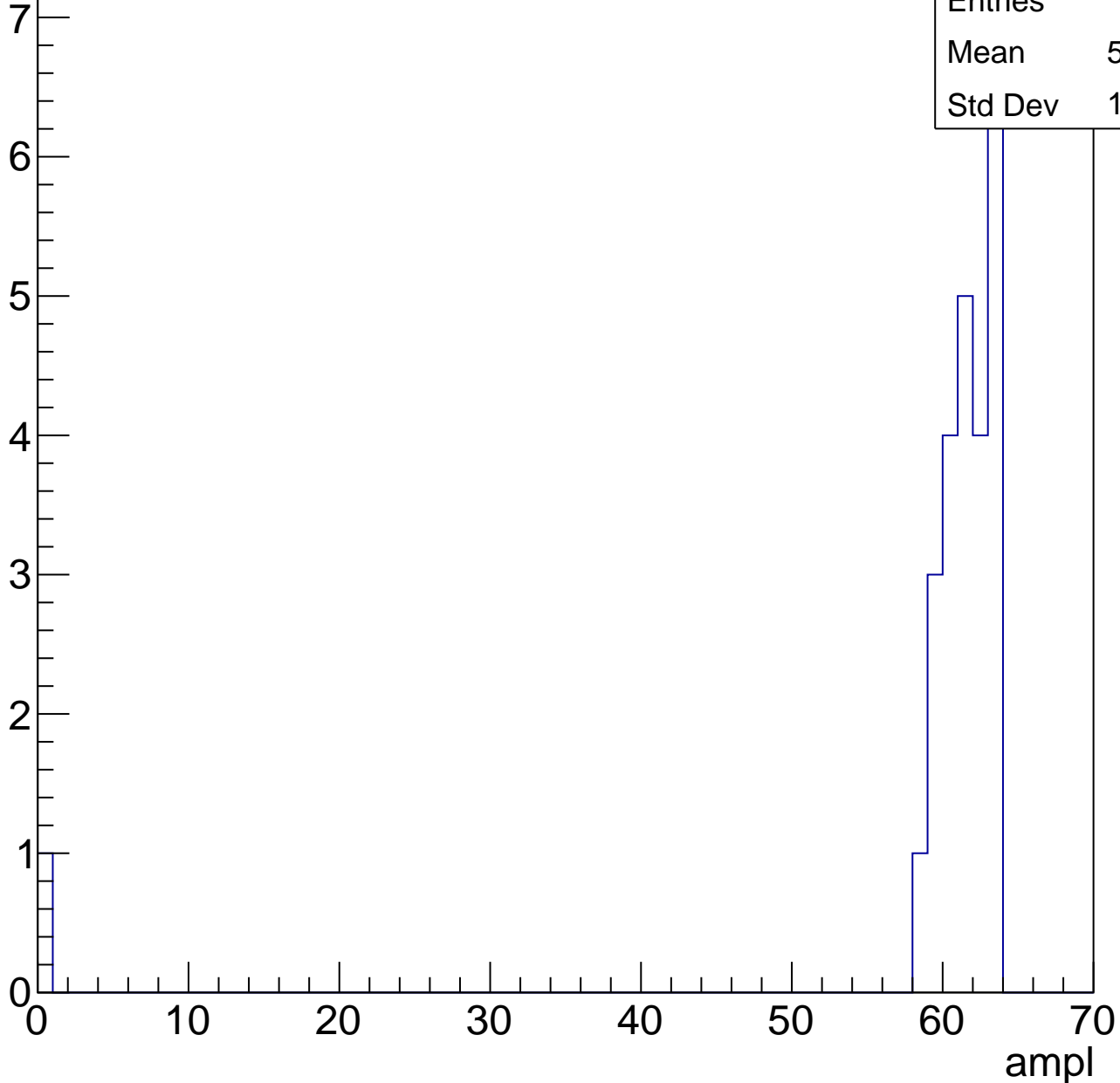
Entries	51
Mean	58.51
Std Dev	2.66

B1L002S, U7-ch113, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.76
Std Dev	12.09



B1L002S, U7-ch113, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch114, adc0

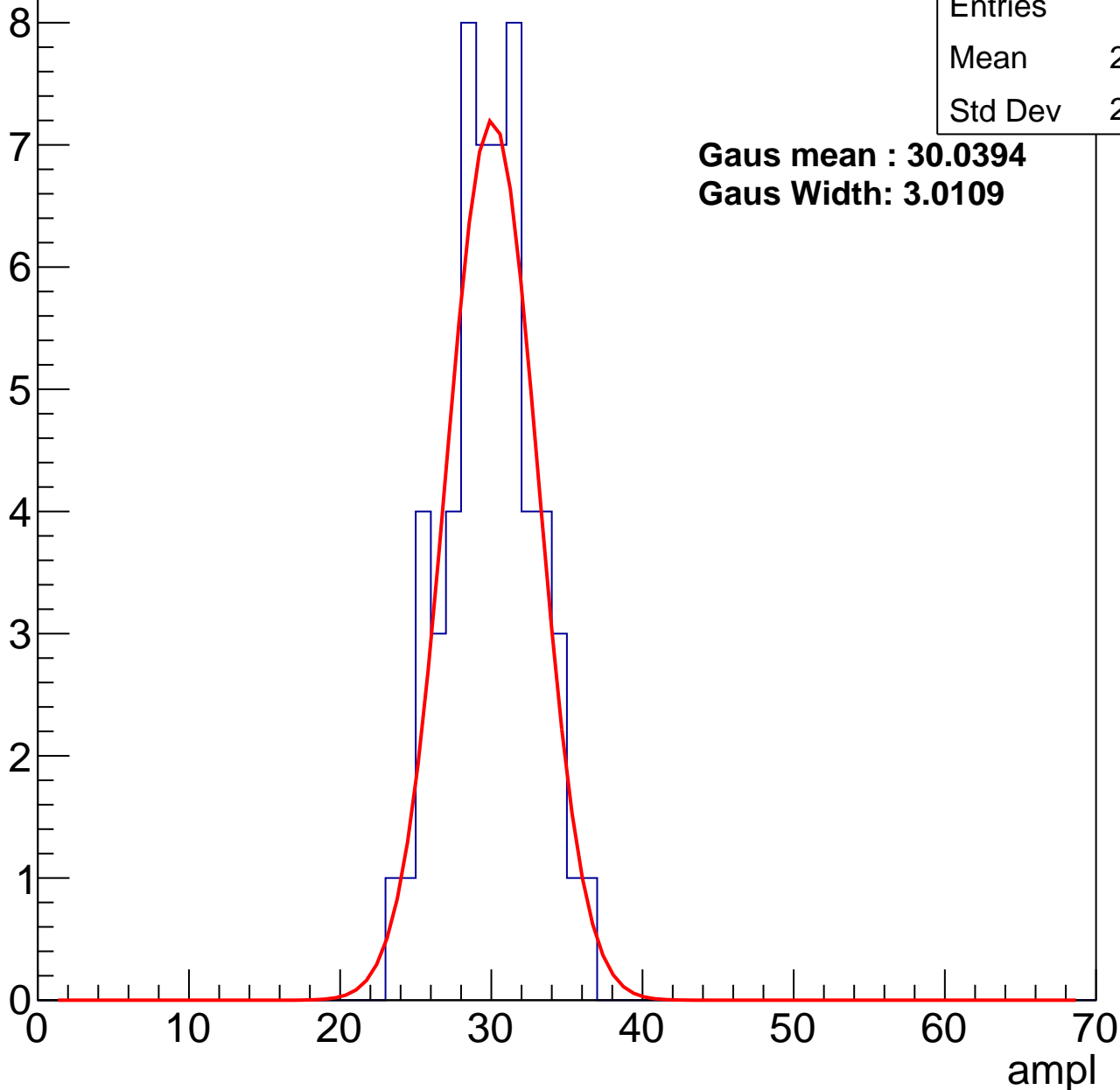
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	29.48
Std Dev	2.872

Gaus mean : 30.0394

Gaus Width: 3.0109



B1L002S, U7-ch114, adc1

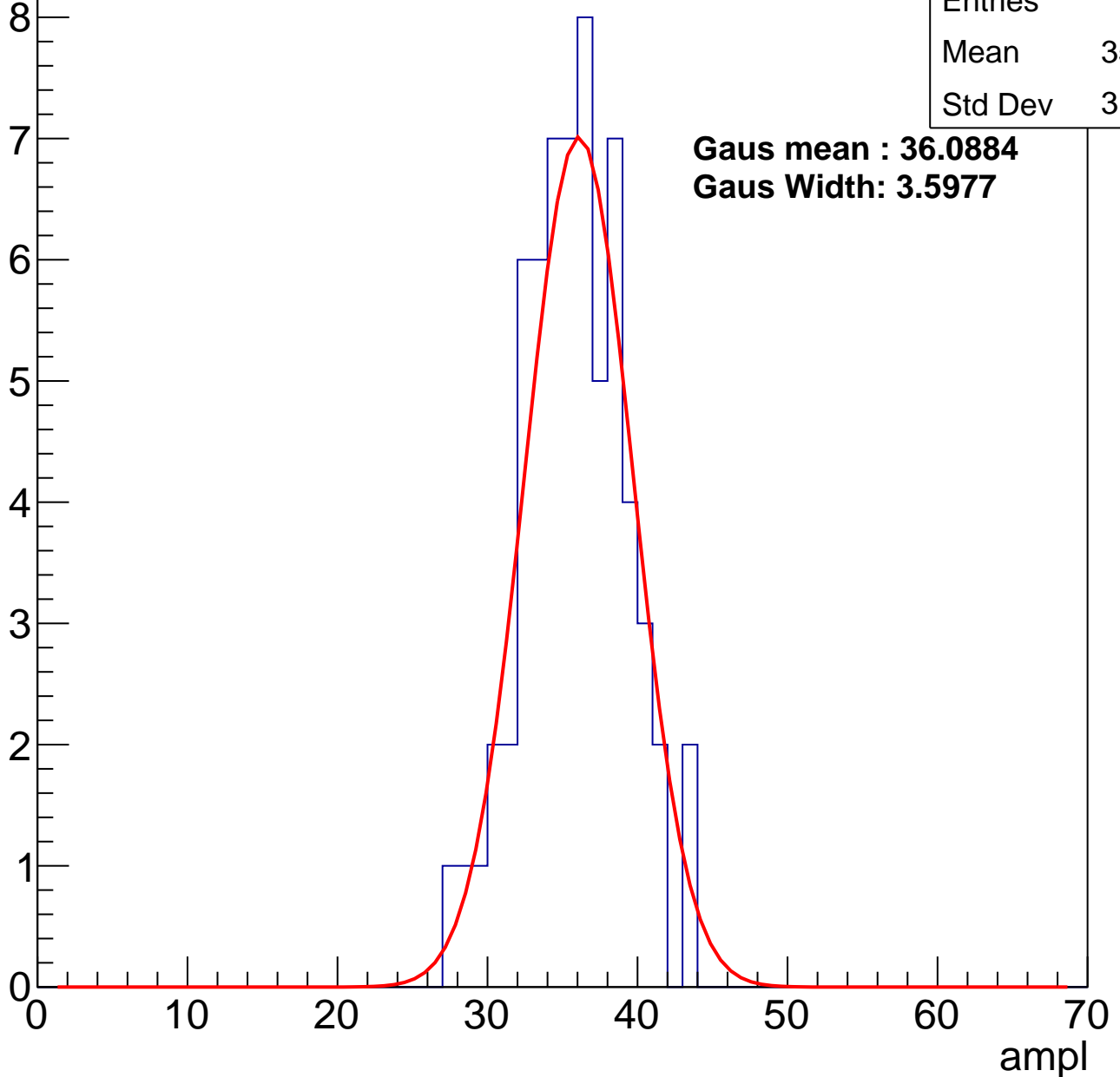
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.34
Std Dev	3.397

Gaus mean : 36.0884

Gaus Width: 3.5977



B1L002S, U7-ch114, adc2

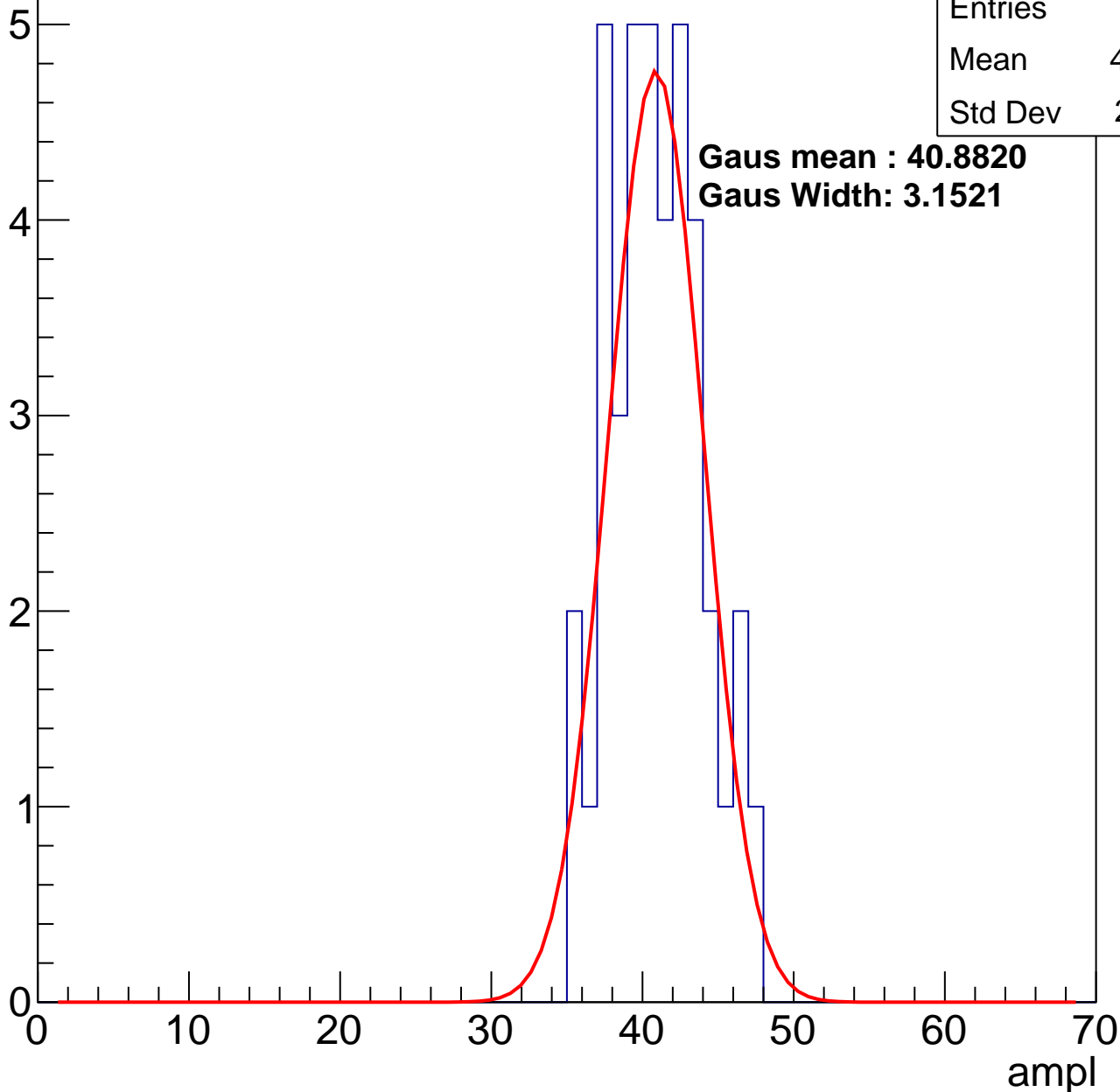
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	40
Mean	40.45
Std Dev	2.991

Gaus mean : 40.8820

Gaus Width: 3.1521

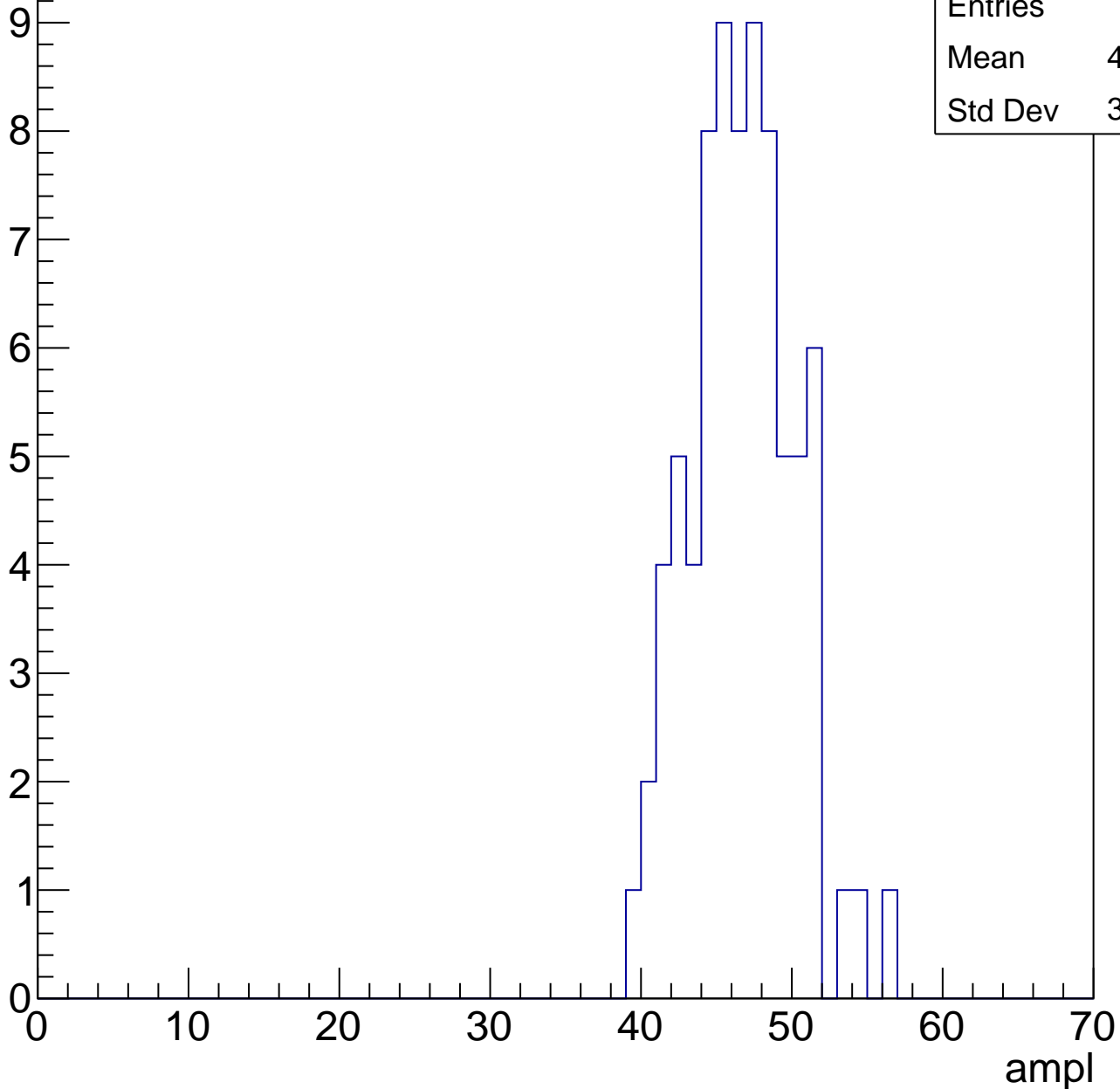


B1L002S, U7-ch114, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	46.25
Std Dev	3.423

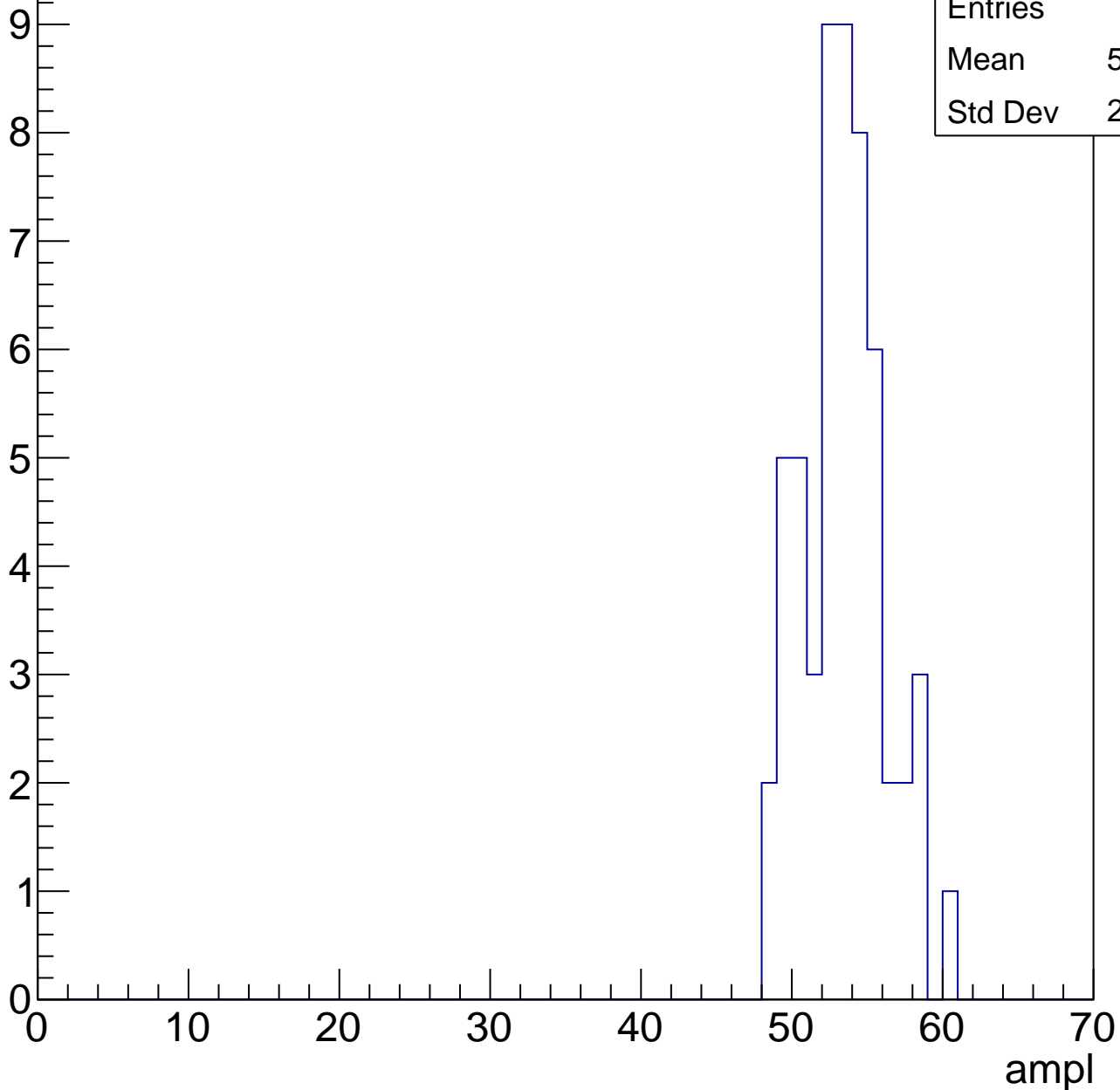


B1L002S, U7-ch114, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.93
Std Dev	2.703

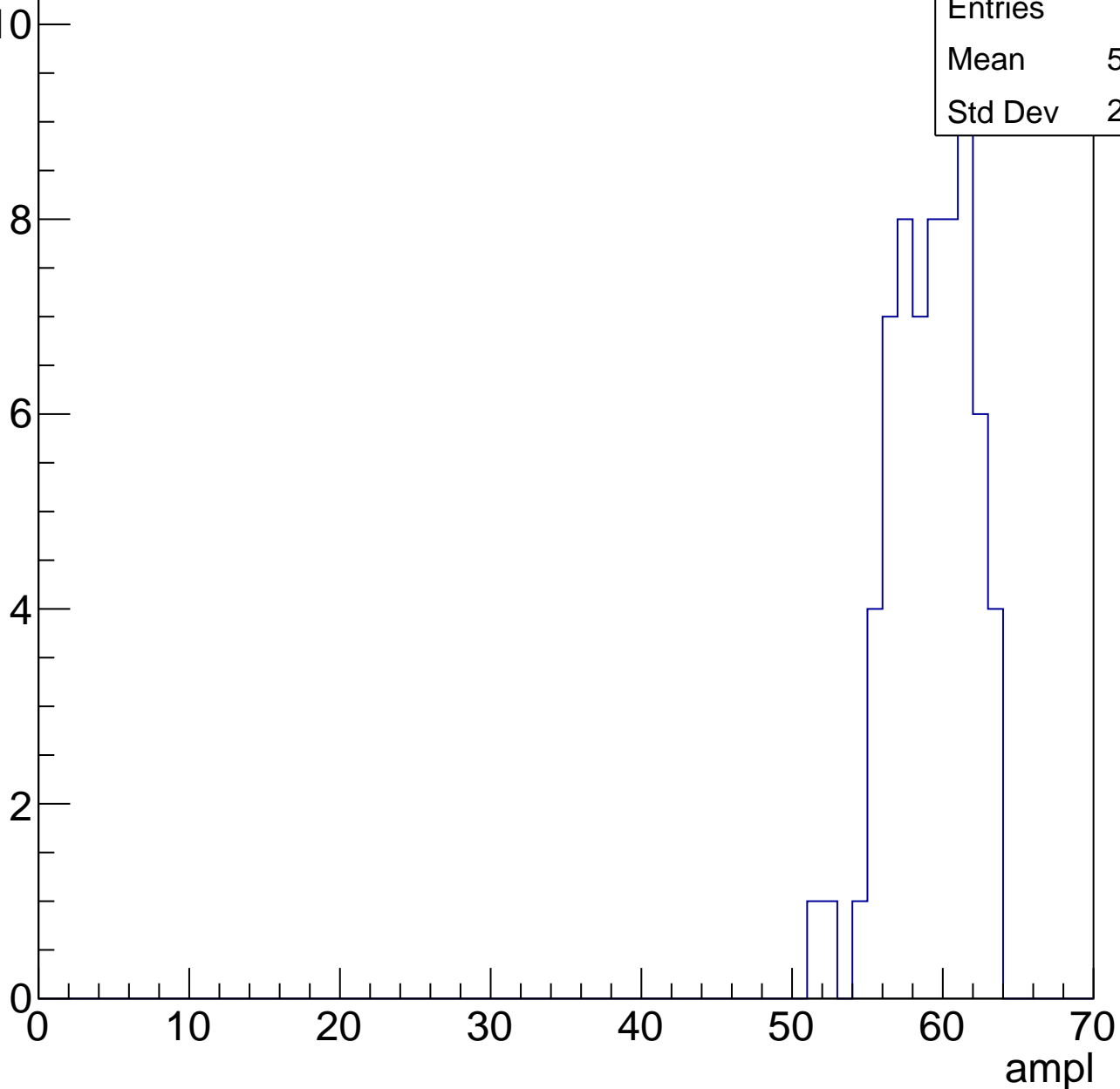


B1L002S, U7-ch114, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	58.72
Std Dev	2.675

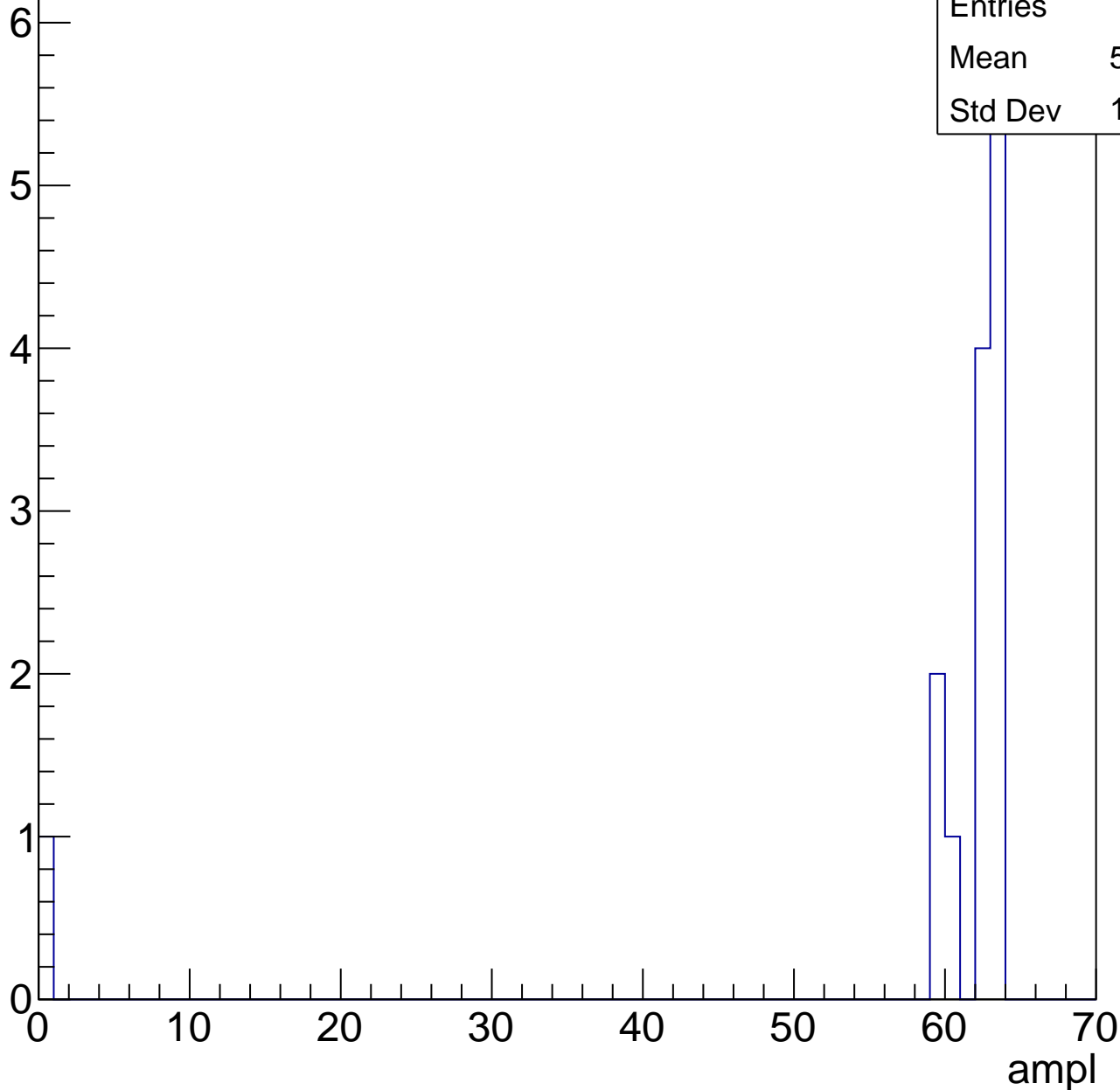


B1L002S, U7-ch114, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.43
Std Dev	15.99



B1L002S, U7-ch114, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L002S, U7-ch115, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	28.3
Std Dev	3.052

Gaus mean : 29.2164

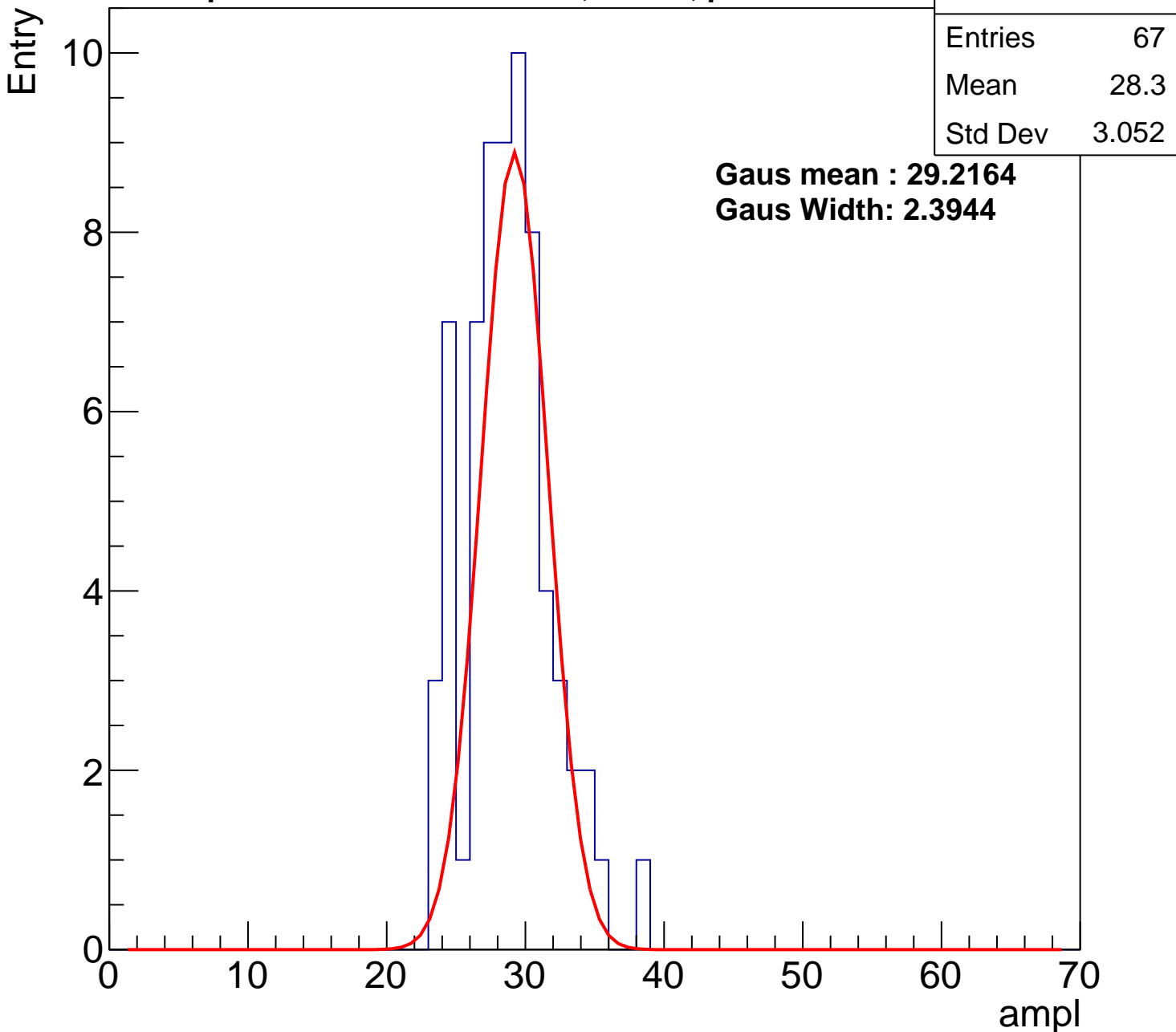
Gaus Width: 2.3944

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch115, adc1

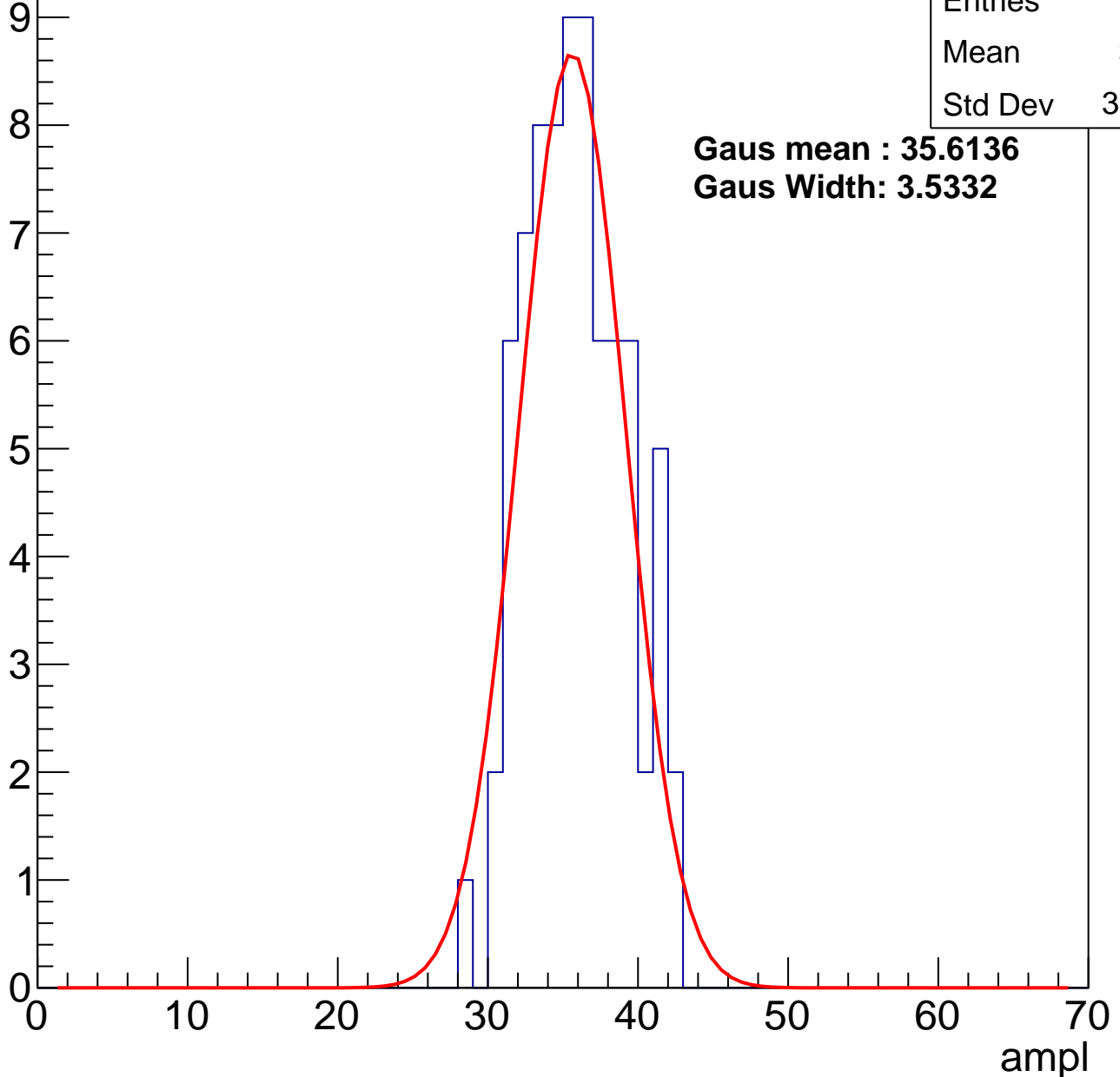
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	35.4
Std Dev	3.216

Gaus mean : 35.6136

Gaus Width: 3.5332



B1L002S, U7-ch115, adc2

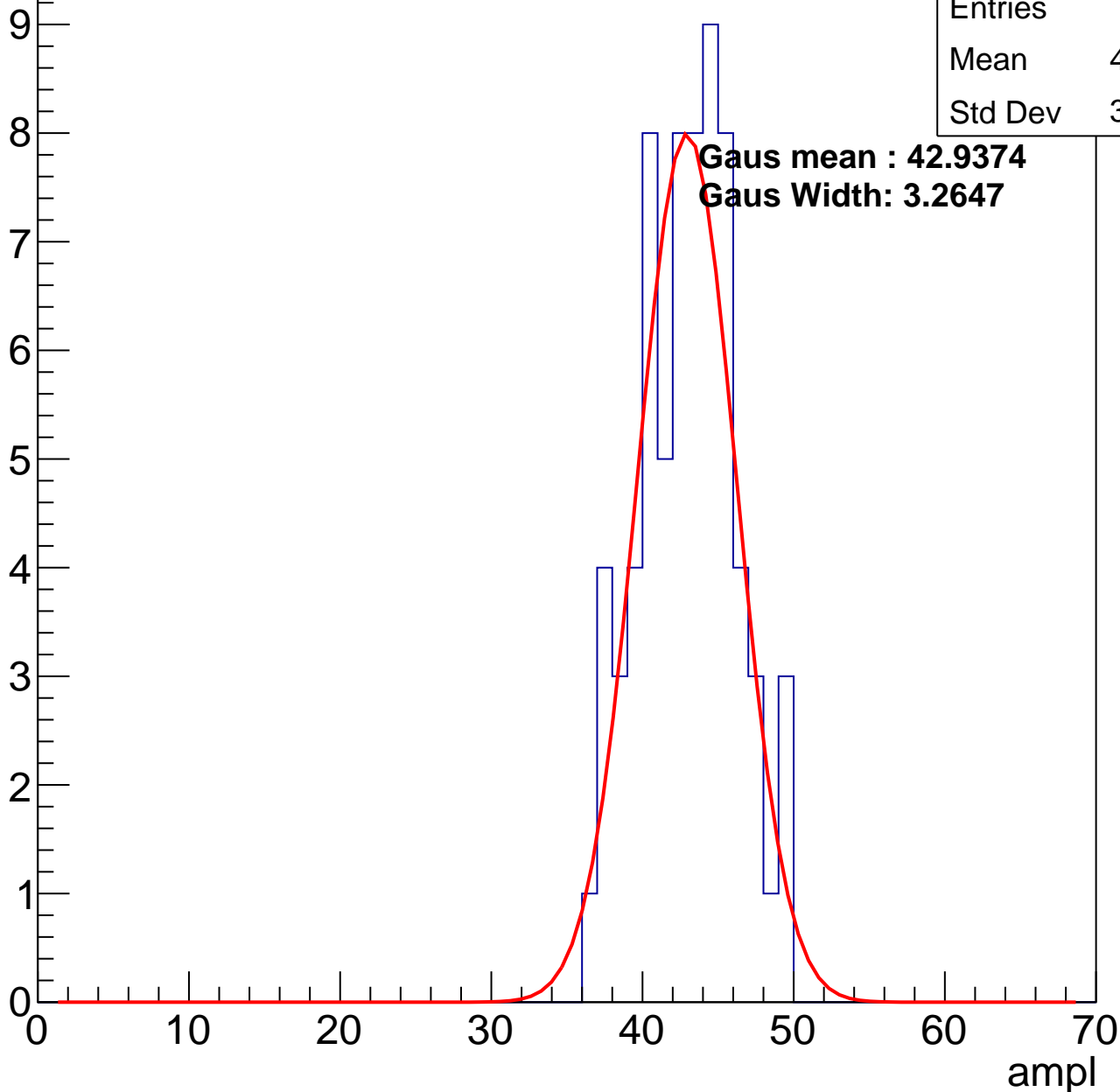
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	69
Mean	42.54
Std Dev	3.128

Gaus mean : 42.9374

Gaus Width: 3.2647

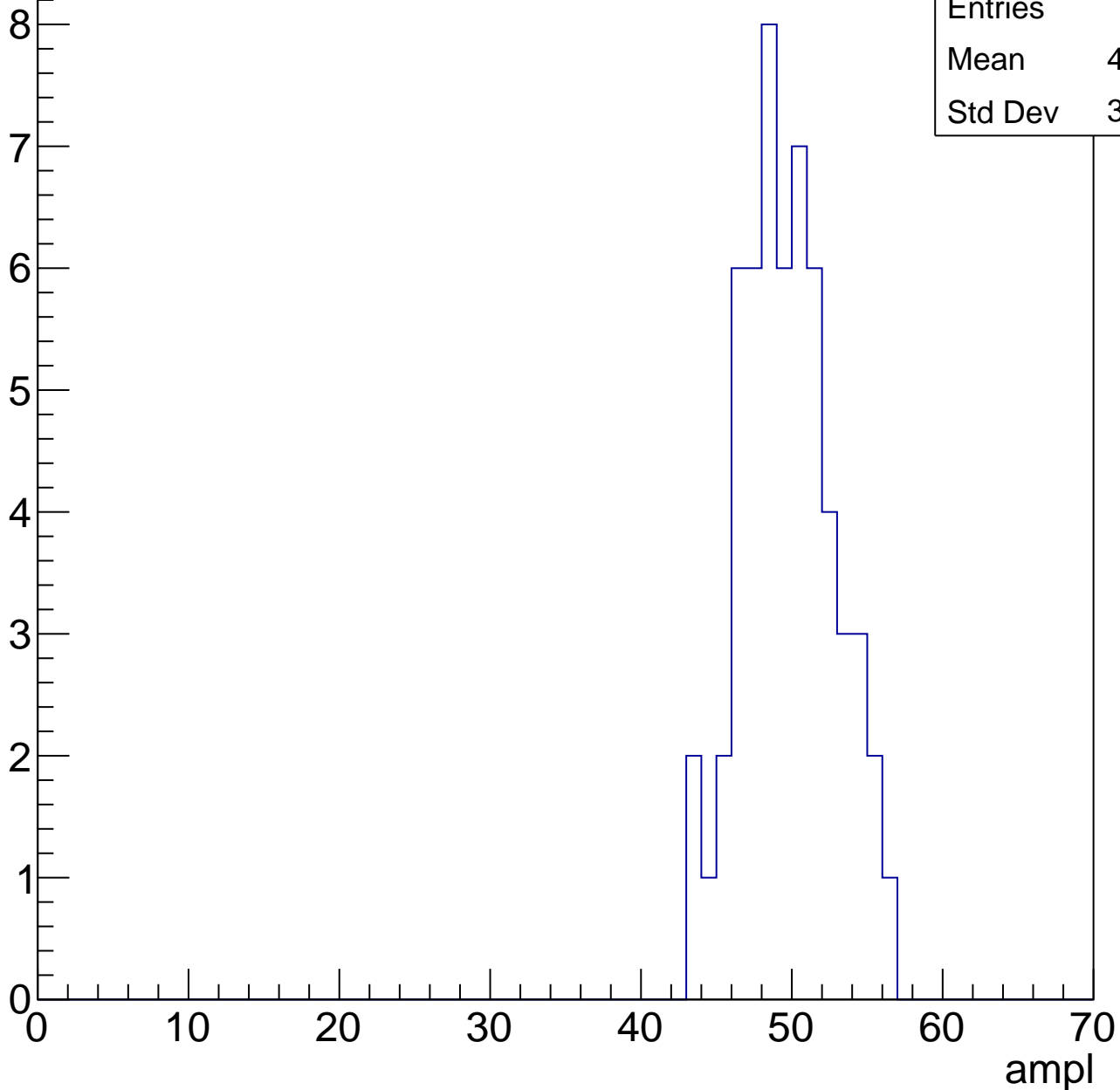


B1L002S, U7-ch115, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	49.25
Std Dev	3.028

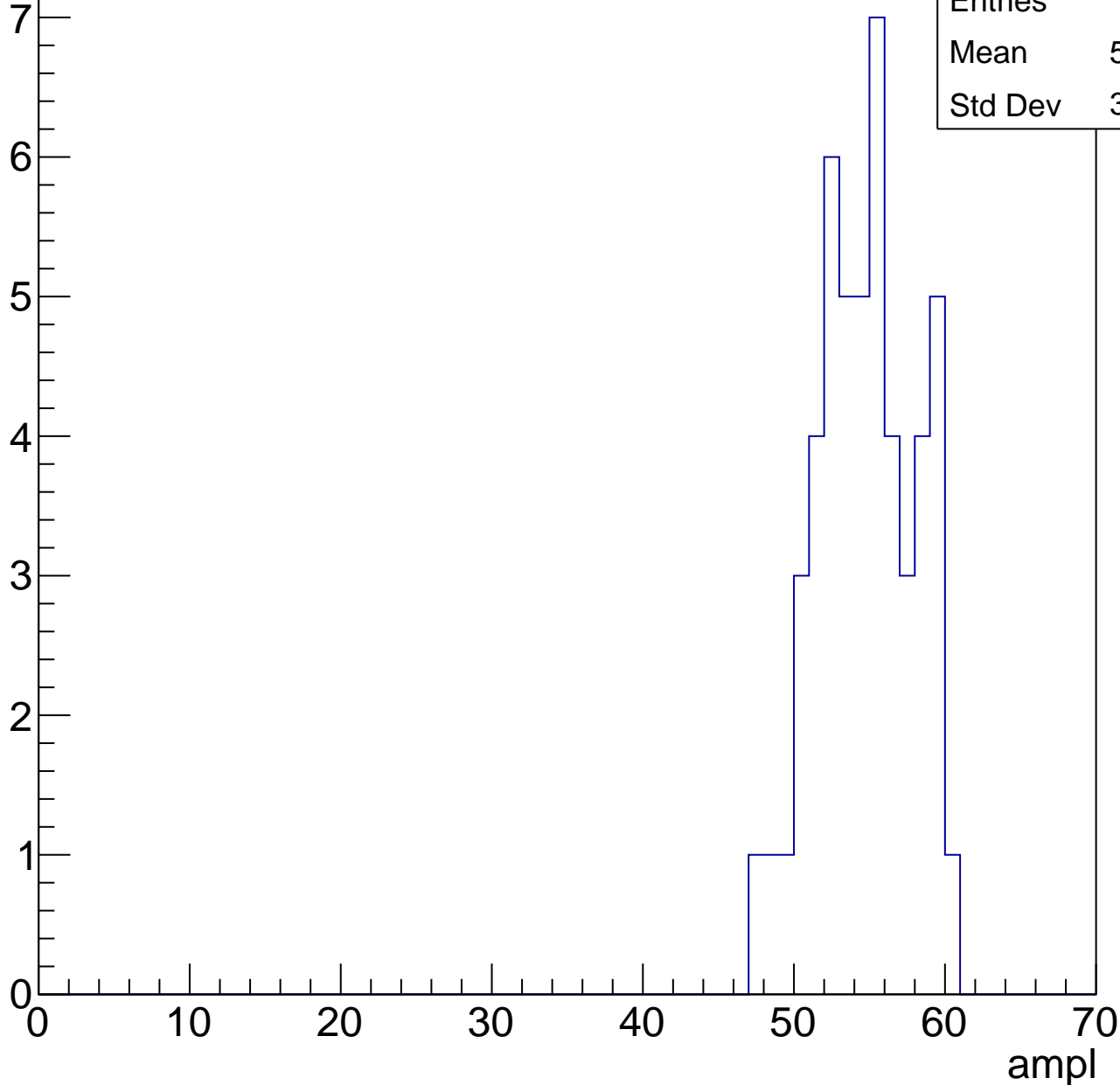


B1L002S, U7-ch115, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	54.24
Std Dev	3.147



B1L002S, U7-ch115, adc5

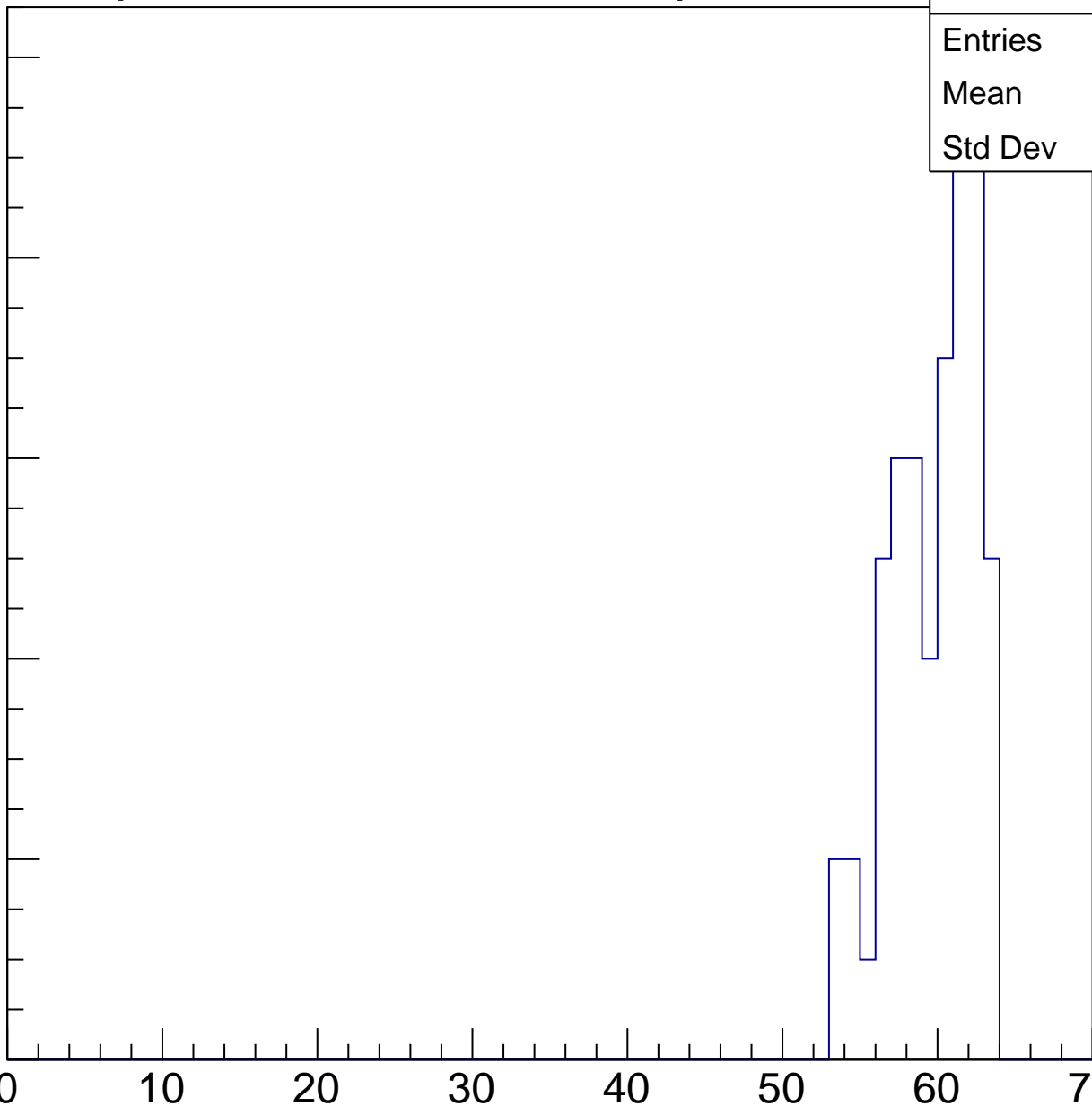
calib_packv5_042523_0143.root, FC#10, port B3

Entry

10
8
6
4
2
0

Entries	57
Mean	59.28
Std Dev	2.719

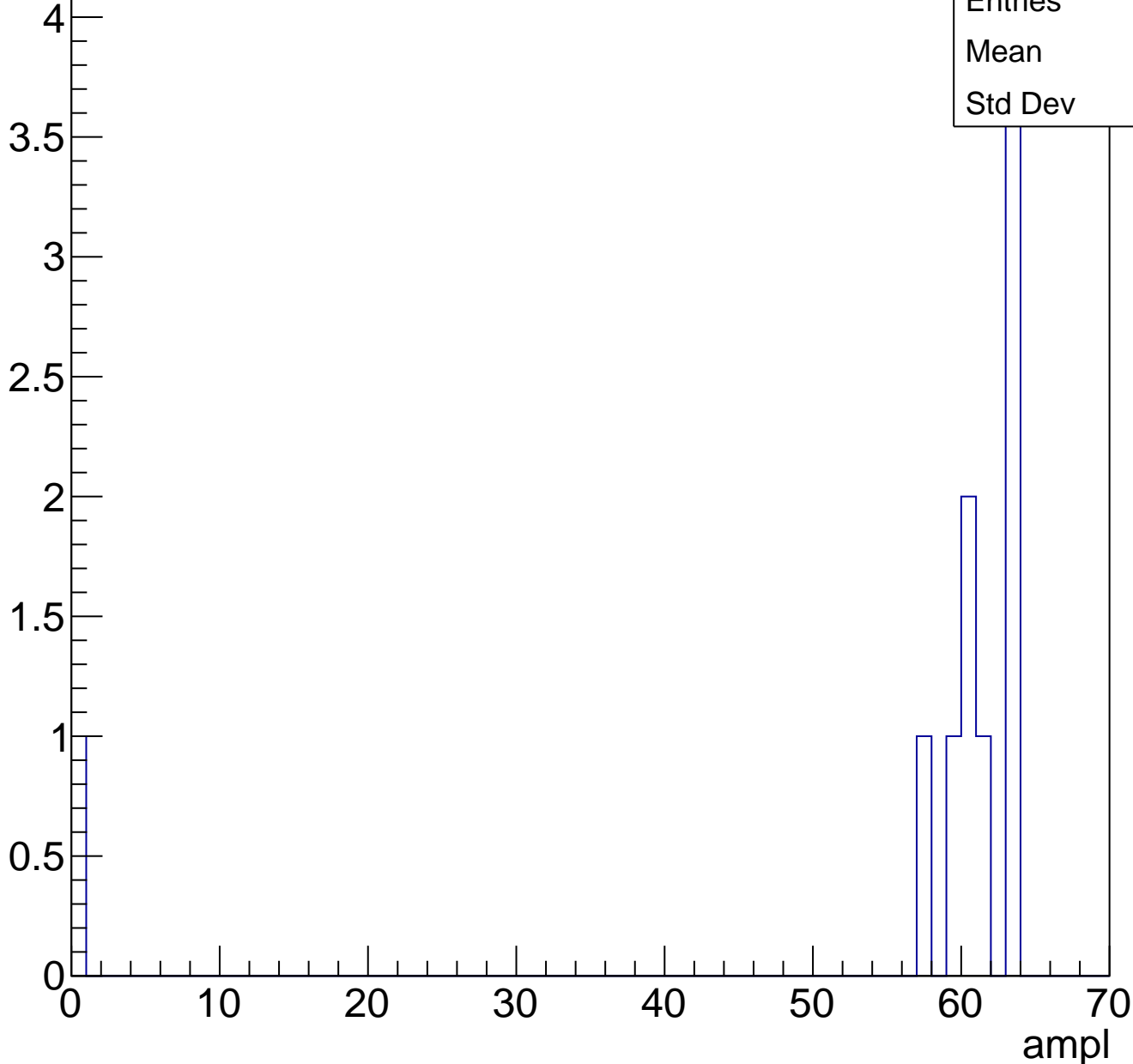
ampl



B1L002S, U7-ch115, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch115, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L002S, U7-ch116, adc0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	62
Mean	29.15
Std Dev	3.089

Gaus mean : 29.2929

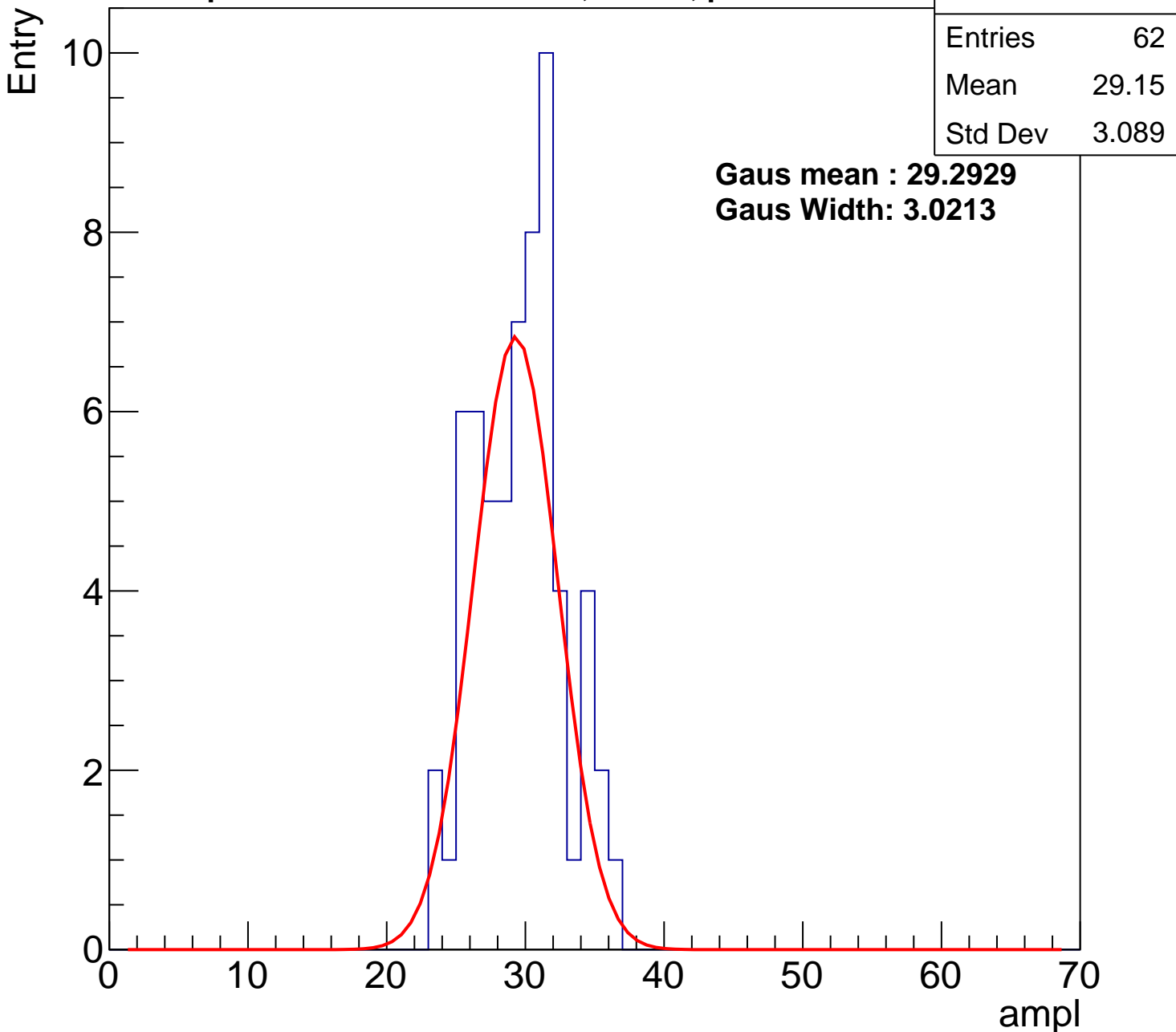
Gaus Width: 3.0213

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch116, adc1

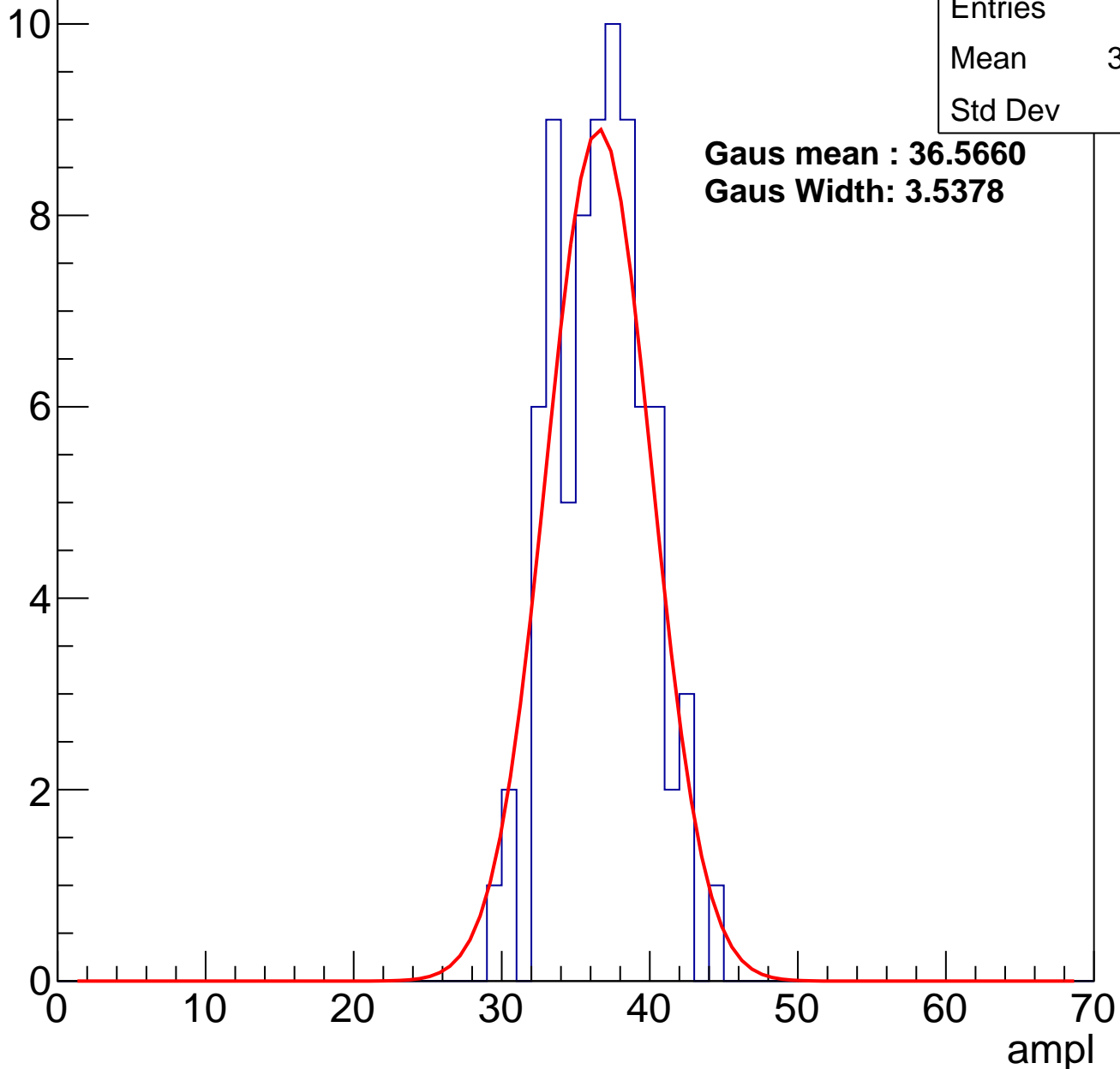
calib_packv5_042523_0143.root, FC#10, port B3

Entries	77
Mean	36.23
Std Dev	3.1

Gaus mean : 36.5660

Gaus Width: 3.5378

Entry



B1L002S, U7-ch116, adc2

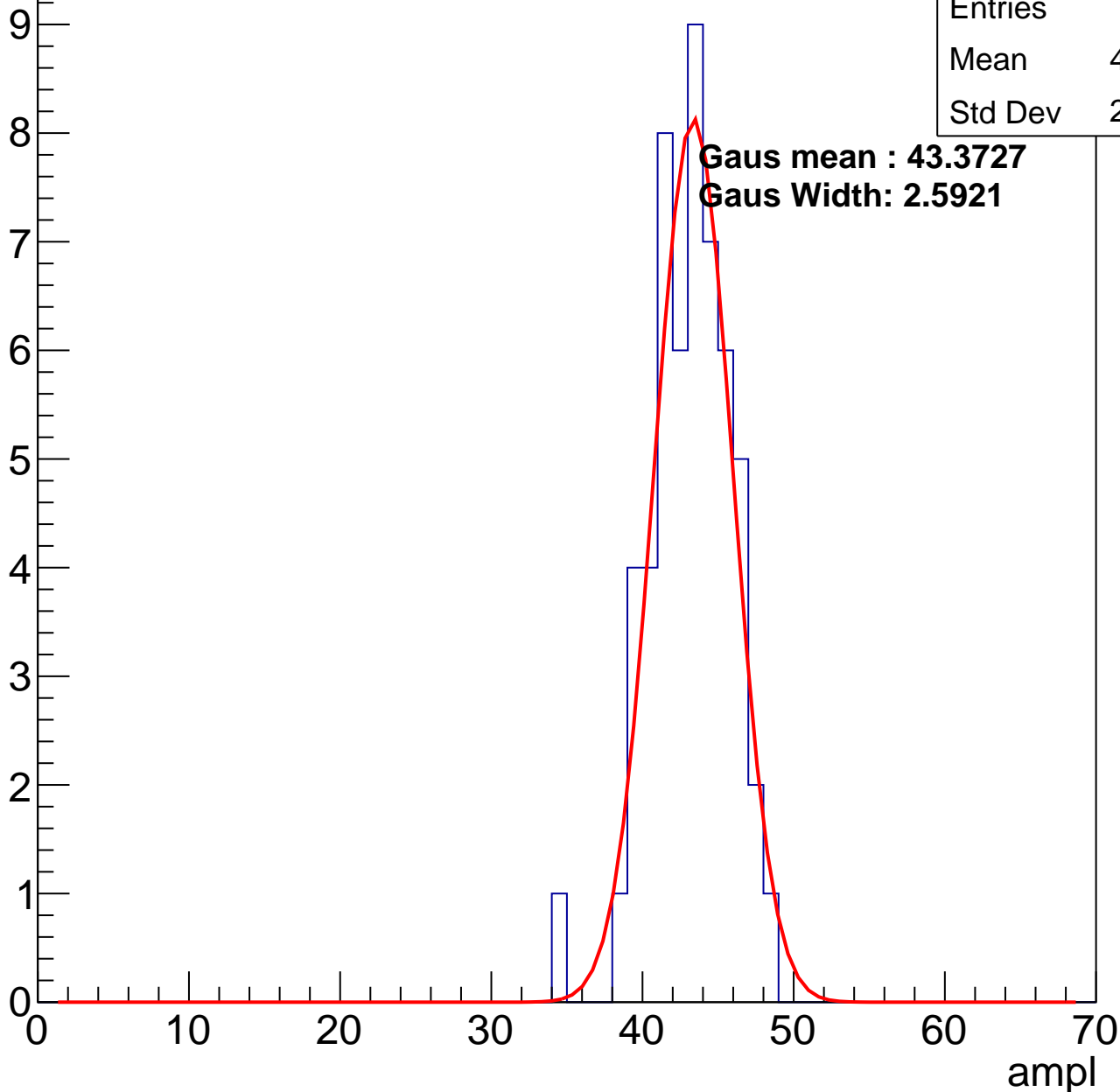
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	42.69
Std Dev	2.623

Gaus mean : 43.3727

Gaus Width: 2.5921

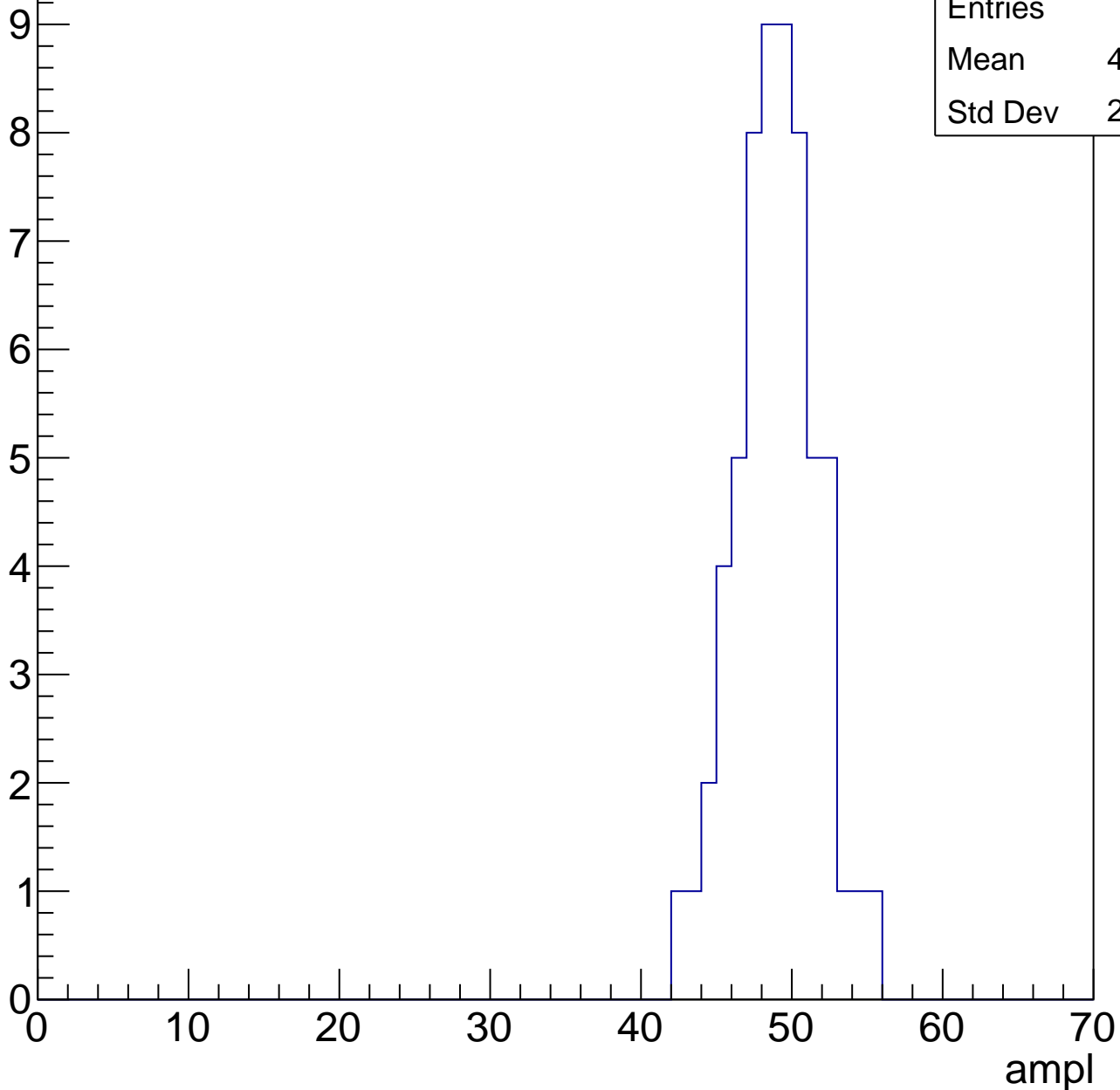


B1L002S, U7-ch116, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.48
Std Dev	2.643

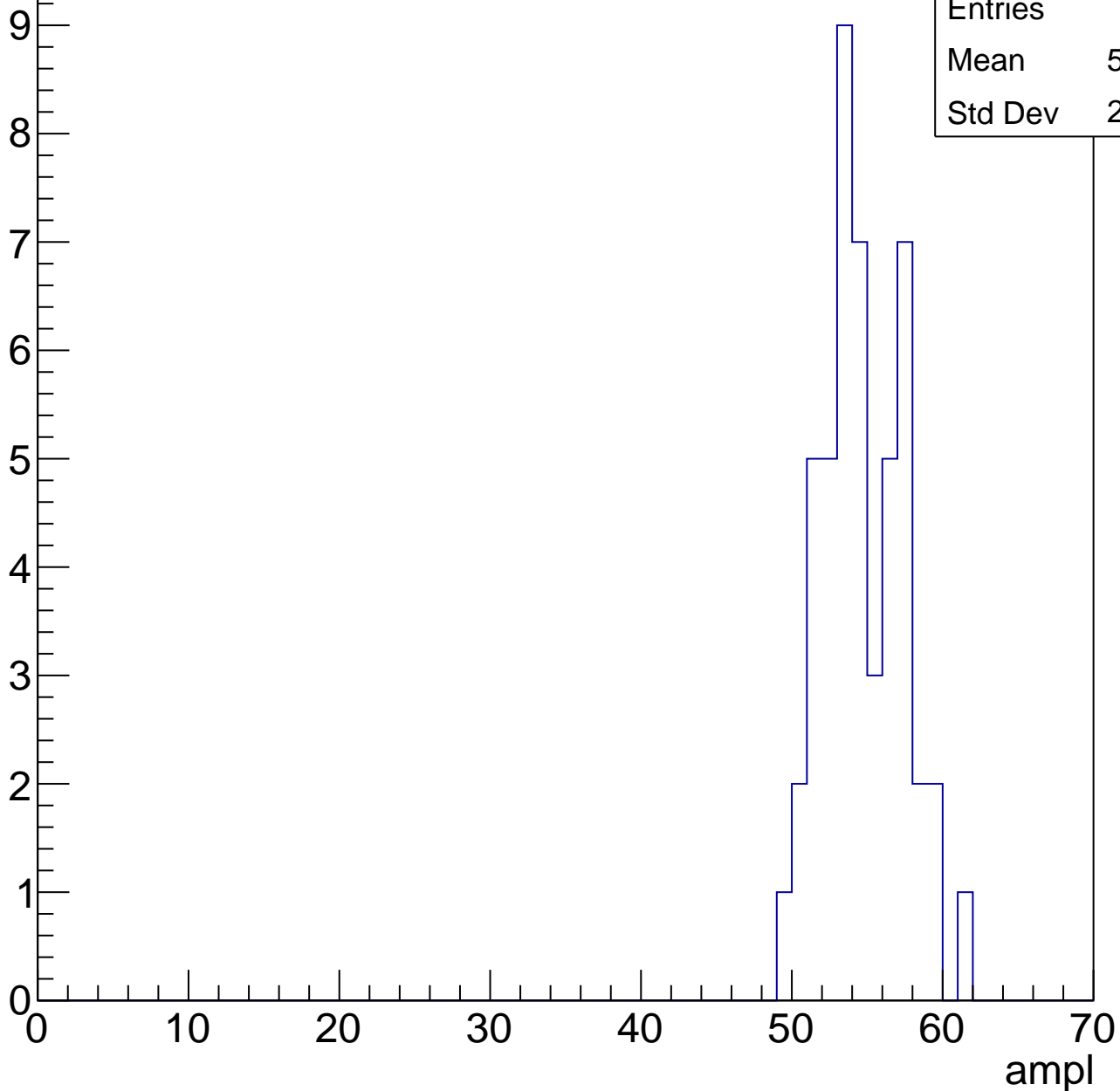


B1L002S, U7-ch116, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	54.24
Std Dev	2.654

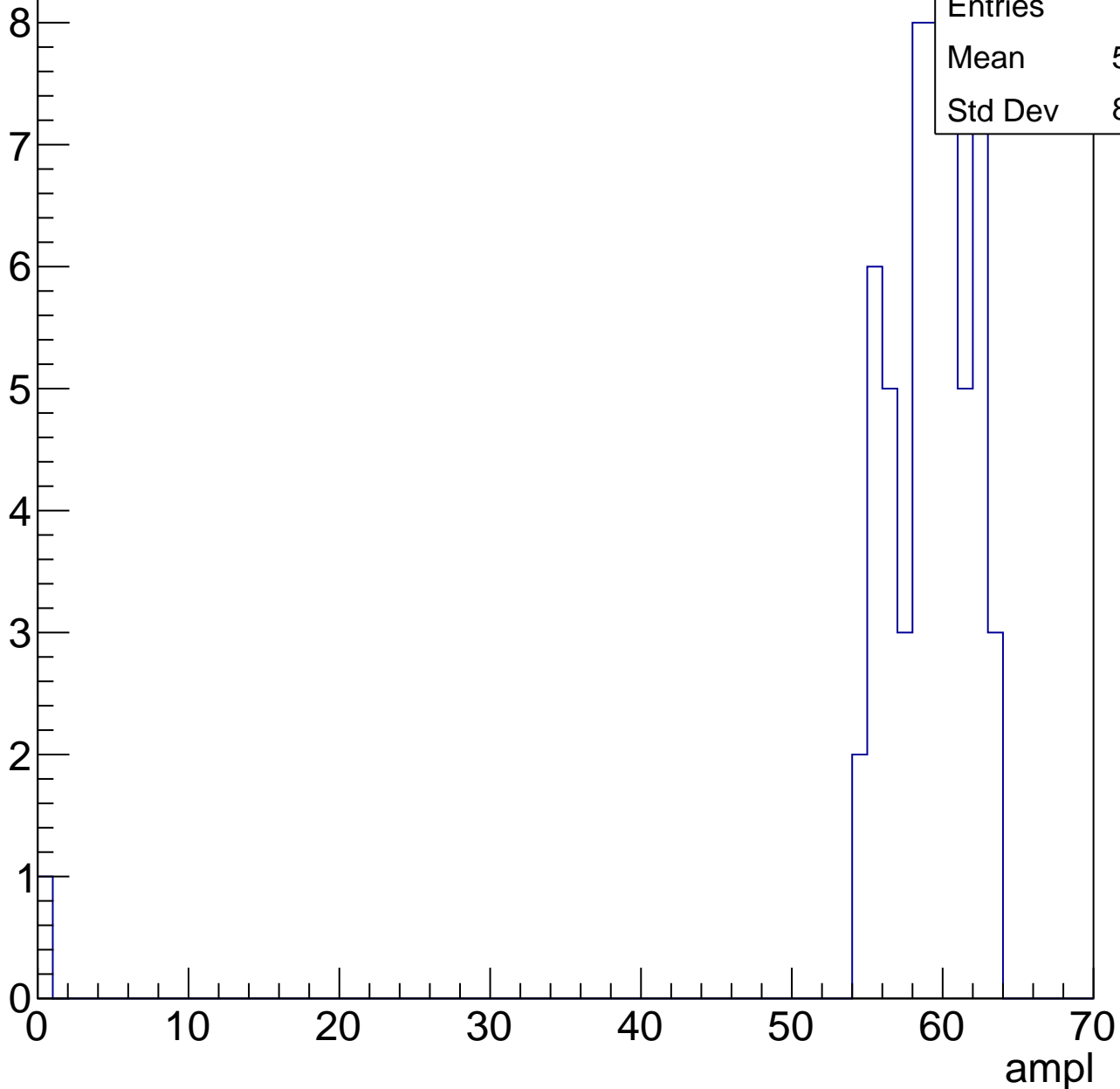


B1L002S, U7-ch116, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.81
Std Dev	8.121

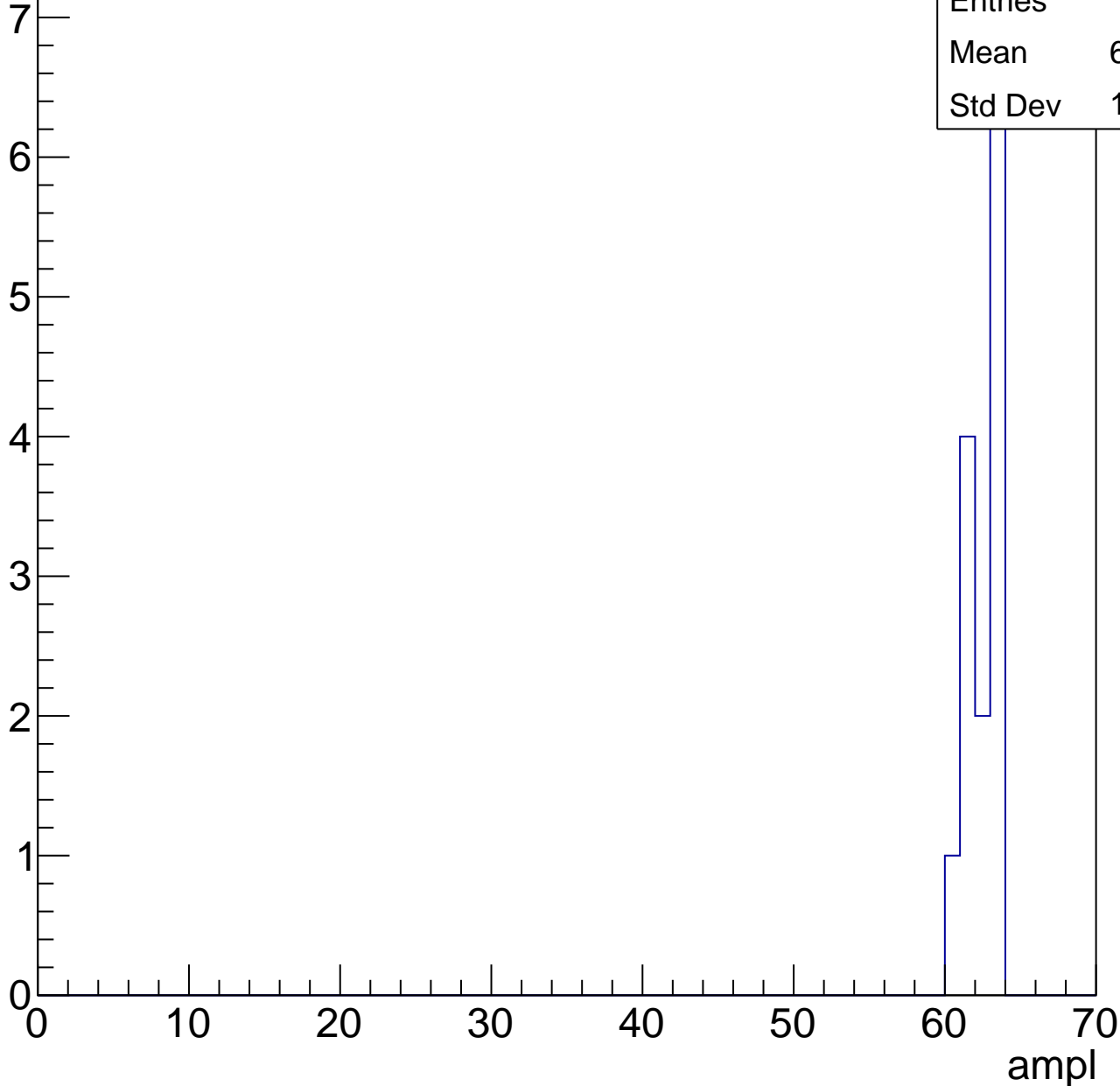


B1L002S, U7-ch116, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	14
Mean	62.07
Std Dev	1.033



B1L002S, U7-ch116, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch117, adc0

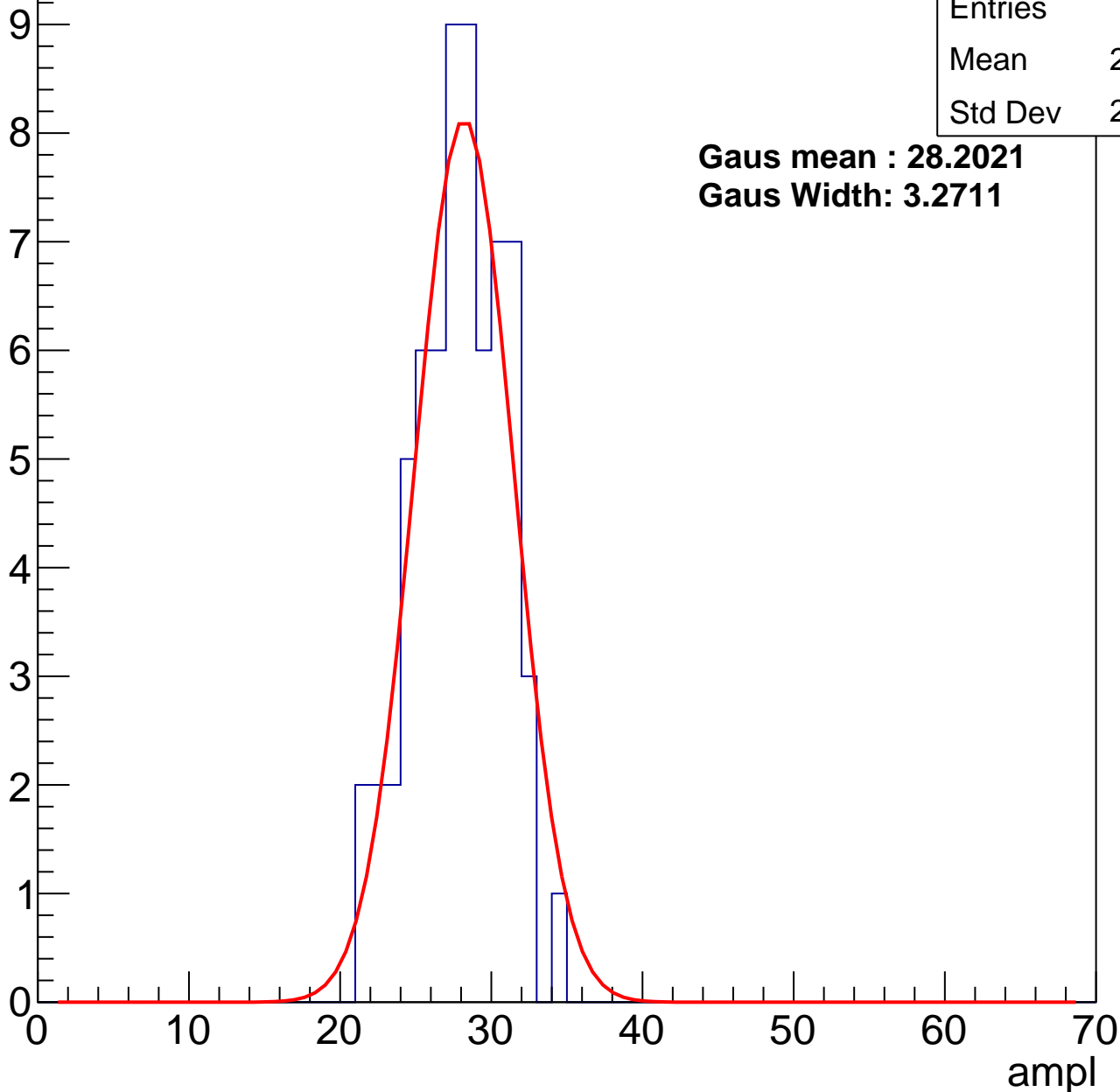
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.45
Std Dev	2.904

Gaus mean : 28.2021

Gaus Width: 3.2711



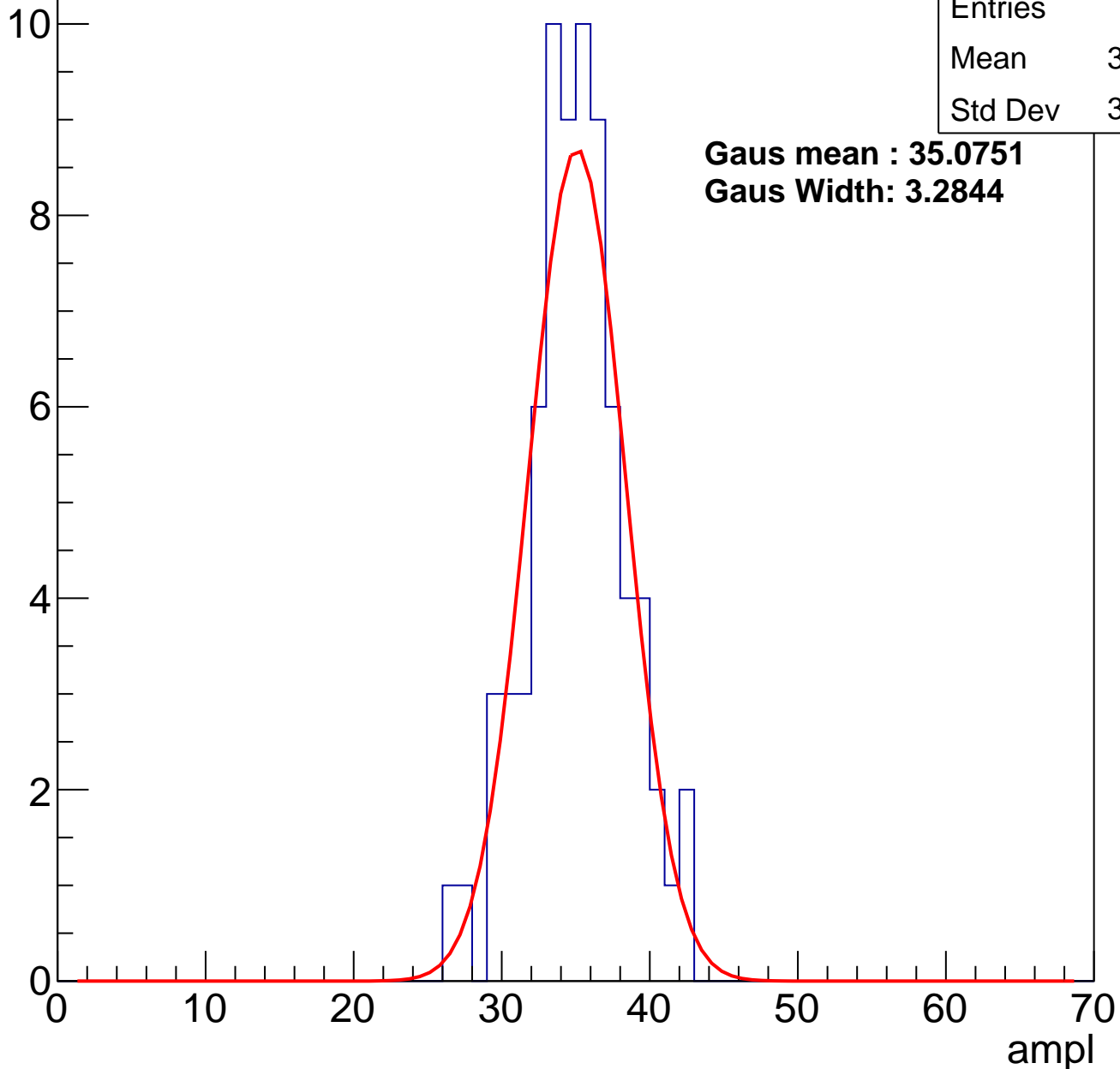
B1L002S, U7-ch117, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	74
Mean	34.59
Std Dev	3.255

Gaus mean : 35.0751
Gaus Width: 3.2844

Entry



B1L002S, U7-ch117, adc2

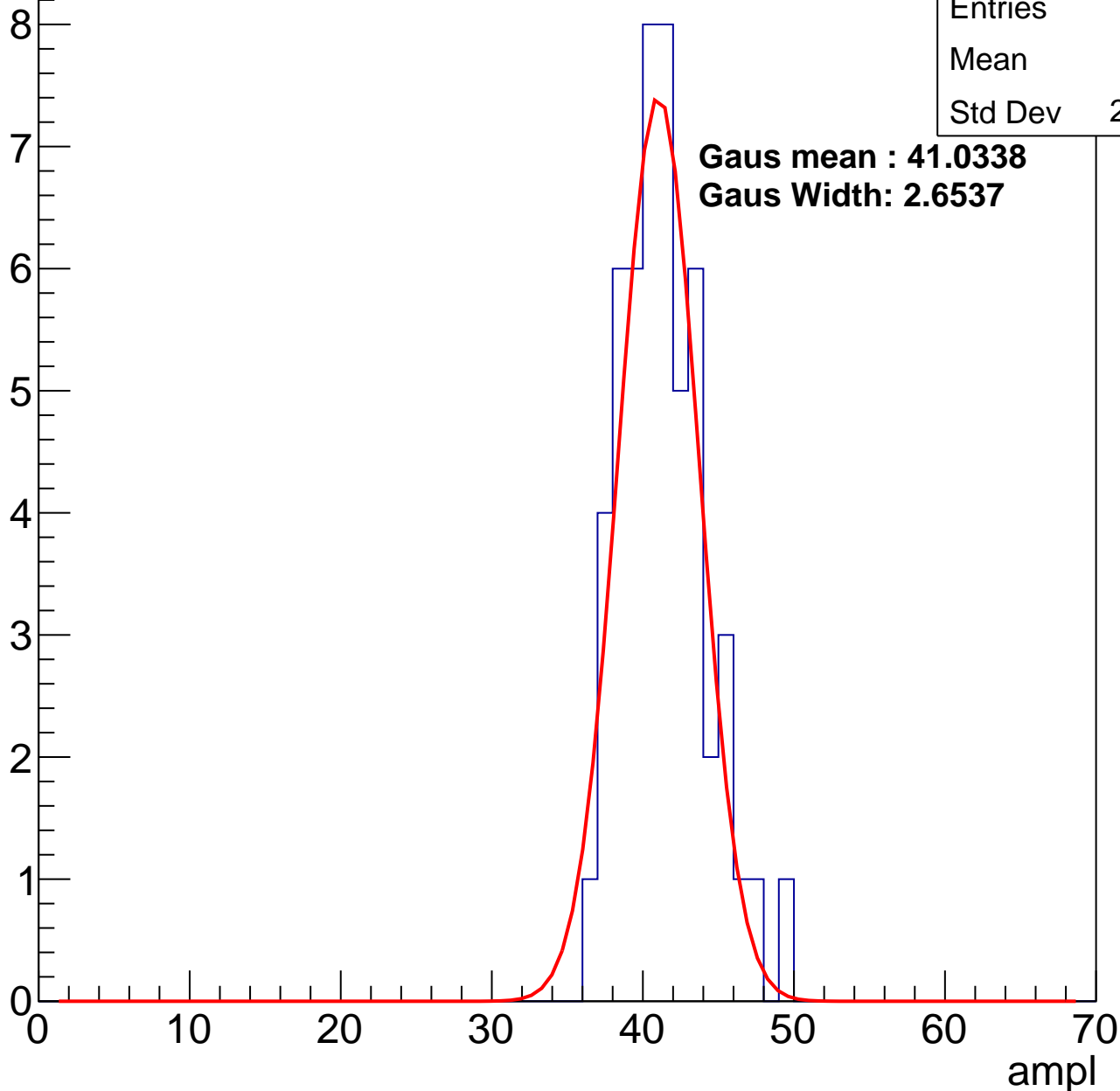
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.9
Std Dev	2.754

Gaus mean : 41.0338

Gaus Width: 2.6537

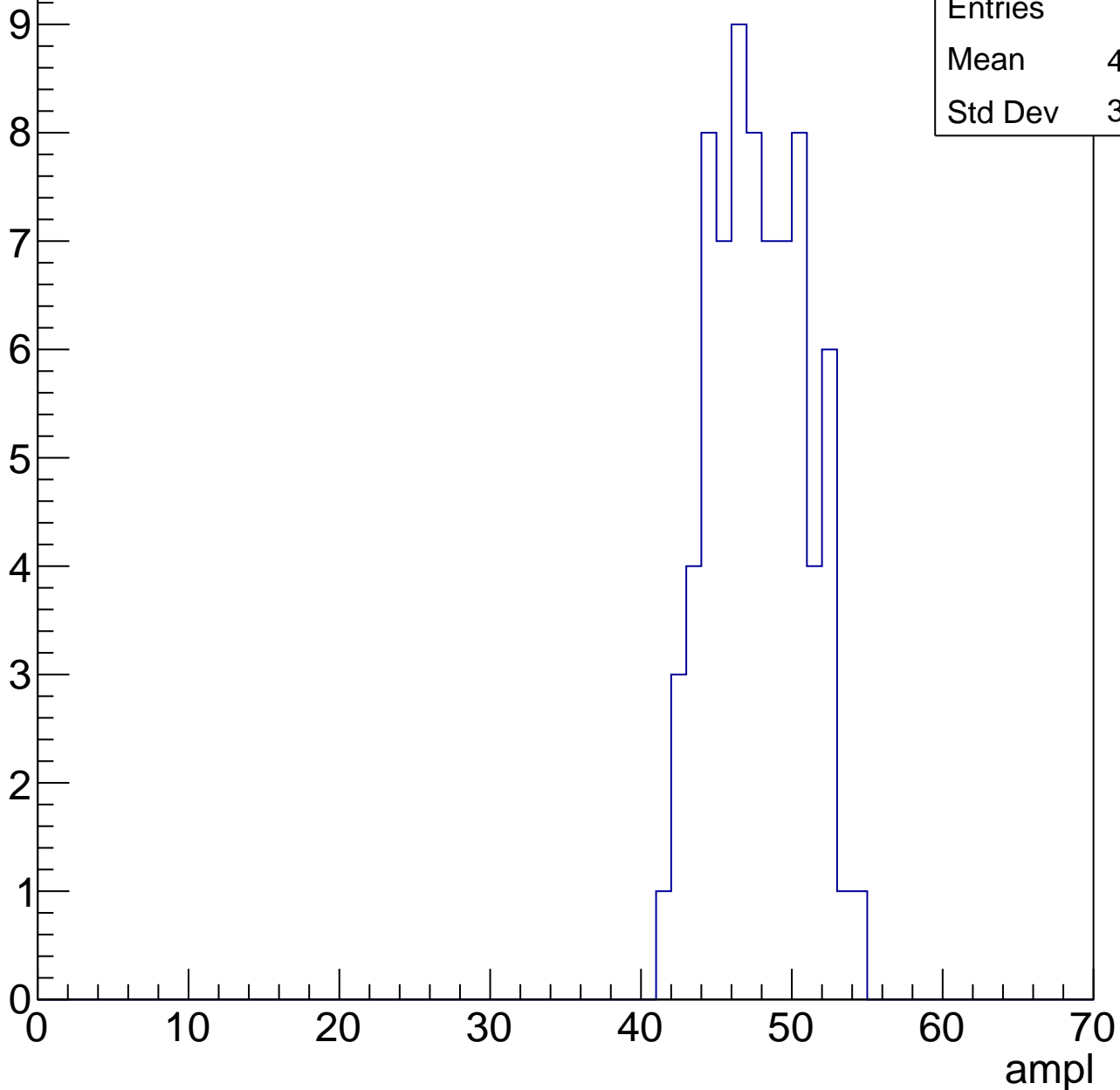


B1L002S, U7-ch117, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	47.27
Std Dev	3.042

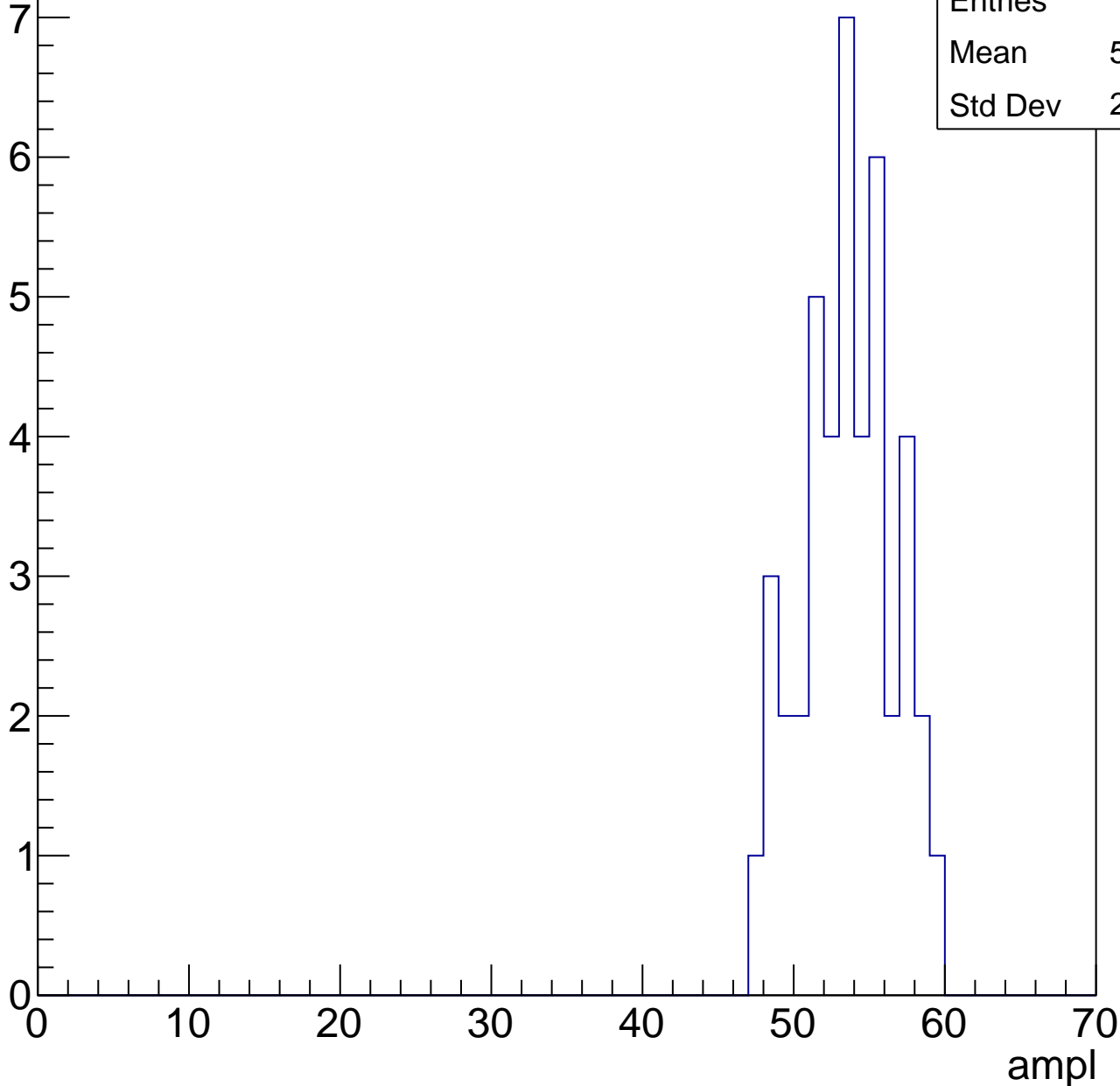


B1L002S, U7-ch117, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	43
Mean	53.12
Std Dev	2.974

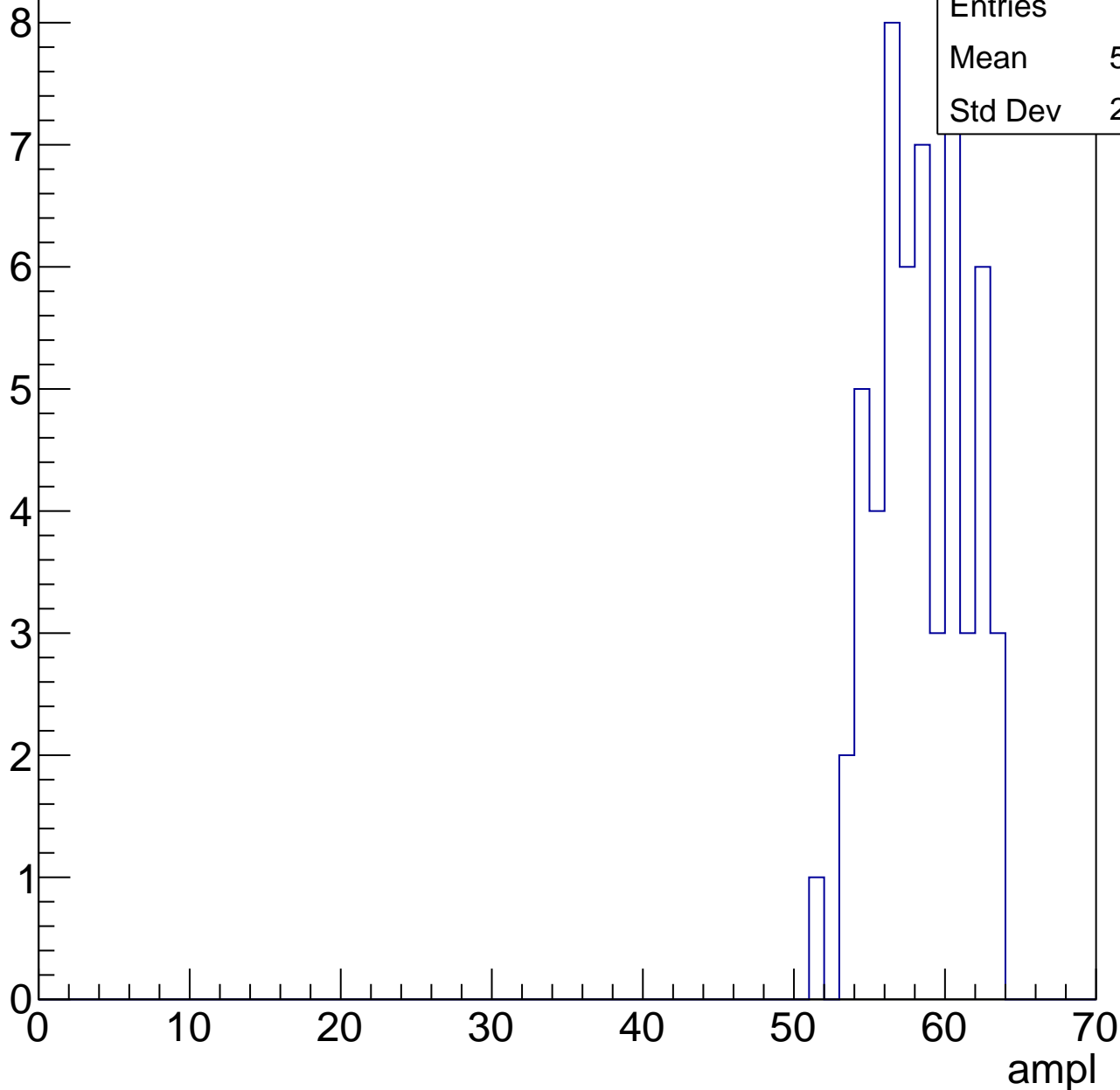


B1L002S, U7-ch117, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.93
Std Dev	2.945

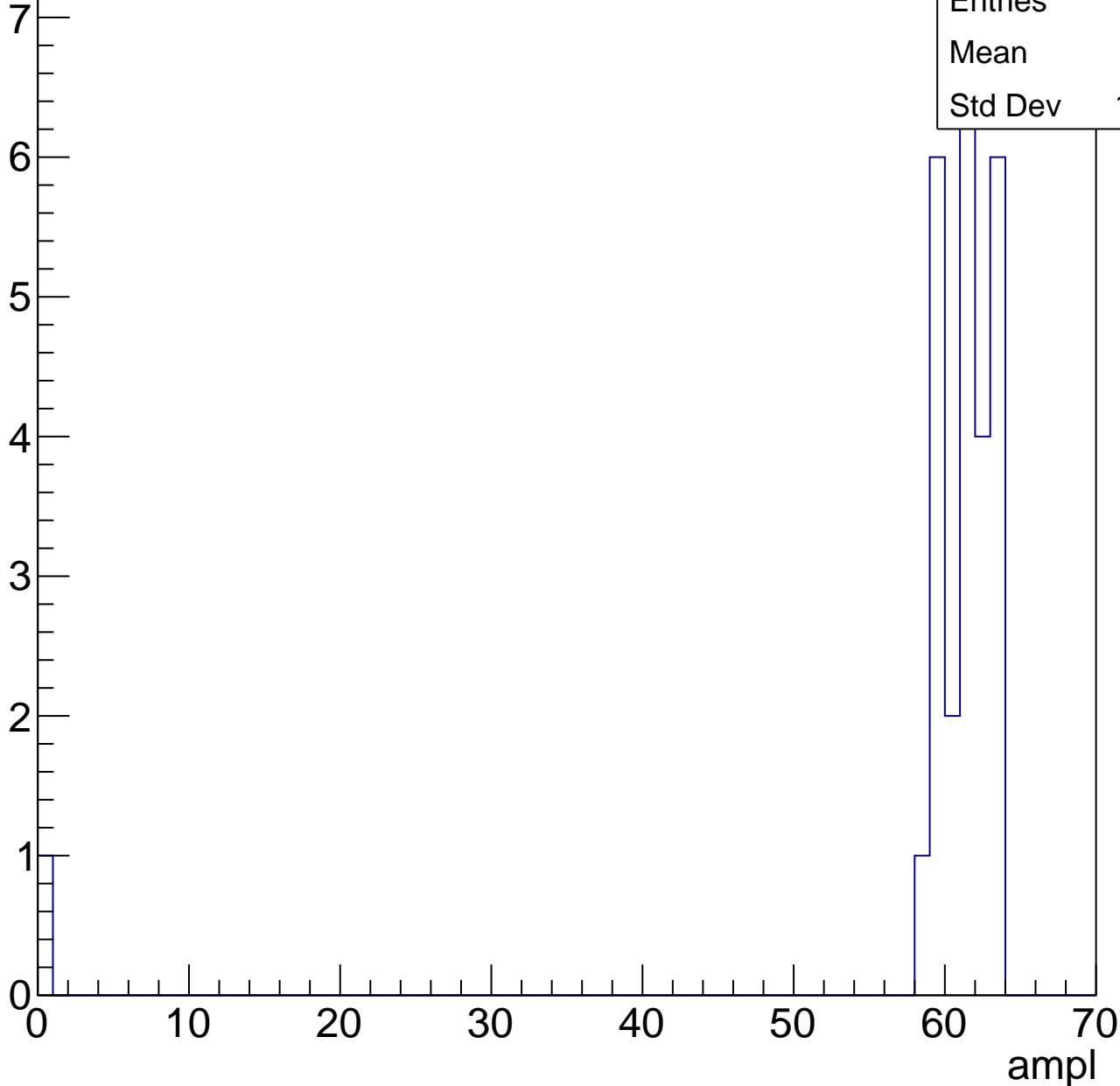


B1L002S, U7-ch117, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.7
Std Dev	11.61



B1L002S, U7-ch117, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch118, adc0

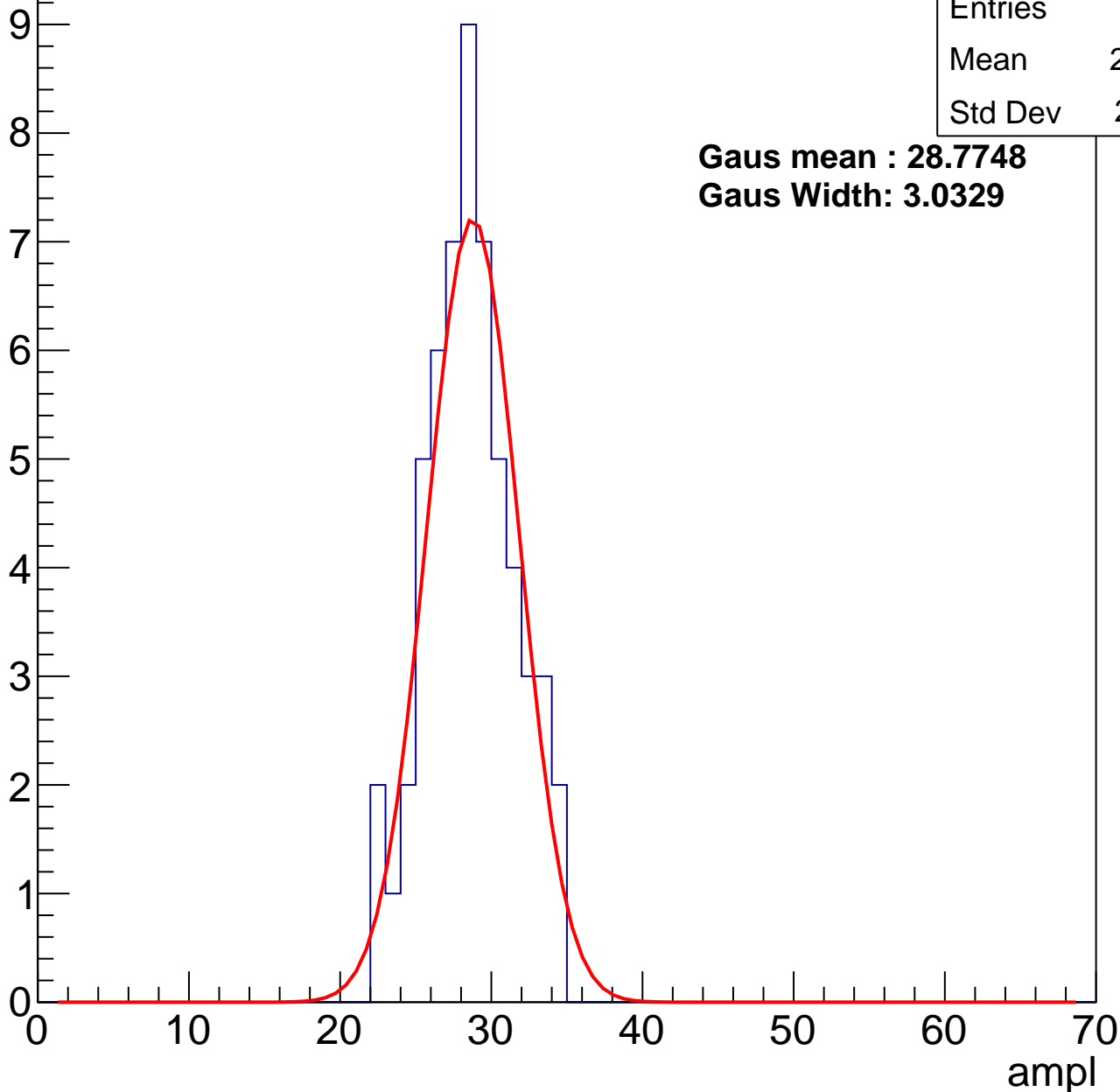
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	28.16
Std Dev	2.871

Gaus mean : 28.7748

Gaus Width: 3.0329



B1L002S, U7-ch118, adc1

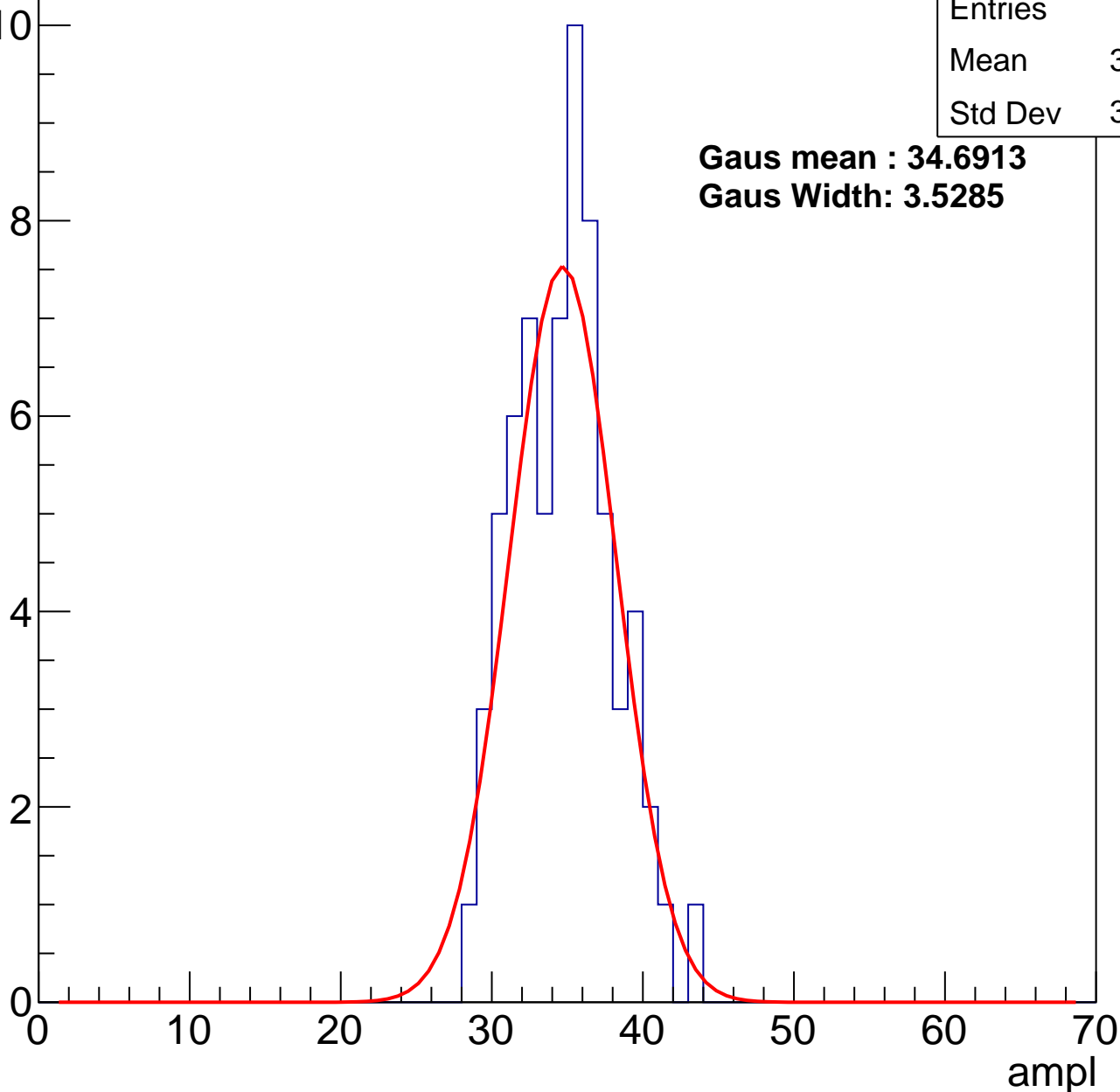
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.34
Std Dev	3.225

Gaus mean : 34.6913

Gaus Width: 3.5285



B1L002S, U7-ch118, adc2

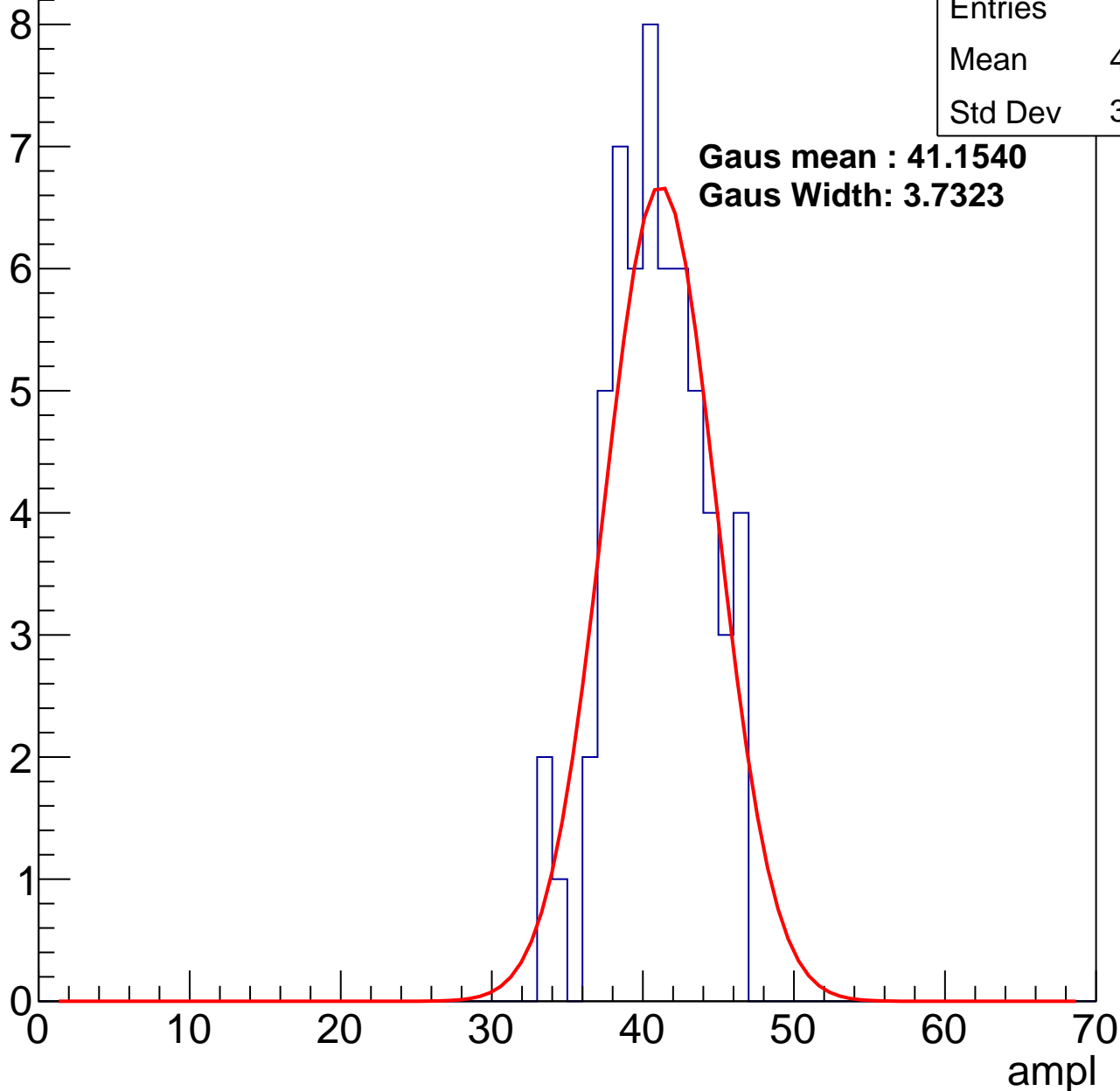
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.42
Std Dev	3.169

Gaus mean : 41.1540

Gaus Width: 3.7323

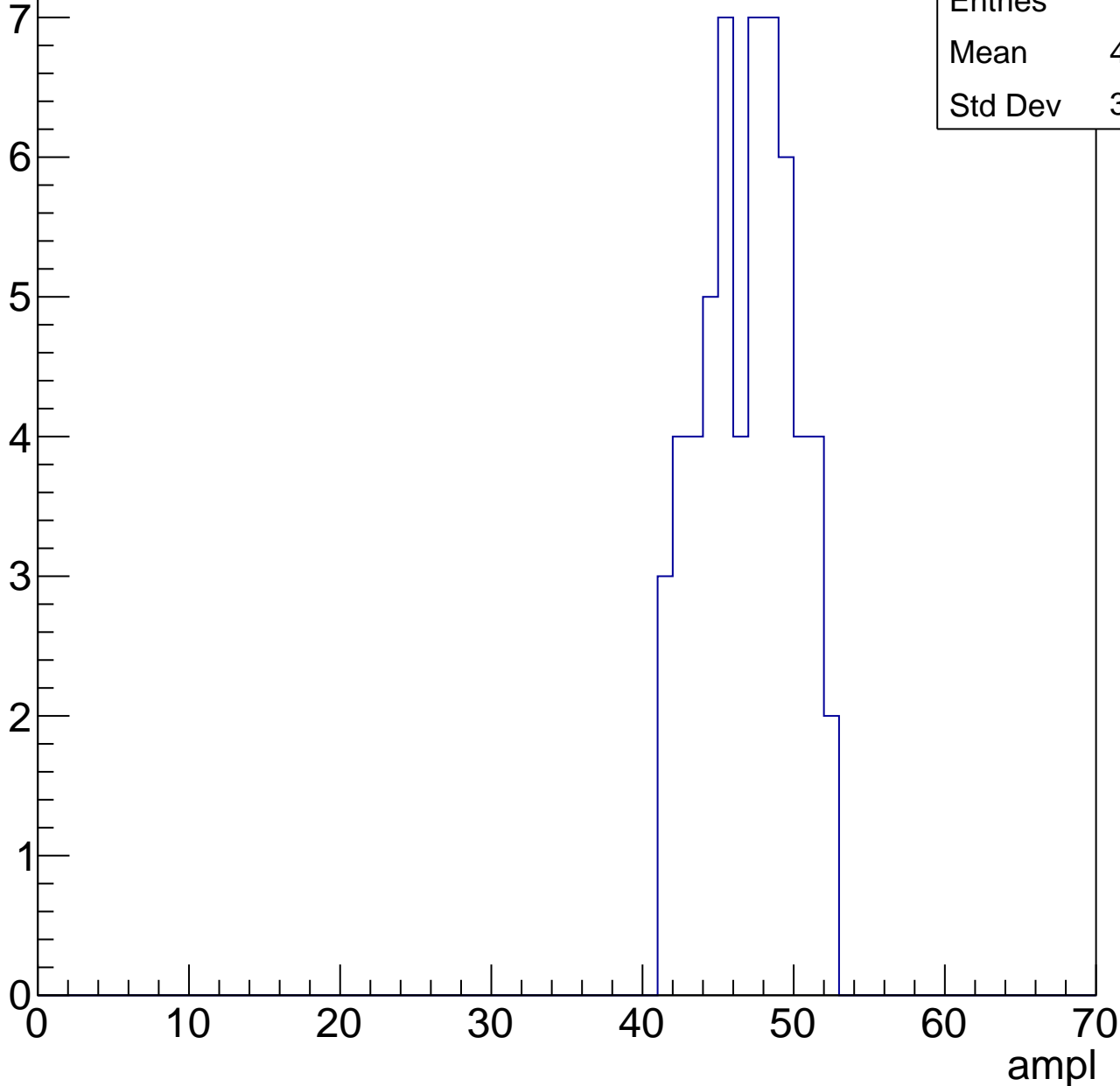


B1L002S, U7-ch118, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.47
Std Dev	3.004

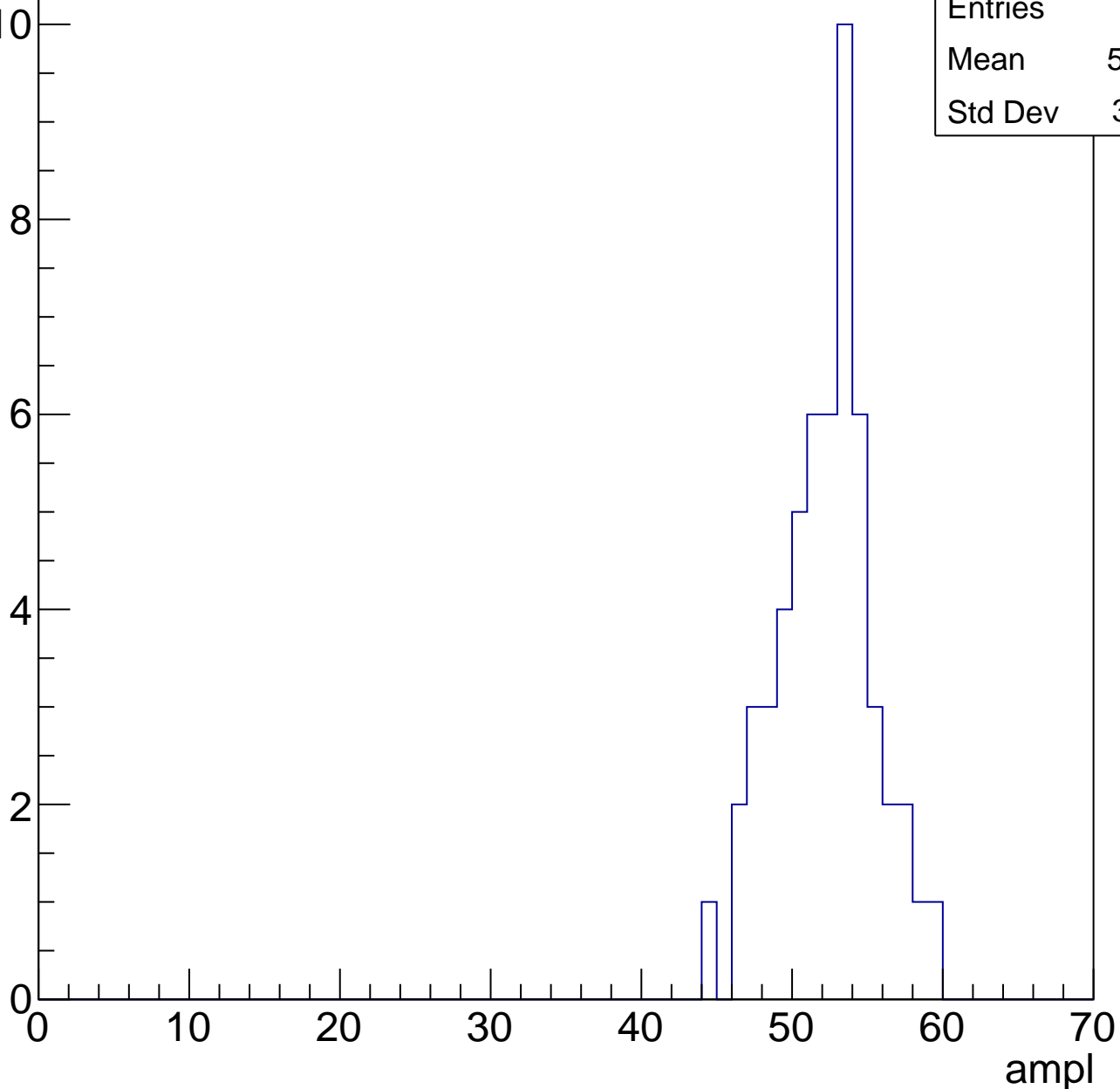


B1L002S, U7-ch118, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	51.76
Std Dev	3.151



B1L002S, U7-ch118, adc5

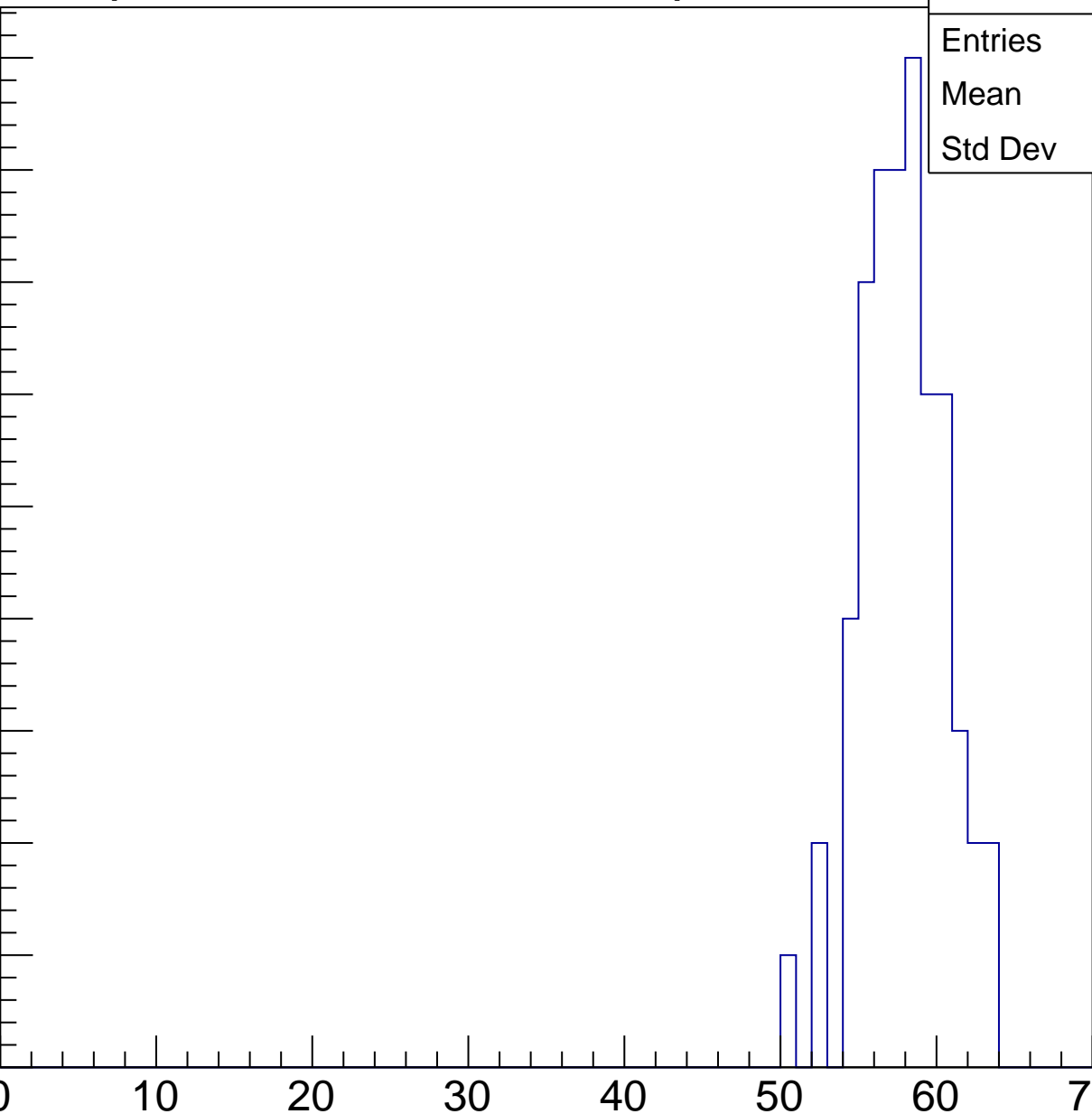
calib_packv5_042523_0143.root, FC#10, port B3

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	57.38
Std Dev	2.69

ampl



B1L002S, U7-ch118, adc6

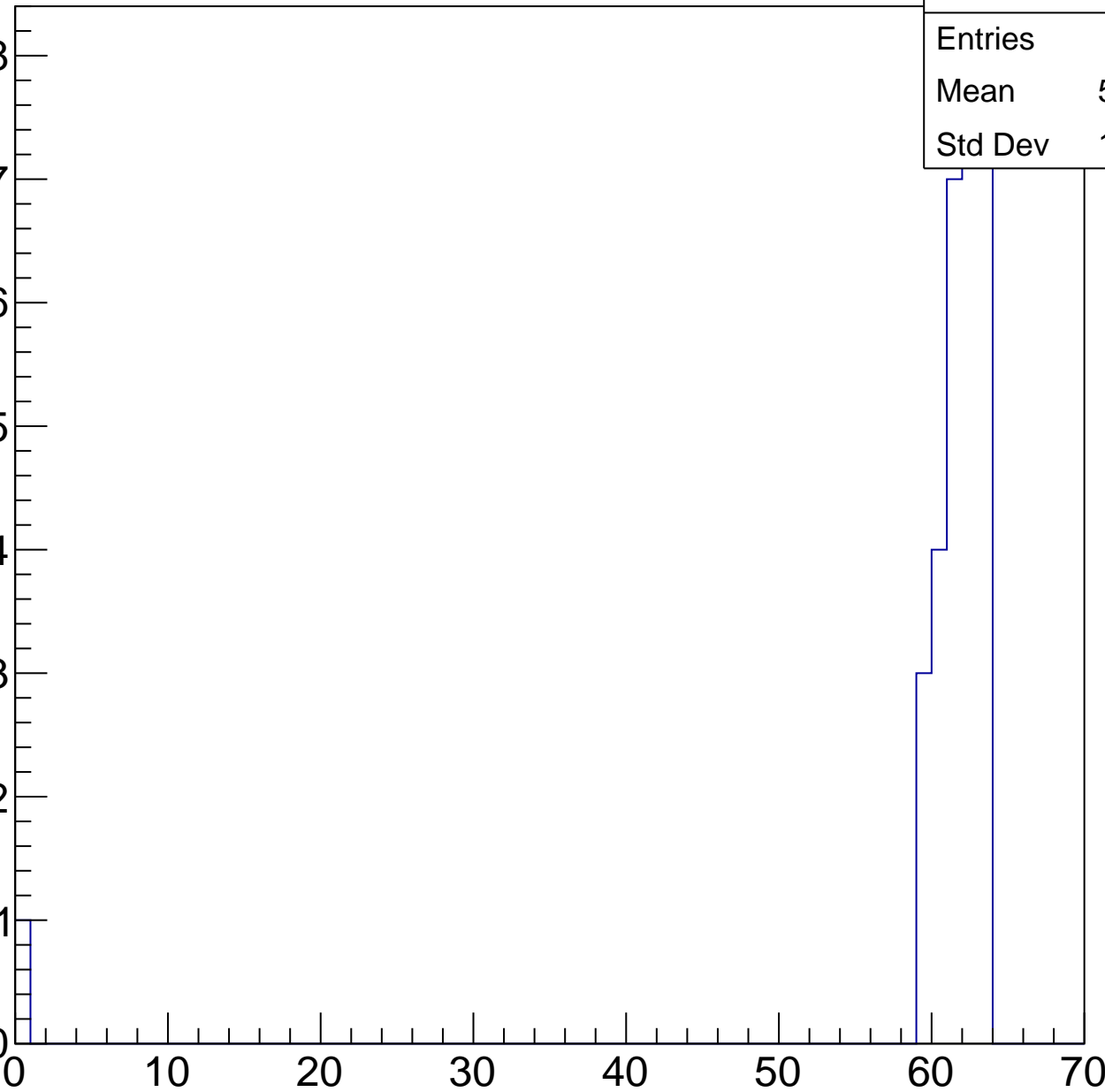
calib_packv5_042523_0143.root, FC#10, port B3

Entry

8
7
6
5
4
3
2
1
0

Entries	31
Mean	59.48
Std Dev	10.93

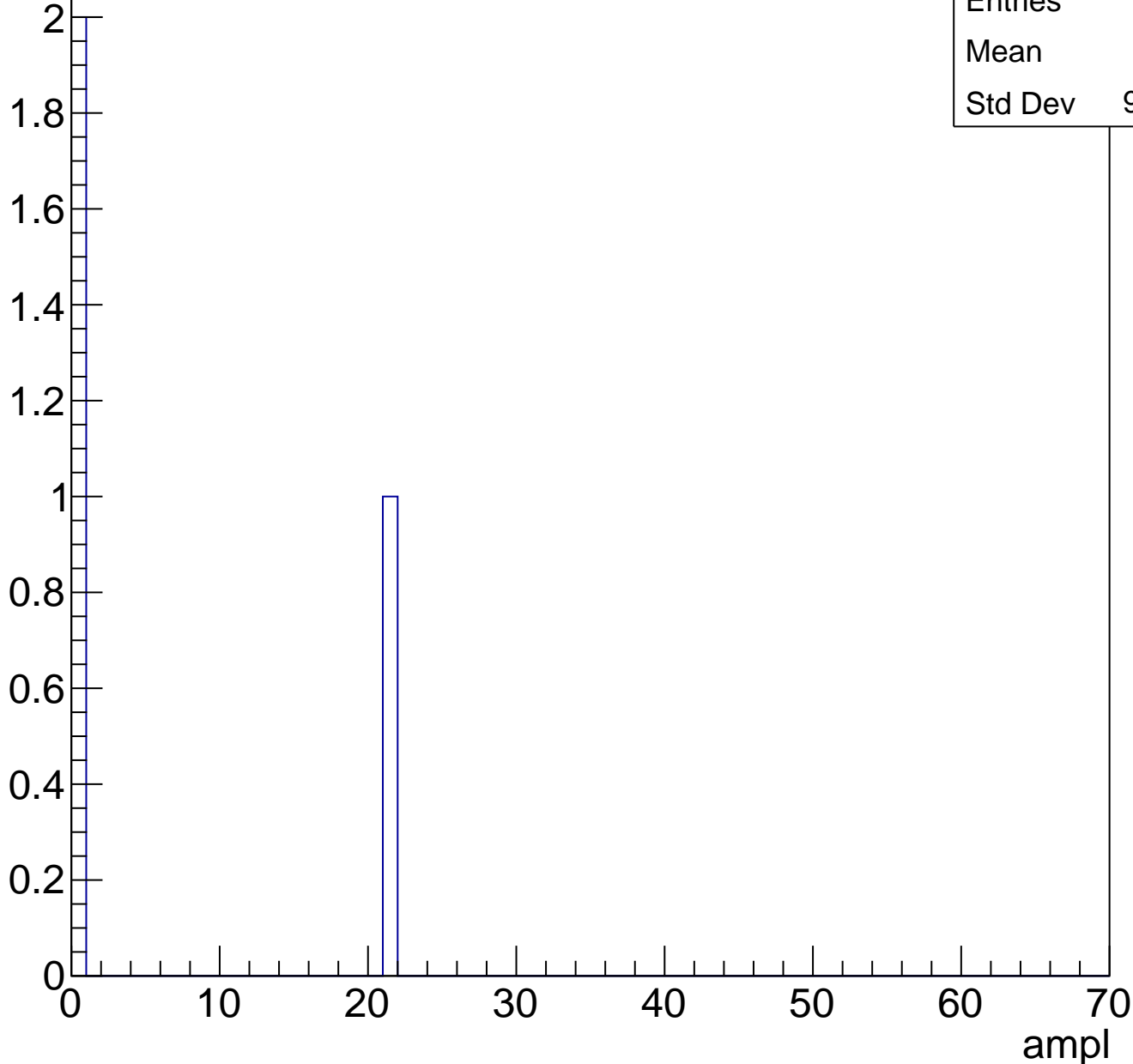
ampl



B1L002S, U7-ch118, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L002S, U7-ch119, adc0

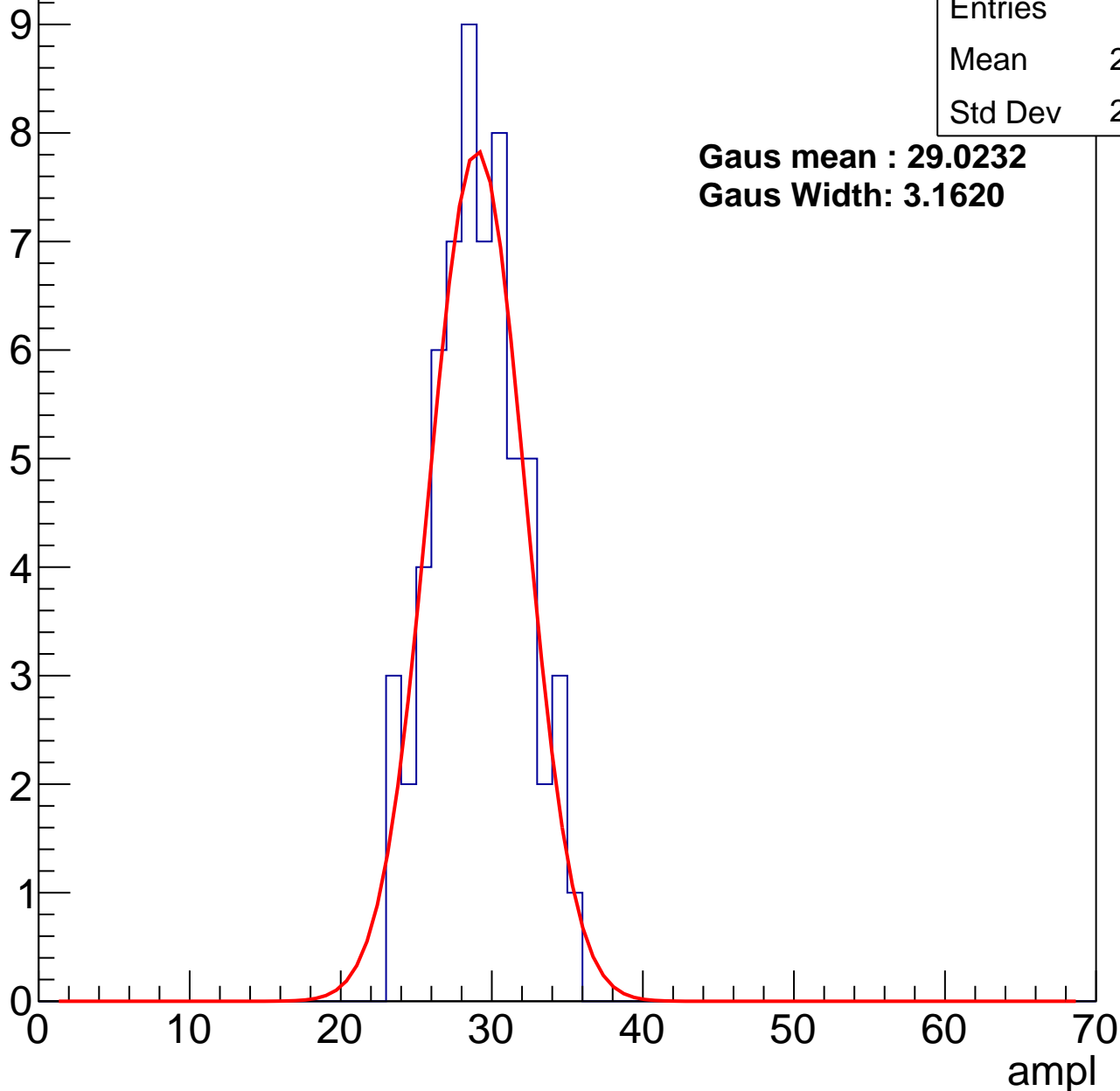
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.63
Std Dev	2.897

Gaus mean : 29.0232

Gaus Width: 3.1620



B1L002S, U7-ch119, adc1

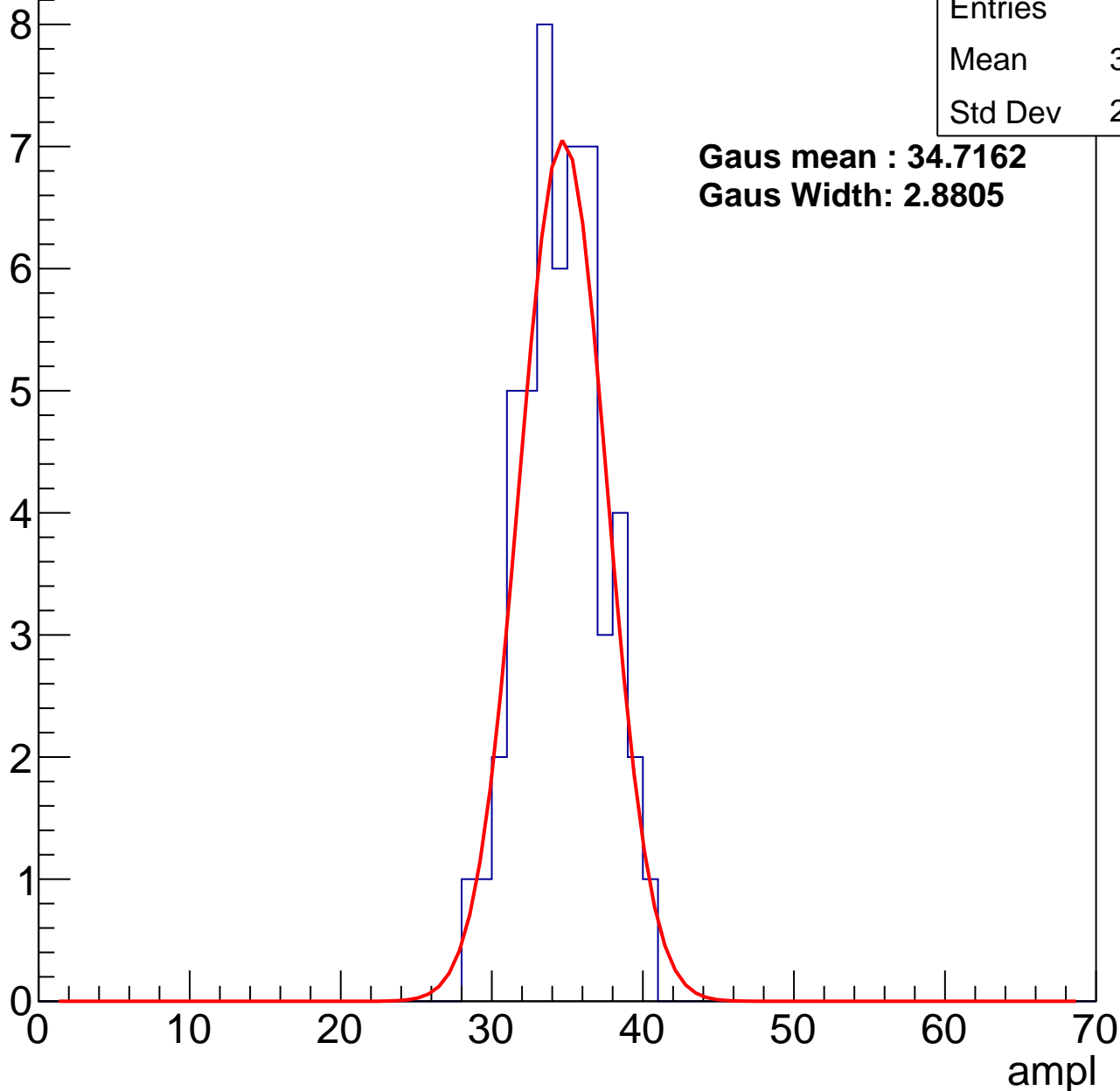
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	34.19
Std Dev	2.689

Gaus mean : 34.7162

Gaus Width: 2.8805



B1L002S, U7-ch119, adc2

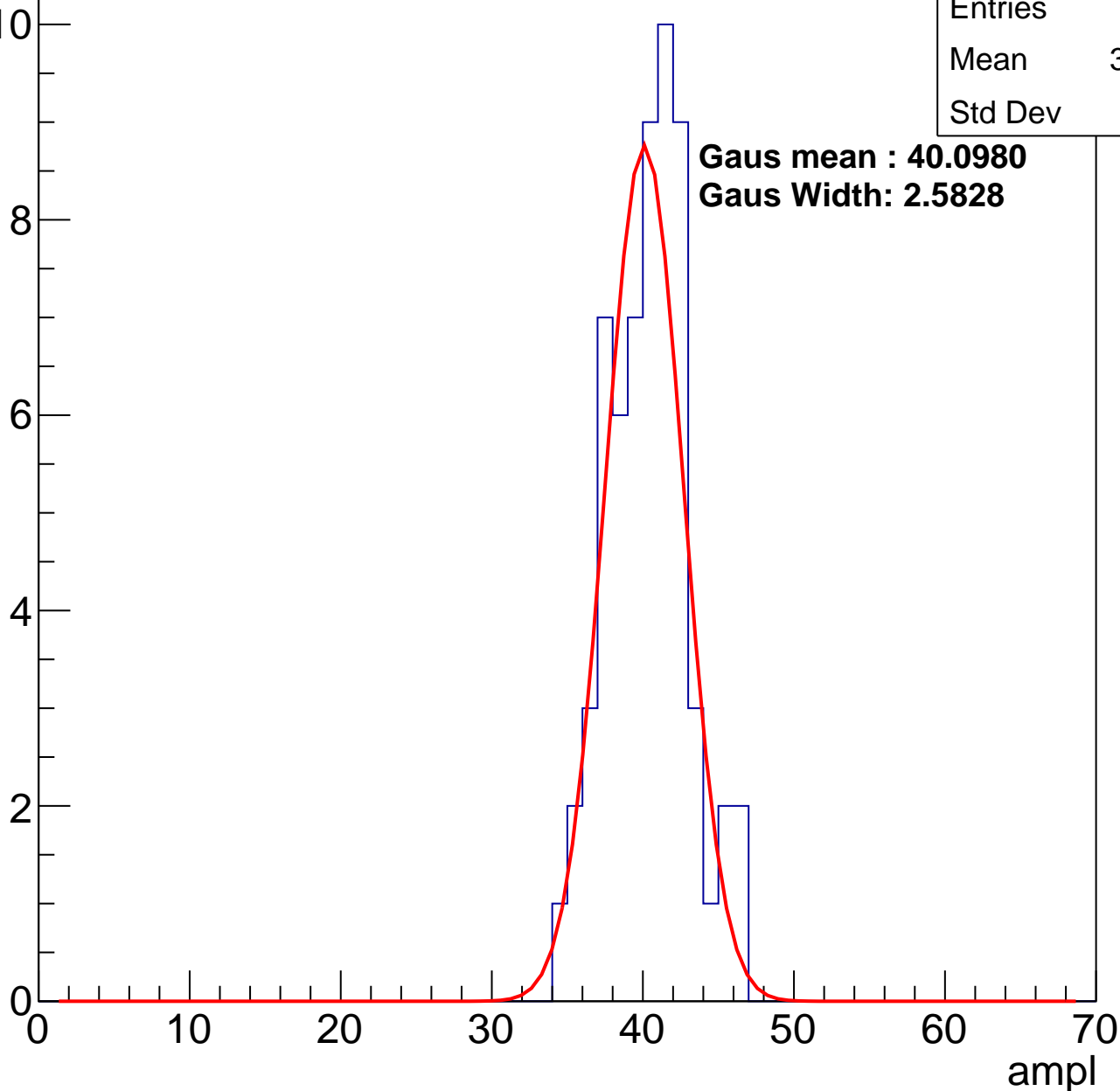
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.92
Std Dev	2.66

Gaus mean : 40.0980

Gaus Width: 2.5828

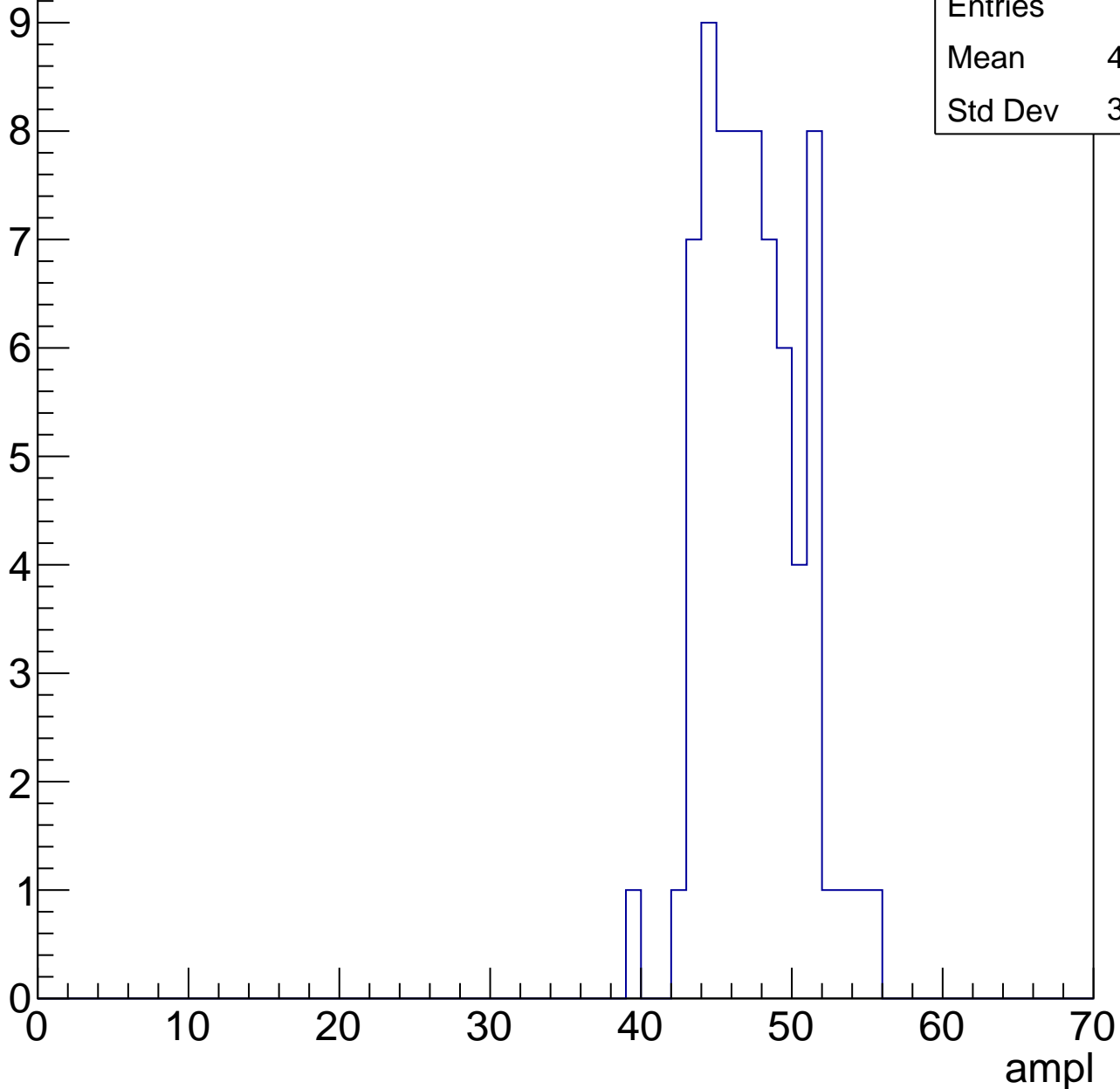


B1L002S, U7-ch119, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	71
Mean	46.96
Std Dev	3.119

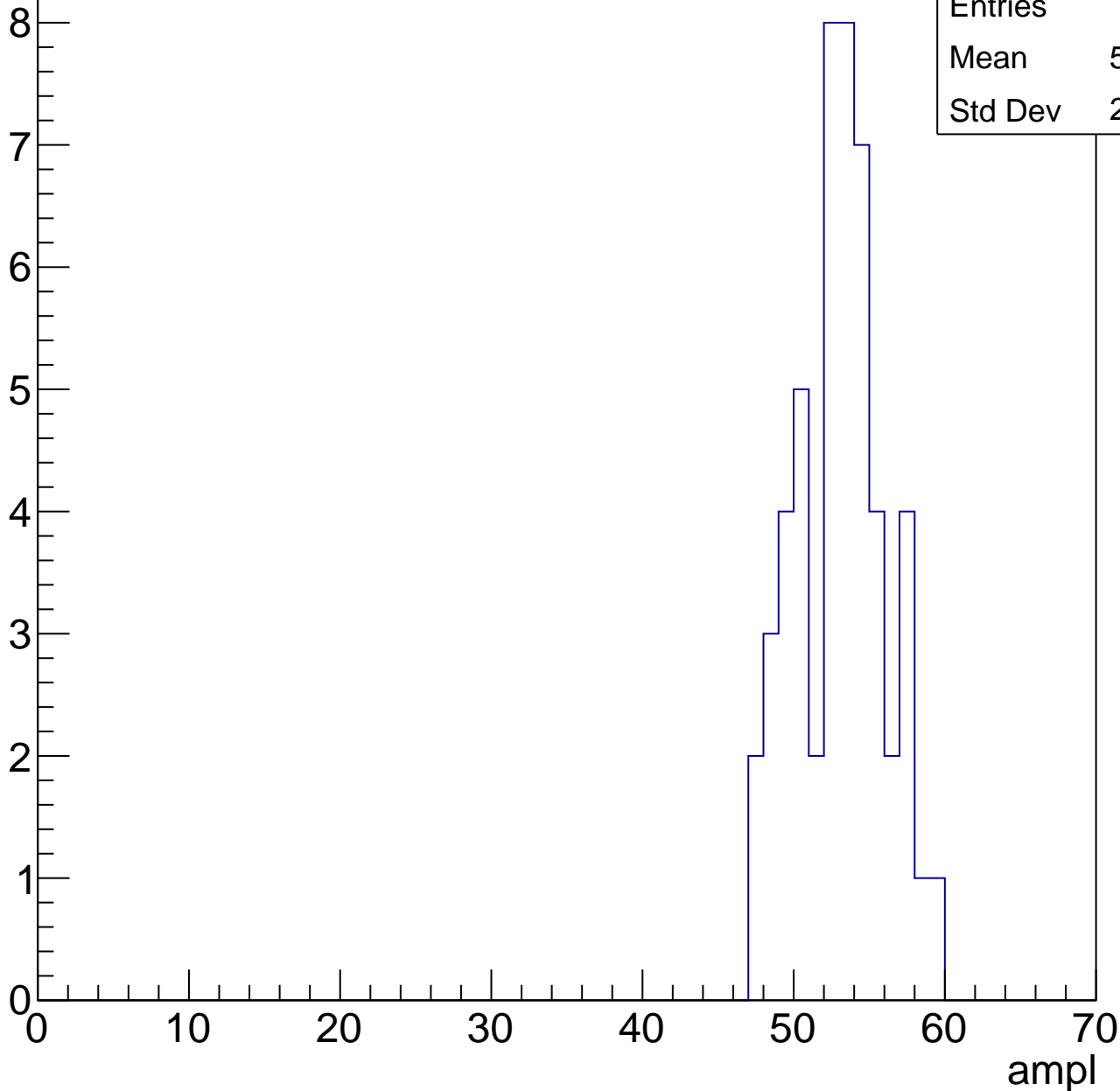


B1L002S, U7-ch119, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	52.57
Std Dev	2.899

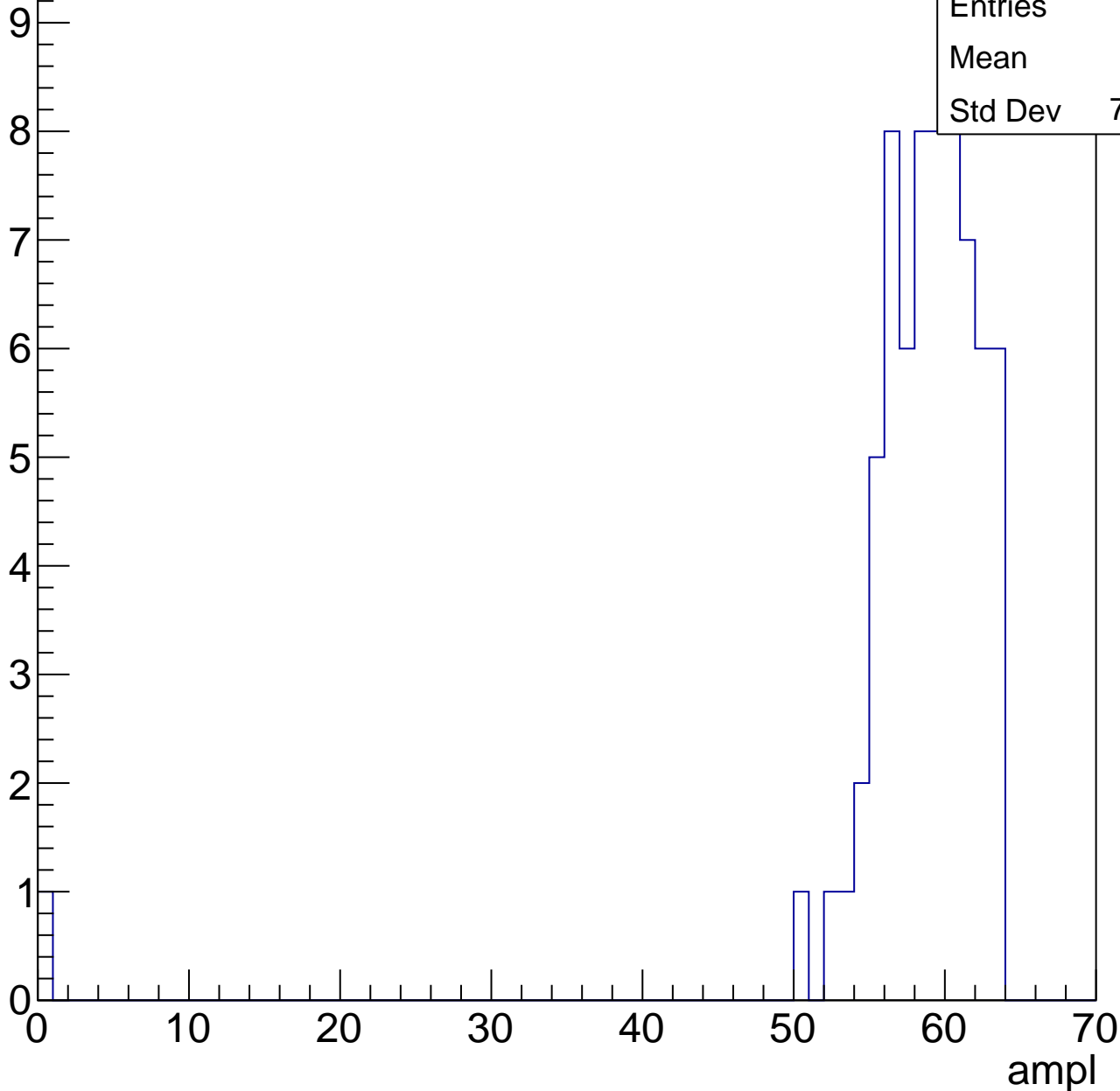


B1L002S, U7-ch119, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

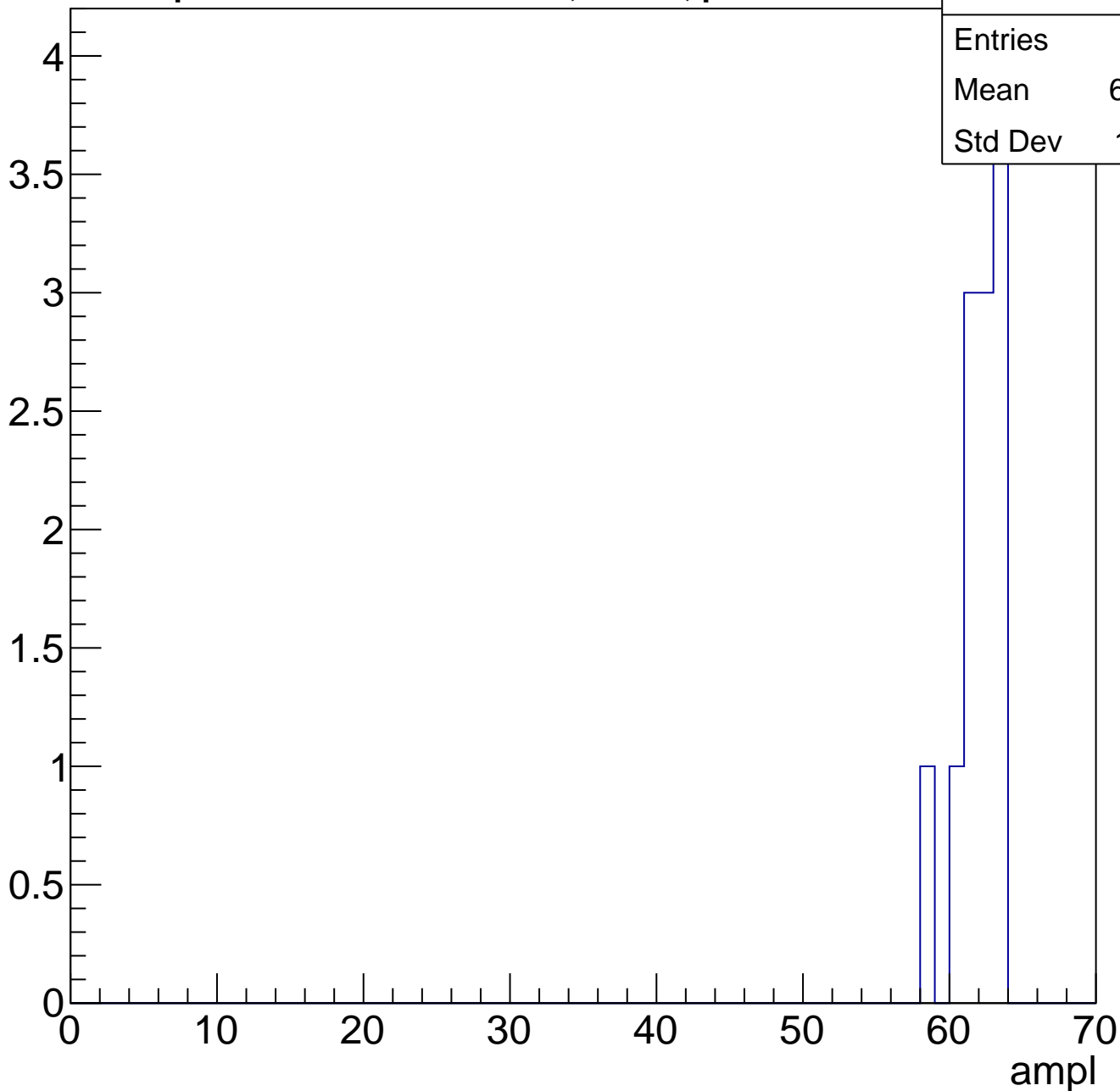
Entries	69
Mean	57.7
Std Dev	7.567



B1L002S, U7-ch119, adc6

calib_packv5_042523_0143.root, FC#10, port B3

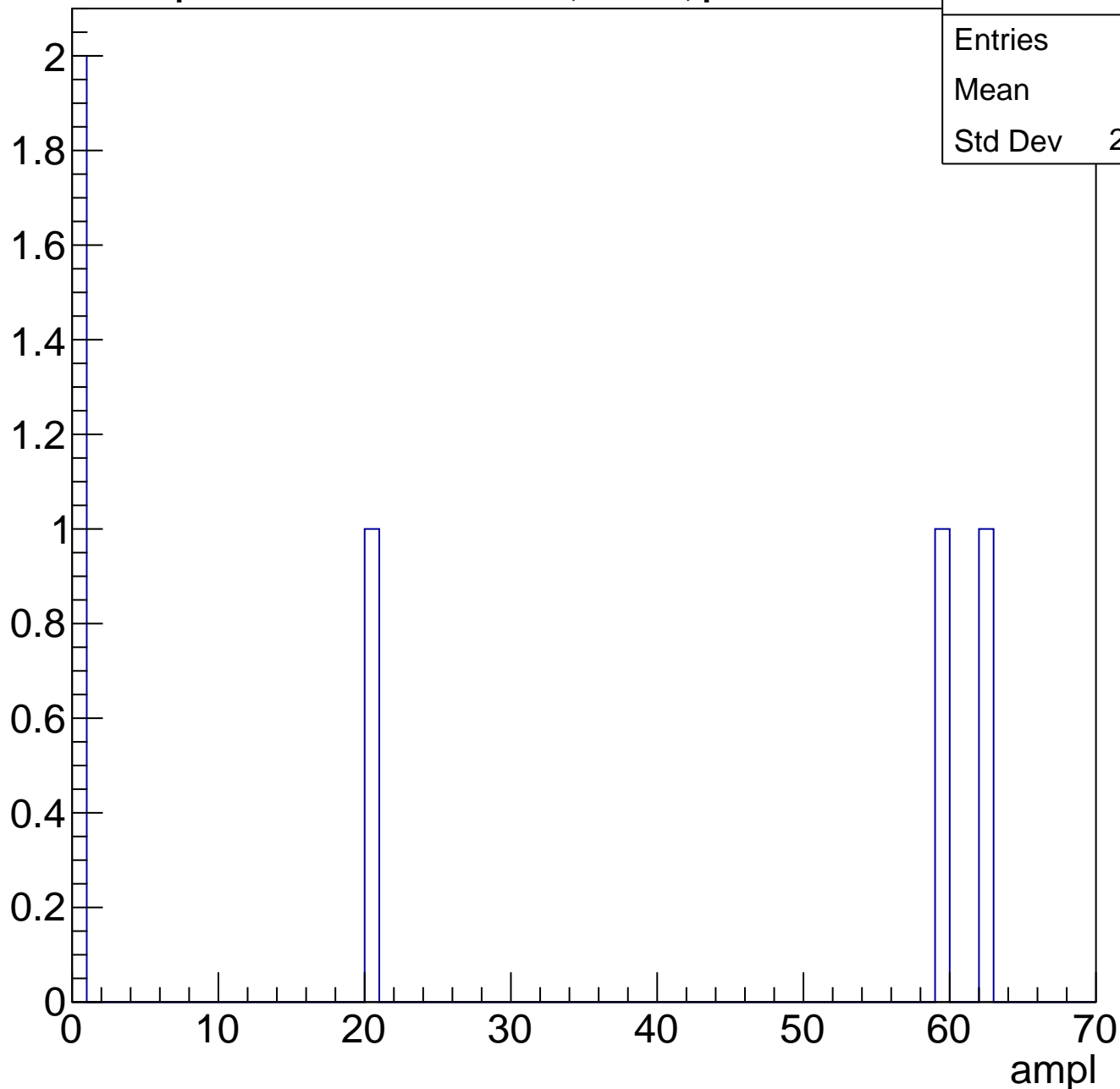
Entry



B1L002S, U7-ch119, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	5
Mean	28.2
Std Dev	27.38

B1L002S, U7-ch120, adc0

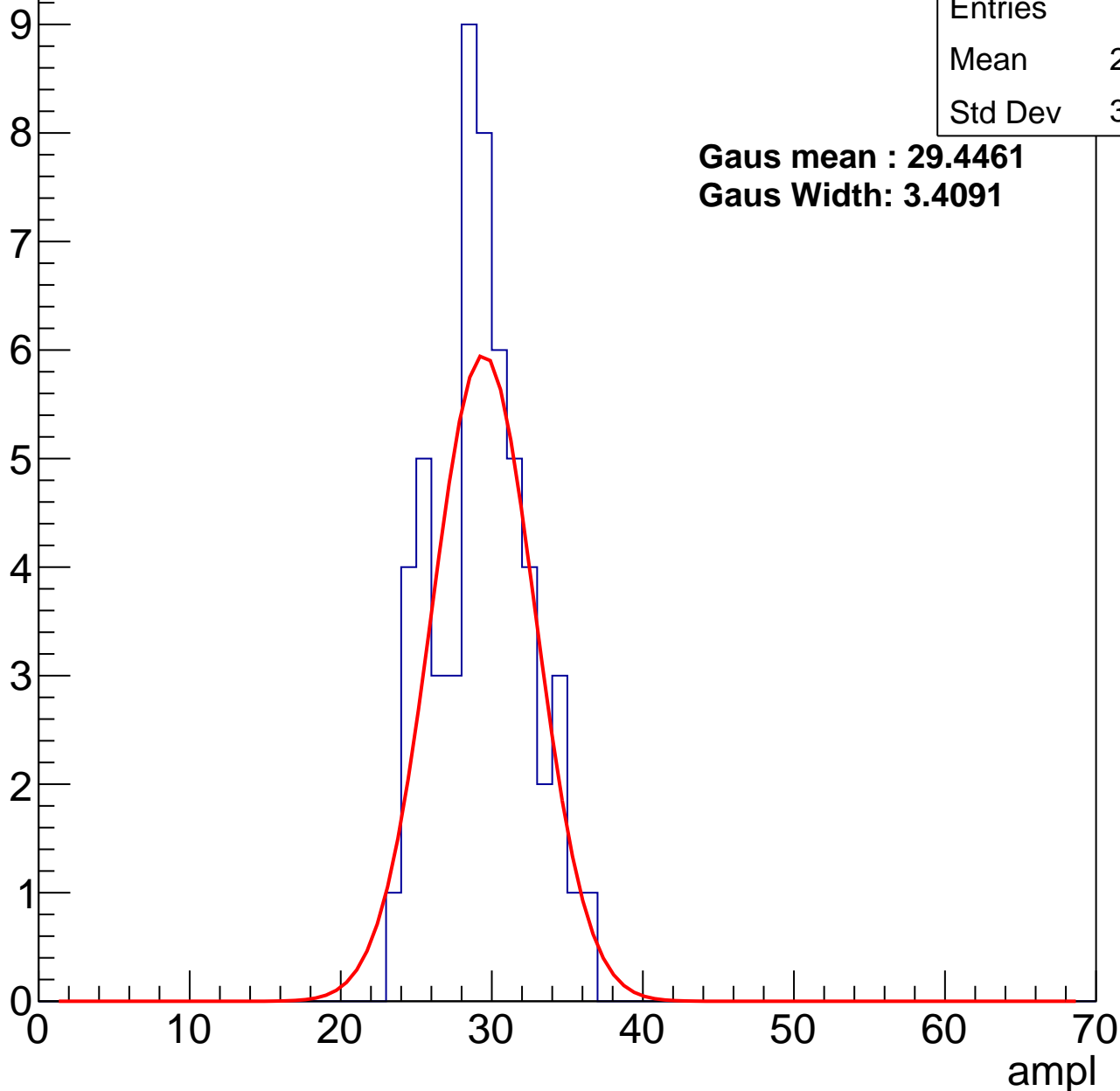
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	28.89
Std Dev	3.067

Gaus mean : 29.4461

Gaus Width: 3.4091



B1L002S, U7-ch120, adc1

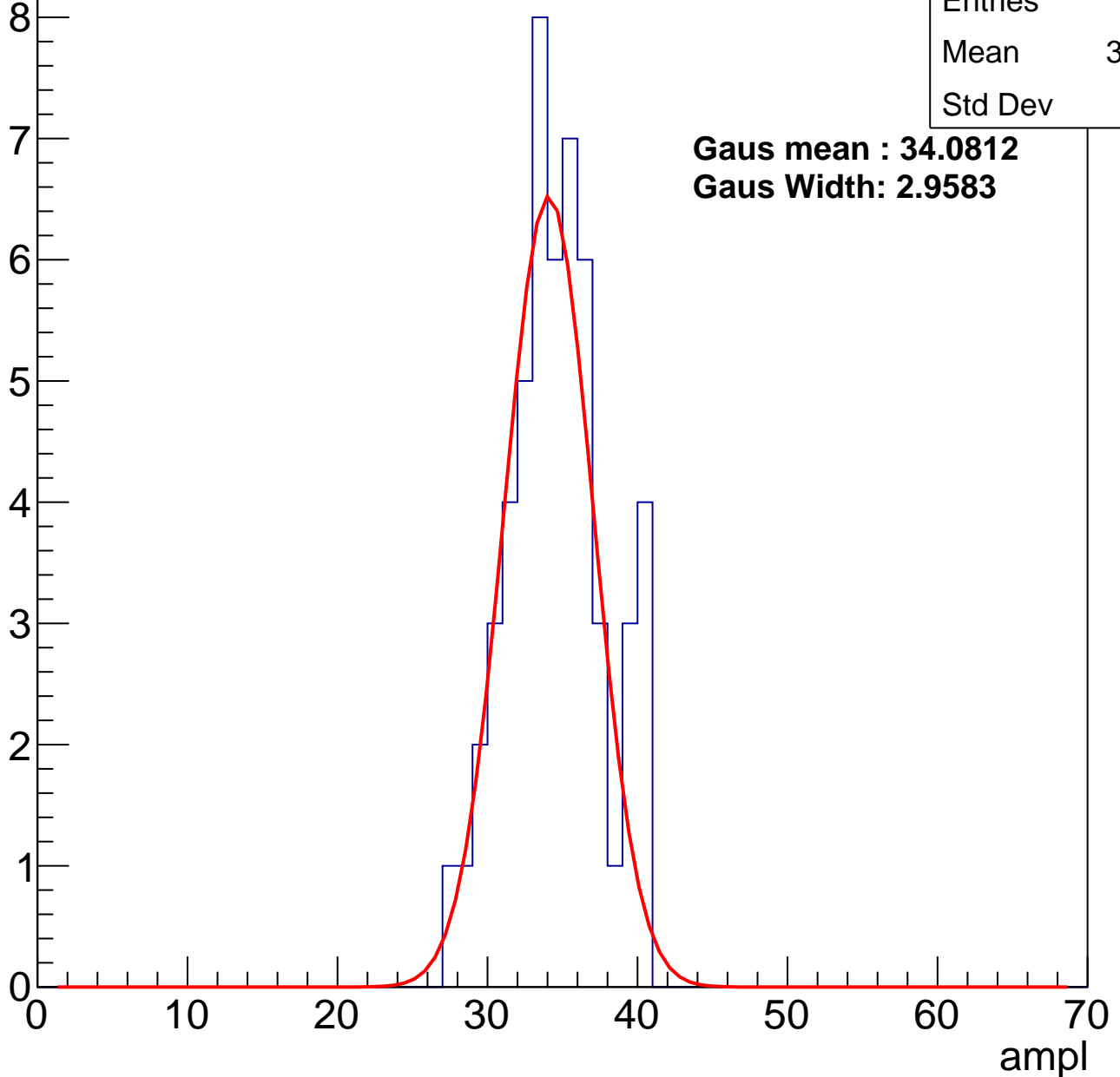
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.11
Std Dev	3.16

Gaus mean : 34.0812

Gaus Width: 2.9583



B1L002S, U7-ch120, adc2

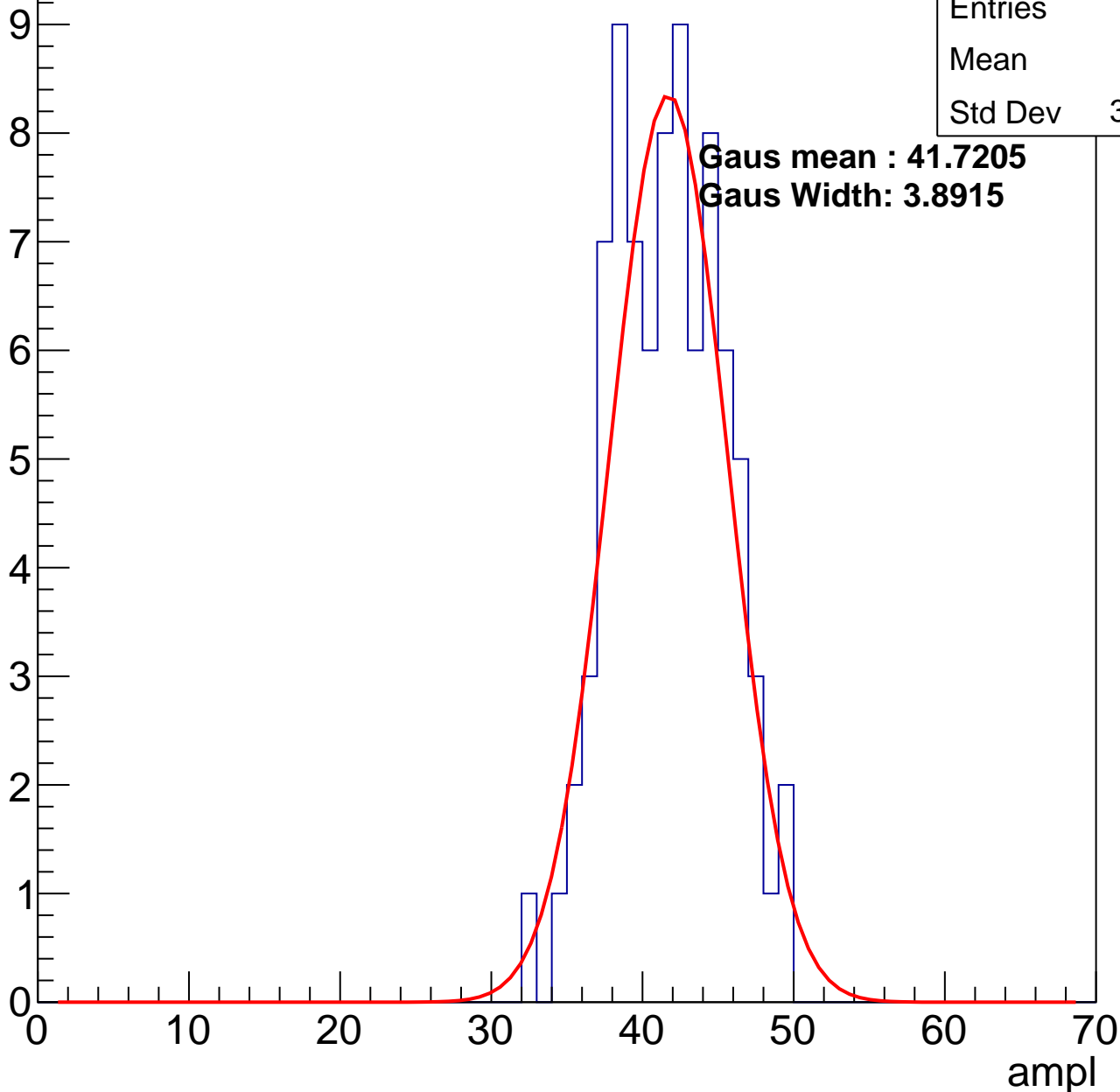
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	84
Mean	41.2
Std Dev	3.638

Gaus mean : 41.7205

Gaus Width: 3.8915

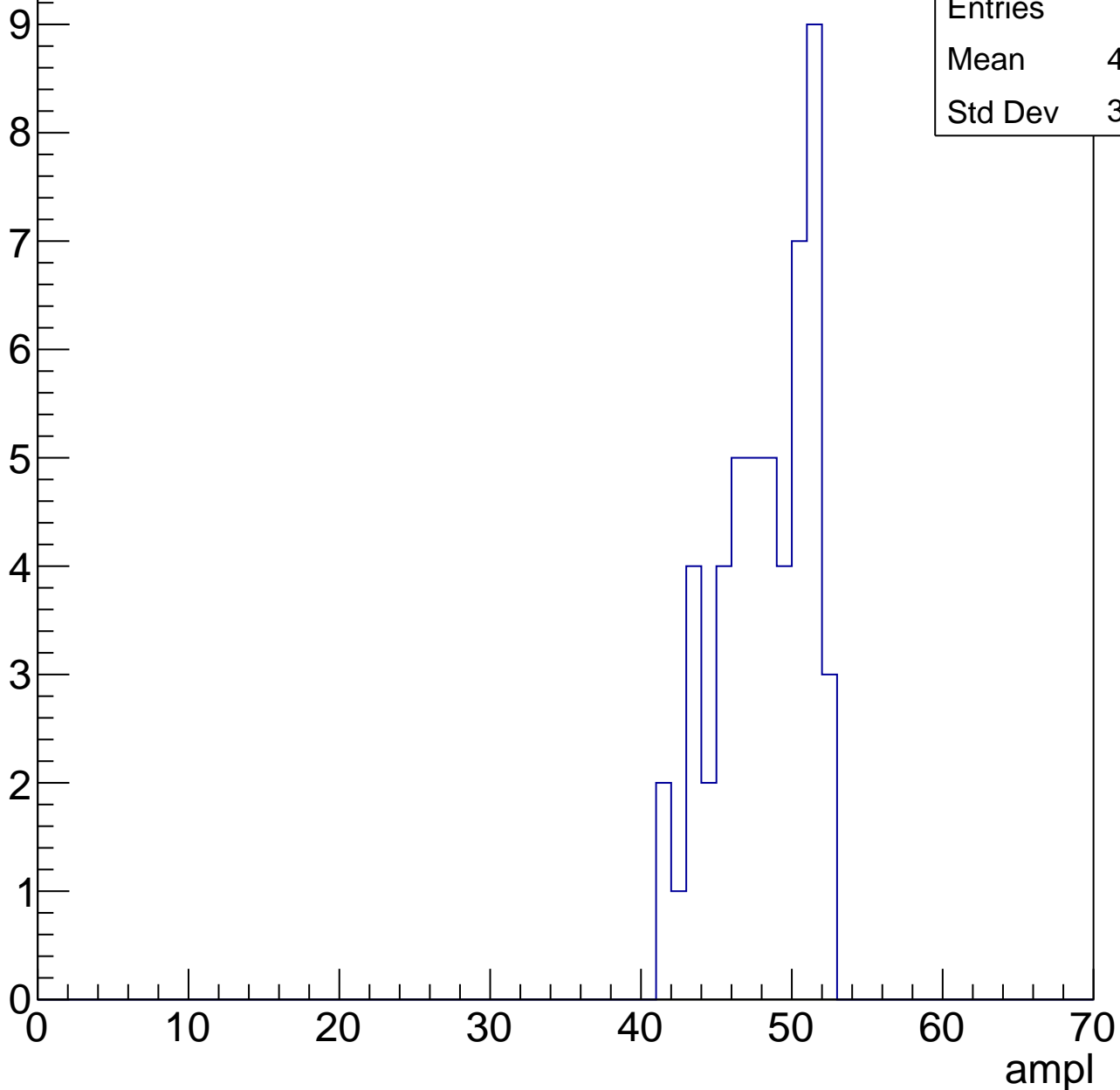


B1L002S, U7-ch120, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	51
Mean	47.65
Std Dev	3.073

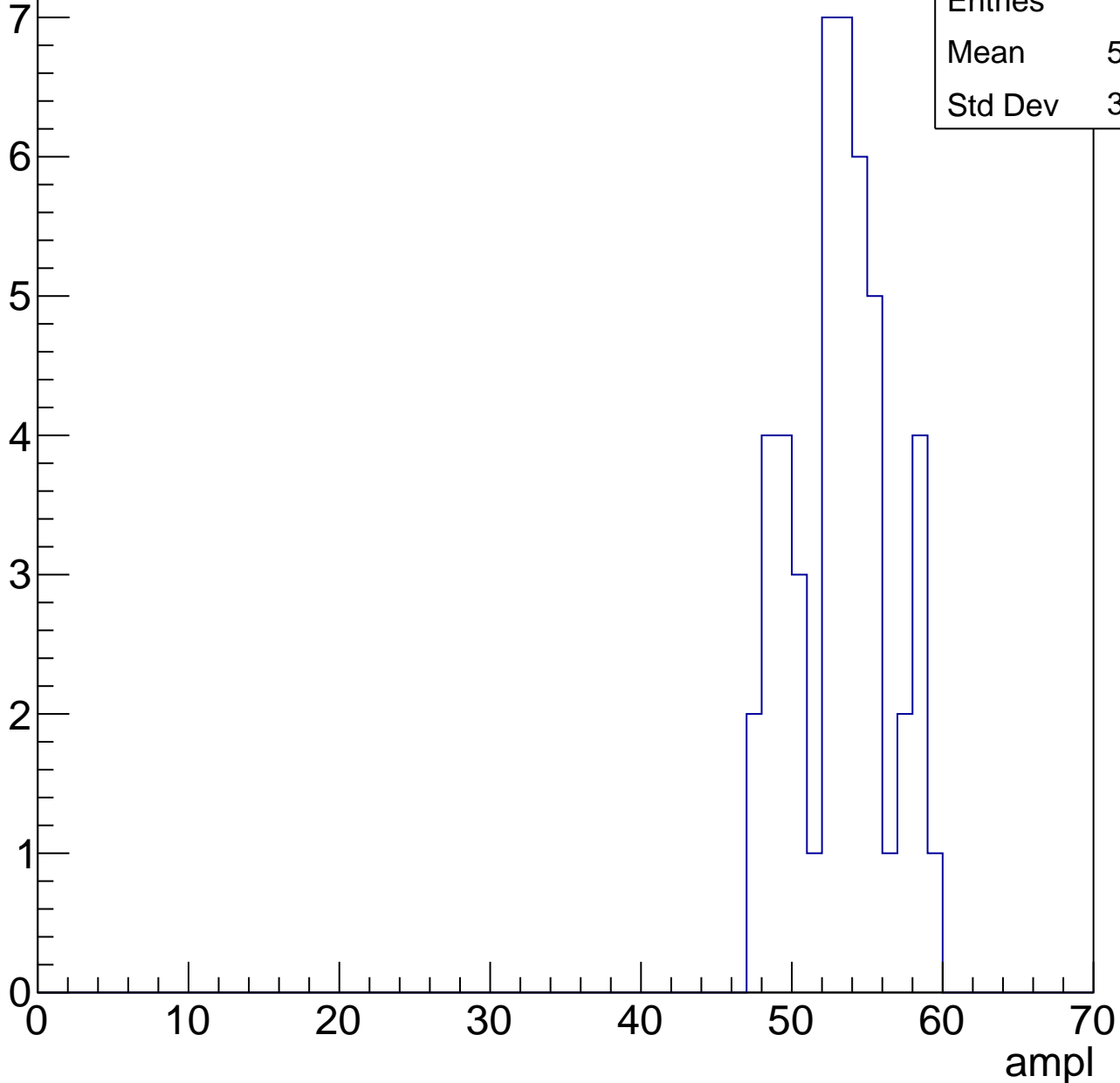


B1L002S, U7-ch120, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	52.72
Std Dev	3.174

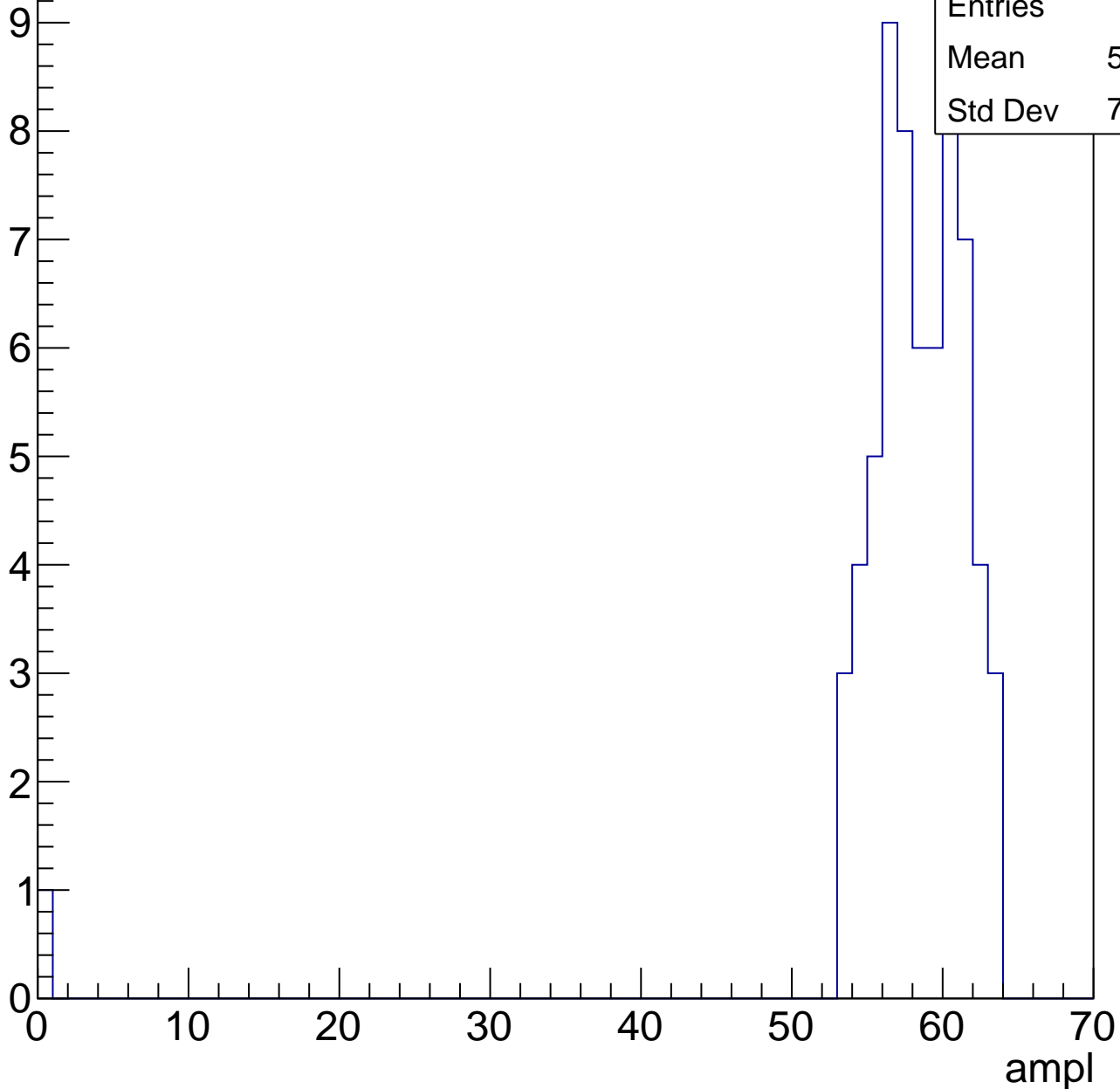


B1L002S, U7-ch120, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	57.17
Std Dev	7.637

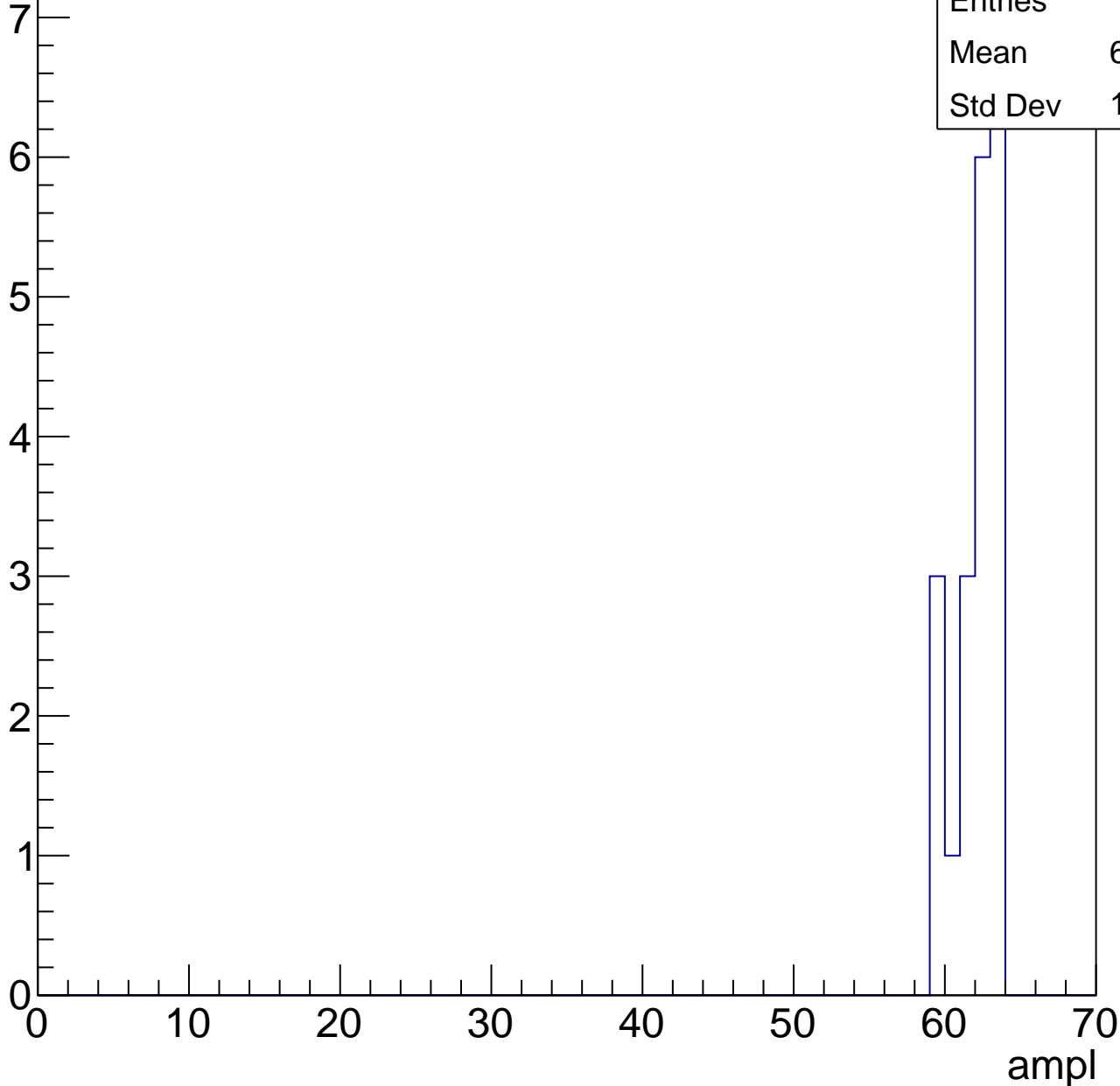


B1L002S, U7-ch120, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.65
Std Dev	1.388



B1L002S, U7-ch120, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch121, adc0

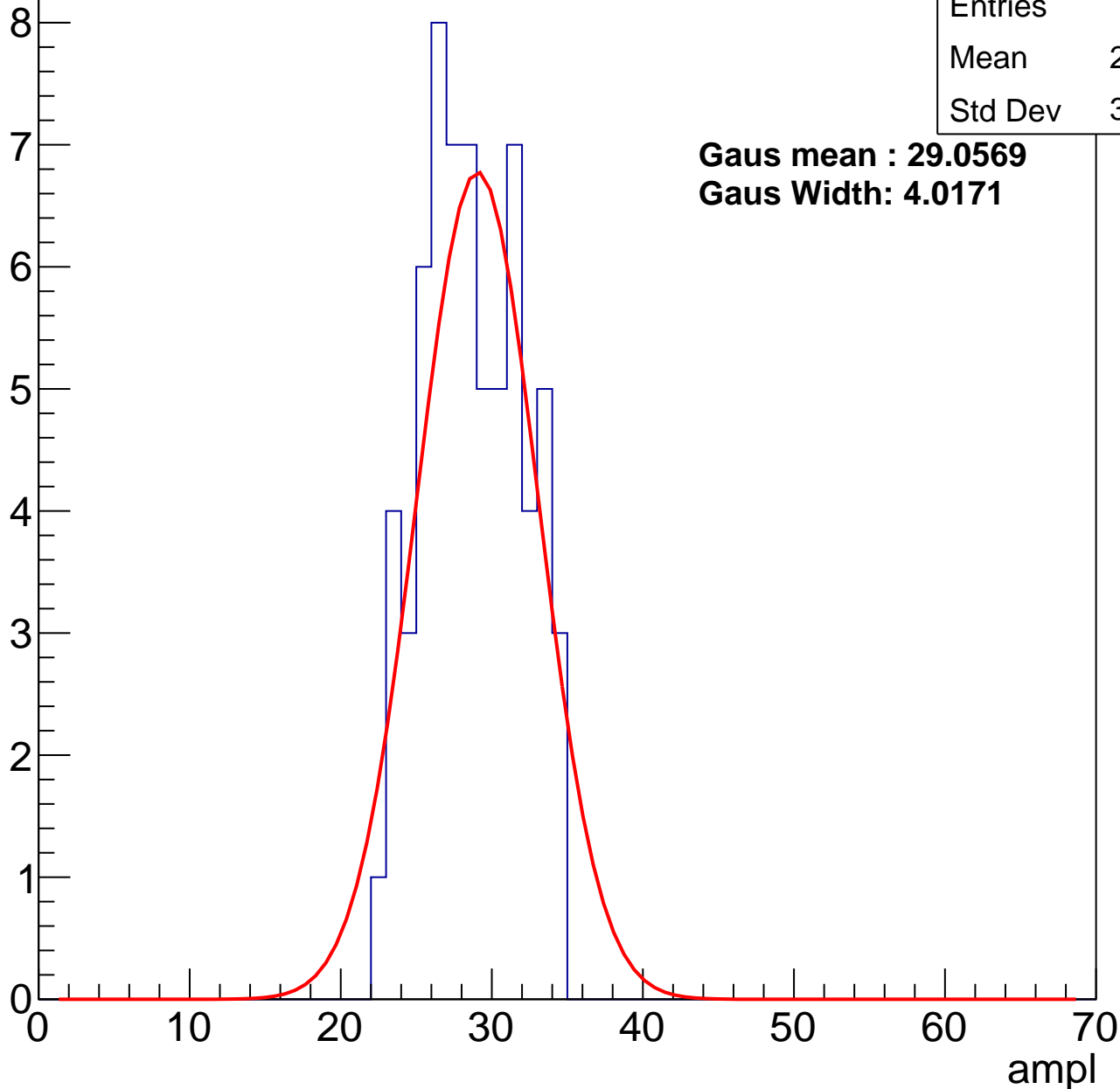
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.25
Std Dev	3.182

Gaus mean : 29.0569

Gaus Width: 4.0171



B1L002S, U7-ch121, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	76
Mean	34.95
Std Dev	3.528

Gaus mean : 35.5469

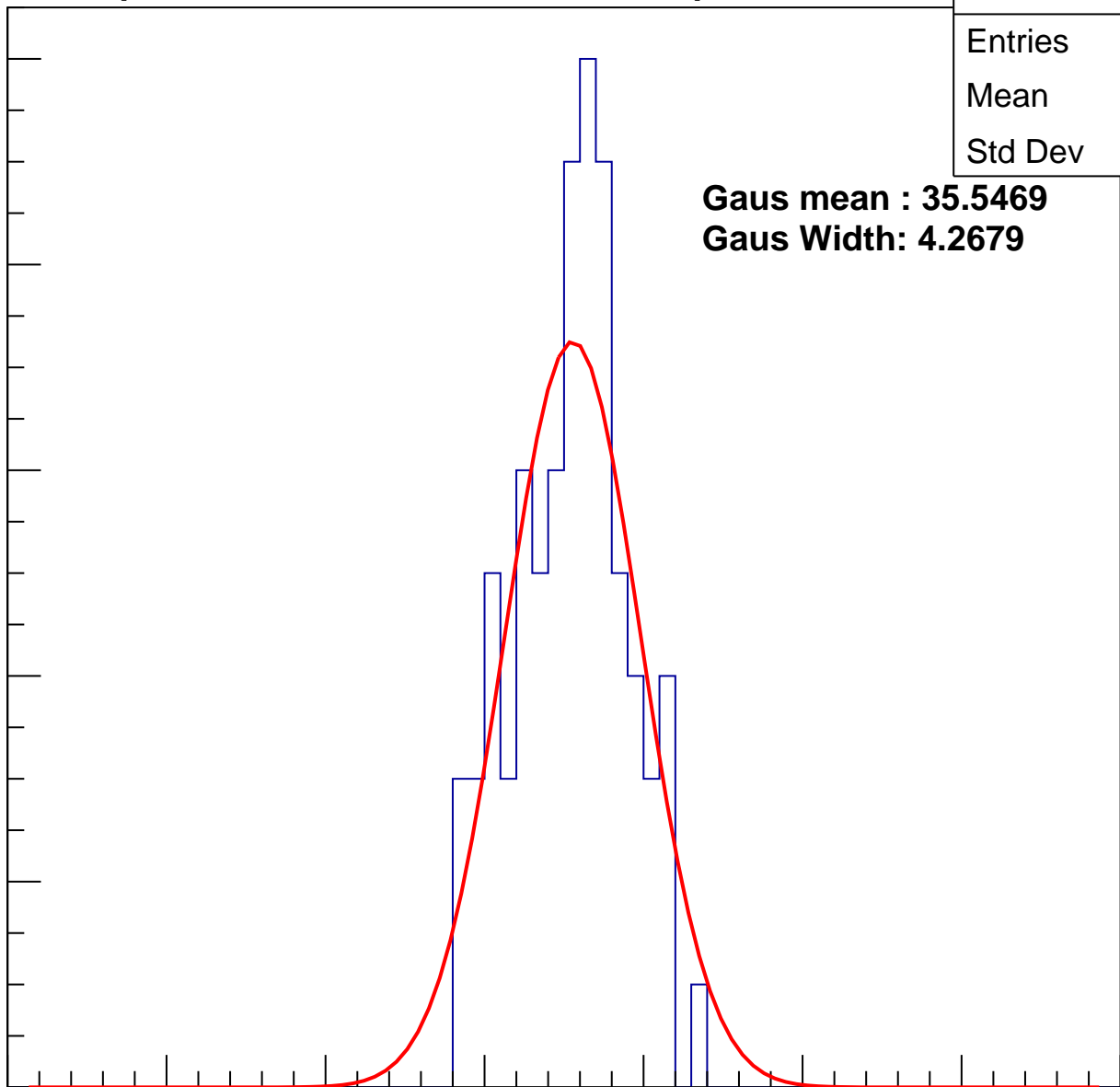
Gaus Width: 4.2679

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch121, adc2

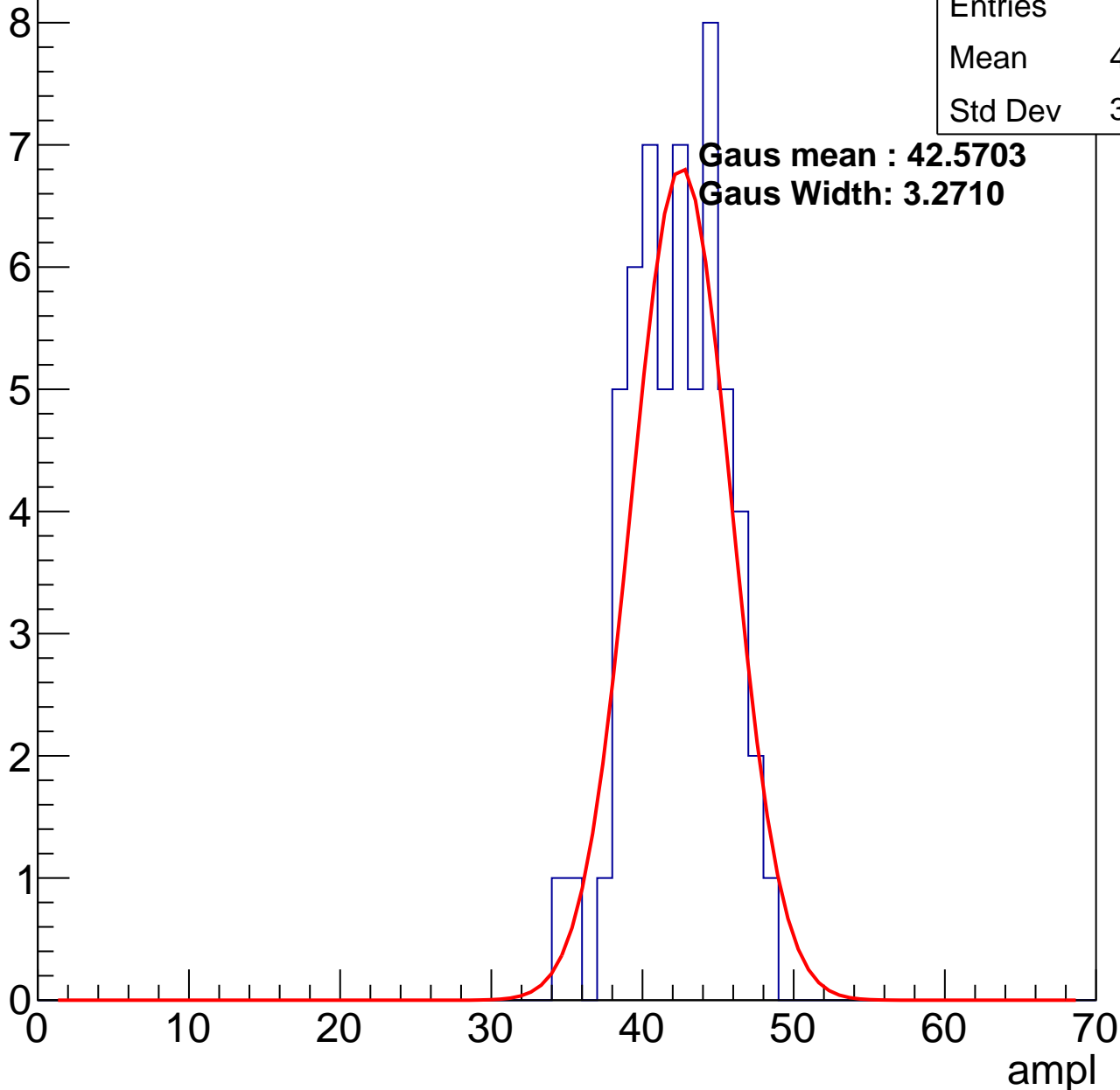
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.84
Std Dev	3.039

Gaus mean : 42.5703

Gaus Width: 3.2710



B1L002S, U7-ch121, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	60
Mean	47.57
Std Dev	3.002

Entry

10

8

6

4

2

0

0

10

20

30

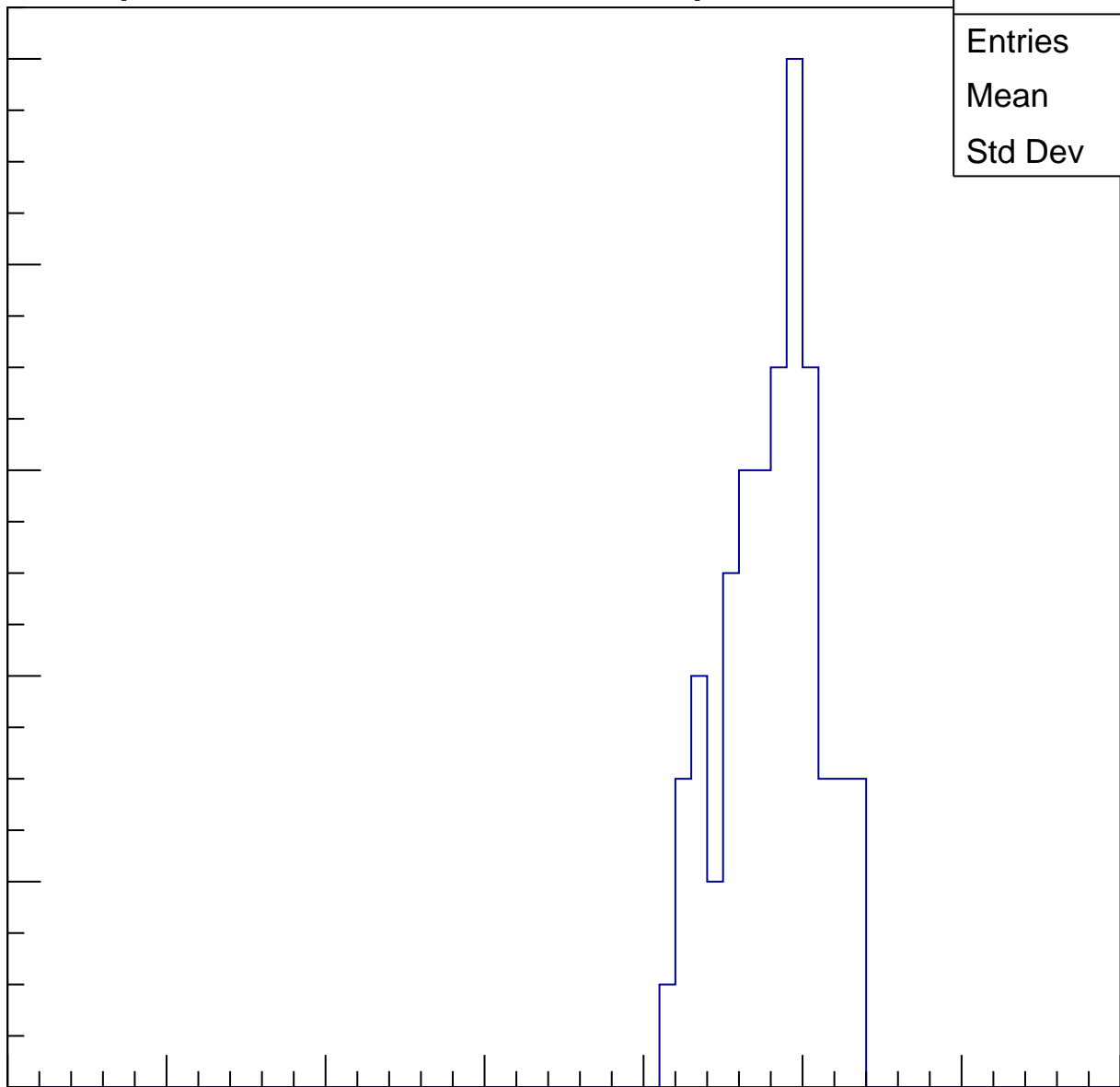
40

50

60

70

ampl



B1L002S, U7-ch121, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	53
Mean	53.25
Std Dev	2.907

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

1

2

3

4

5

6

7

8

9

10

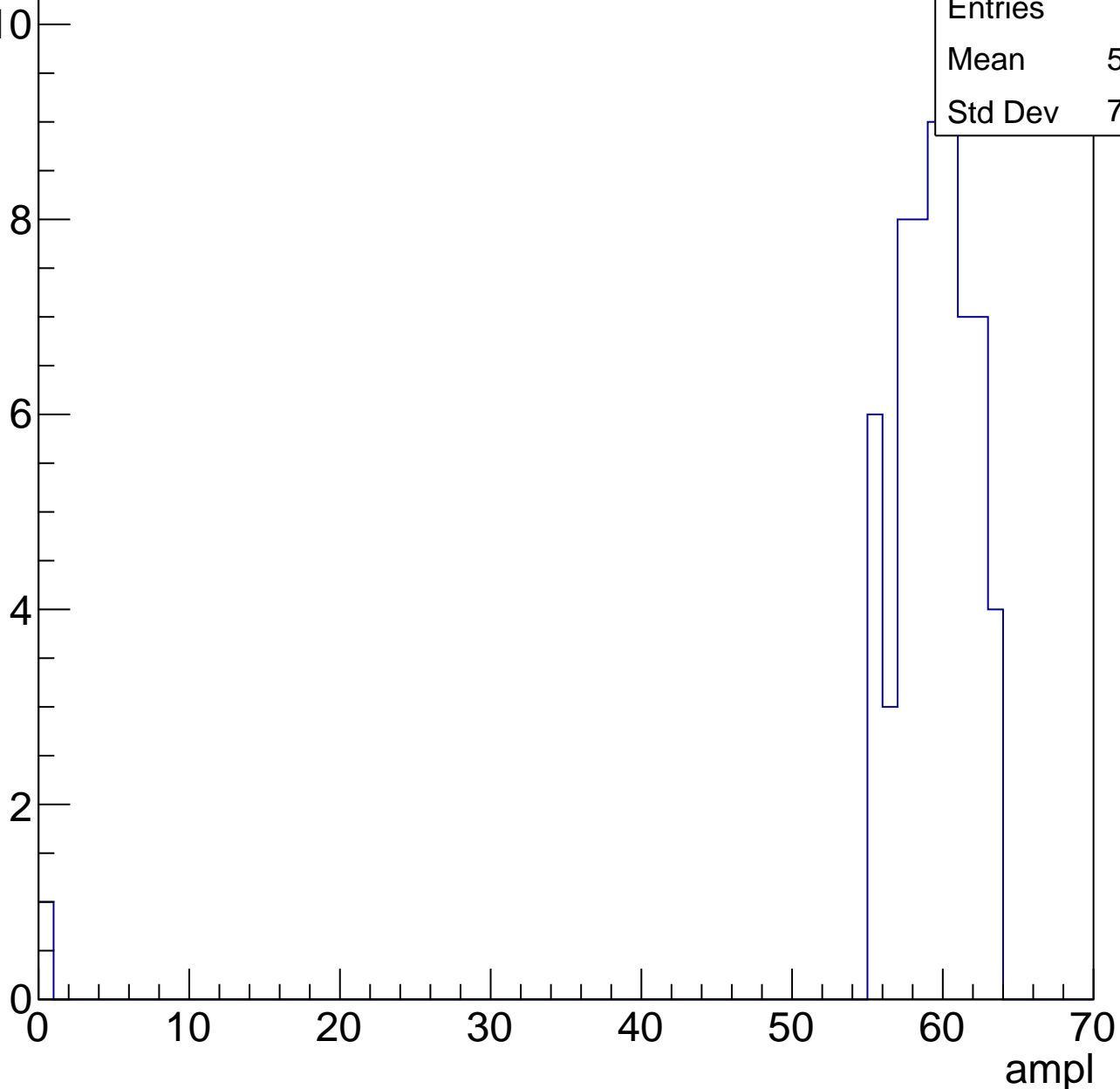
11

B1L002S, U7-ch121, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	63
Mean	58.13
Std Dev	7.726

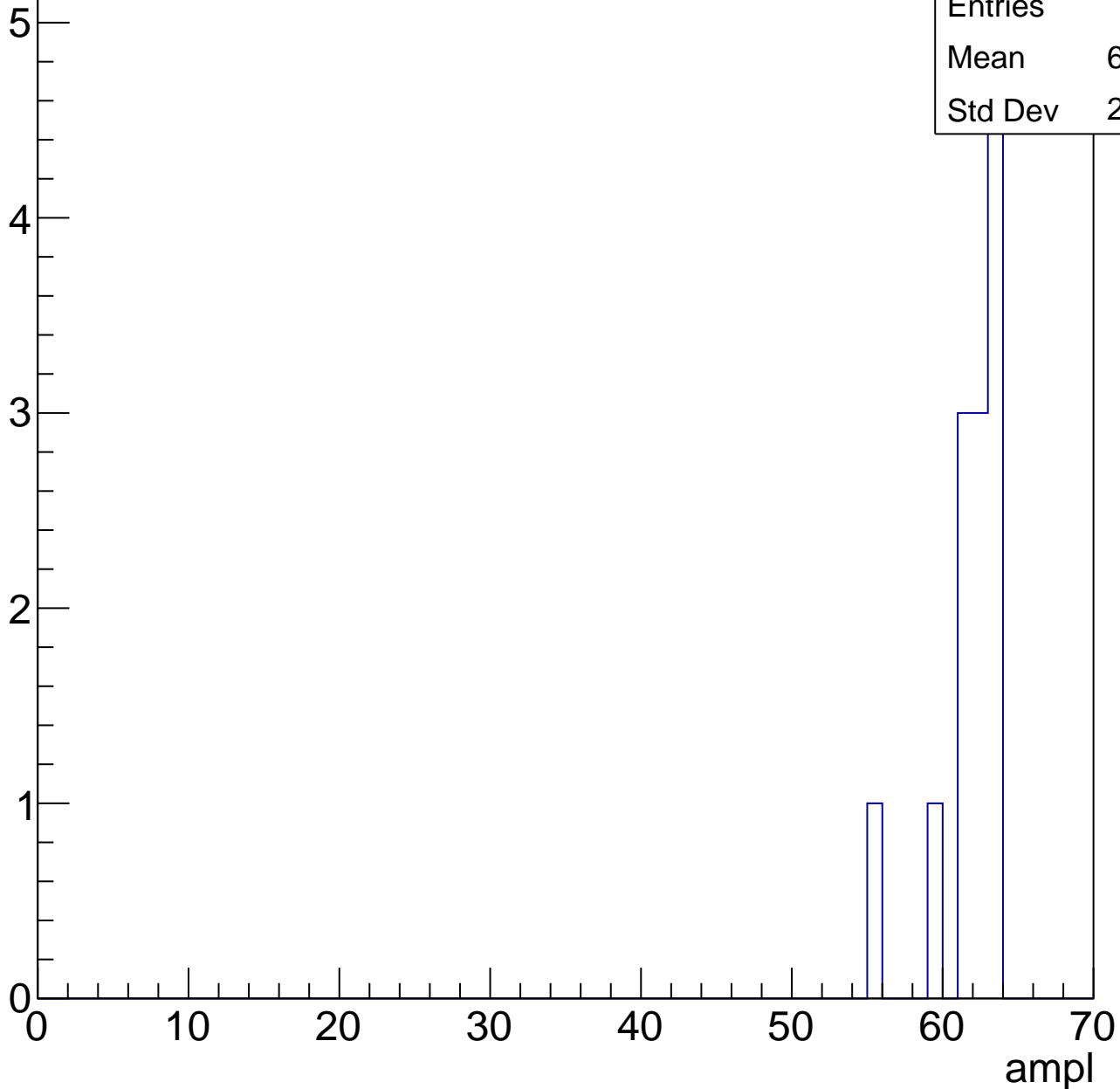


B1L002S, U7-ch121, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.38
Std Dev	2.168



B1L002S, U7-ch121, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

B1L002S, U7-ch122, adc0

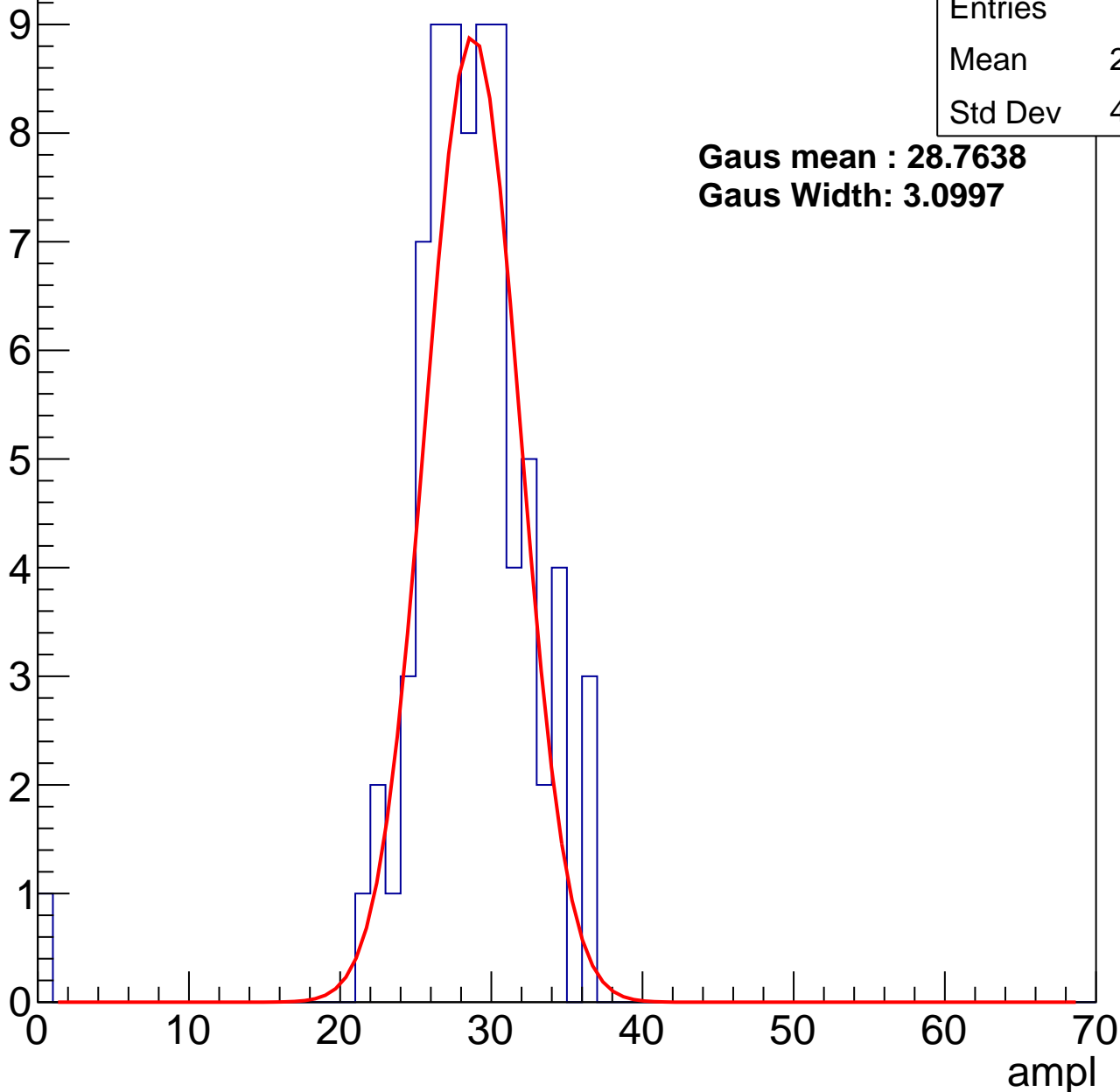
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	77
Mean	28.06
Std Dev	4.605

Gaus mean : 28.7638

Gaus Width: 3.0997



B1L002S, U7-ch122, adc1

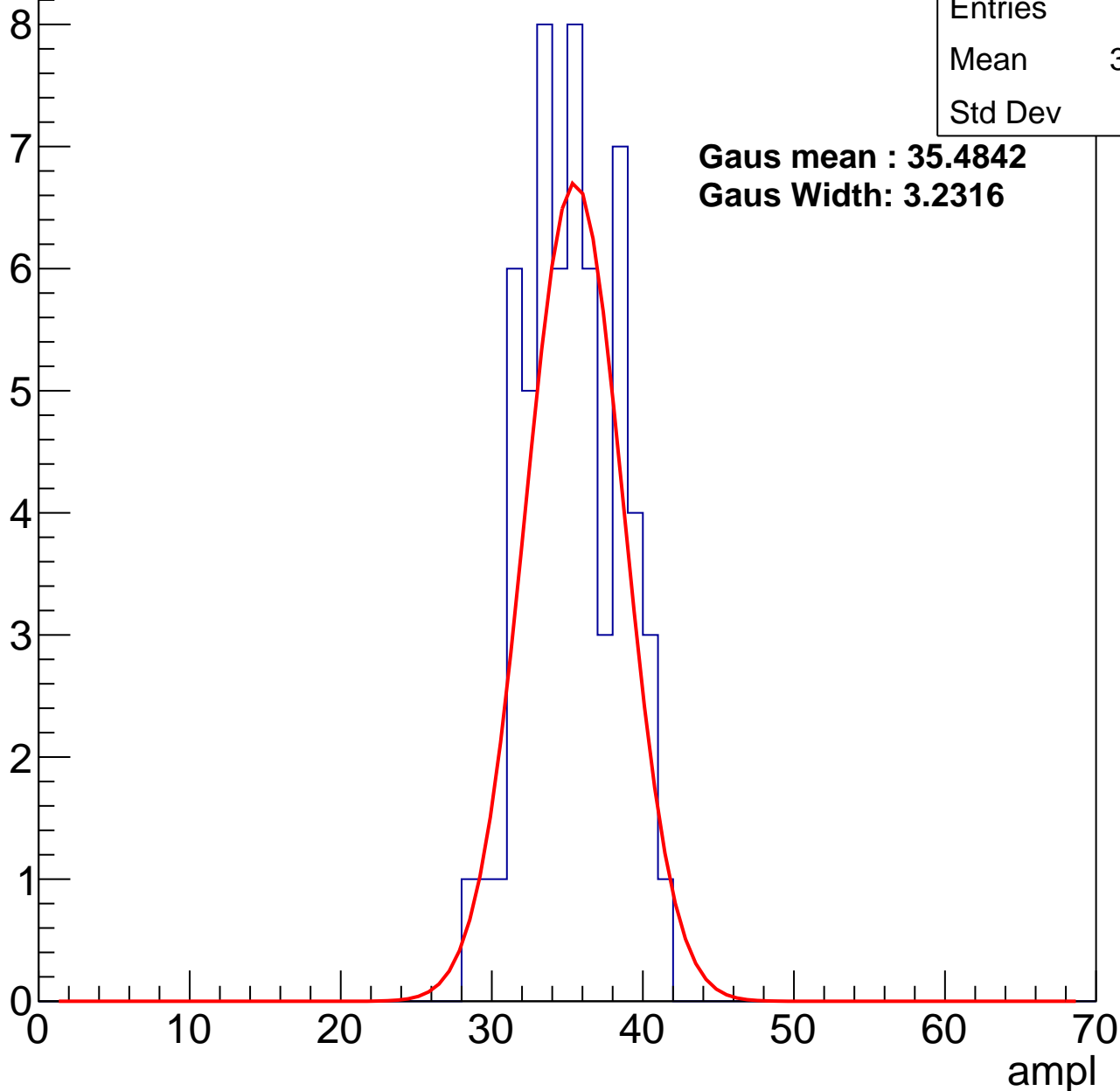
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.85
Std Dev	3.01

Gaus mean : 35.4842

Gaus Width: 3.2316



B1L002S, U7-ch122, adc2

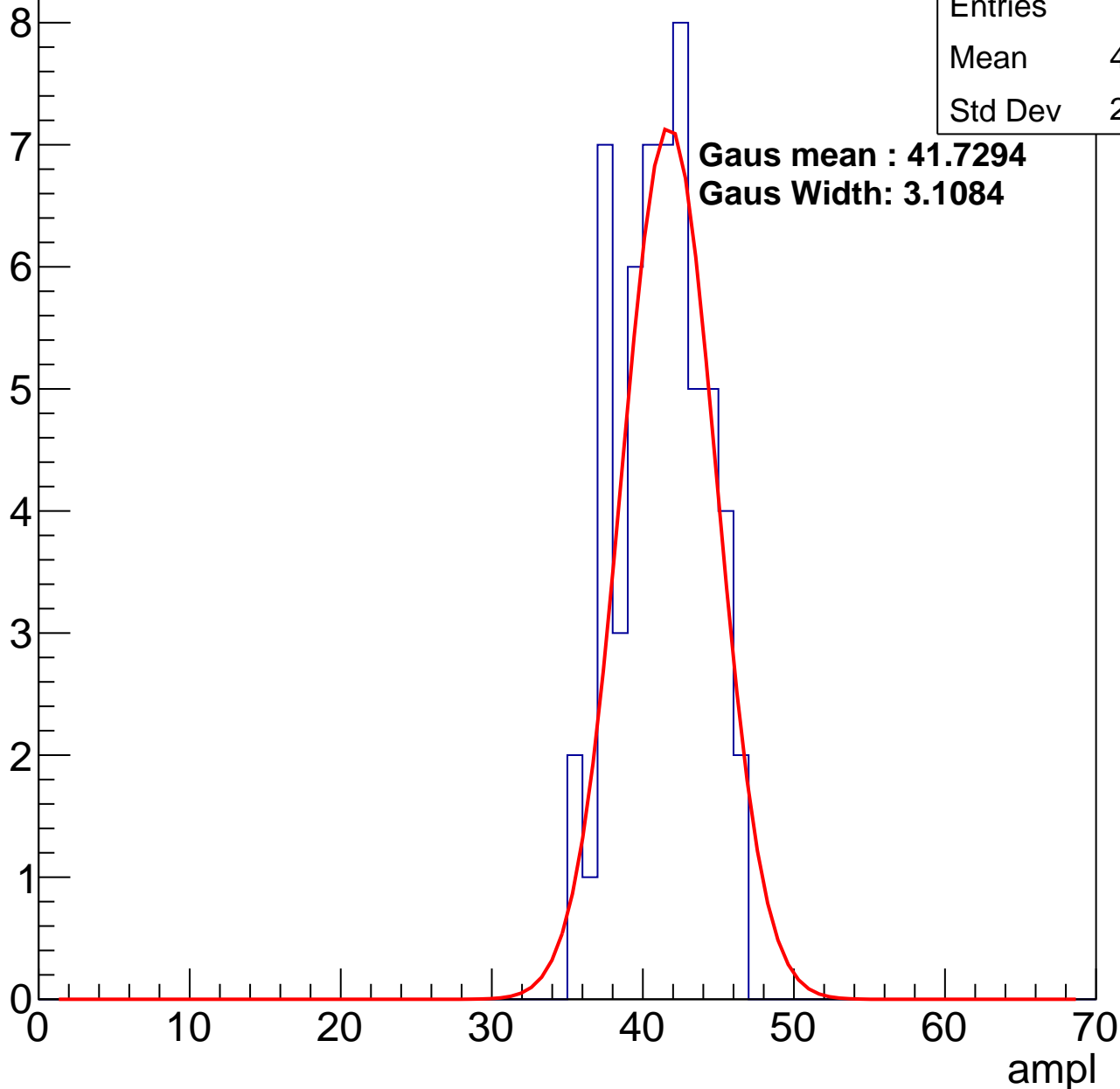
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.75
Std Dev	2.812

Gaus mean : 41.7294

Gaus Width: 3.1084

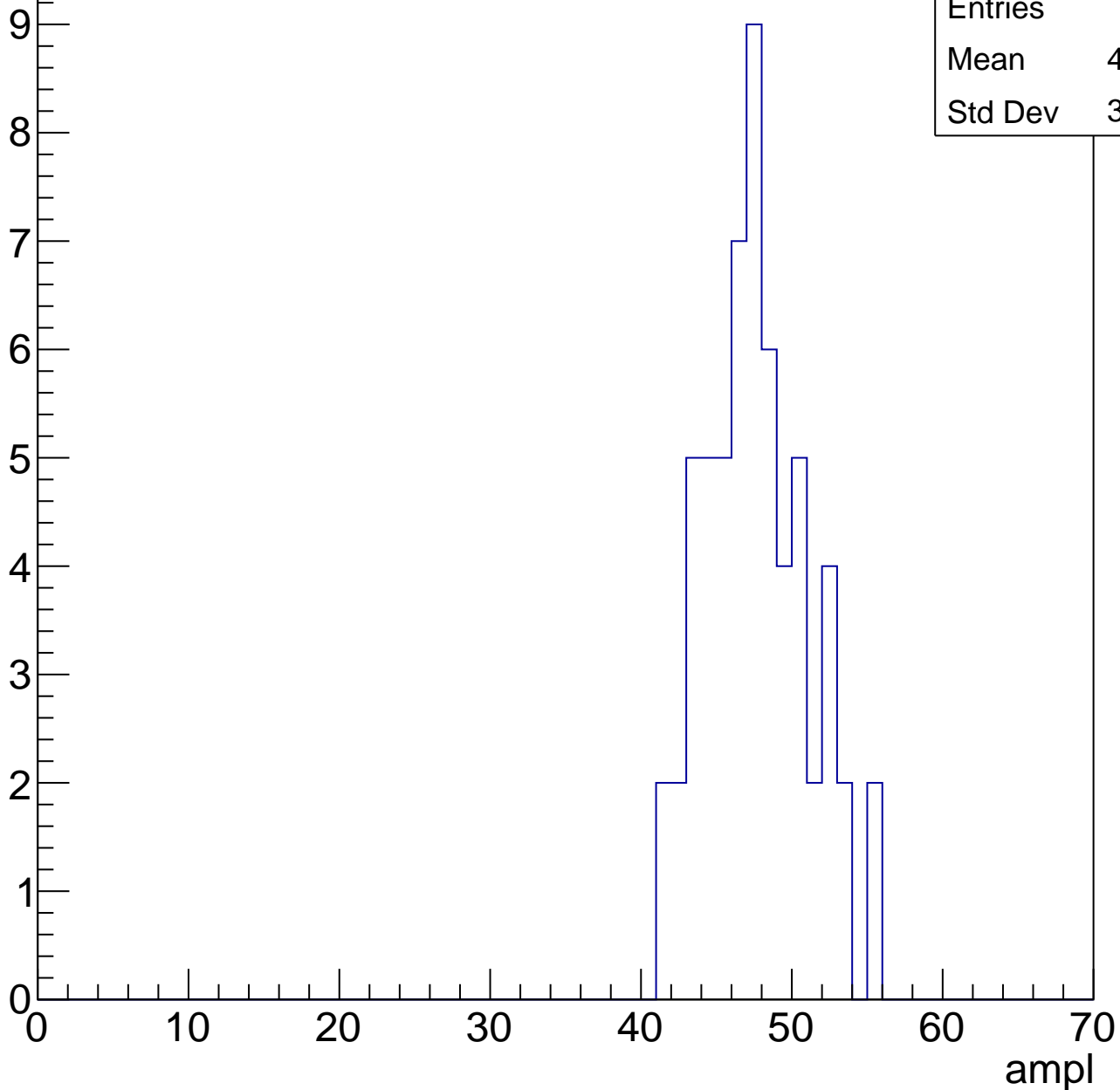


B1L002S, U7-ch122, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	60
Mean	47.18
Std Dev	3.344

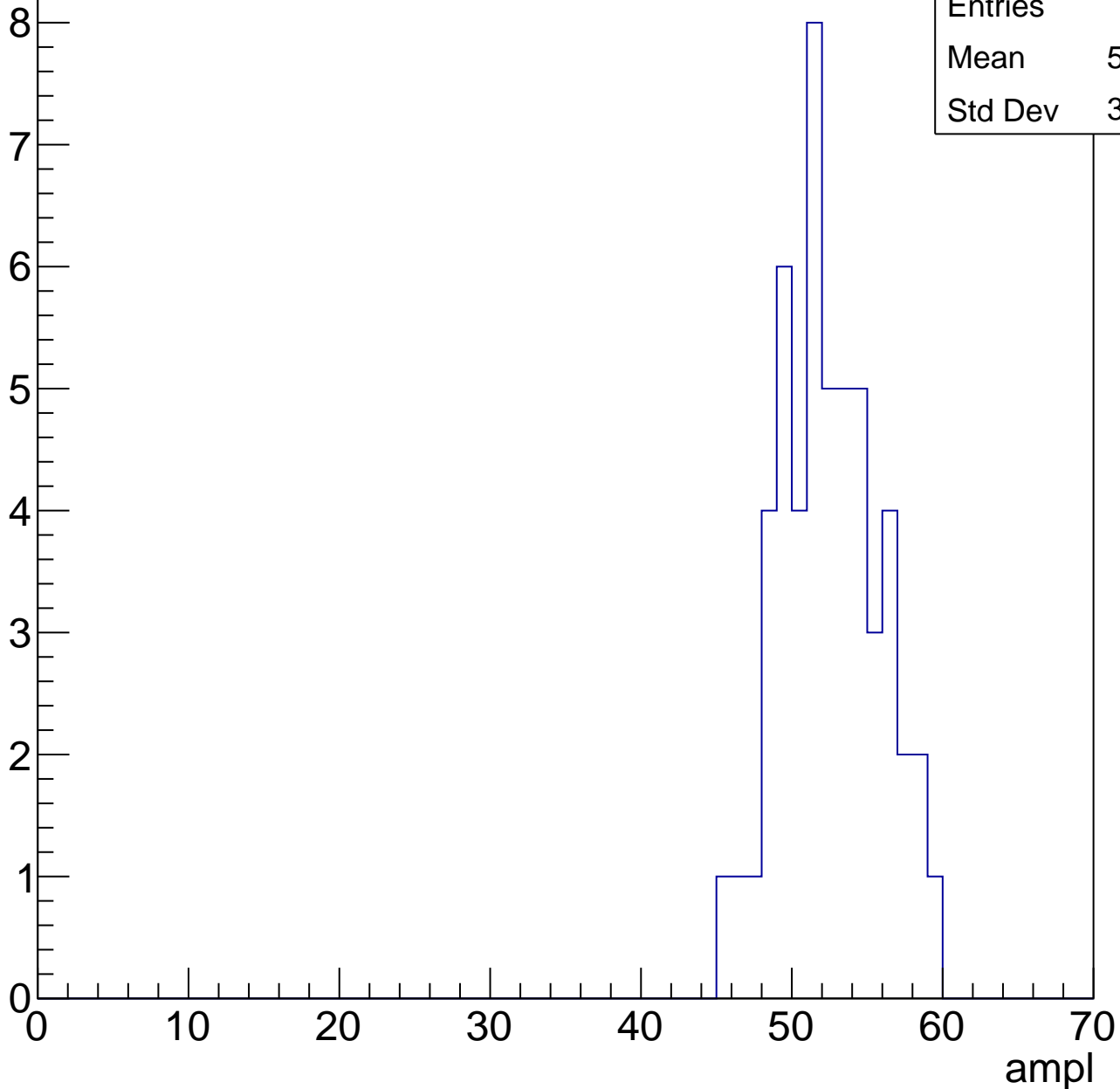


B1L002S, U7-ch122, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.02
Std Dev	3.219

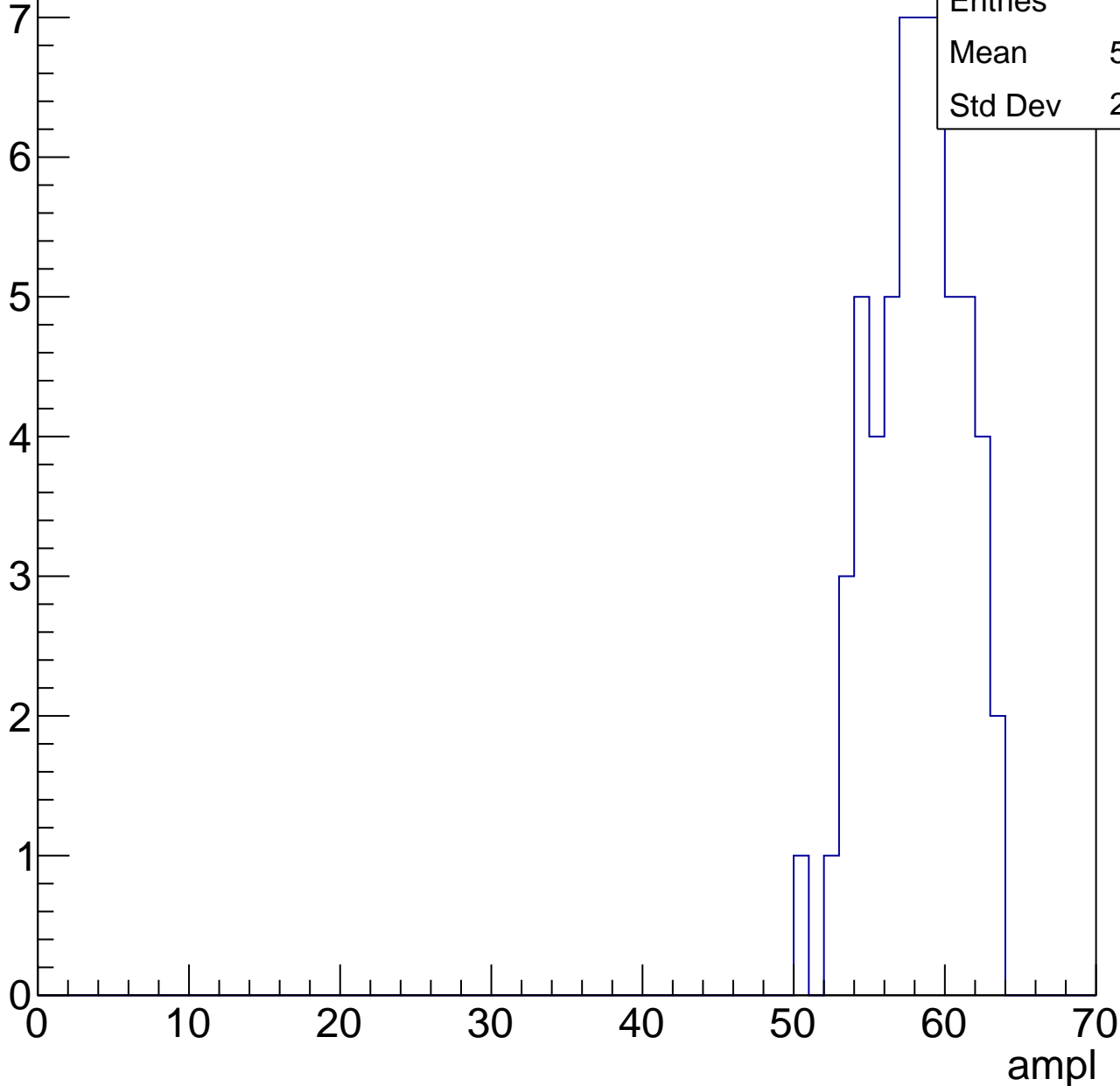


B1L002S, U7-ch122, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.64
Std Dev	2.979

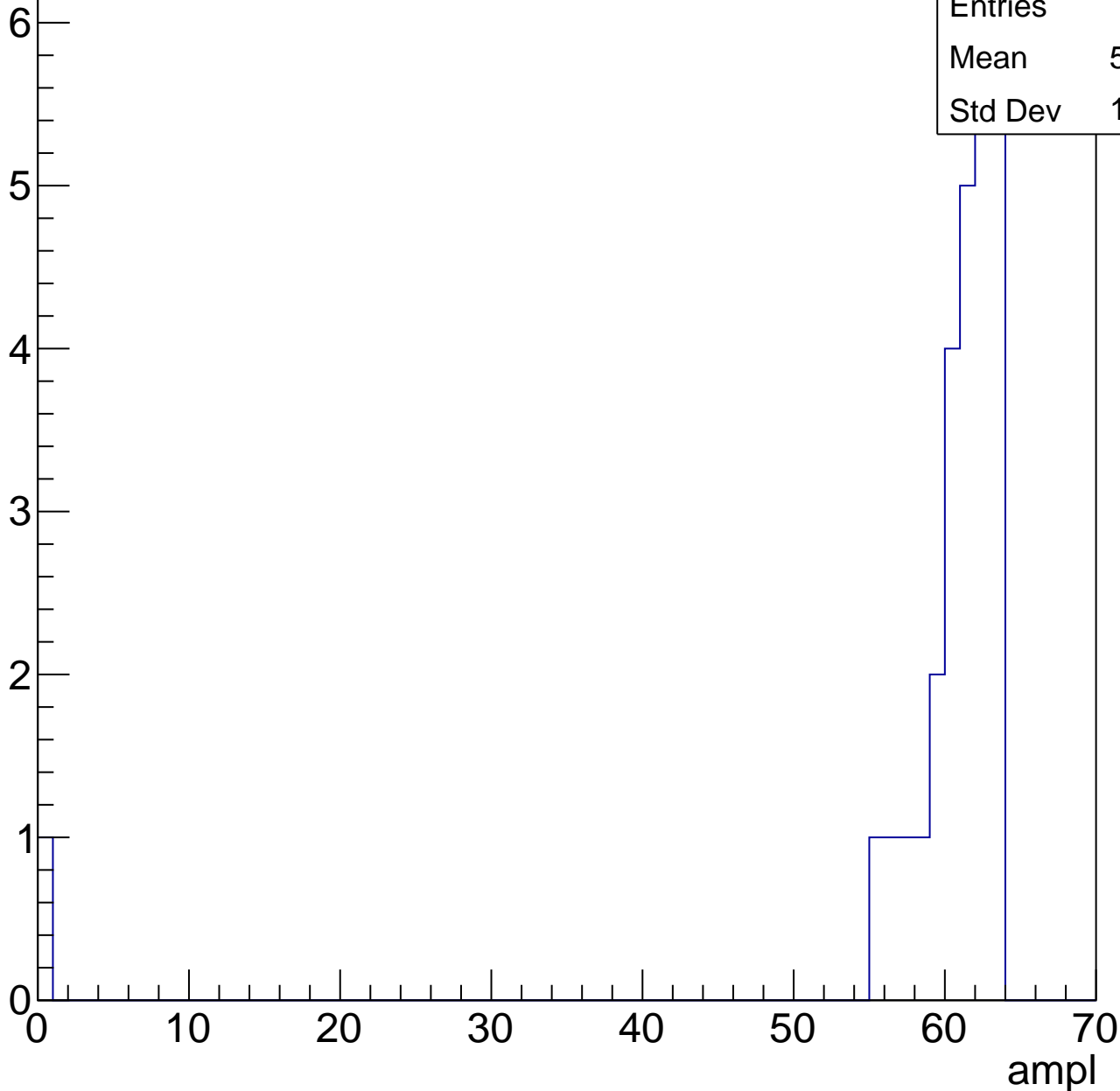


B1L002S, U7-ch122, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

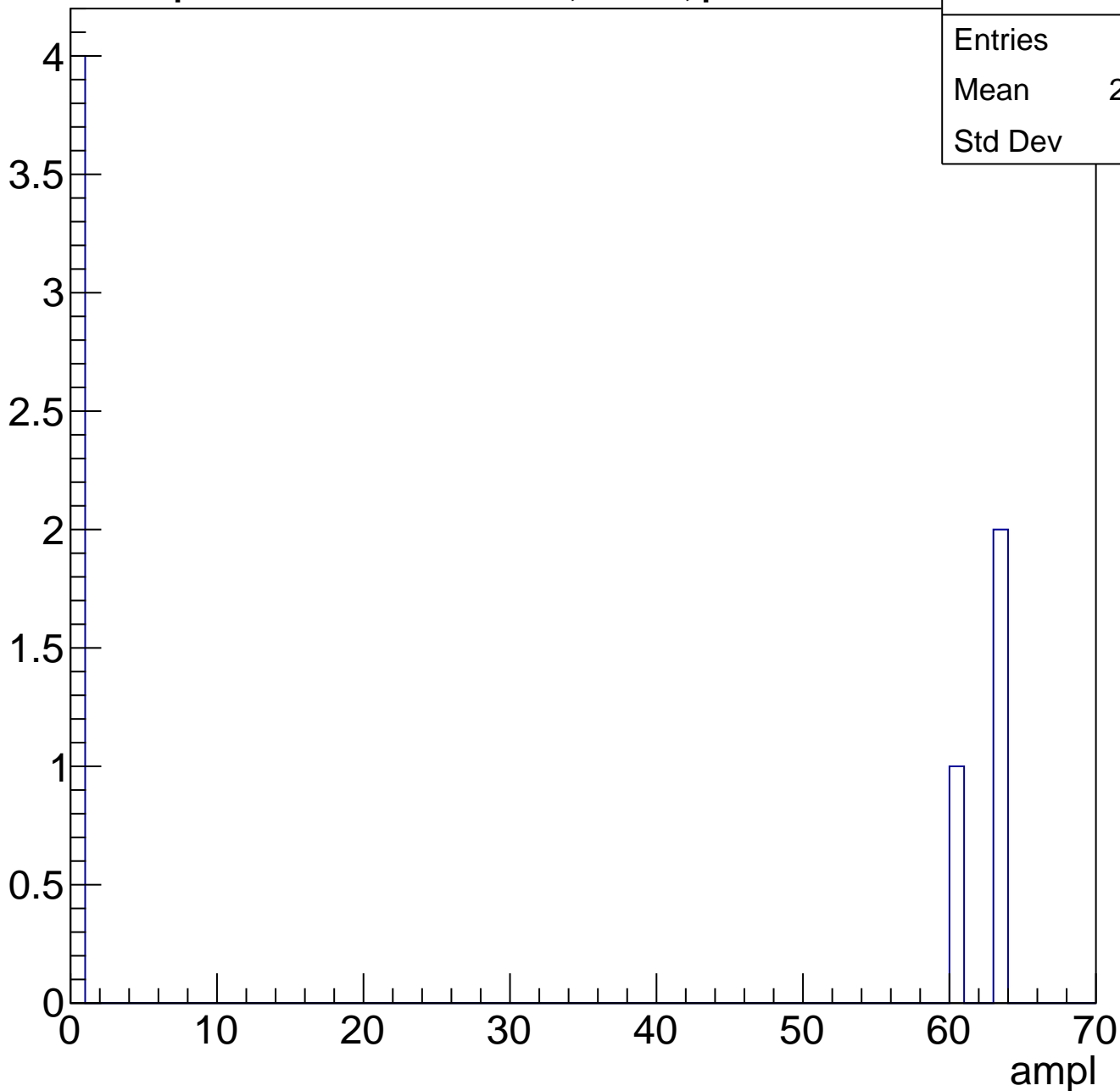
Entries	28
Mean	58.54
Std Dev	11.46



B1L002S, U7-ch122, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch123, adc0

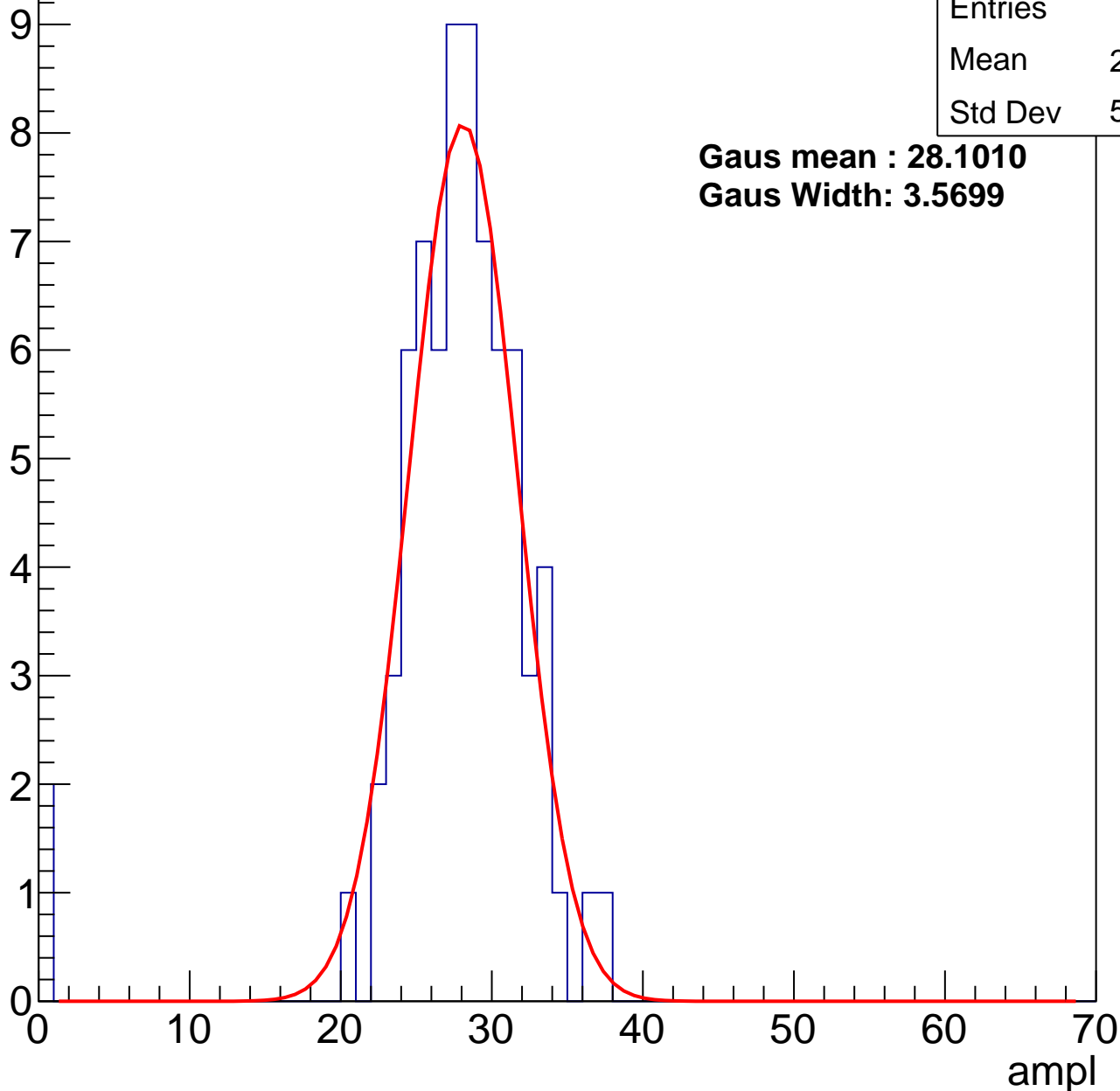
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	74
Mean	27.12
Std Dev	5.609

Gaus mean : 28.1010

Gaus Width: 3.5699



B1L002S, U7-ch123, adc1

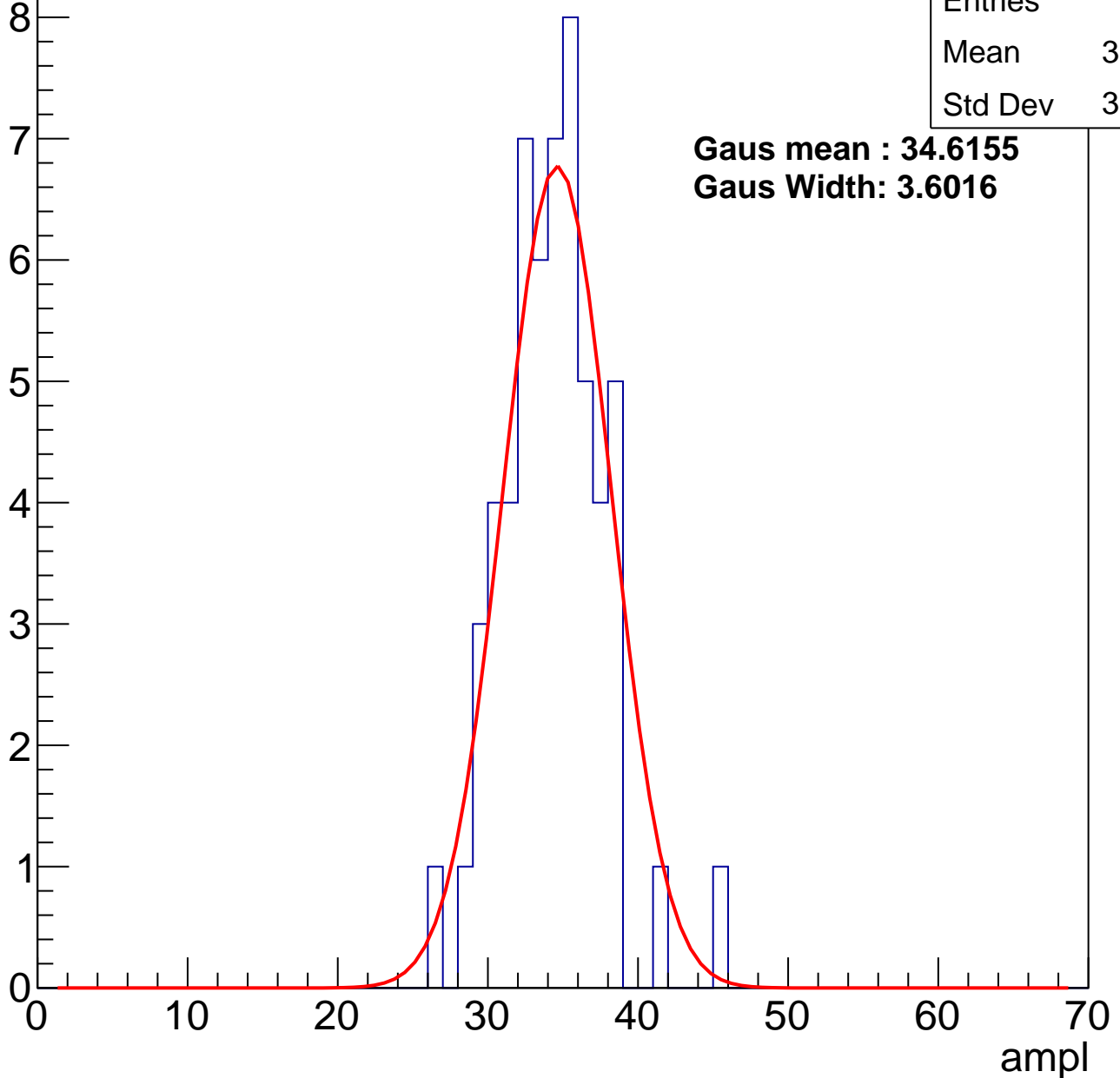
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.84
Std Dev	3.302

Gaus mean : 34.6155

Gaus Width: 3.6016



B1L002S, U7-ch123, adc2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	62
Mean	39.89
Std Dev	2.795

Gaus mean : 40.3766

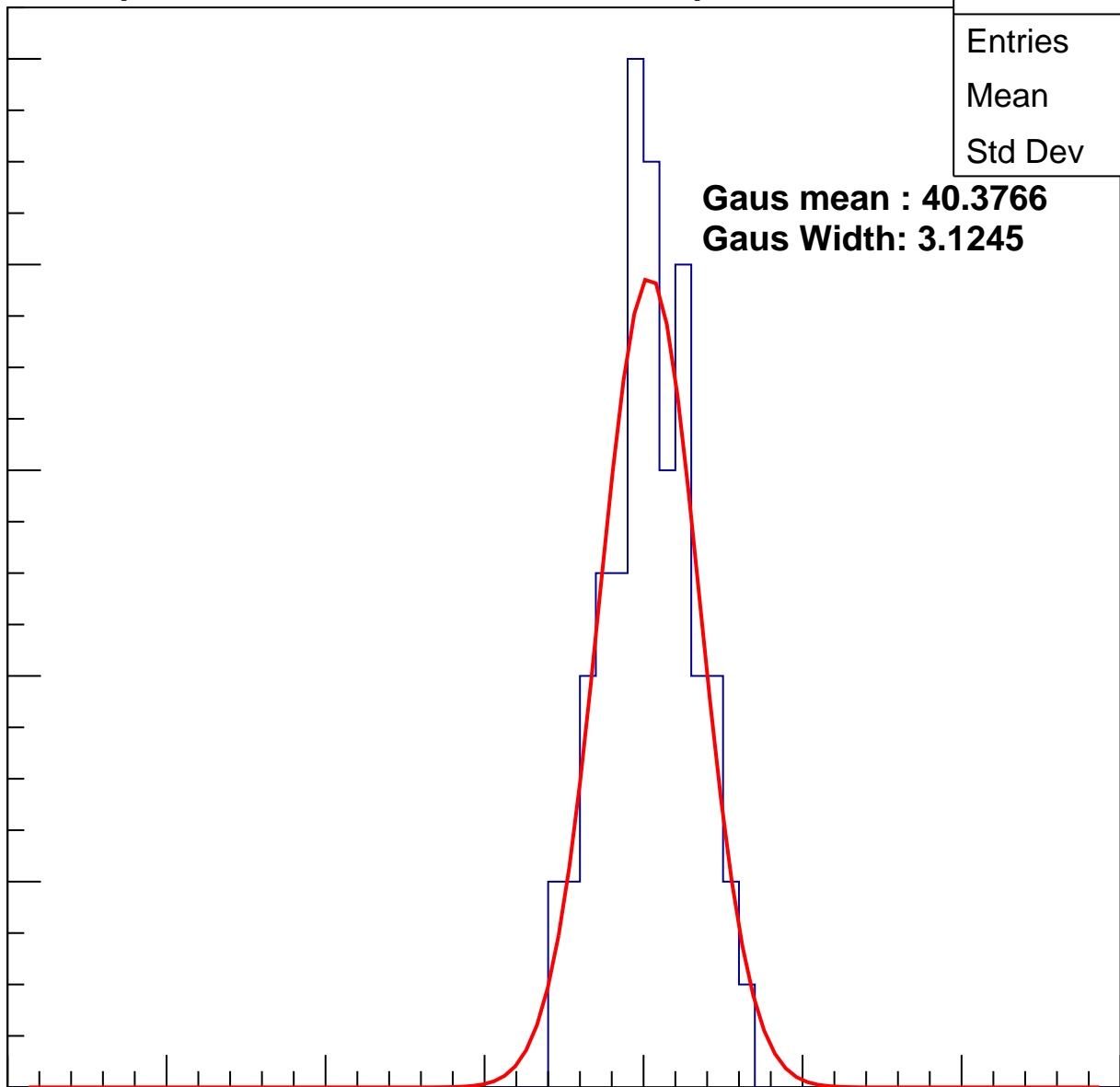
Gaus Width: 3.1245

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

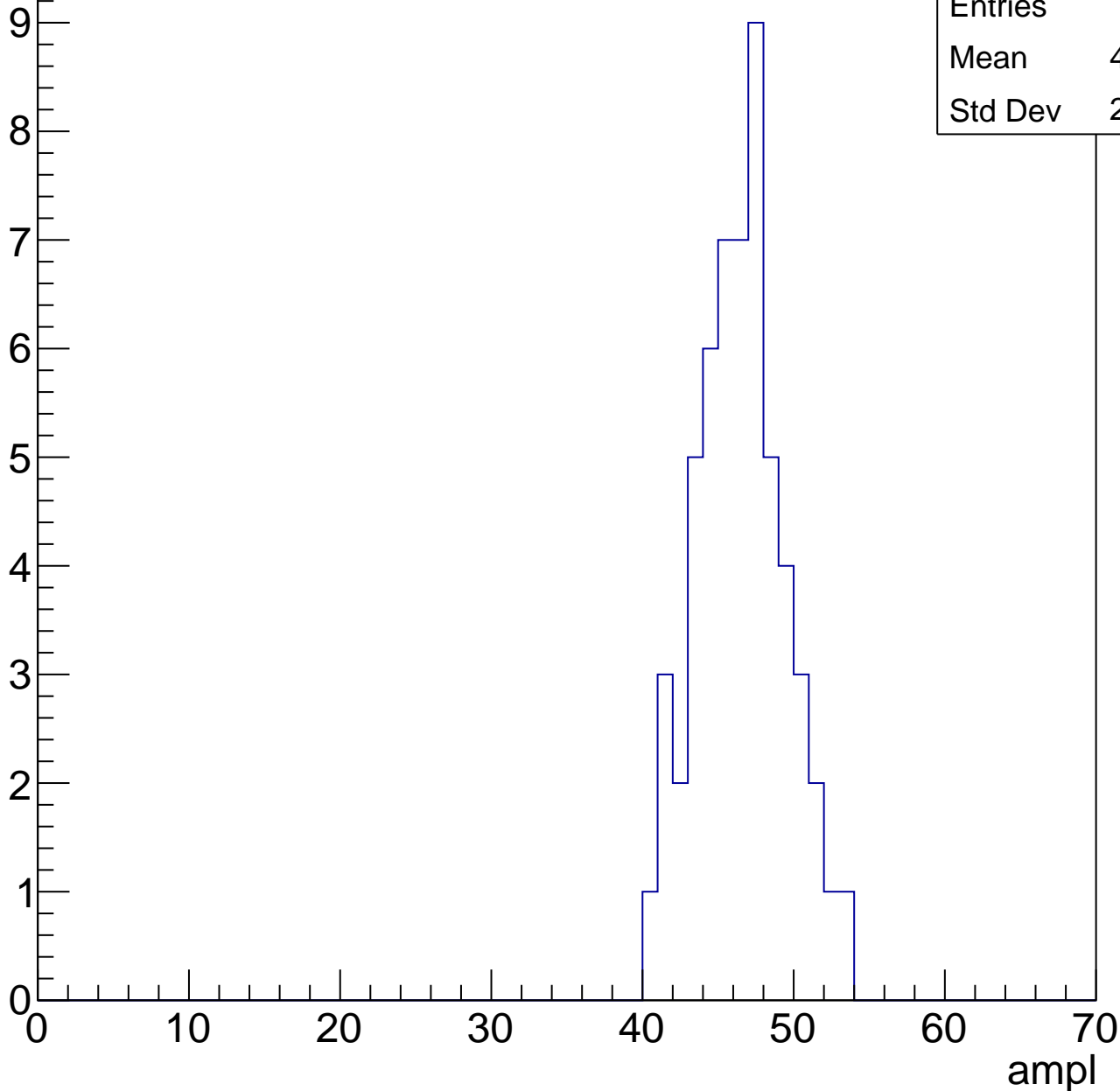


B1L002S, U7-ch123, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	46.05
Std Dev	2.887

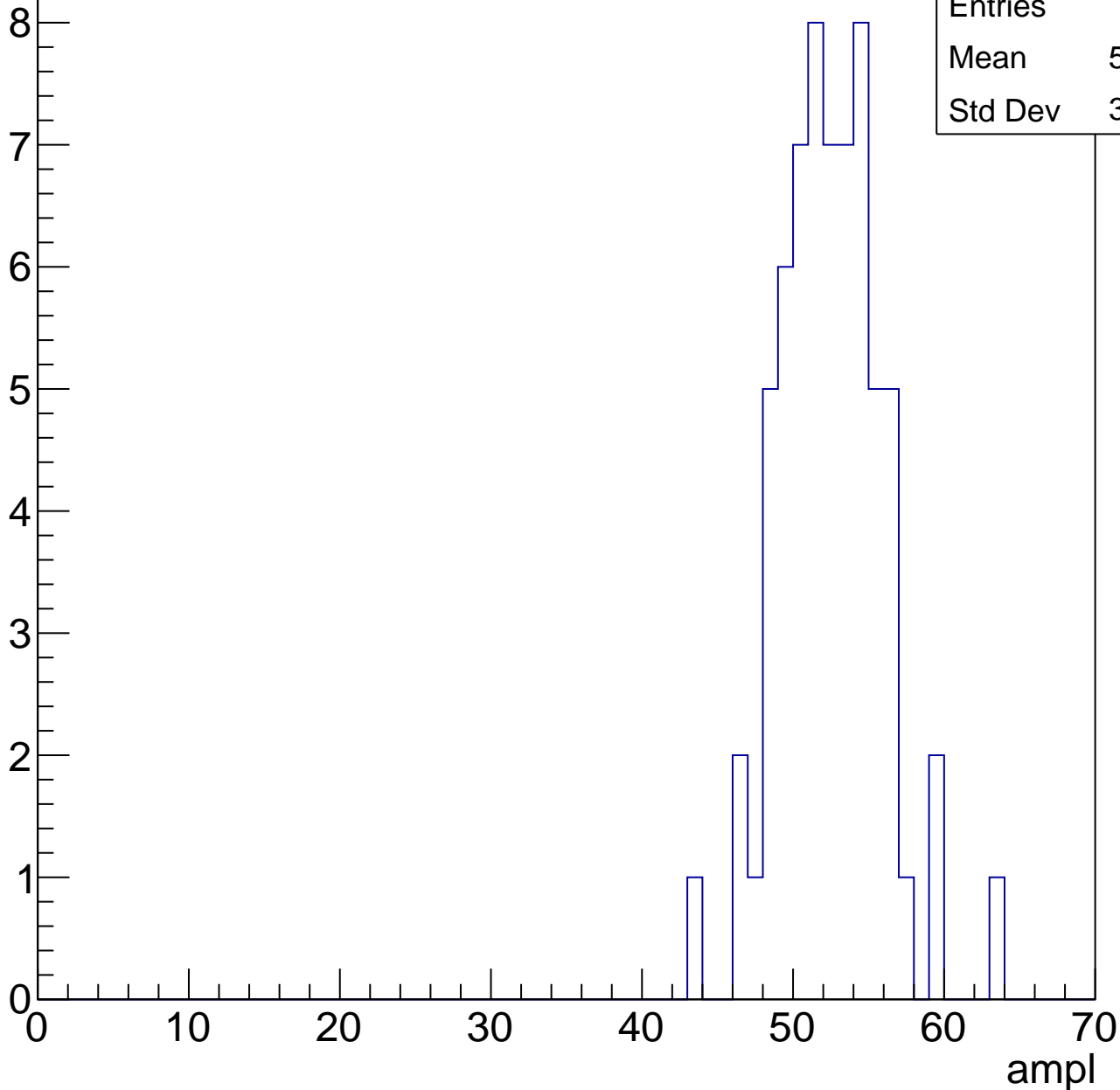


B1L002S, U7-ch123, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	66
Mean	52.03
Std Dev	3.384

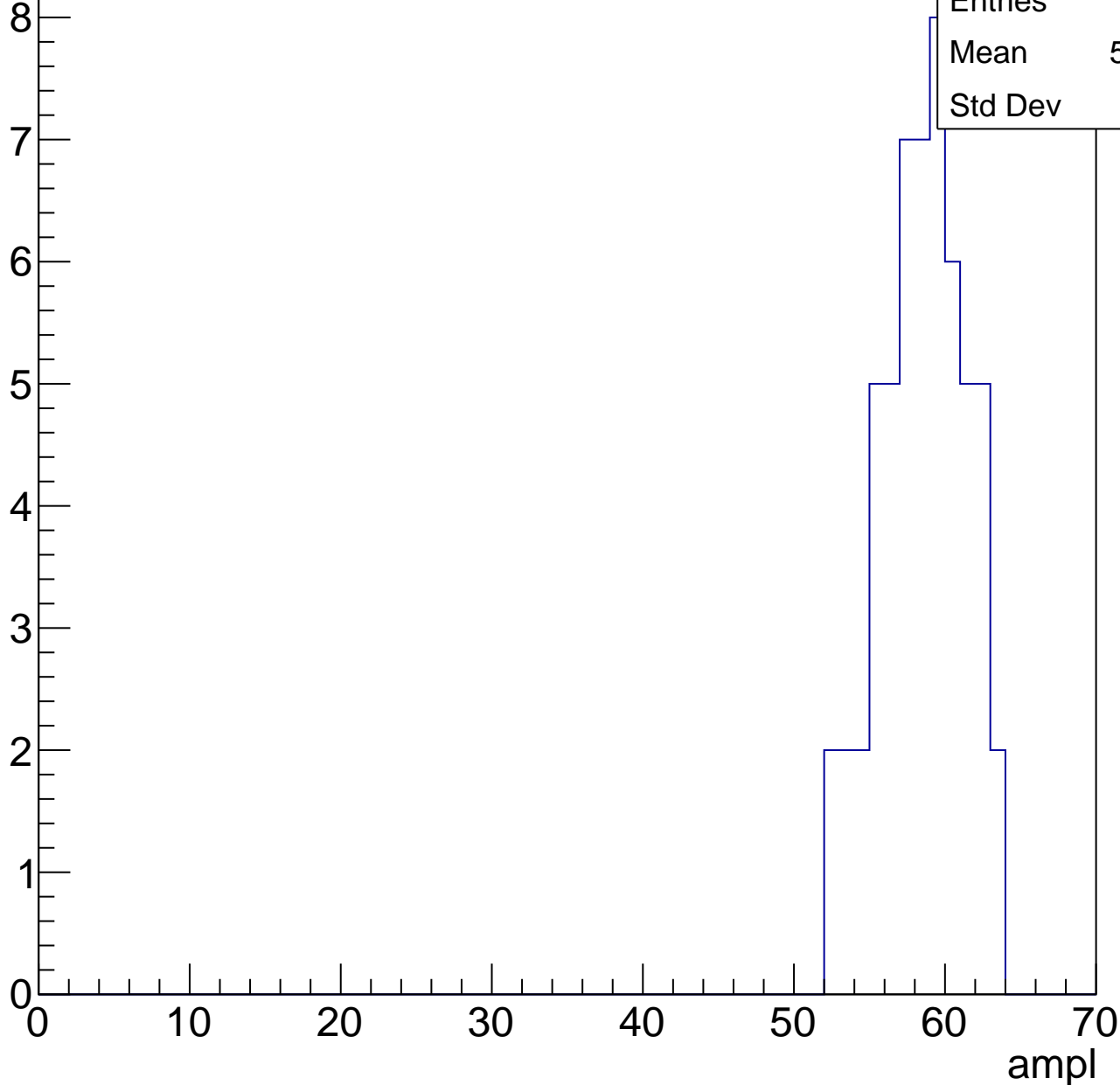


B1L002S, U7-ch123, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	56
Mean	58.05
Std Dev	2.78

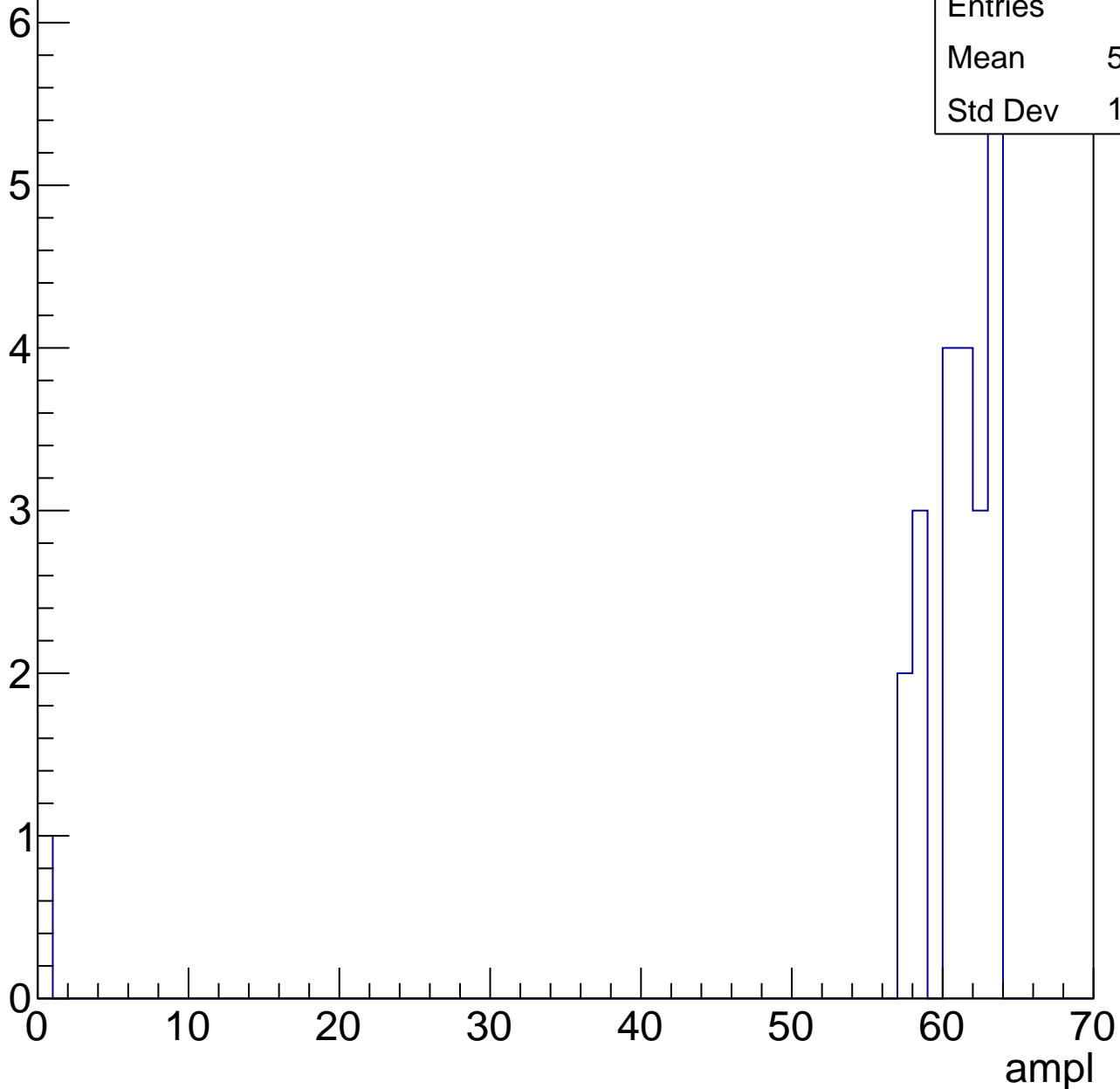


B1L002S, U7-ch123, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

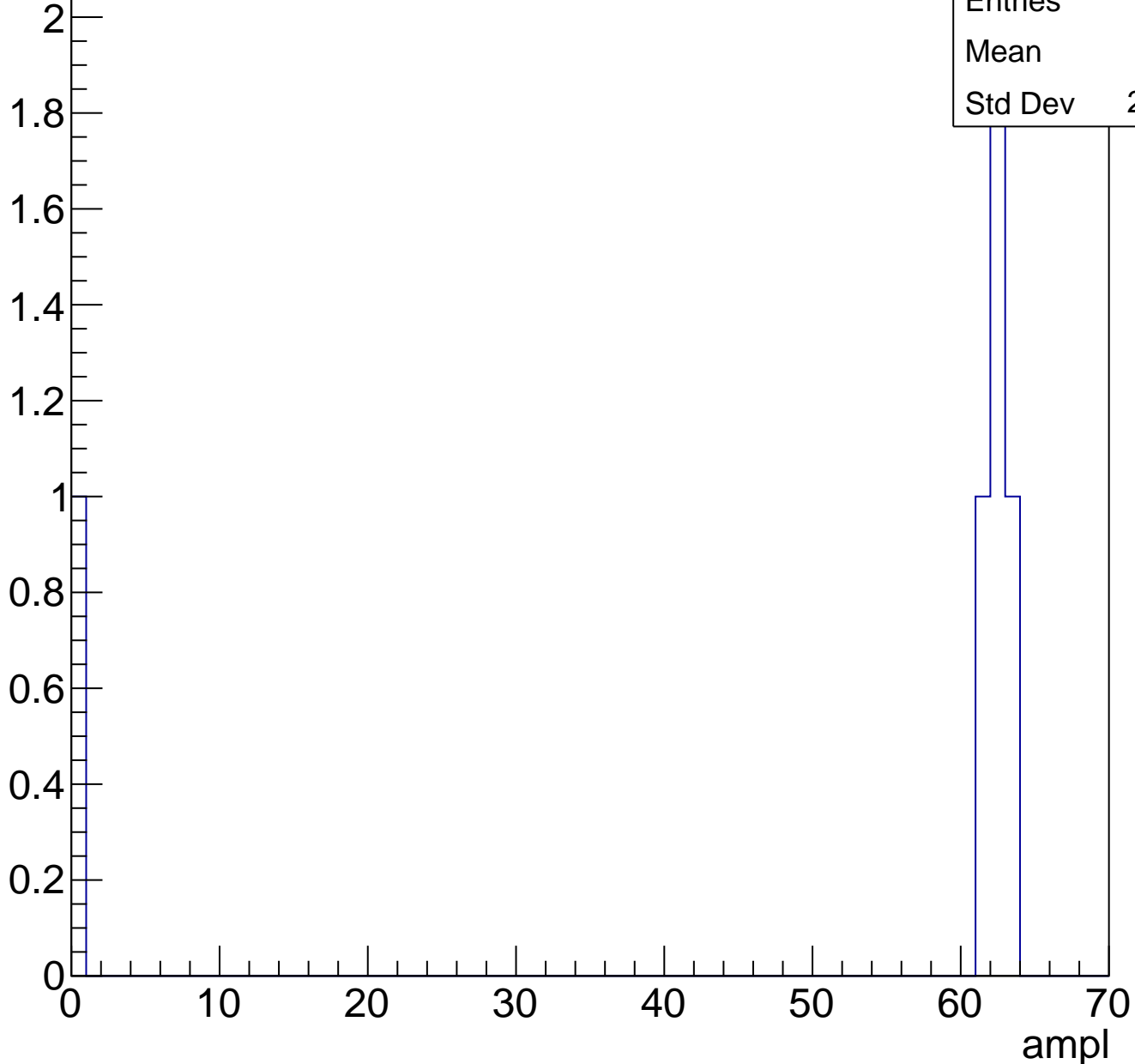
Entries	23
Mean	58.09
Std Dev	12.54



B1L002S, U7-ch123, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B1L002S, U7-ch124, adc0

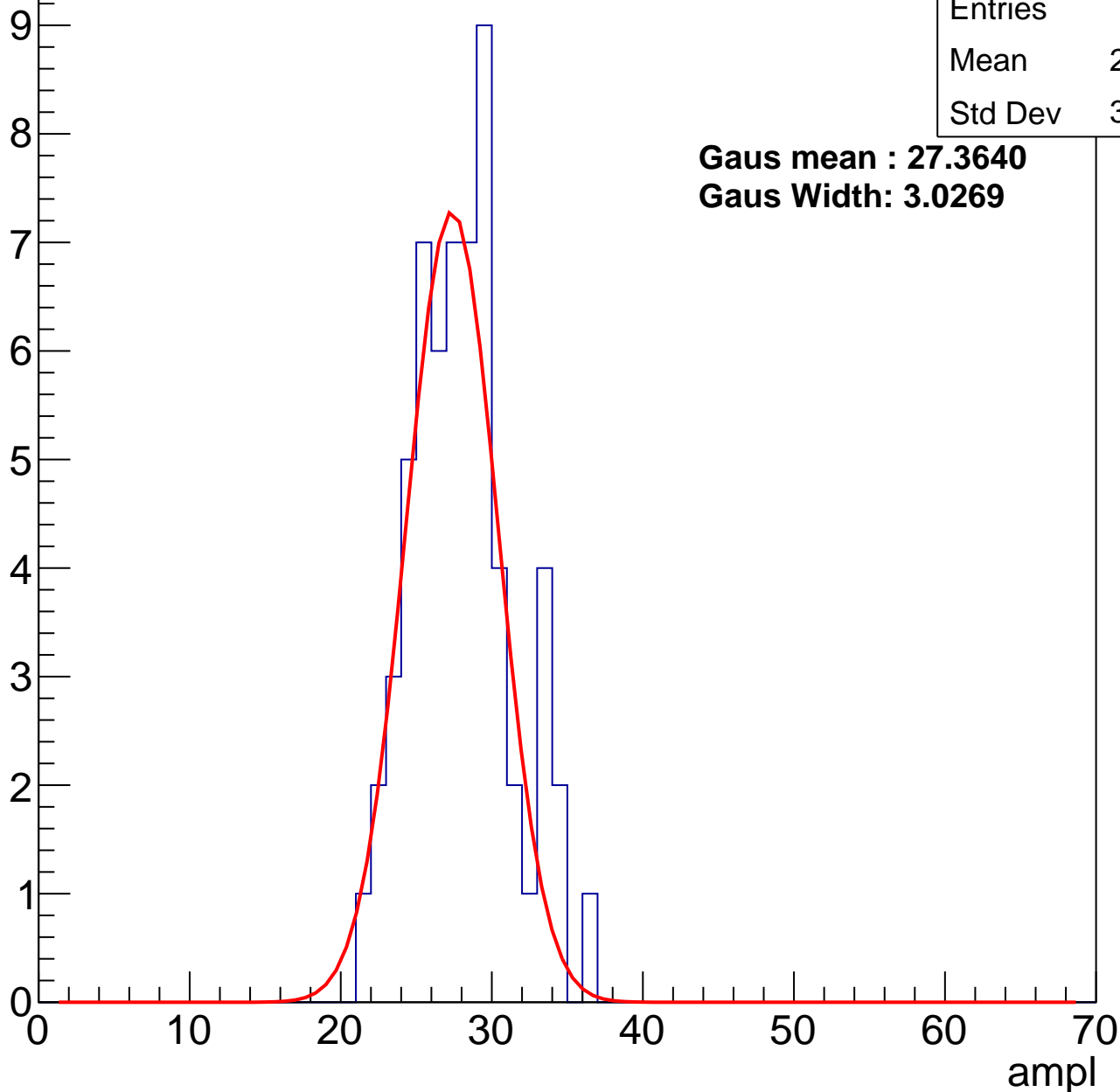
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.56
Std Dev	3.272

Gaus mean : 27.3640

Gaus Width: 3.0269



B1L002S, U7-ch124, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	76
Mean	34.21
Std Dev	3.537

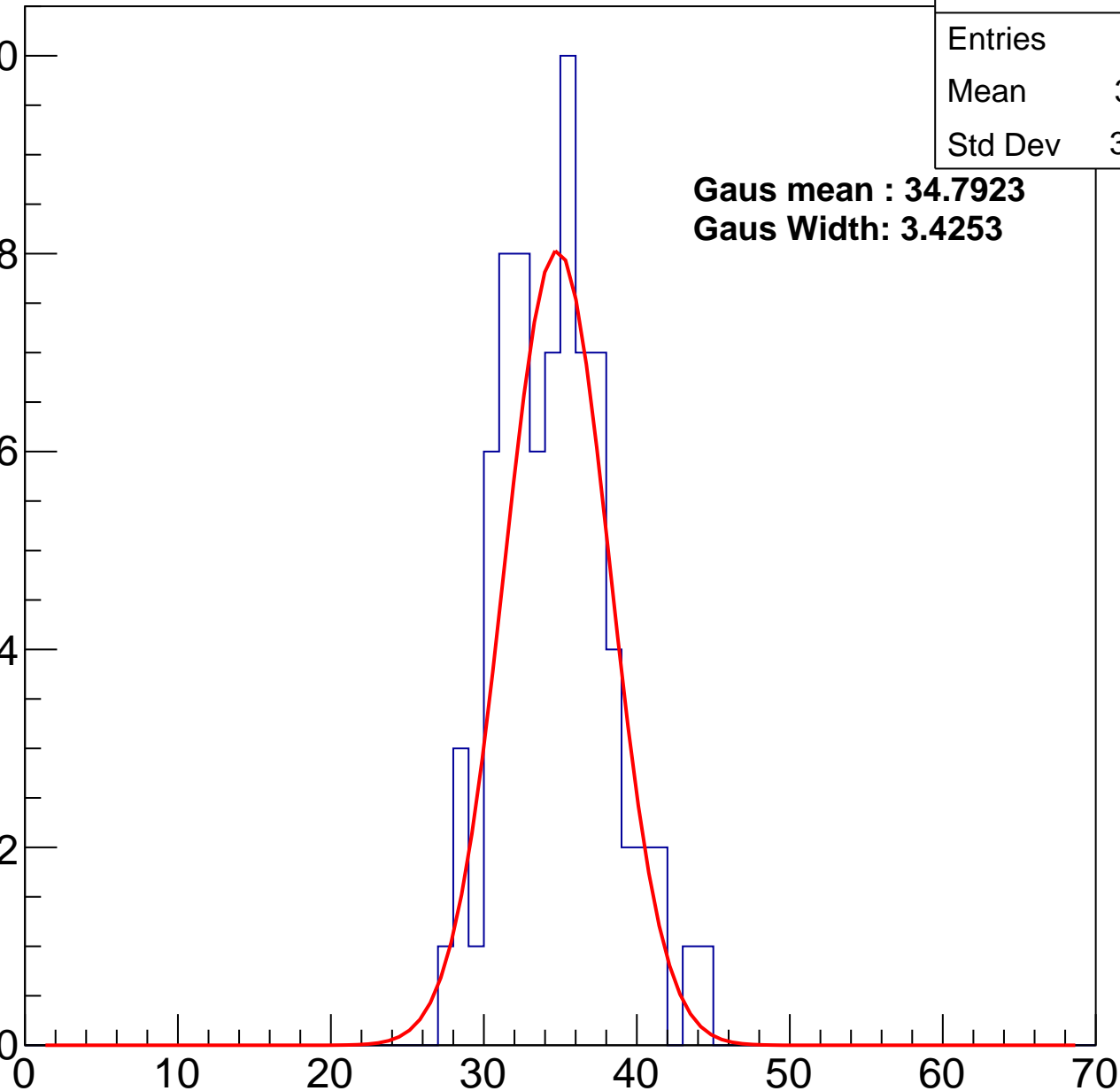
Gaus mean : 34.7923

Gaus Width: 3.4253

Entry

10
8
6
4
2
0

ampl



B1L002S, U7-ch124, adc2

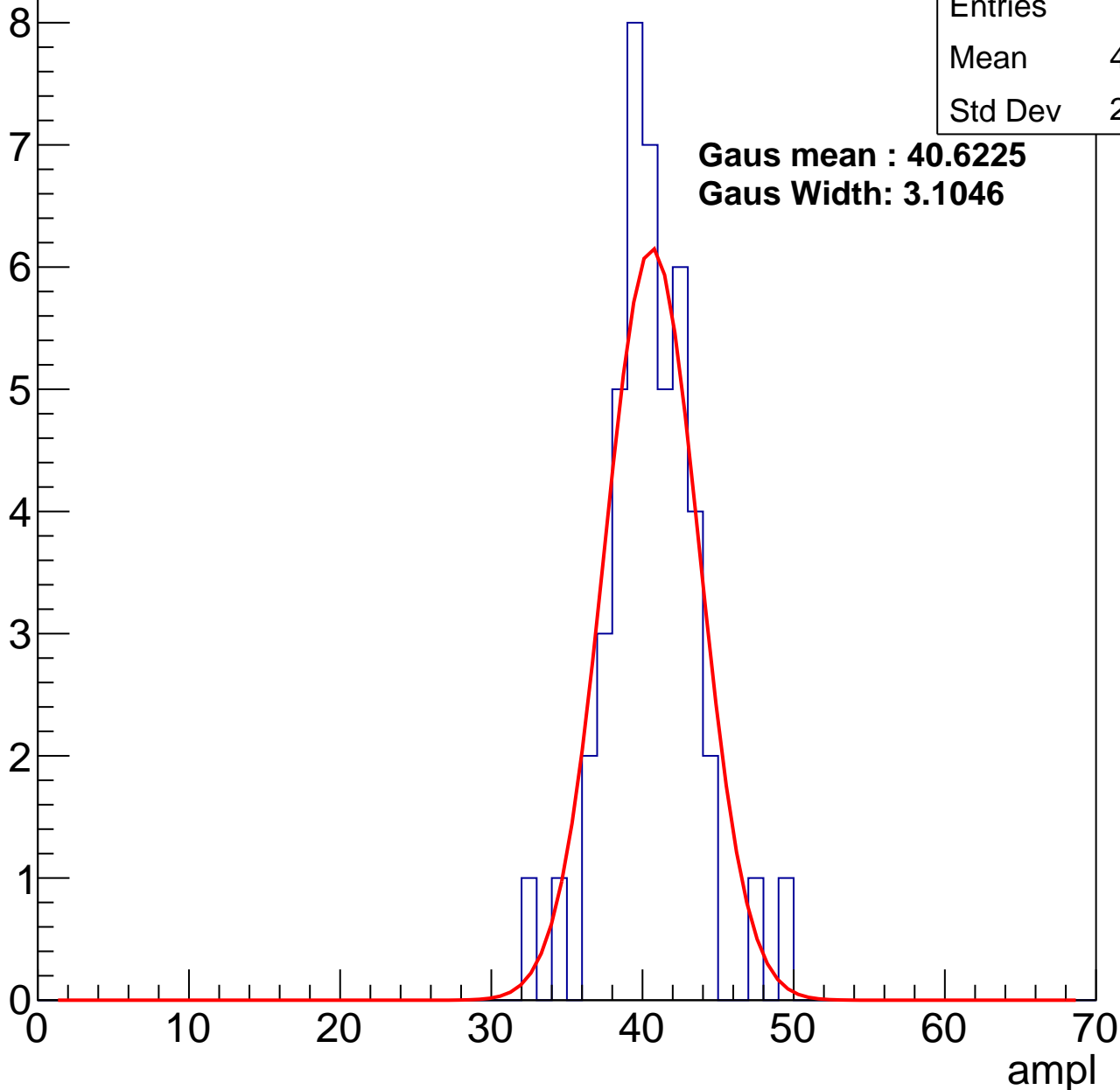
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	46
Mean	40.09
Std Dev	2.999

Gaus mean : 40.6225

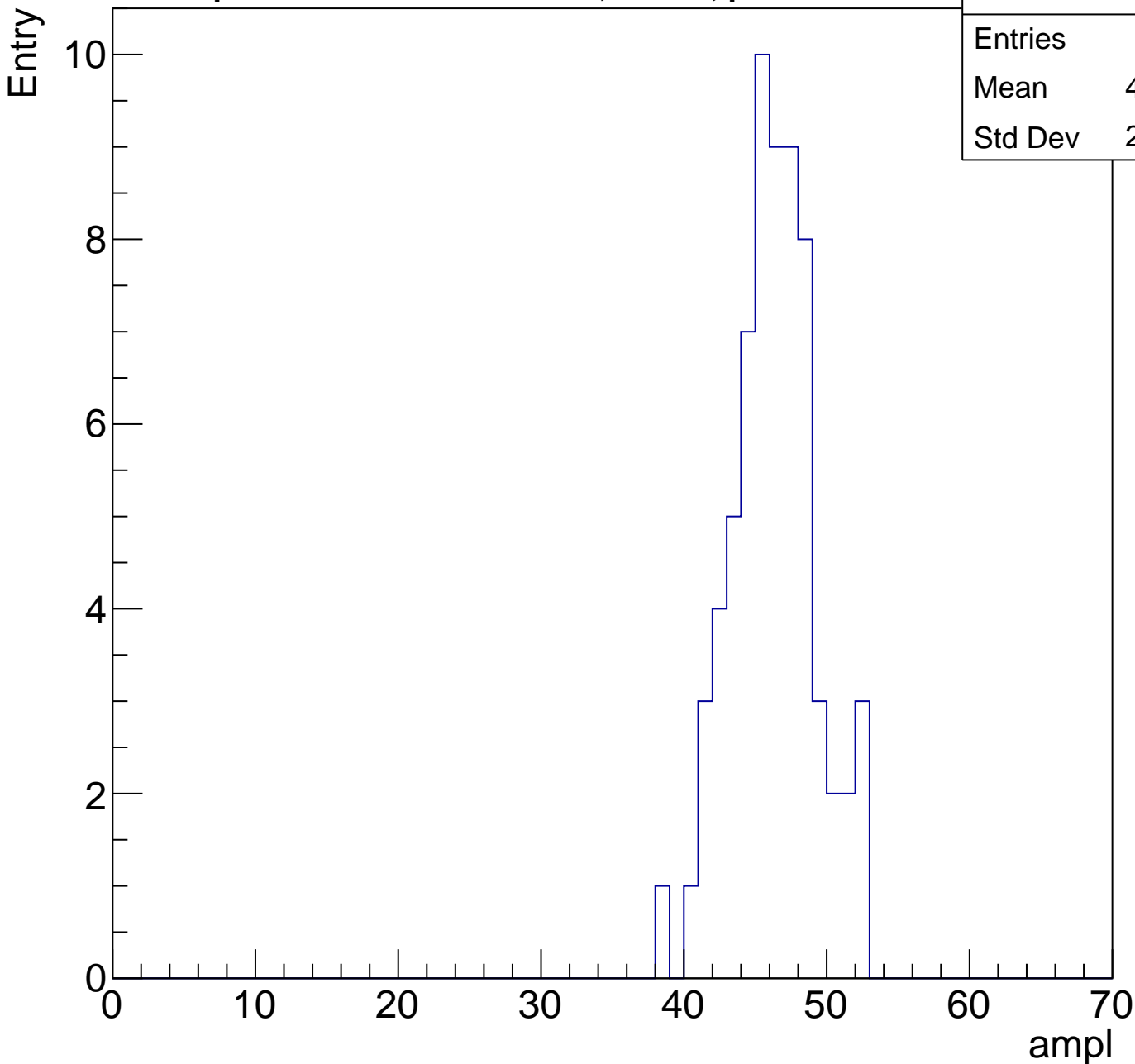
Gaus Width: 3.1046



B1L002S, U7-ch124, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	67
Mean	45.79
Std Dev	2.935

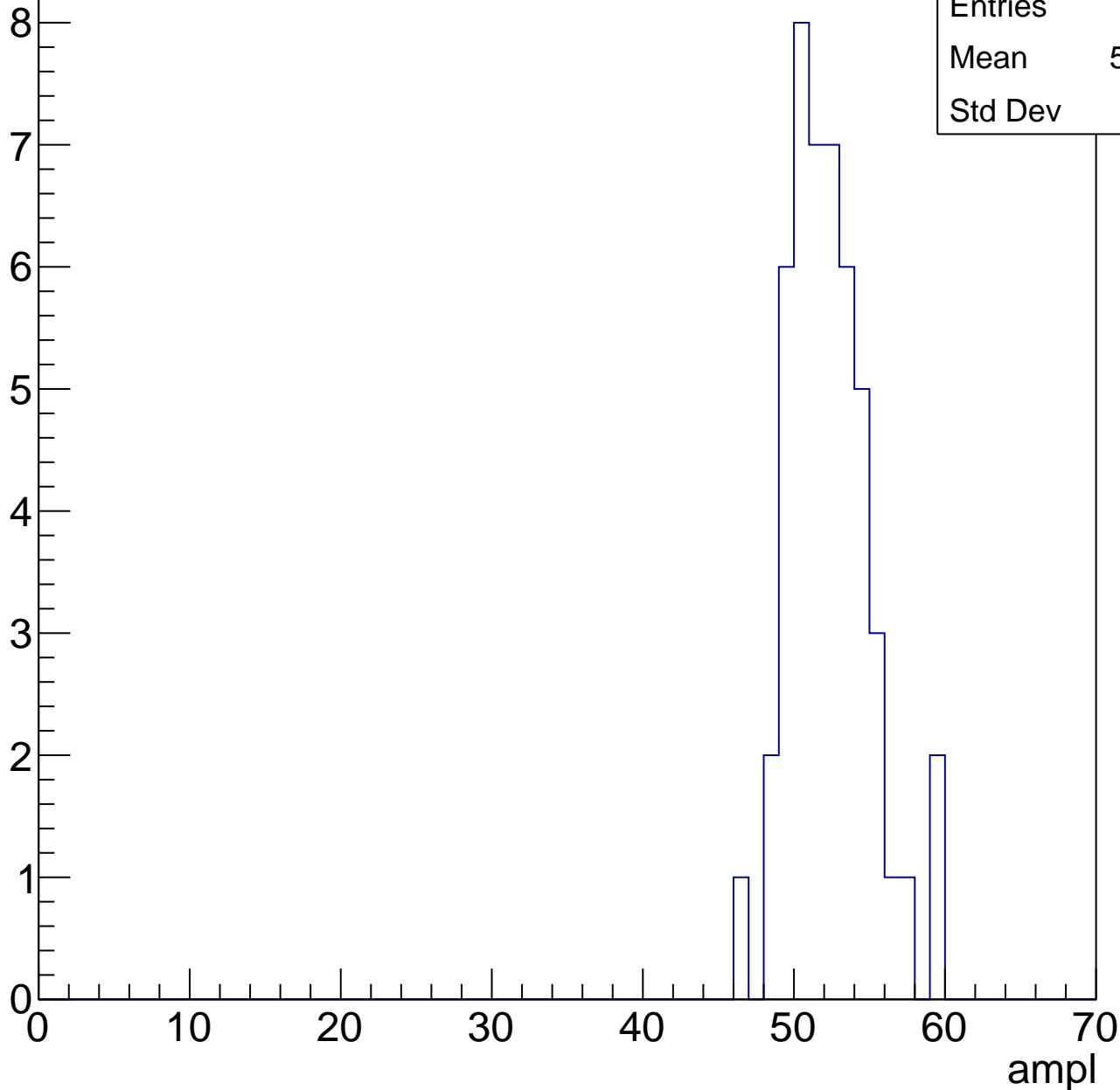


B1L002S, U7-ch124, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	51.86
Std Dev	2.68

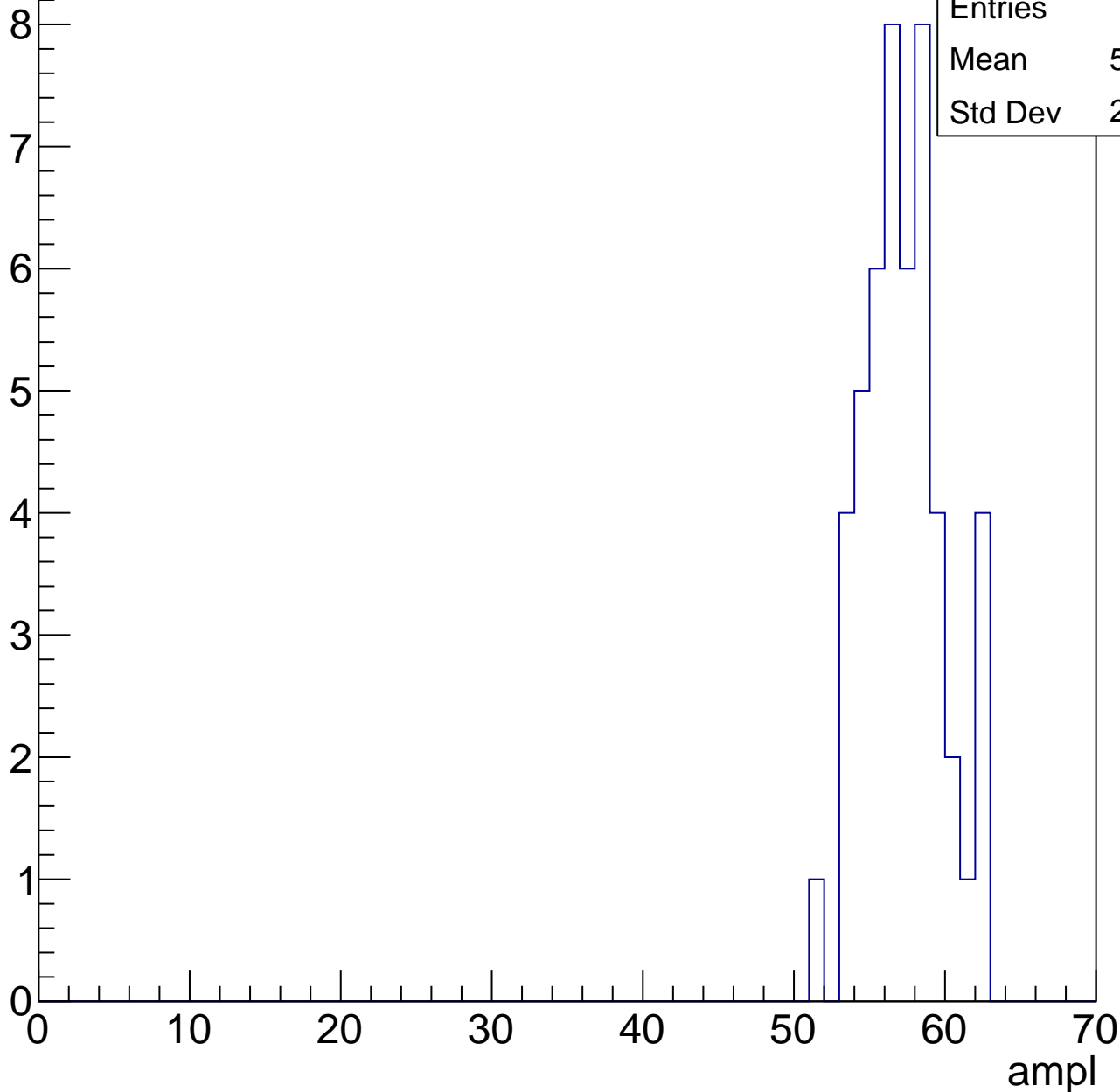


B1L002S, U7-ch124, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	56.78
Std Dev	2.605

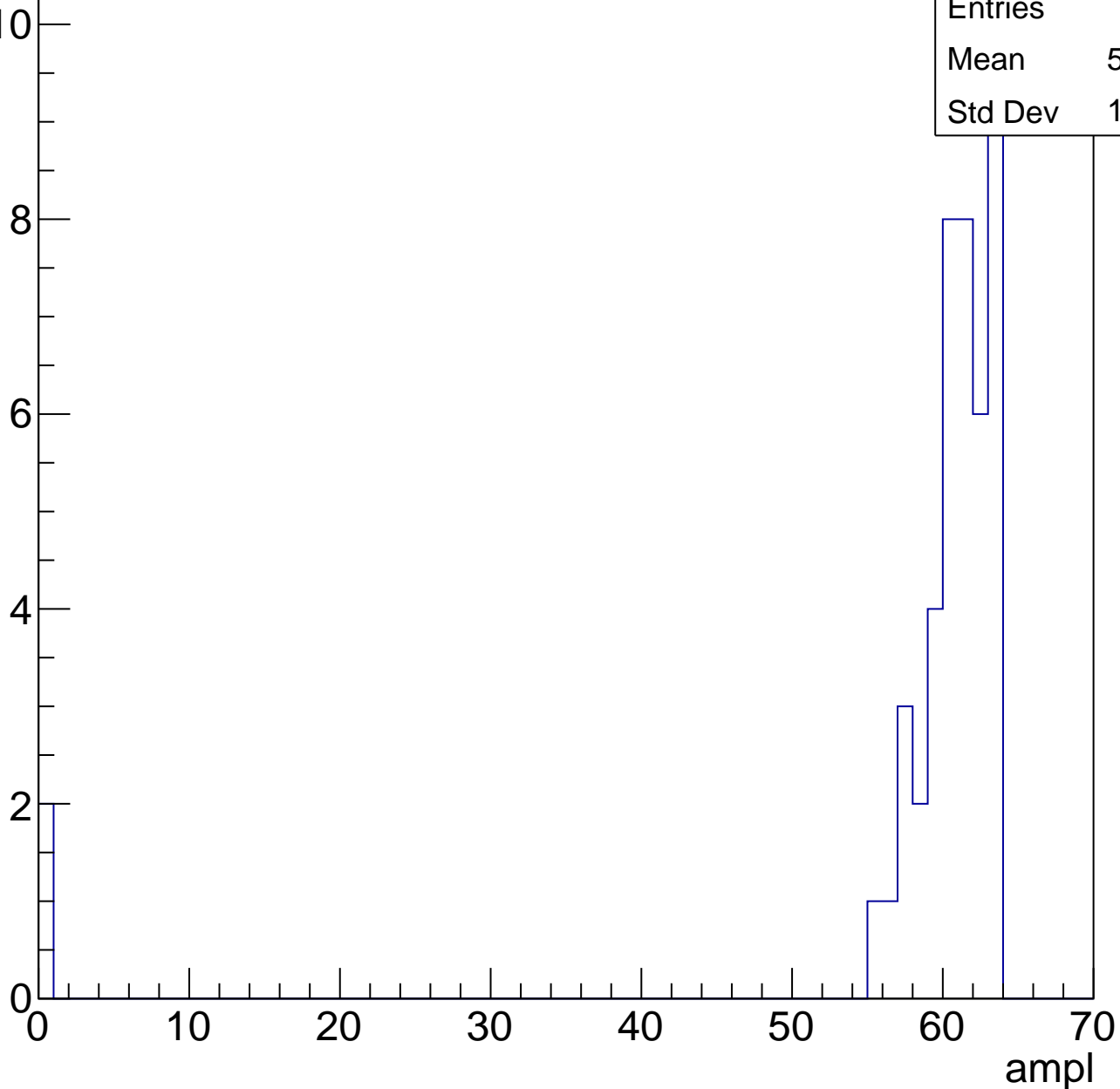


B1L002S, U7-ch124, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

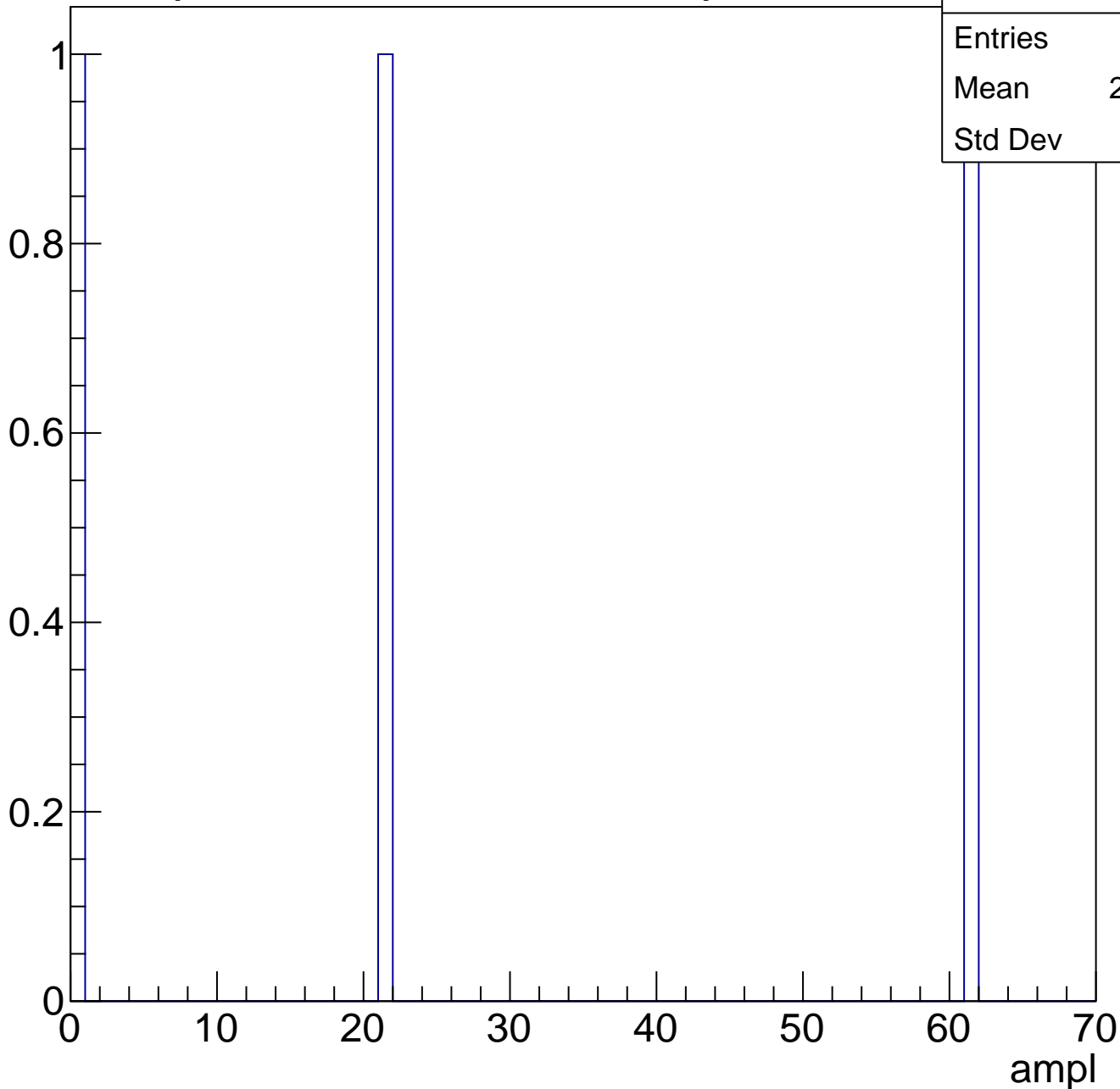
Entries	45
Mean	57.87
Std Dev	12.65



B1L002S, U7-ch124, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.33
Std Dev	25.3

B1L002S, U7-ch125, adc0

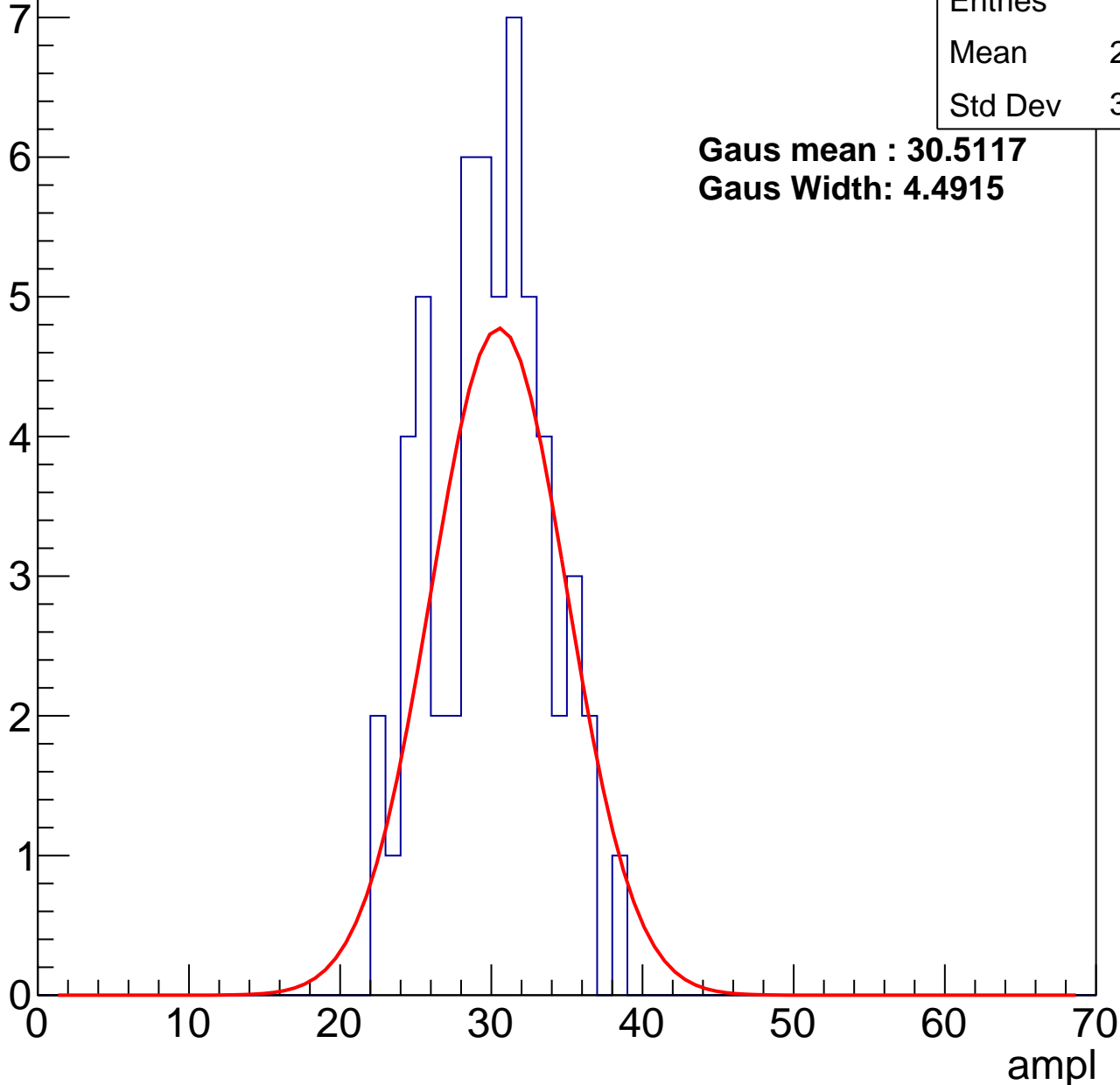
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	57
Mean	29.44
Std Dev	3.779

Gaus mean : 30.5117

Gaus Width: 4.4915



B1L002S, U7-ch125, adc1

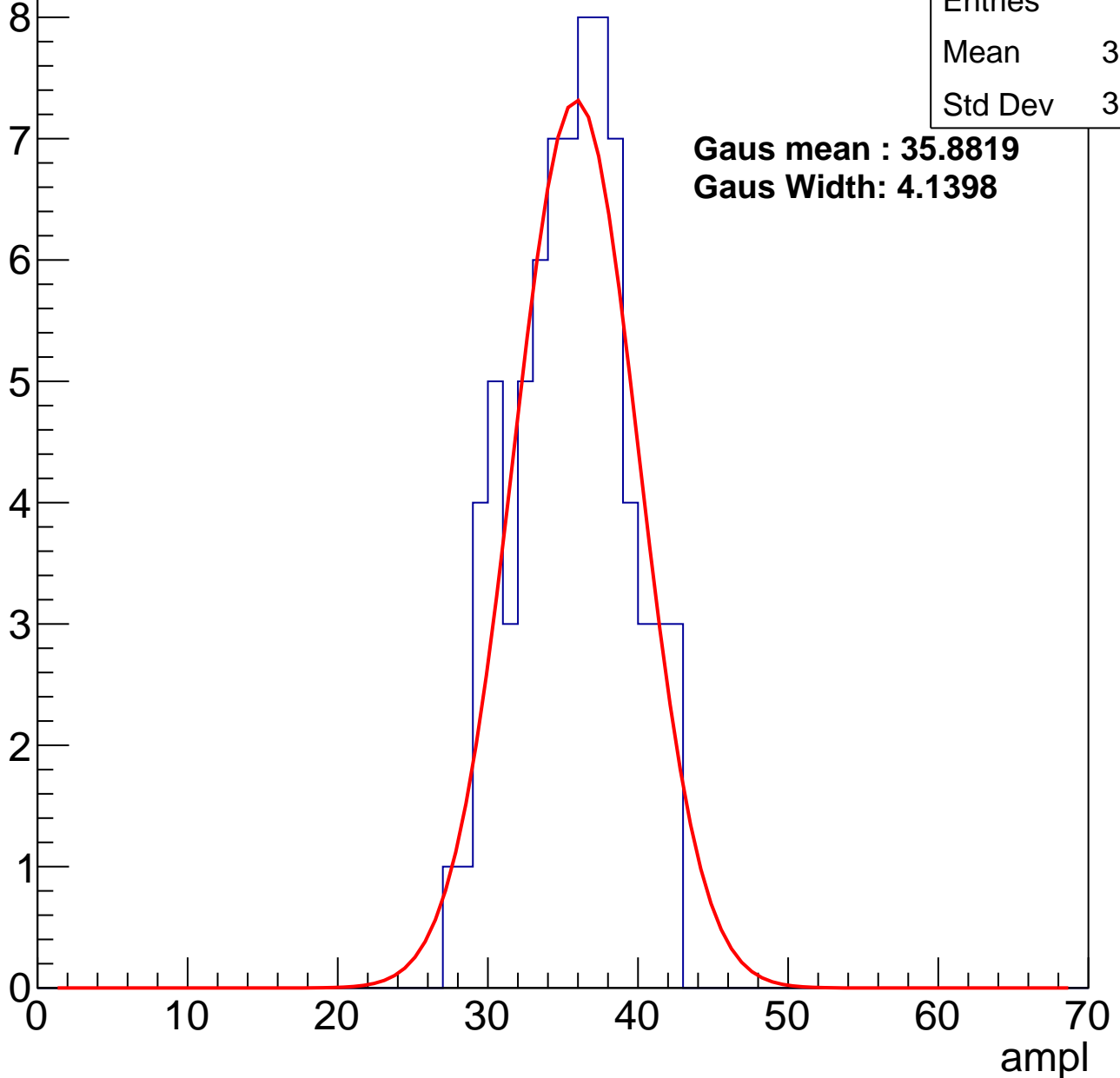
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	75
Mean	35.07
Std Dev	3.656

Gaus mean : 35.8819

Gaus Width: 4.1398



B1L002S, U7-ch125, adc2

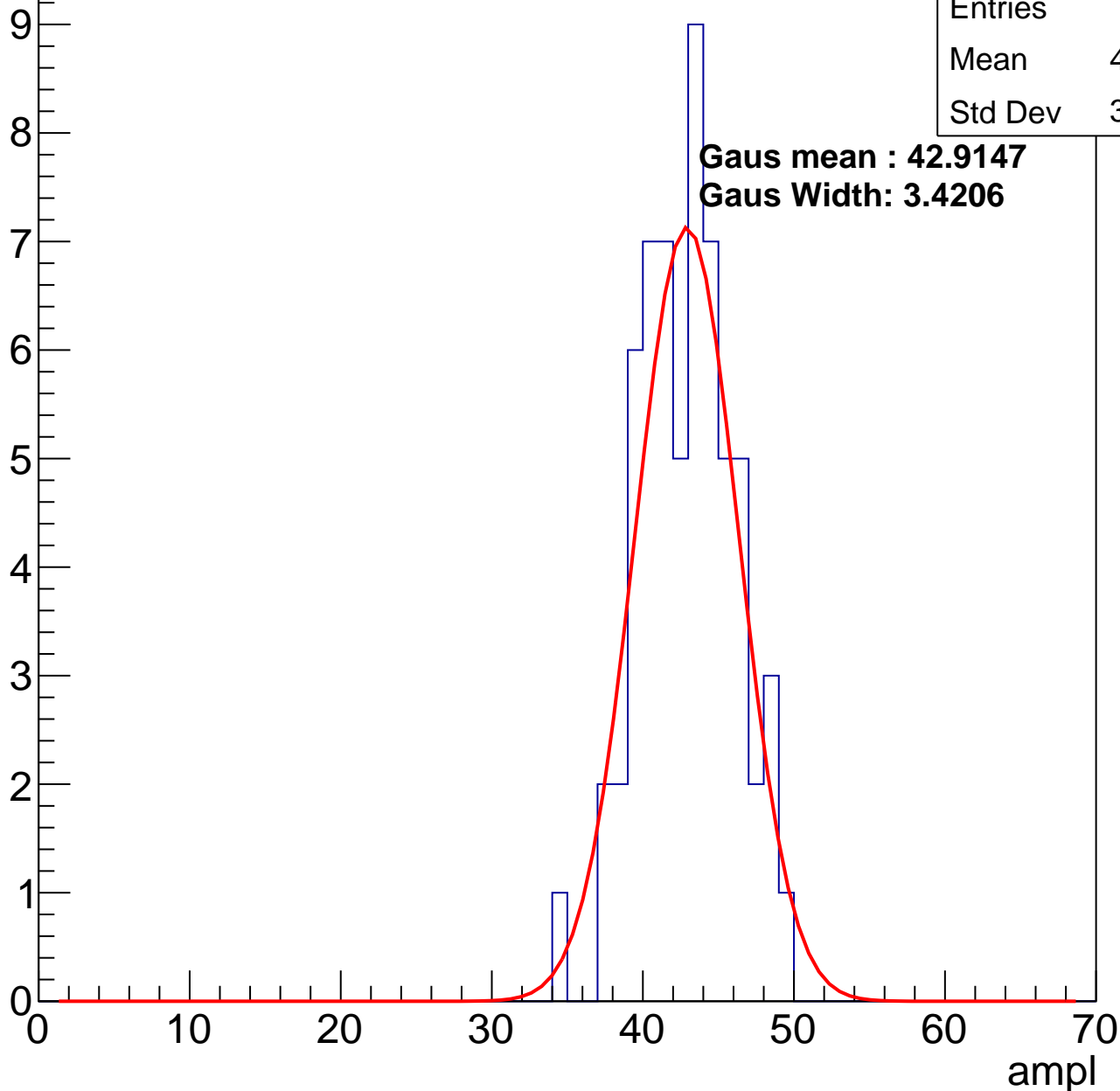
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	62
Mean	42.45
Std Dev	3.088

Gaus mean : 42.9147

Gaus Width: 3.4206

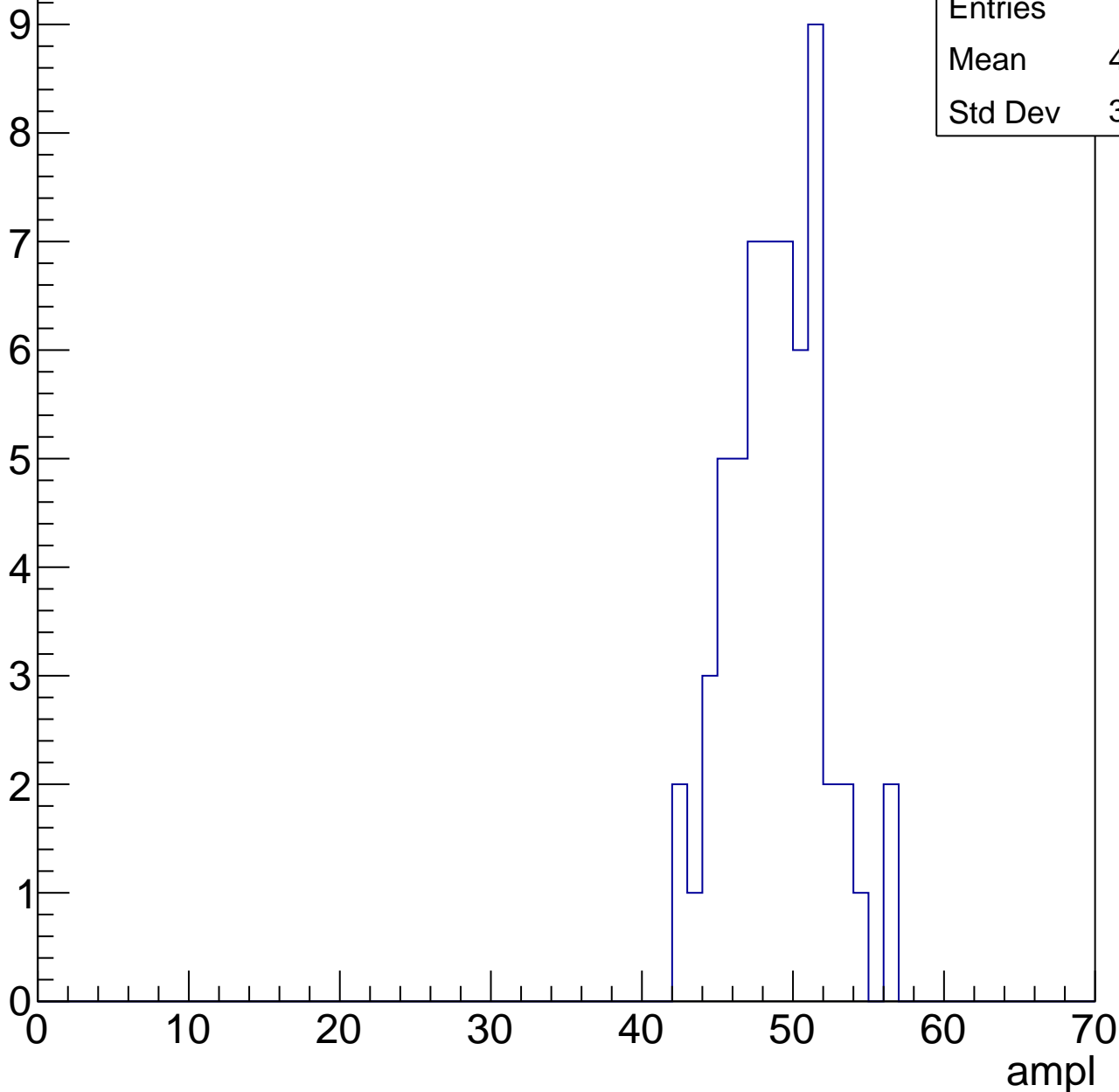


B1L002S, U7-ch125, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	59
Mean	48.42
Std Dev	3.093

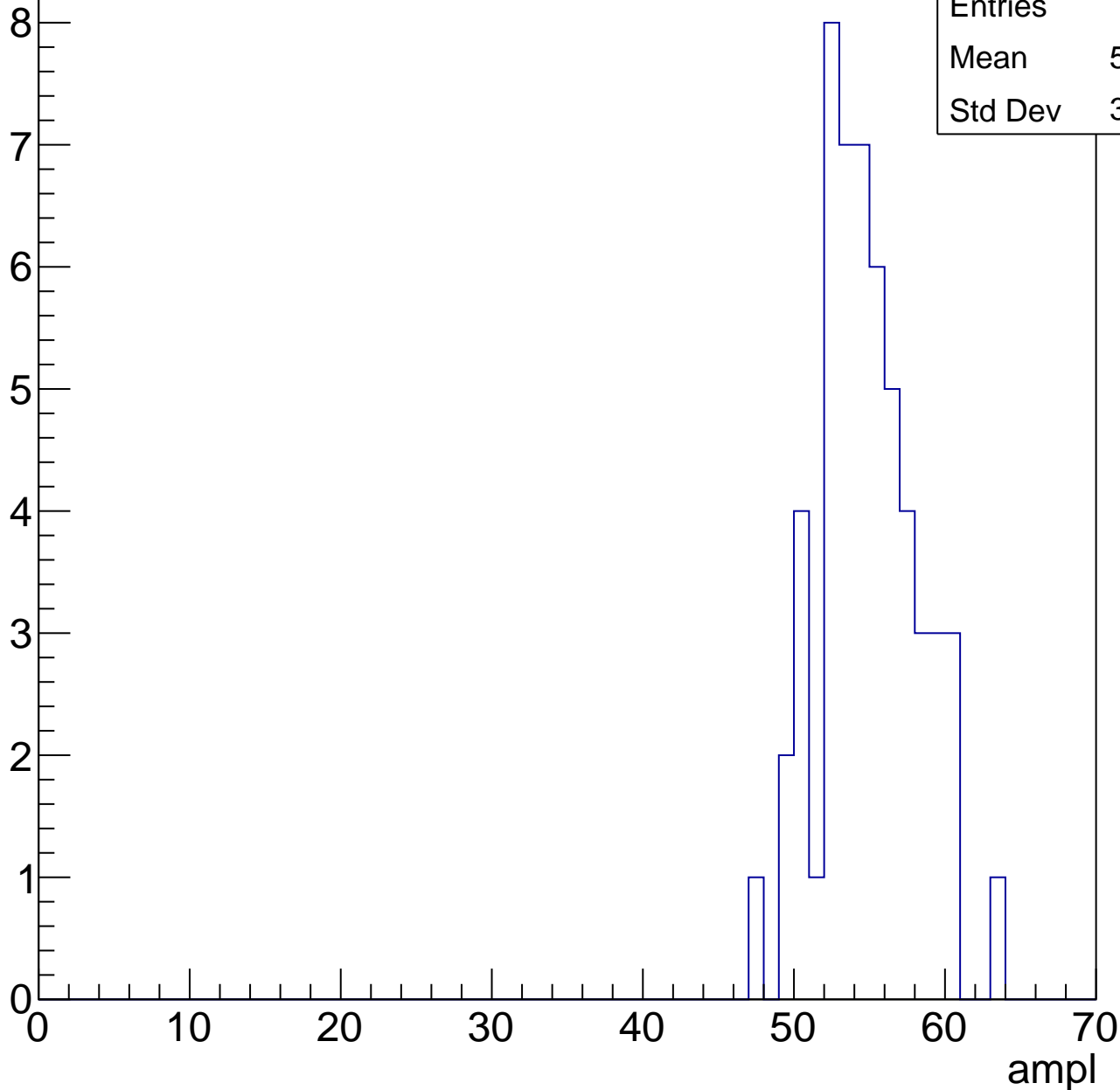


B1L002S, U7-ch125, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	55
Mean	54.42
Std Dev	3.234

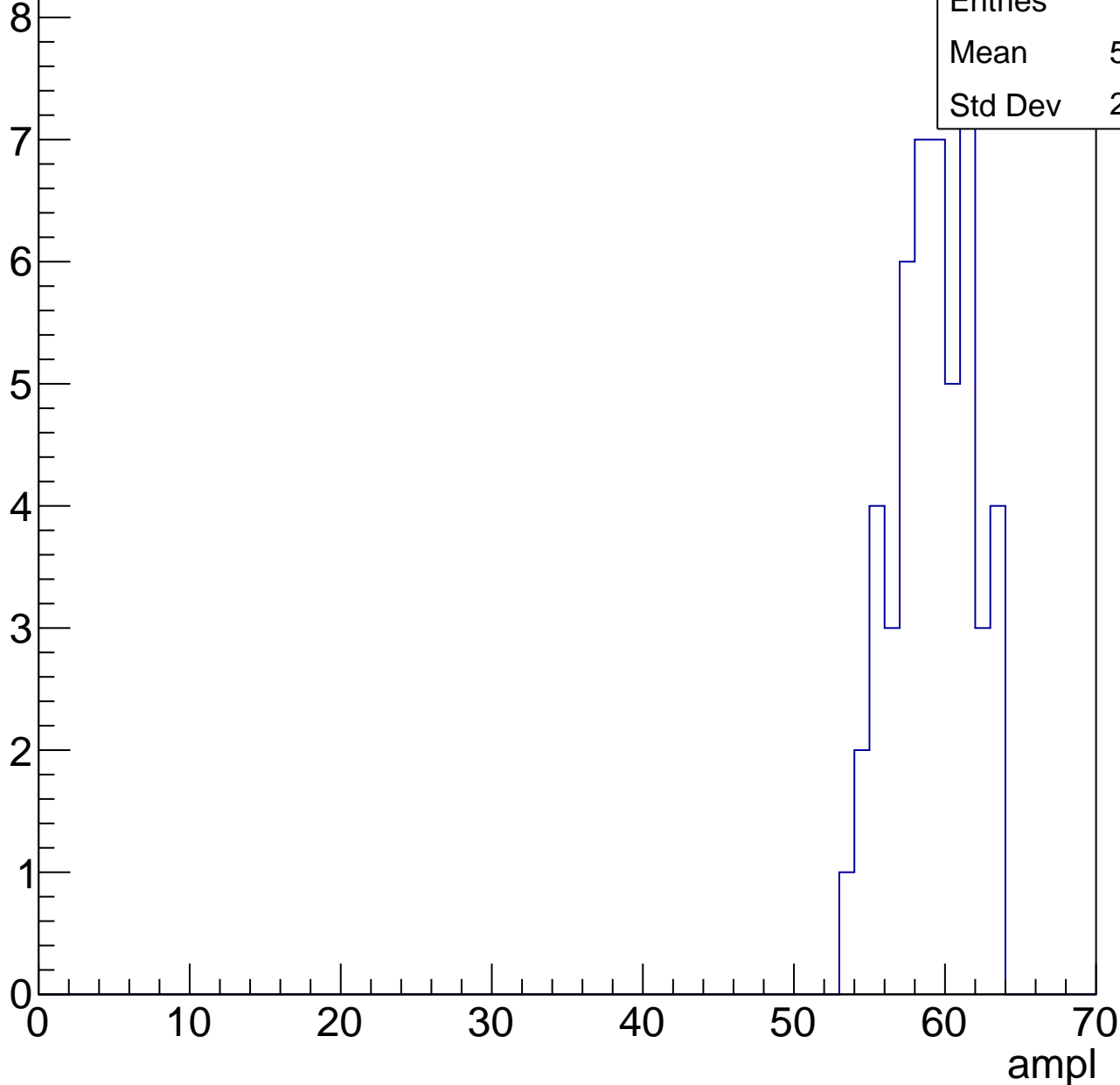


B1L002S, U7-ch125, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	50
Mean	58.72
Std Dev	2.577

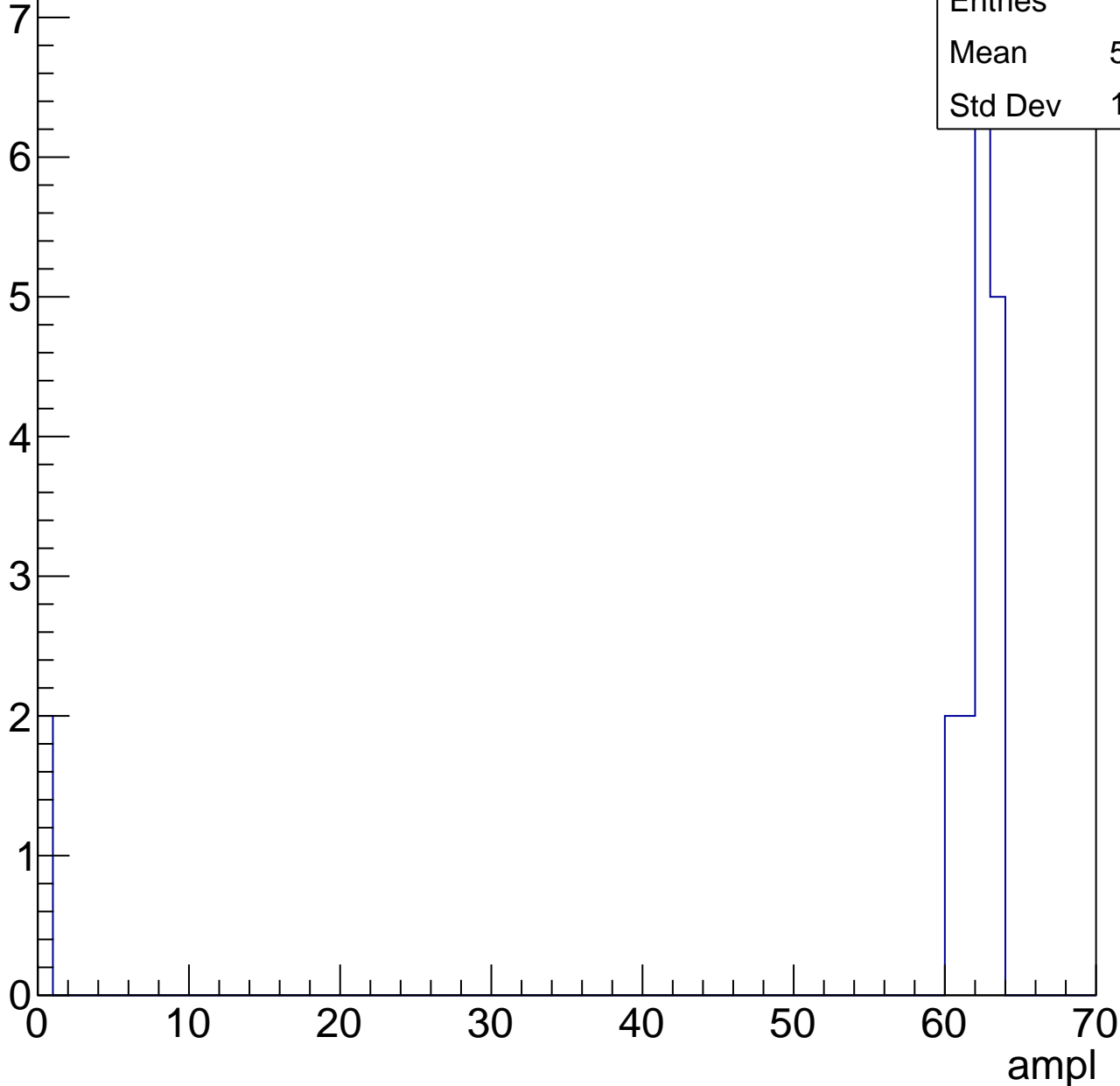


B1L002S, U7-ch125, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

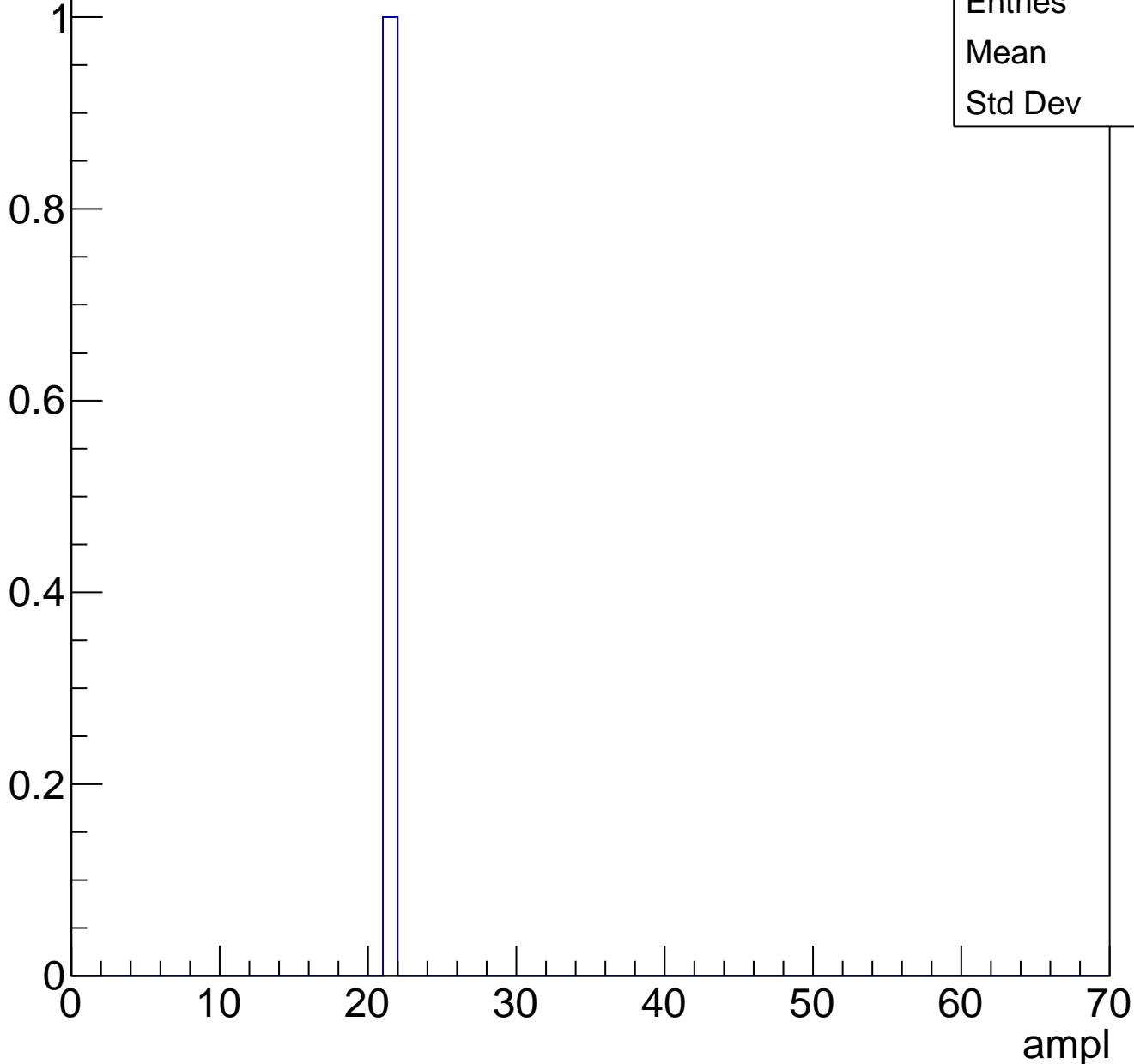
Entries	18
Mean	55.06
Std Dev	19.49



B1L002S, U7-ch125, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch126, adc0

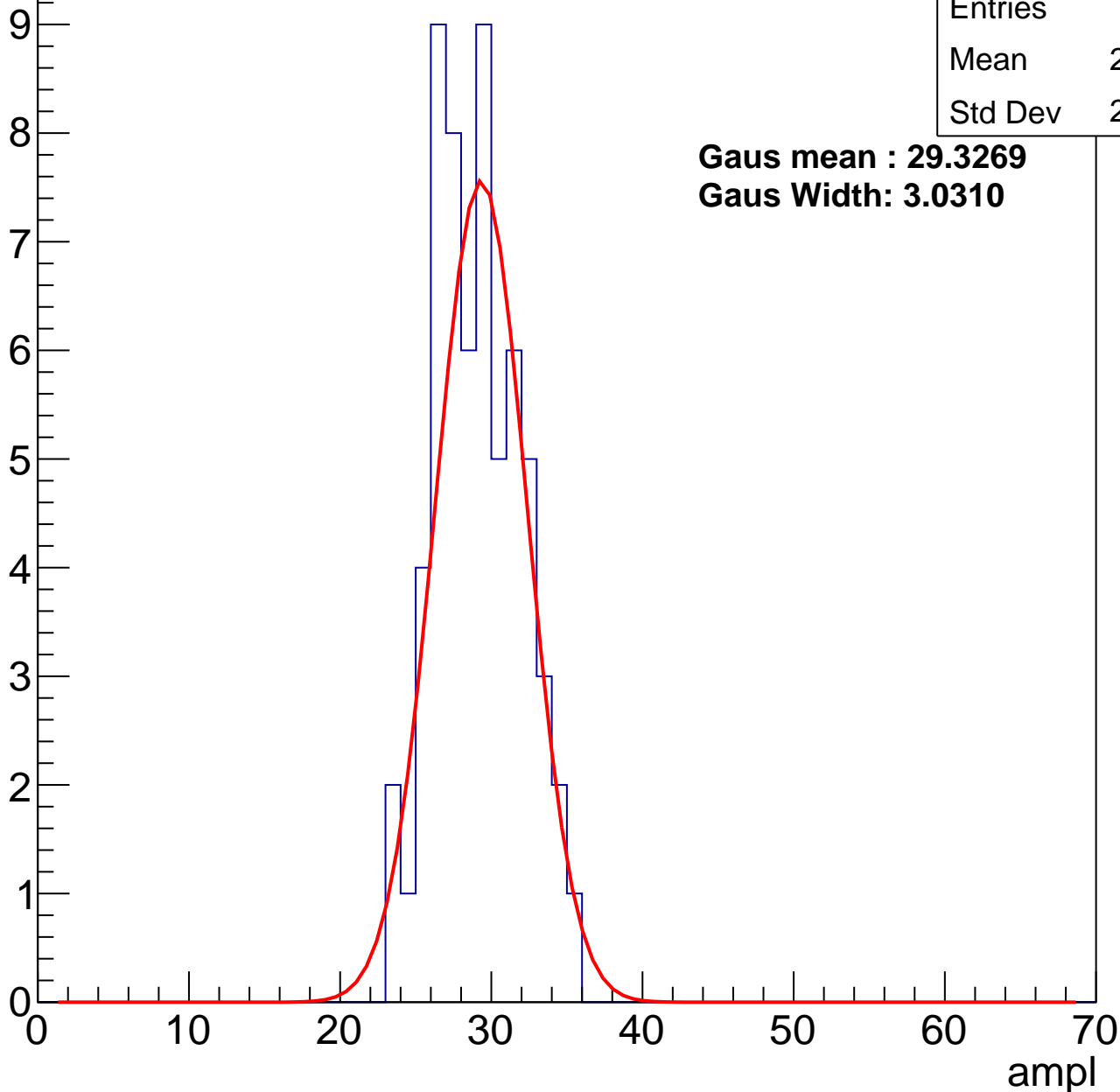
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.64
Std Dev	2.805

Gaus mean : 29.3269

Gaus Width: 3.0310



B1L002S, U7-ch126, adc1

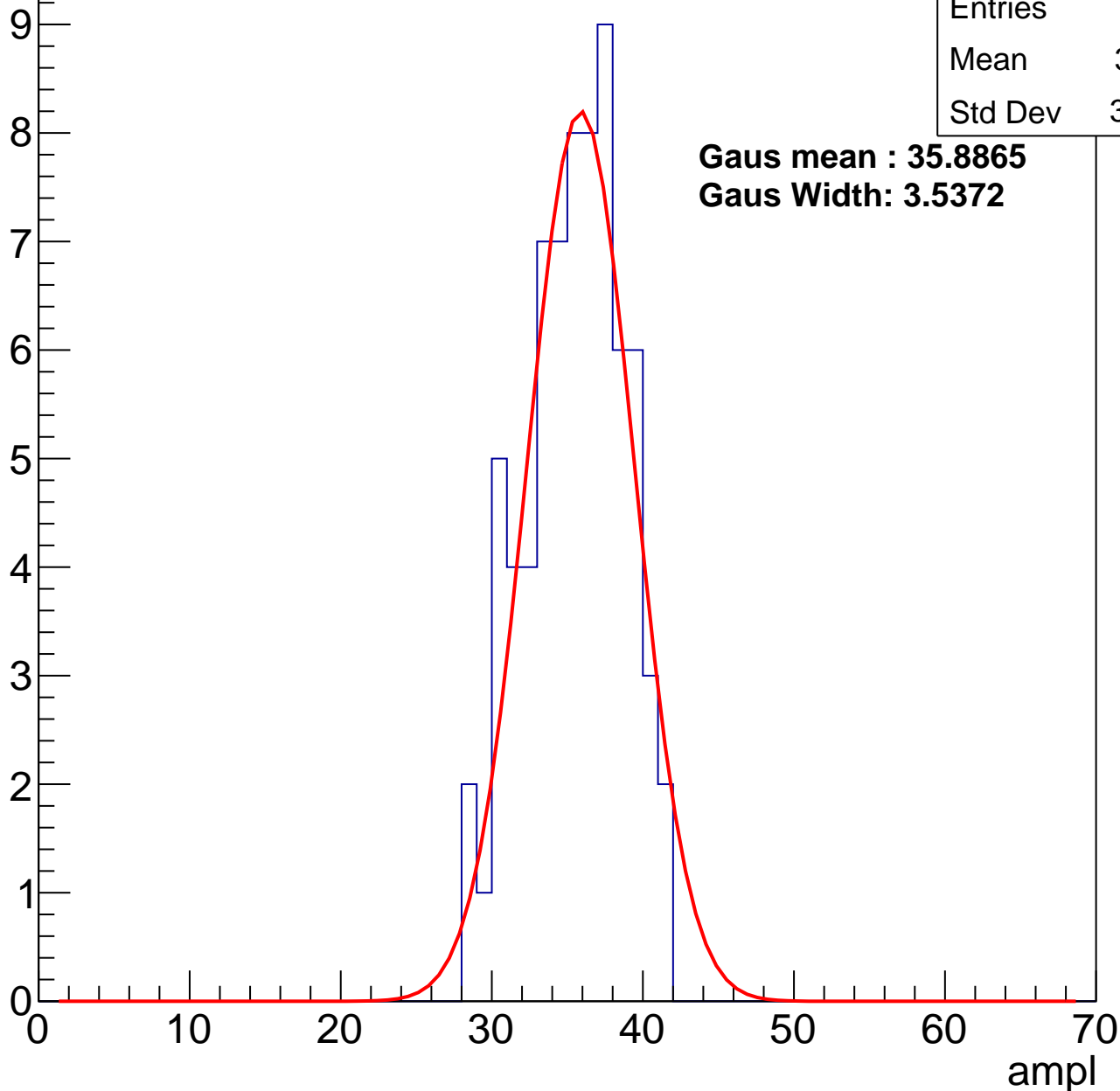
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	72
Mean	35.01
Std Dev	3.195

Gaus mean : 35.8865

Gaus Width: 3.5372



B1L002S, U7-ch126, adc2

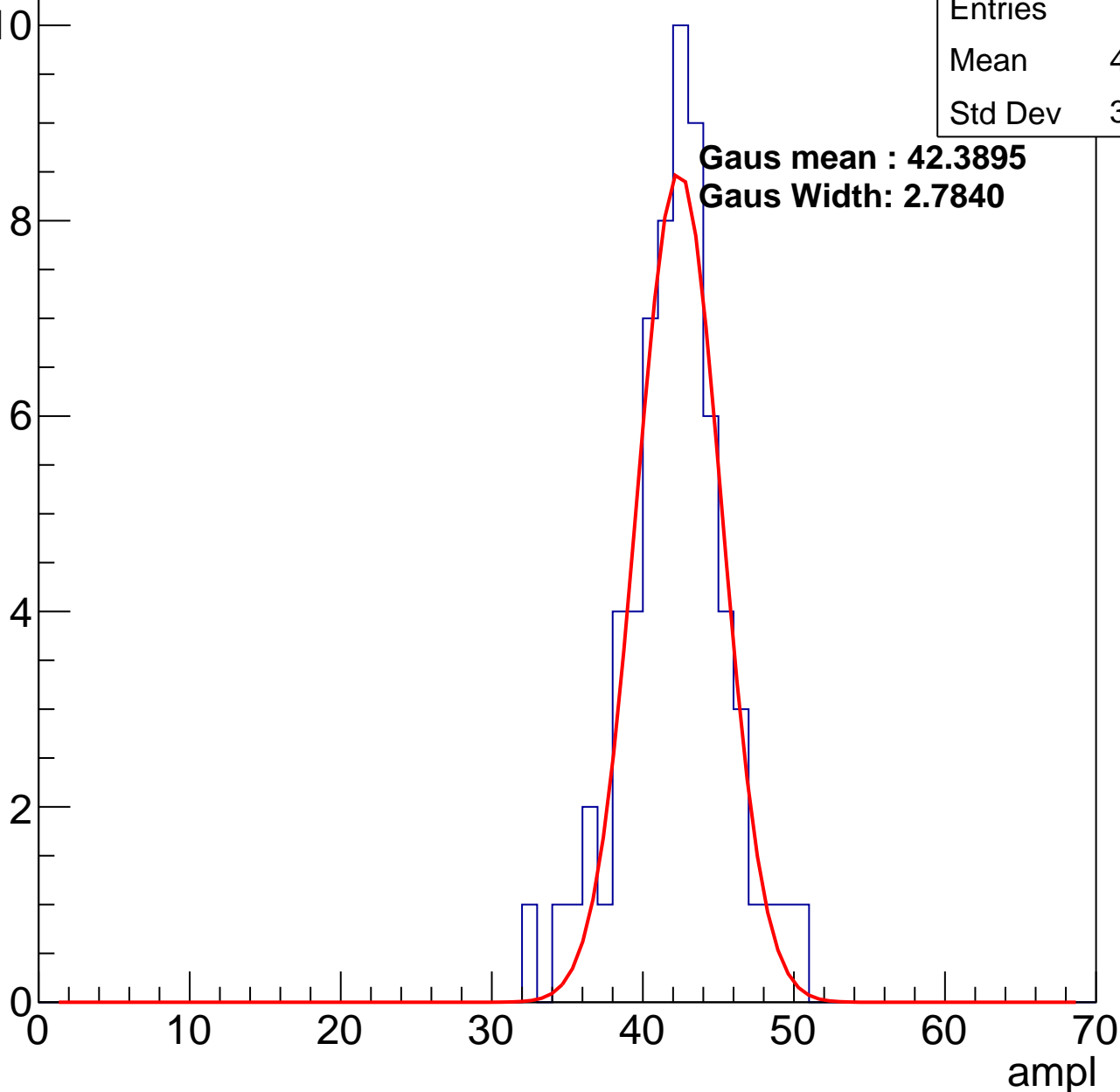
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	65
Mean	41.68
Std Dev	3.352

Gaus mean : 42.3895

Gaus Width: 2.7840

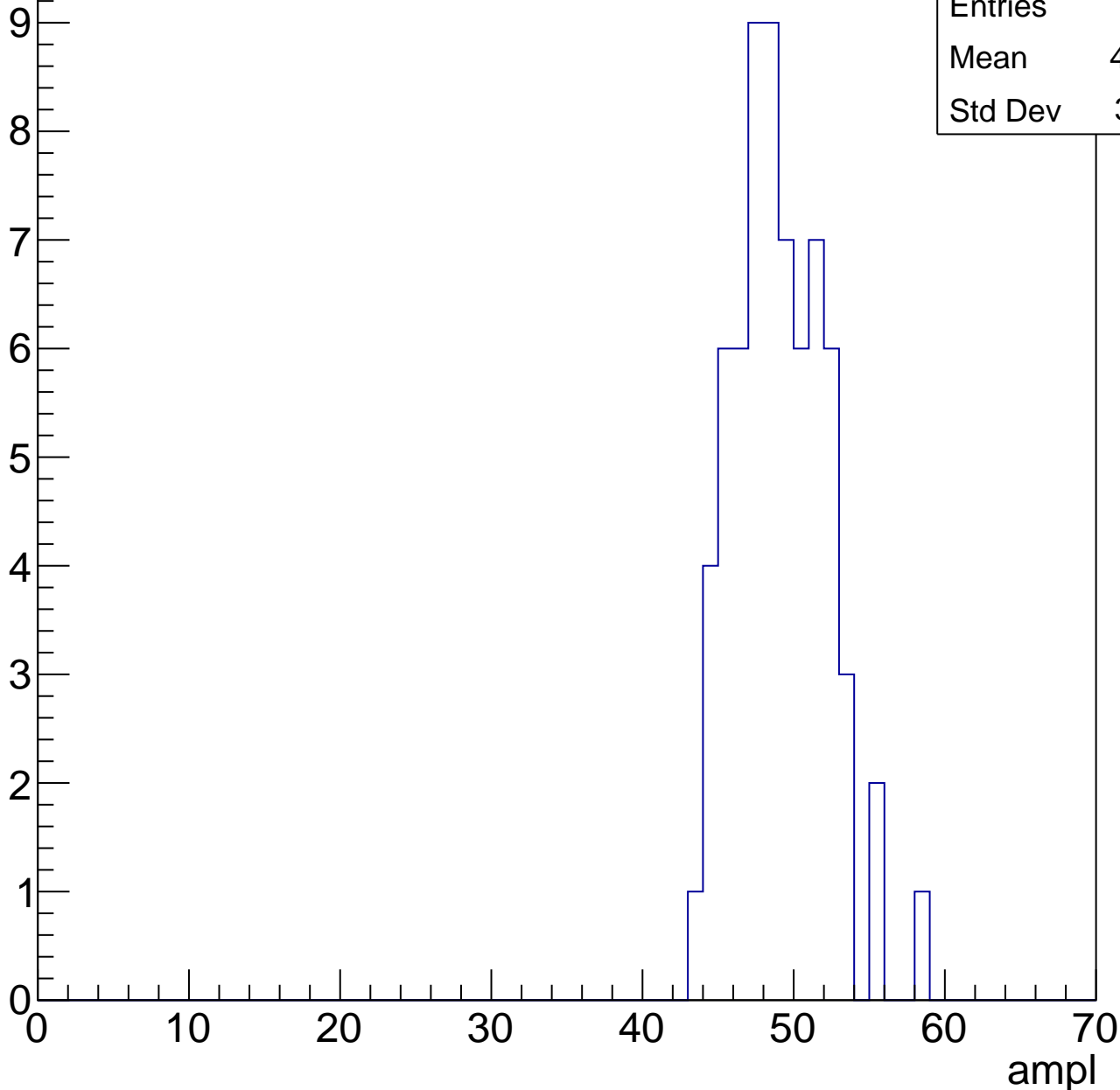


B1L002S, U7-ch126, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.64
Std Dev	3.021

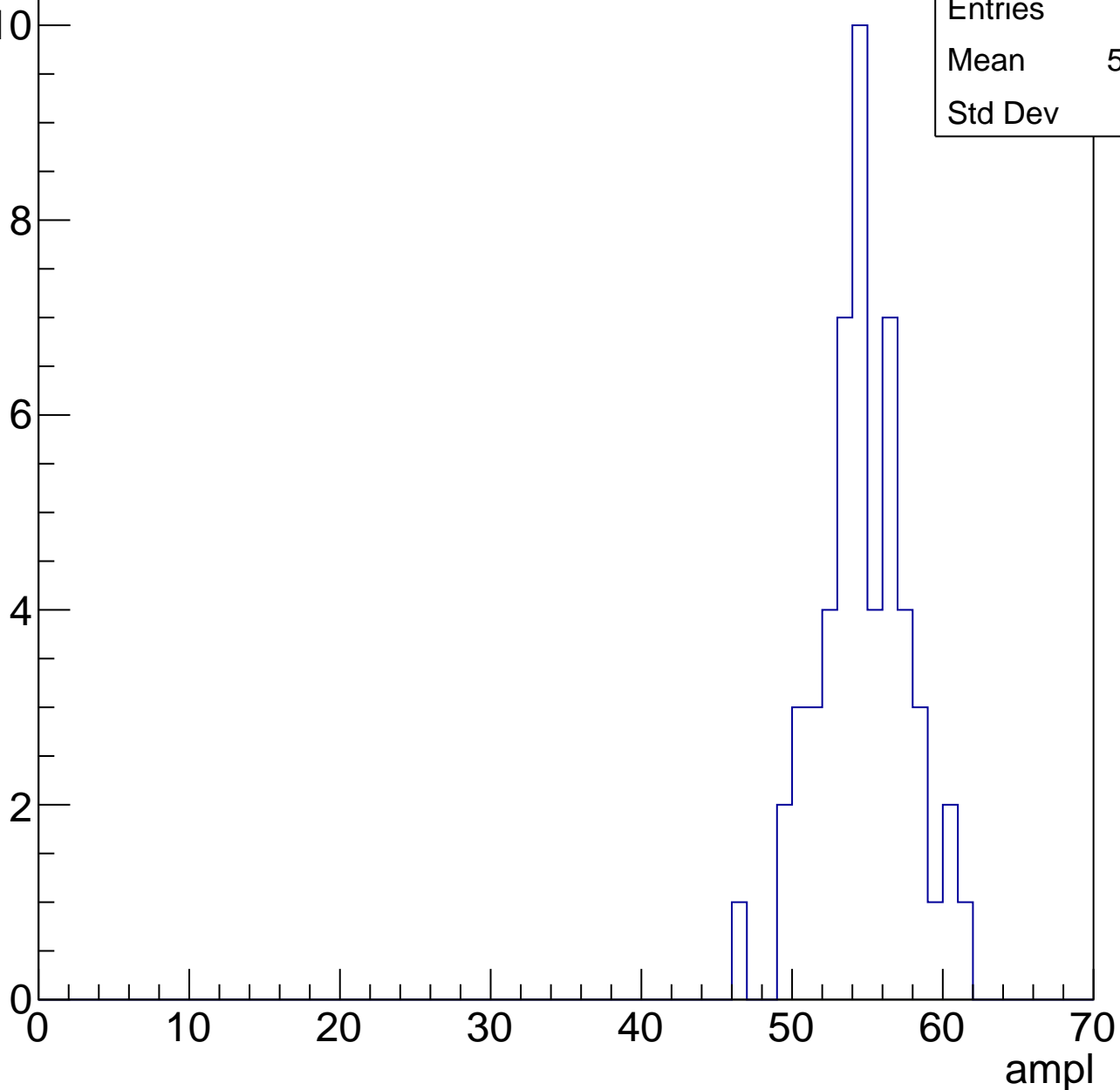


B1L002S, U7-ch126, adc4

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	52
Mean	54.23
Std Dev	3.01

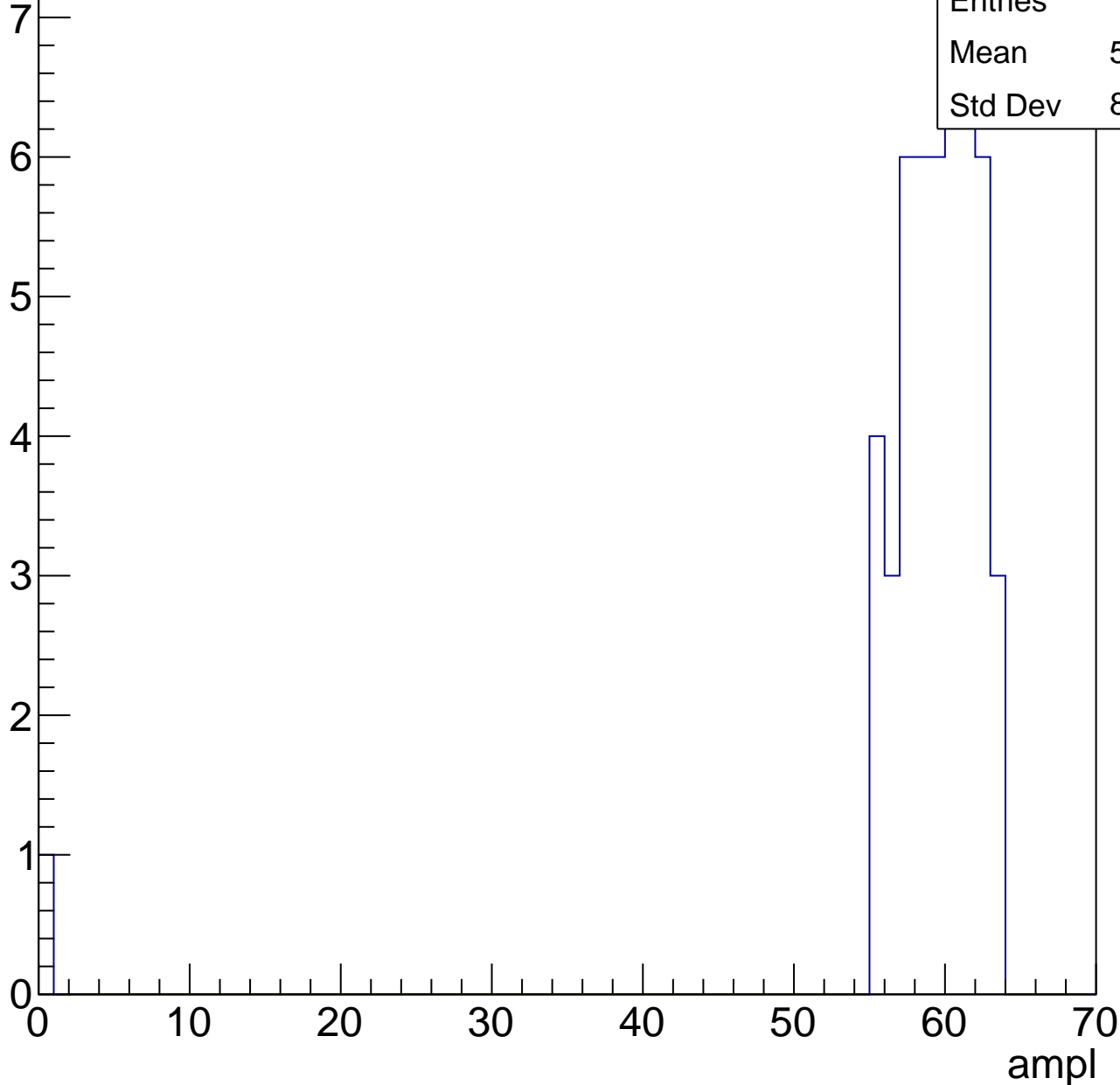


B1L002S, U7-ch126, adc5

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	49
Mean	57.96
Std Dev	8.673

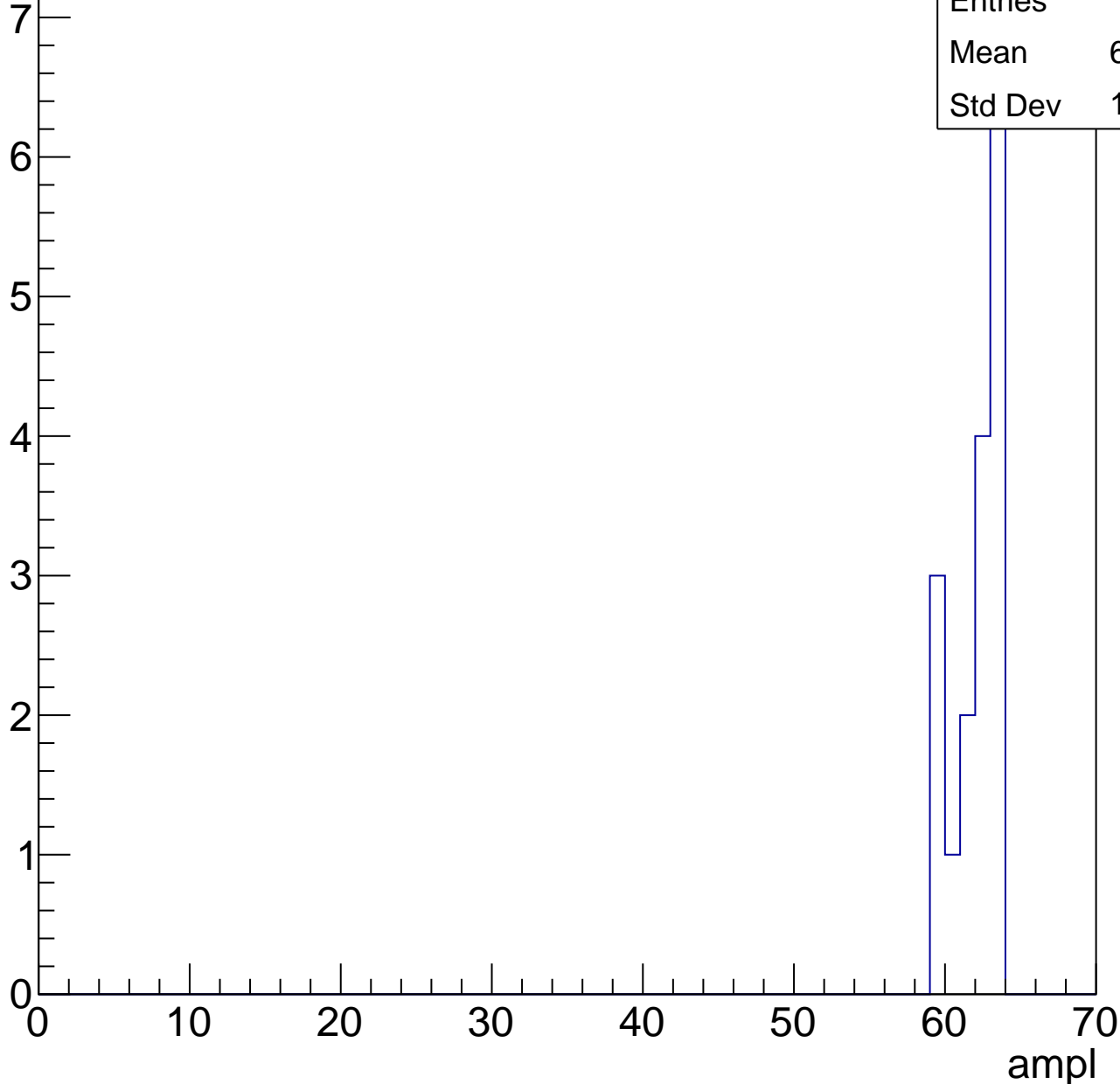


B1L002S, U7-ch126, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.65
Std Dev	1.493



B1L002S, U7-ch126, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

B1L002S, U7-ch127, adc0

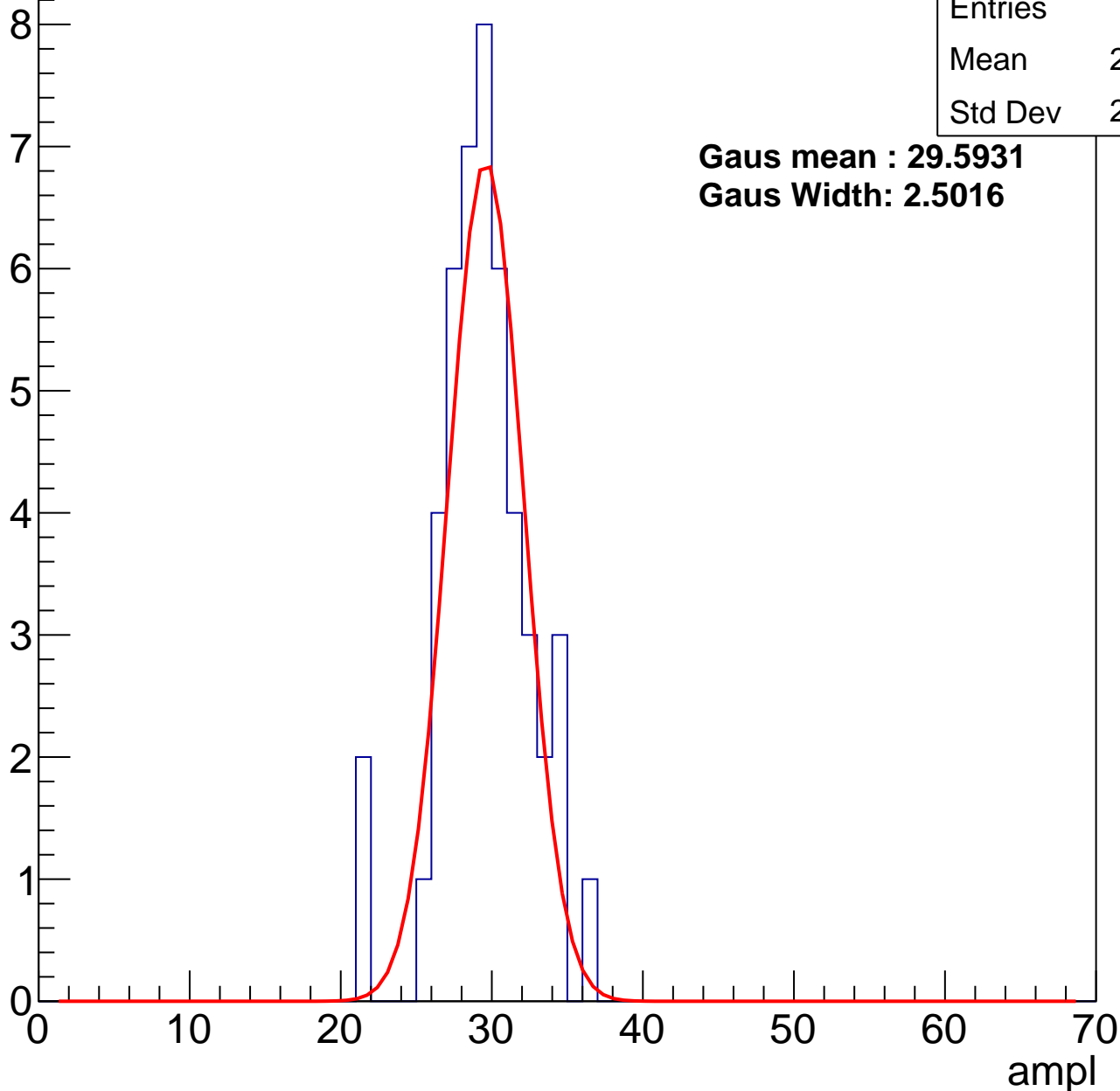
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	47
Mean	29.04
Std Dev	2.975

Gaus mean : 29.5931

Gaus Width: 2.5016



B1L002S, U7-ch127, adc1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	74
Mean	35.18
Std Dev	3.223

Gaus mean : 35.4542

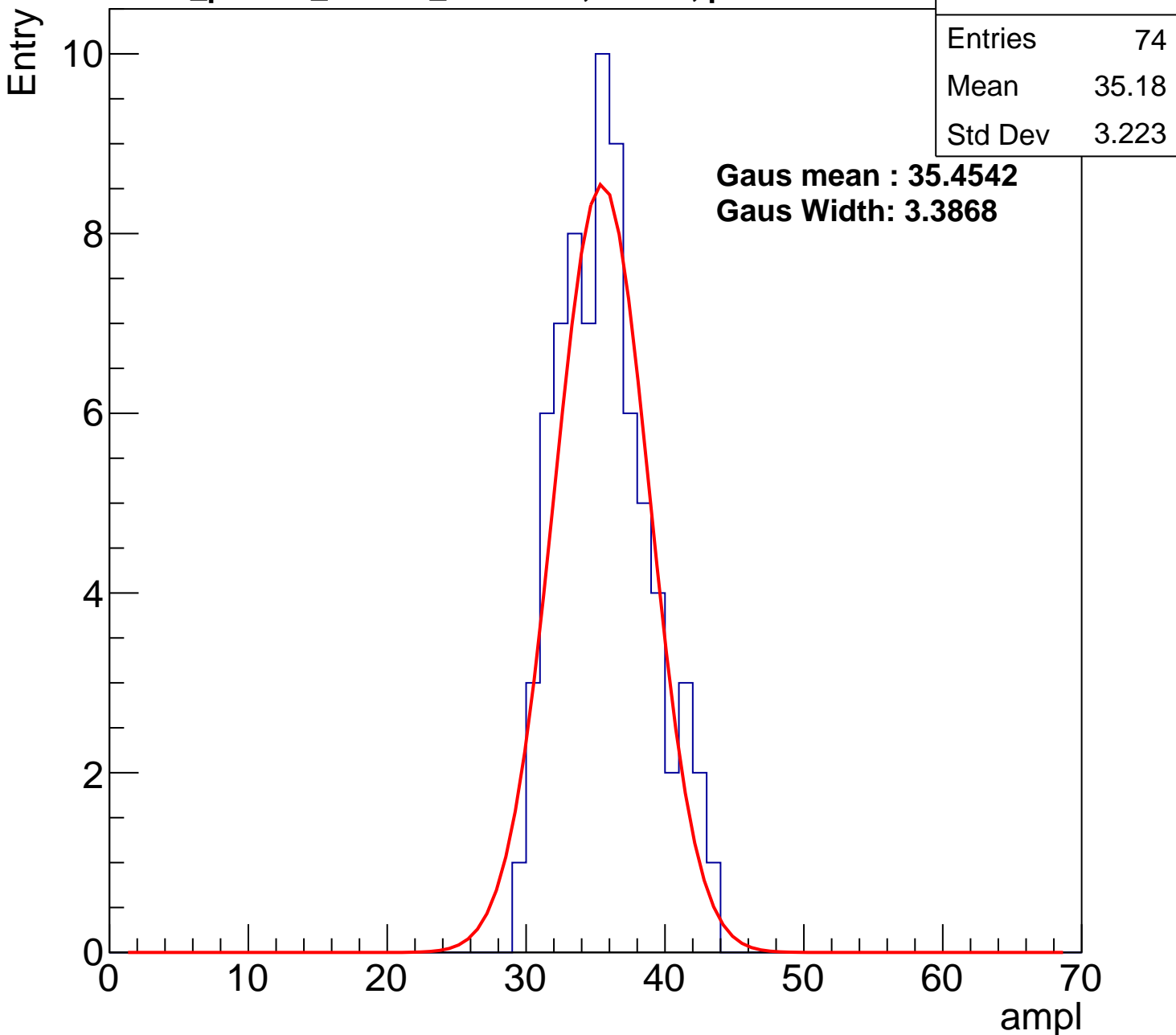
Gaus Width: 3.3868

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U7-ch127, adc2

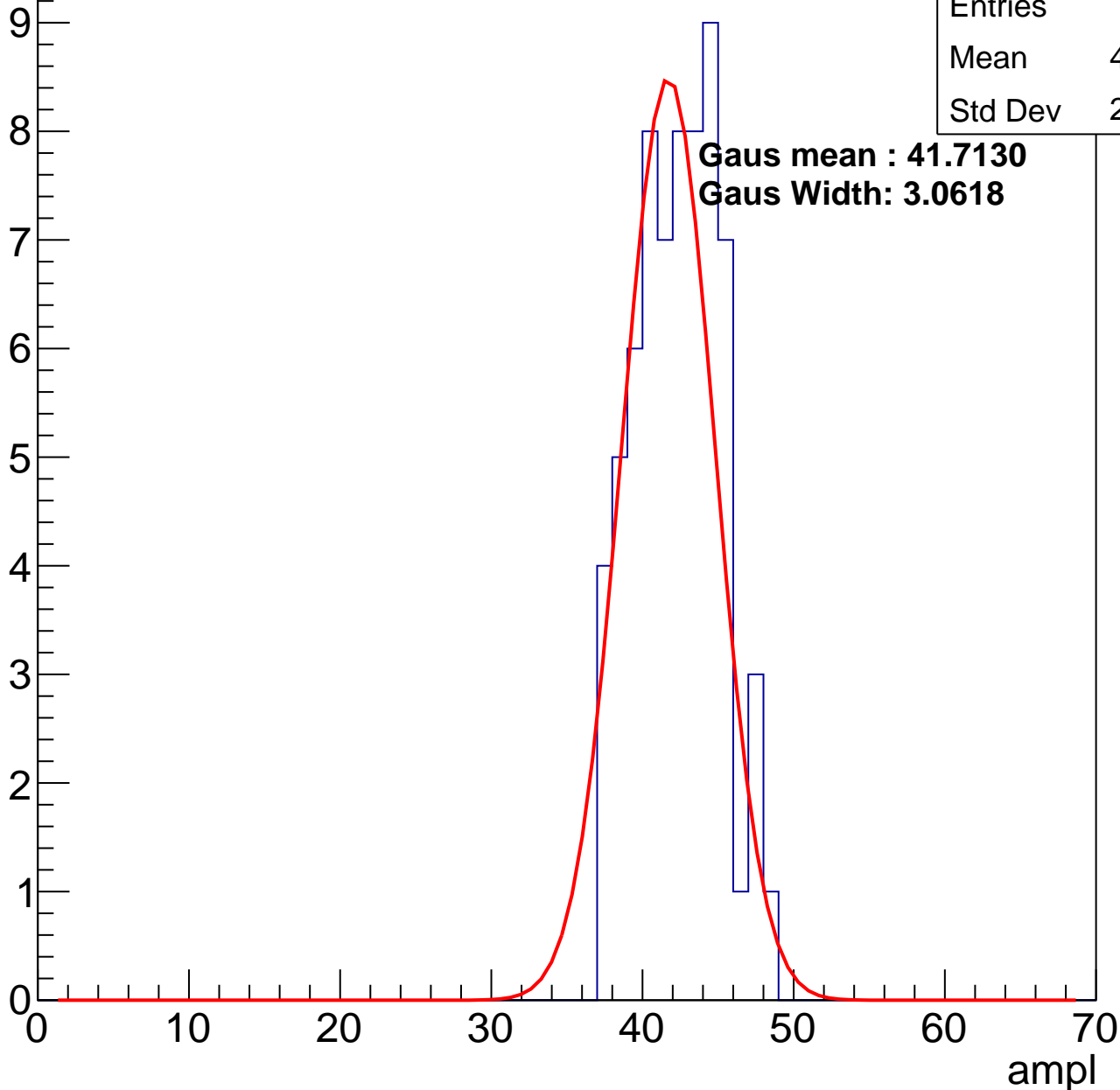
calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	67
Mean	41.87
Std Dev	2.748

Gaus mean : 41.7130

Gaus Width: 3.0618

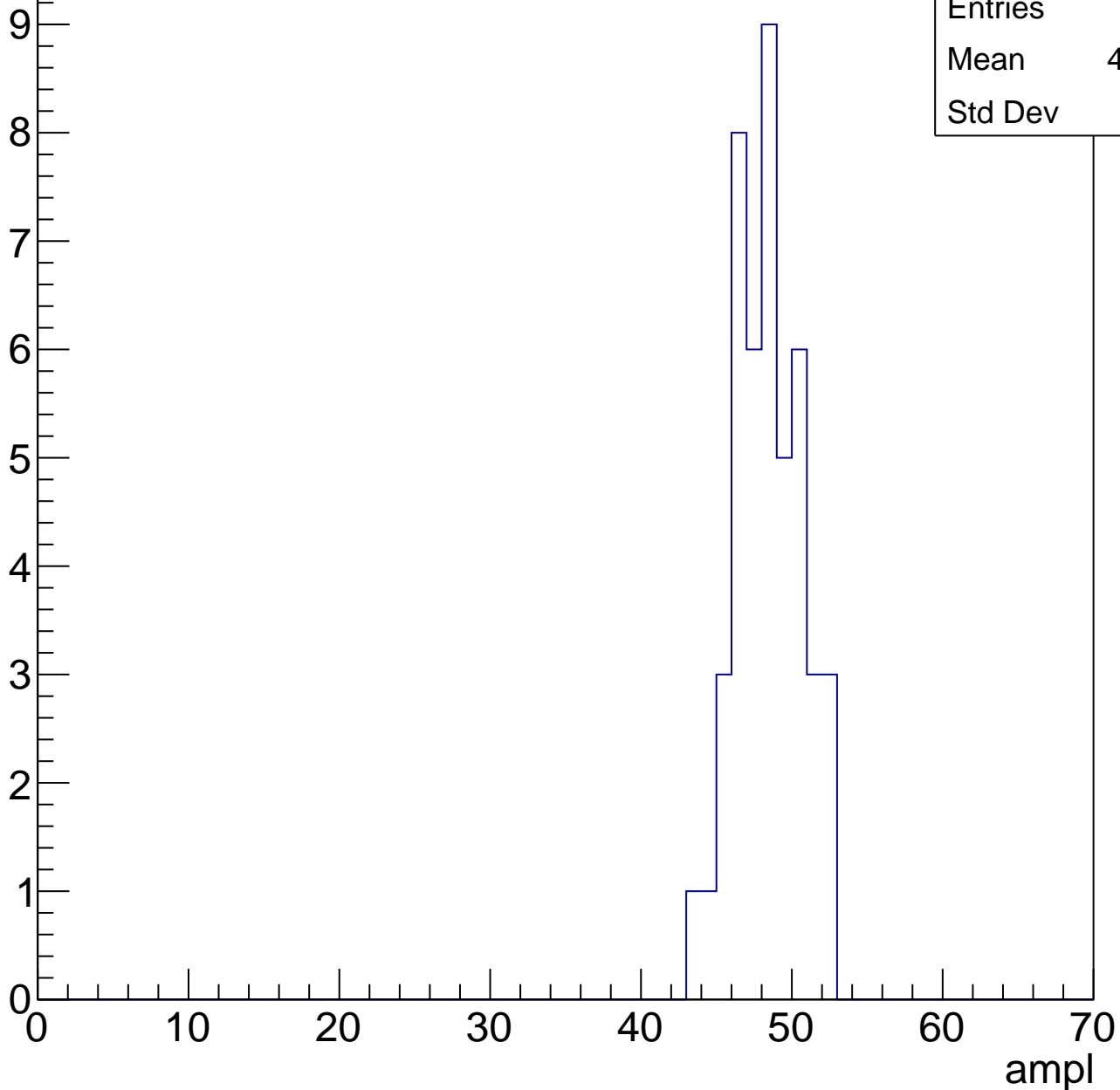


B1L002S, U7-ch127, adc3

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	45
Mean	47.96
Std Dev	2.16



B1L002S, U7-ch127, adc4

calib_packv5_042523_0143.root, FC#10, port B3

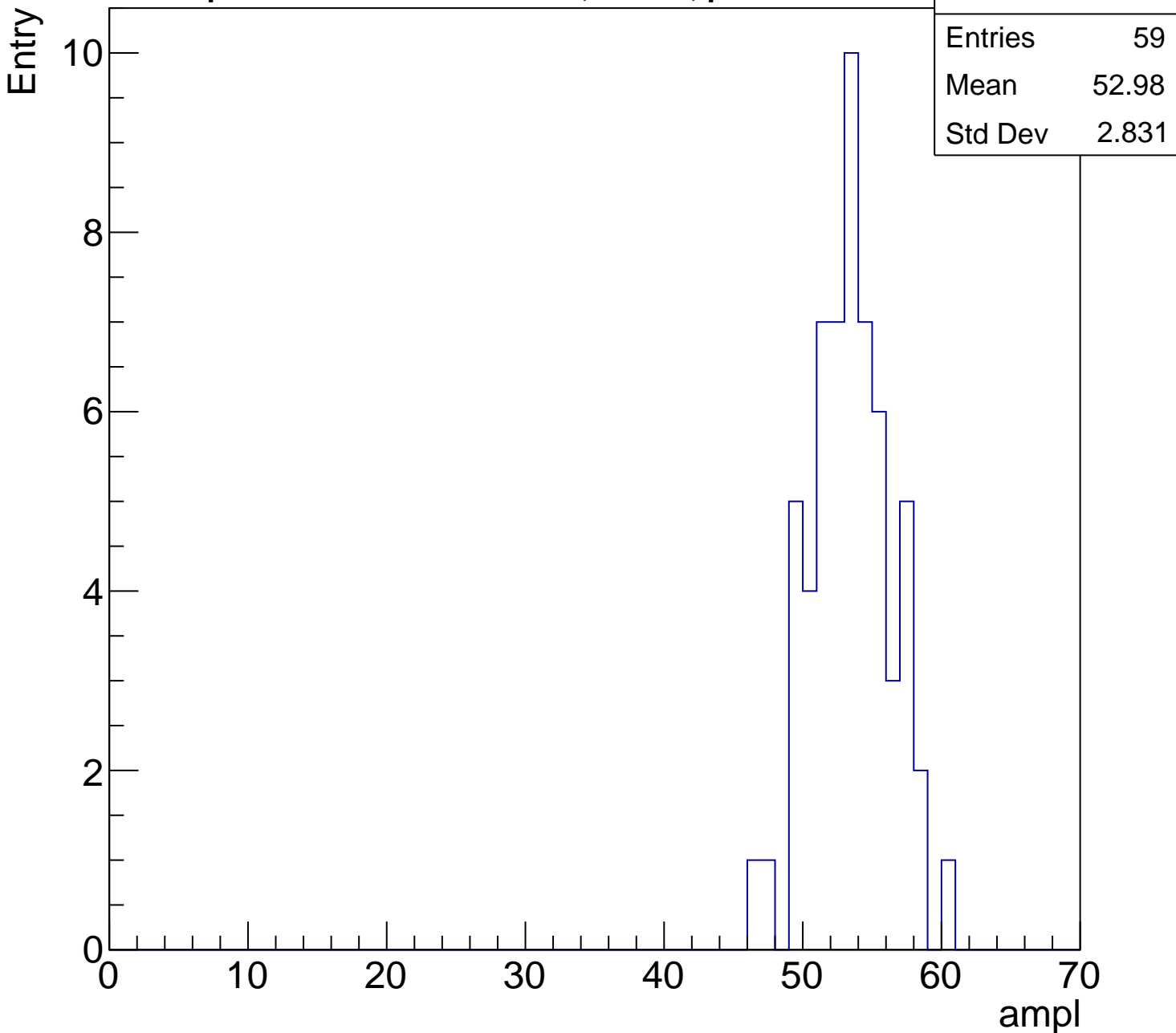
Entries	59
Mean	52.98
Std Dev	2.831

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U7-ch127, adc5

calib_packv5_042523_0143.root, FC#10, port B3

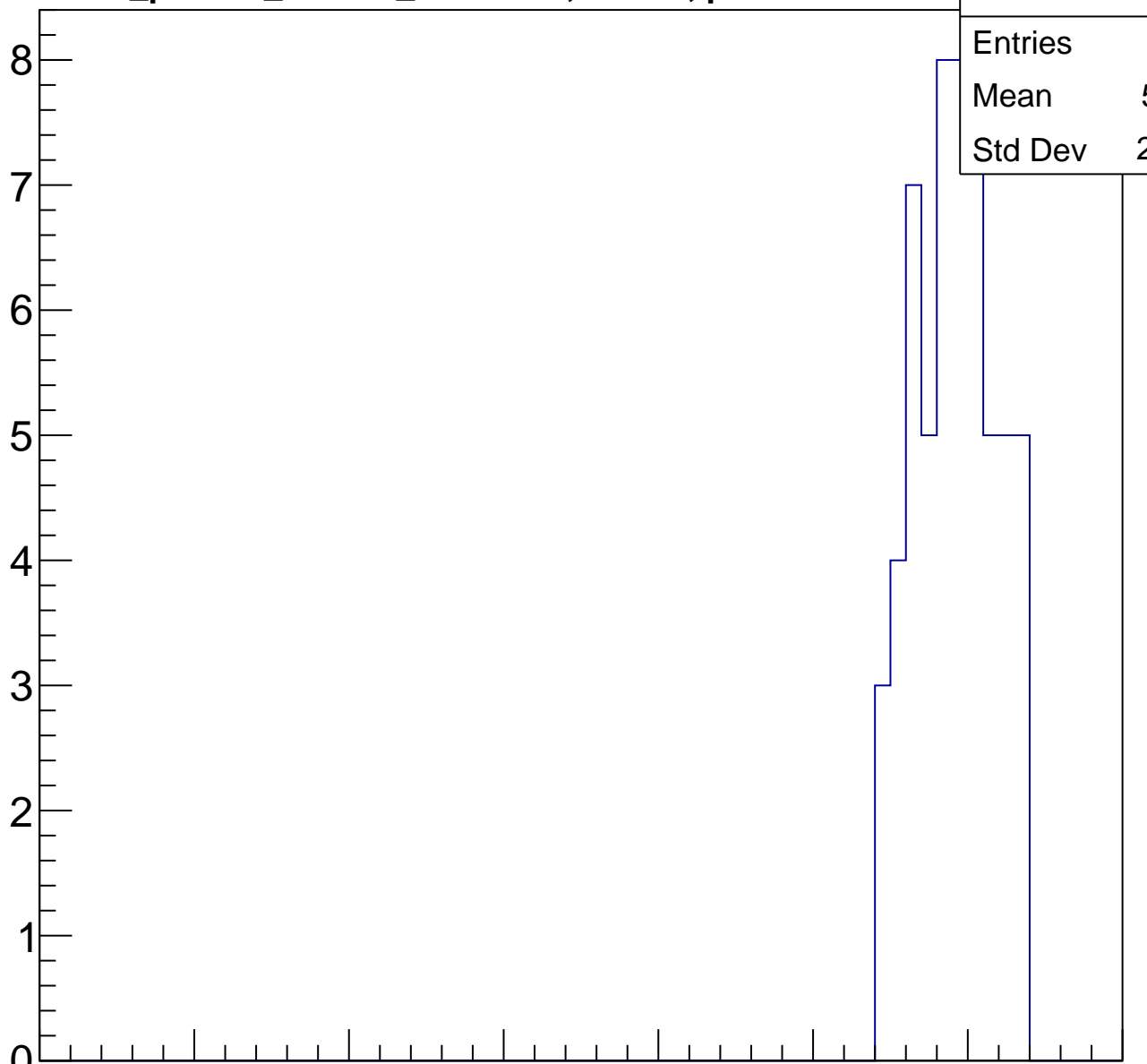
Entry

8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.71
Std Dev	2.553

ampl

0 10 20 30 40 50 60 70

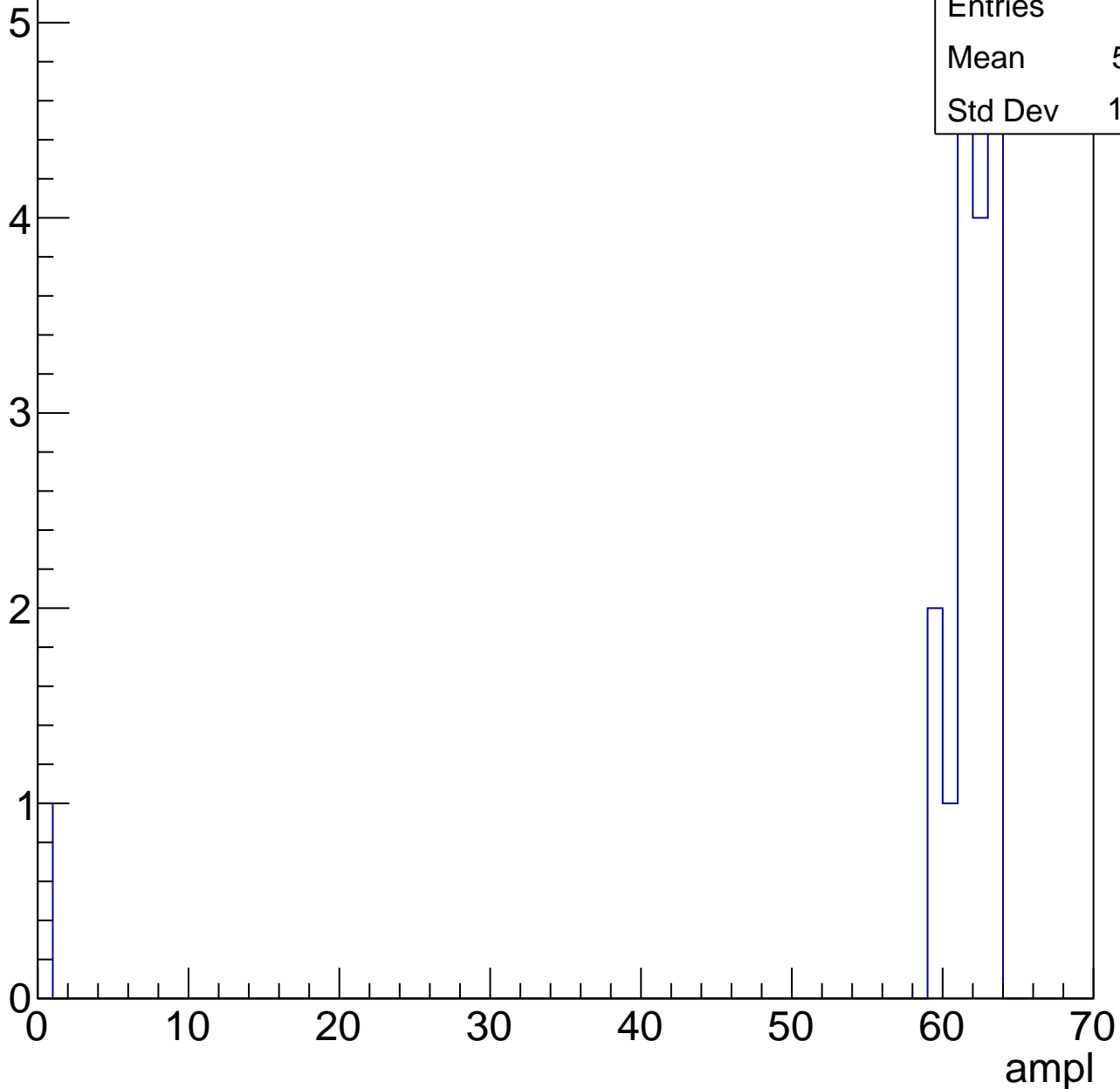


B1L002S, U7-ch127, adc6

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.11
Std Dev	14.15



B1L002S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry



B1L002S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#10, port B3

Entry

