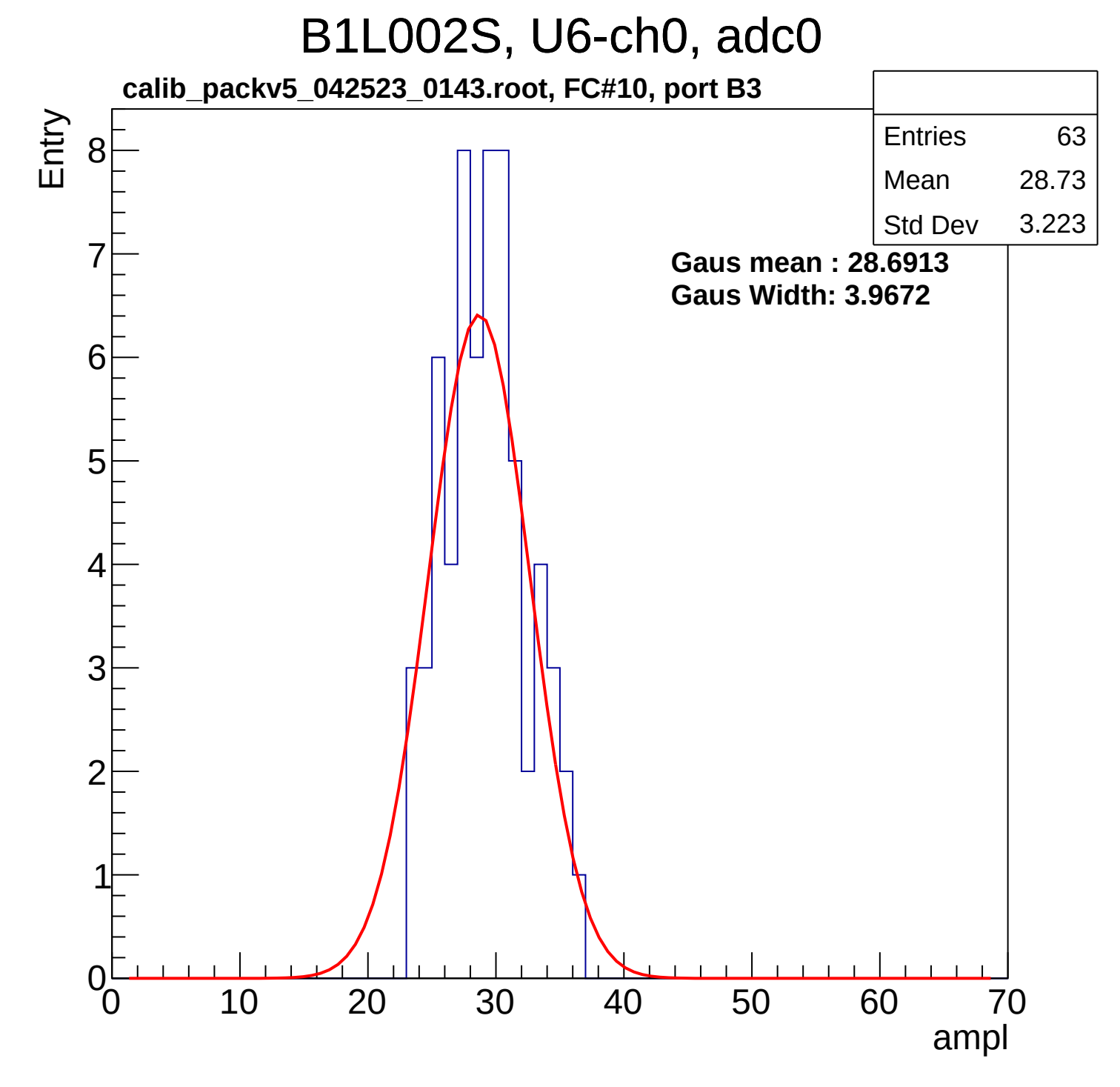
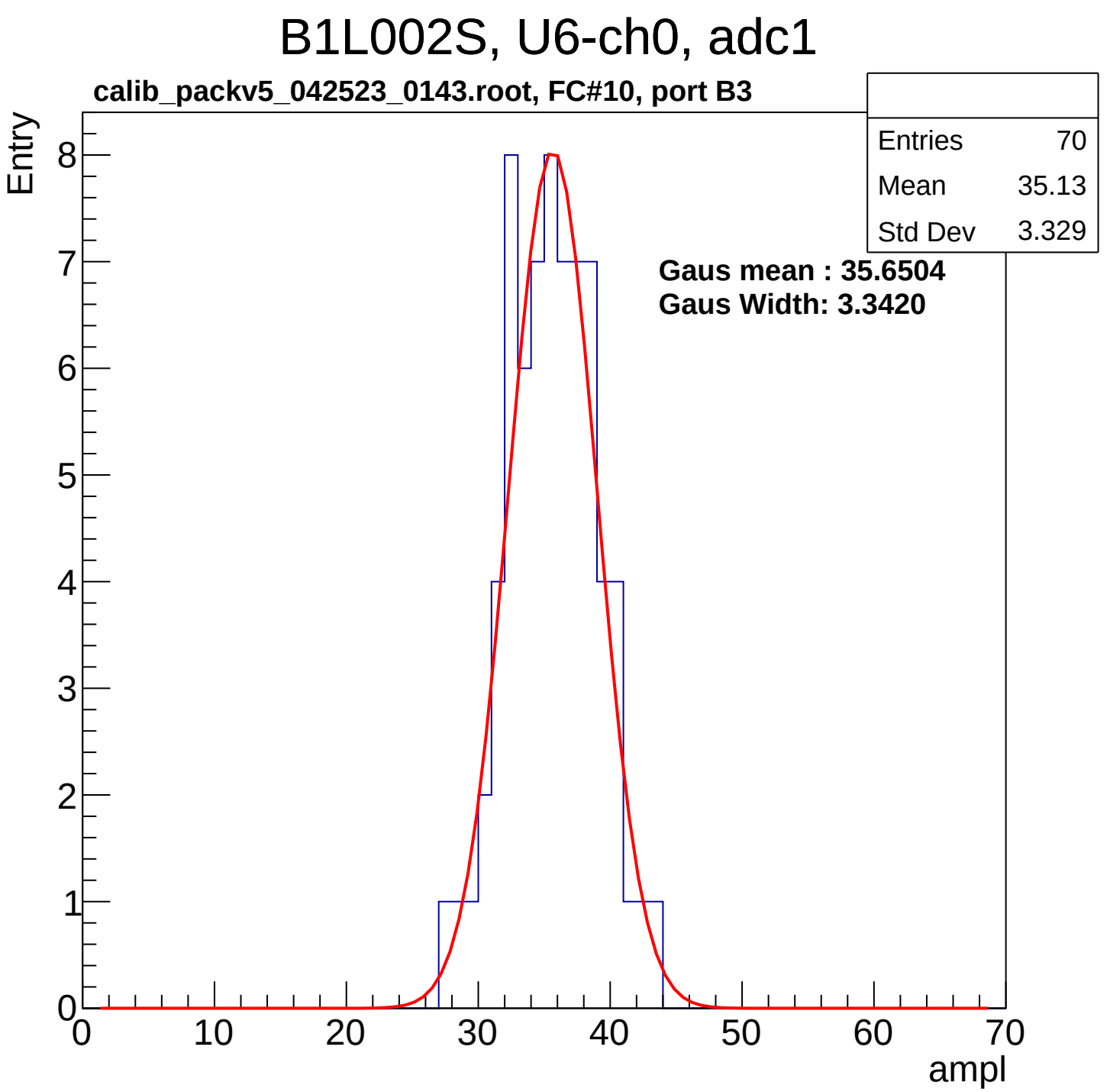
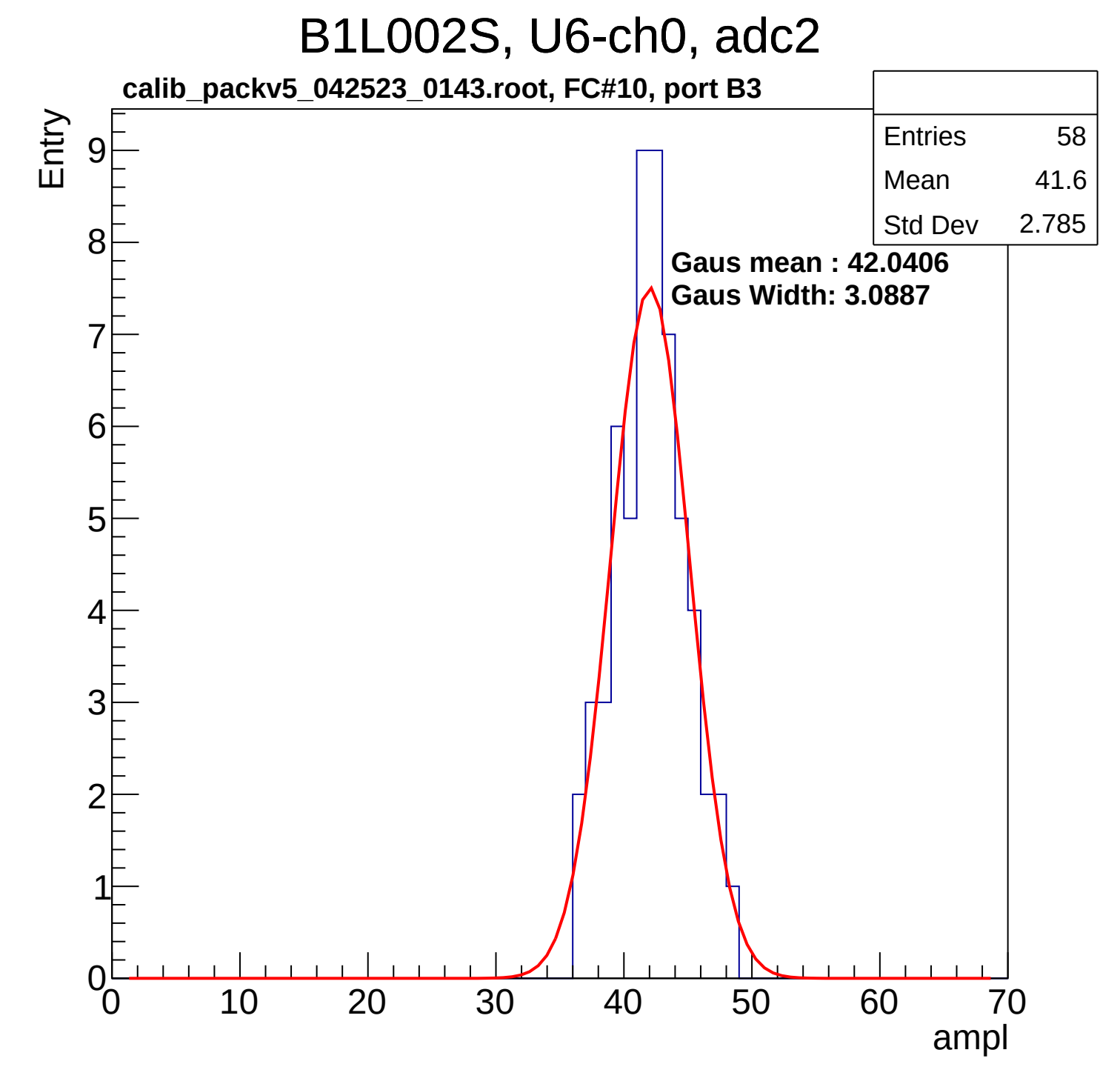
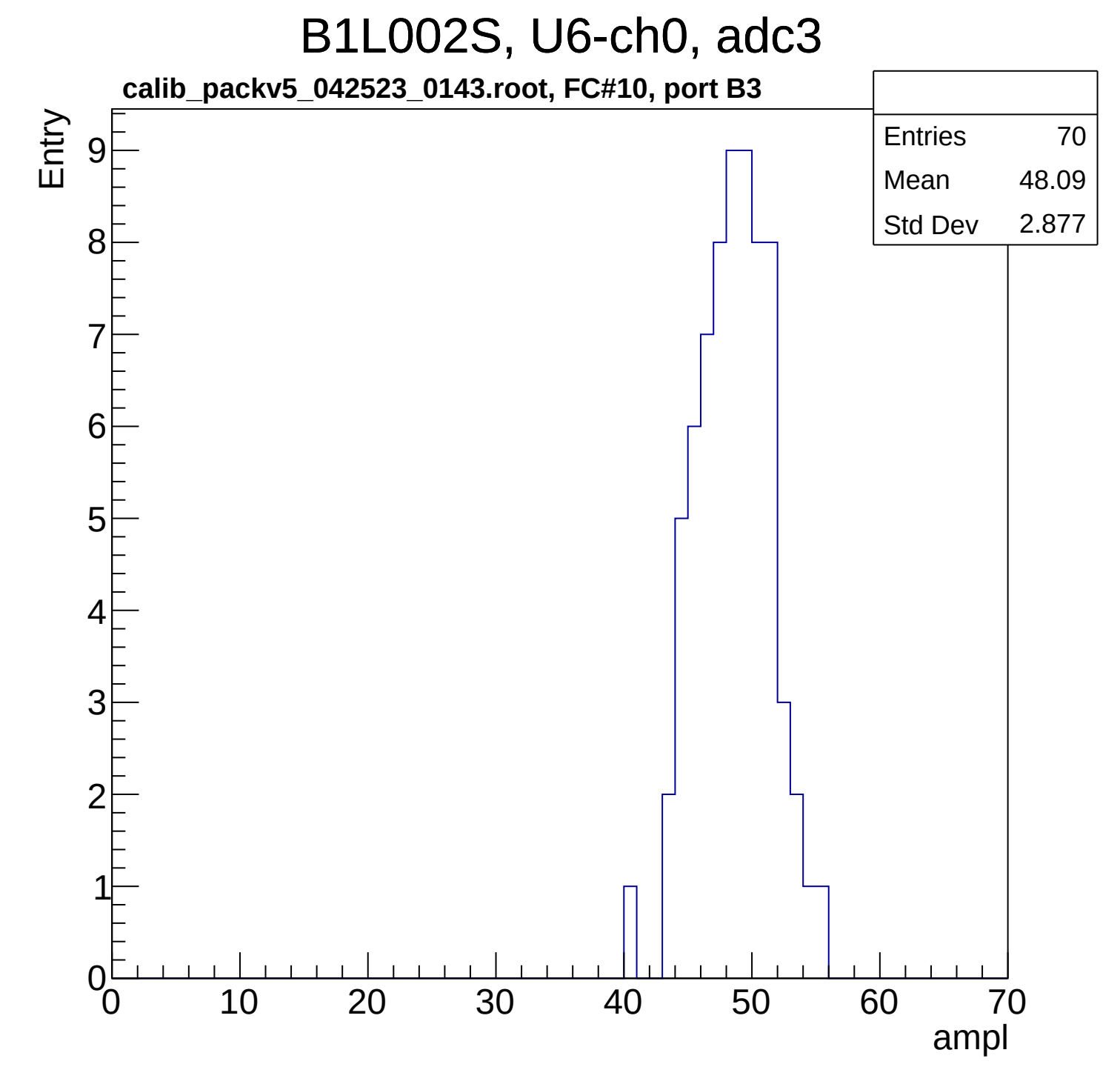
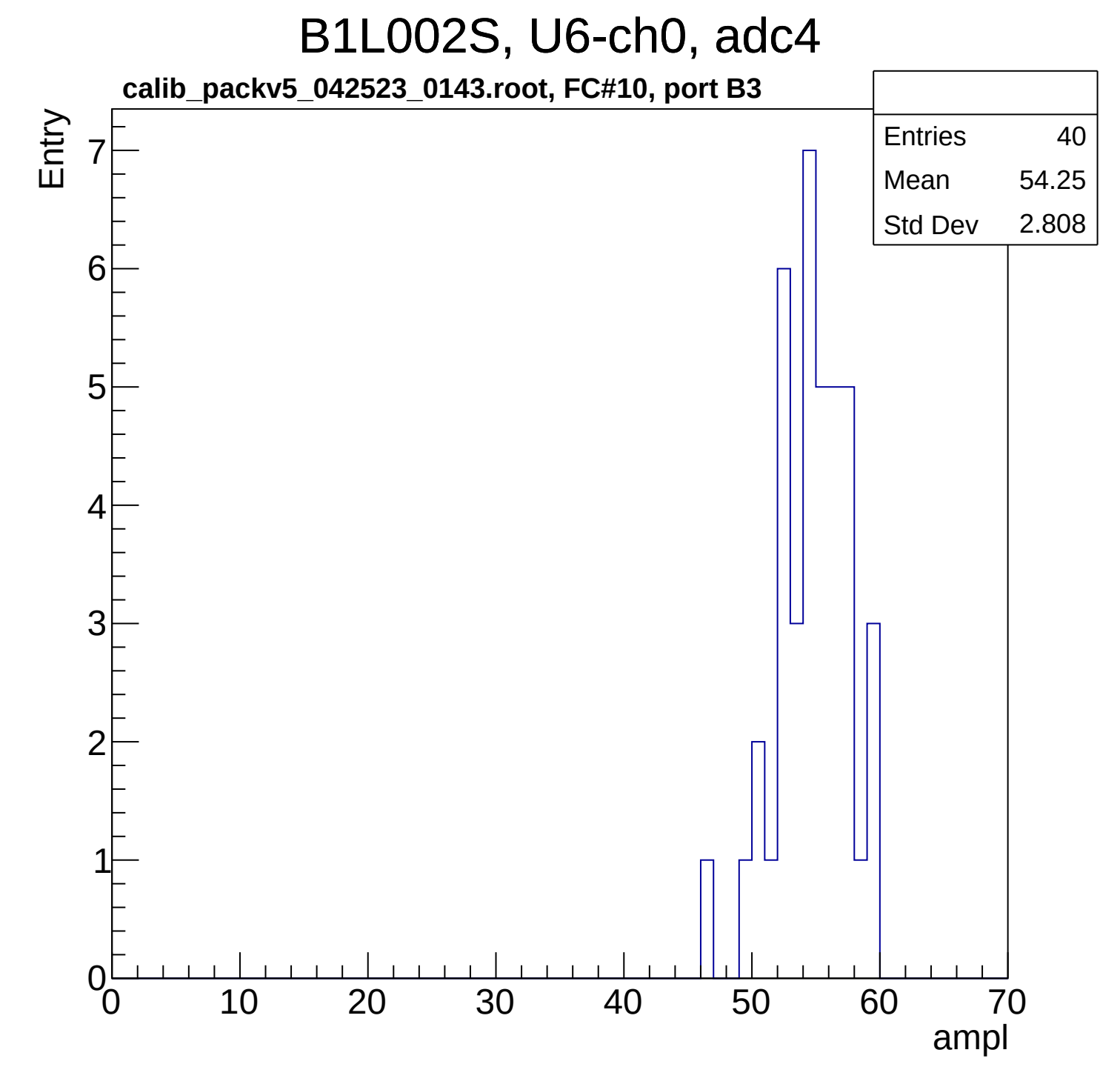
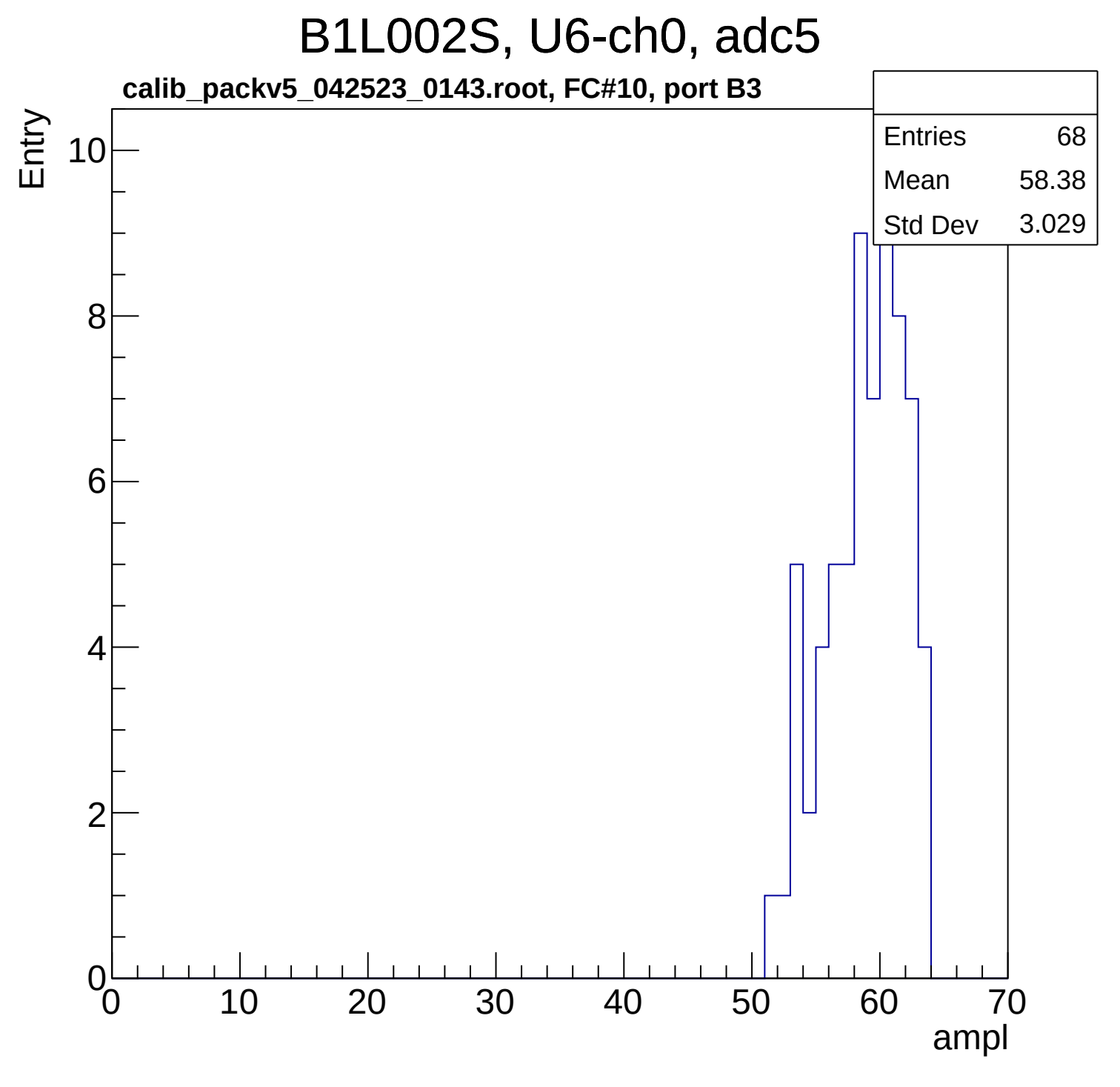

B1L002S, U6-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.73
Std Dev 3.223
Gaus mean : 28.6913
Gaus Width: 3.9672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

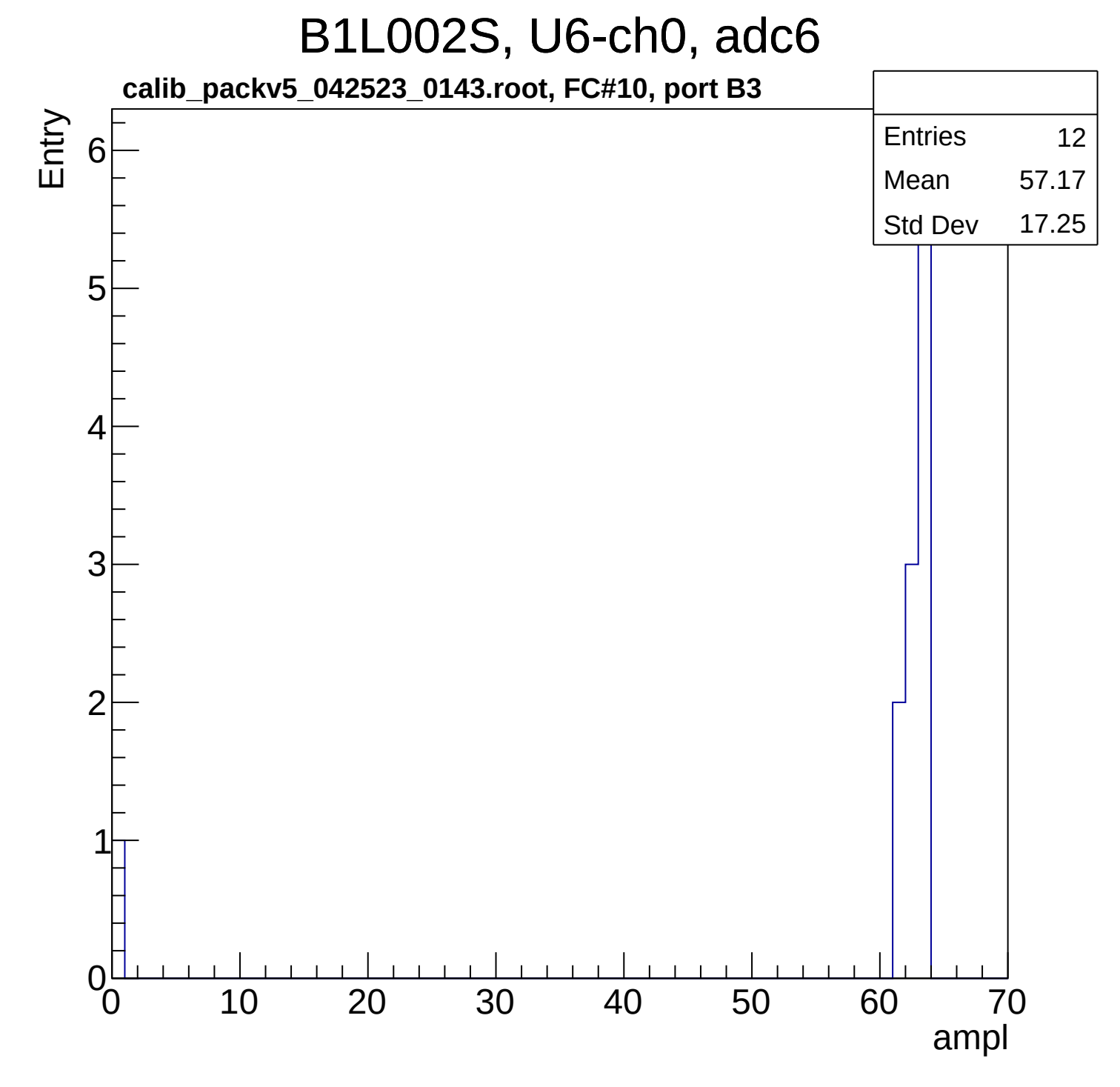

B1L002S, U6-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.13
Std Dev 3.329
Gaus mean : 35.6504
Gaus Width: 3.3420
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

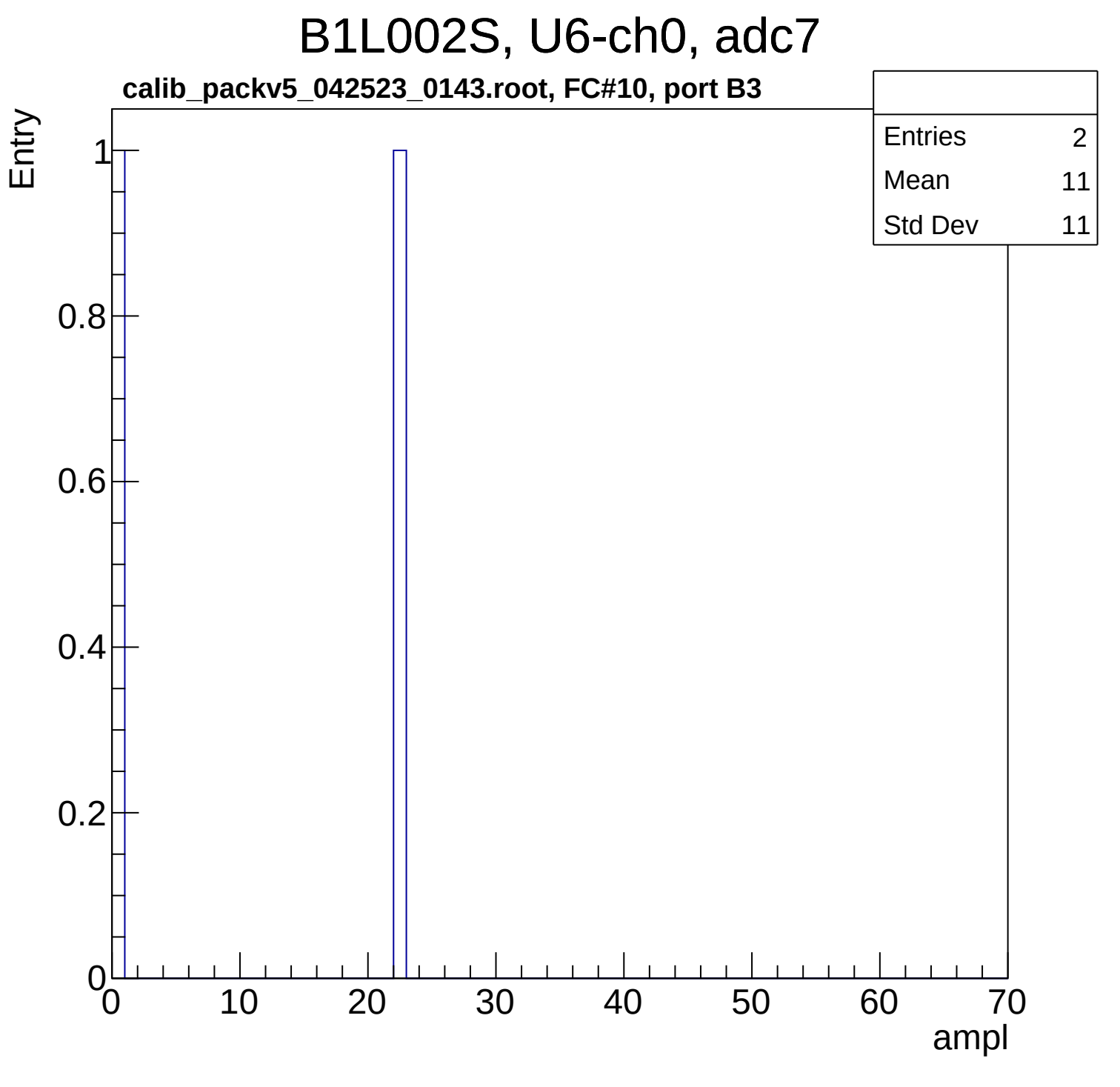

B1L002S, U6-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.6
Std Dev 2.785
Gaus mean : 42.0406
Gaus Width: 3.0887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

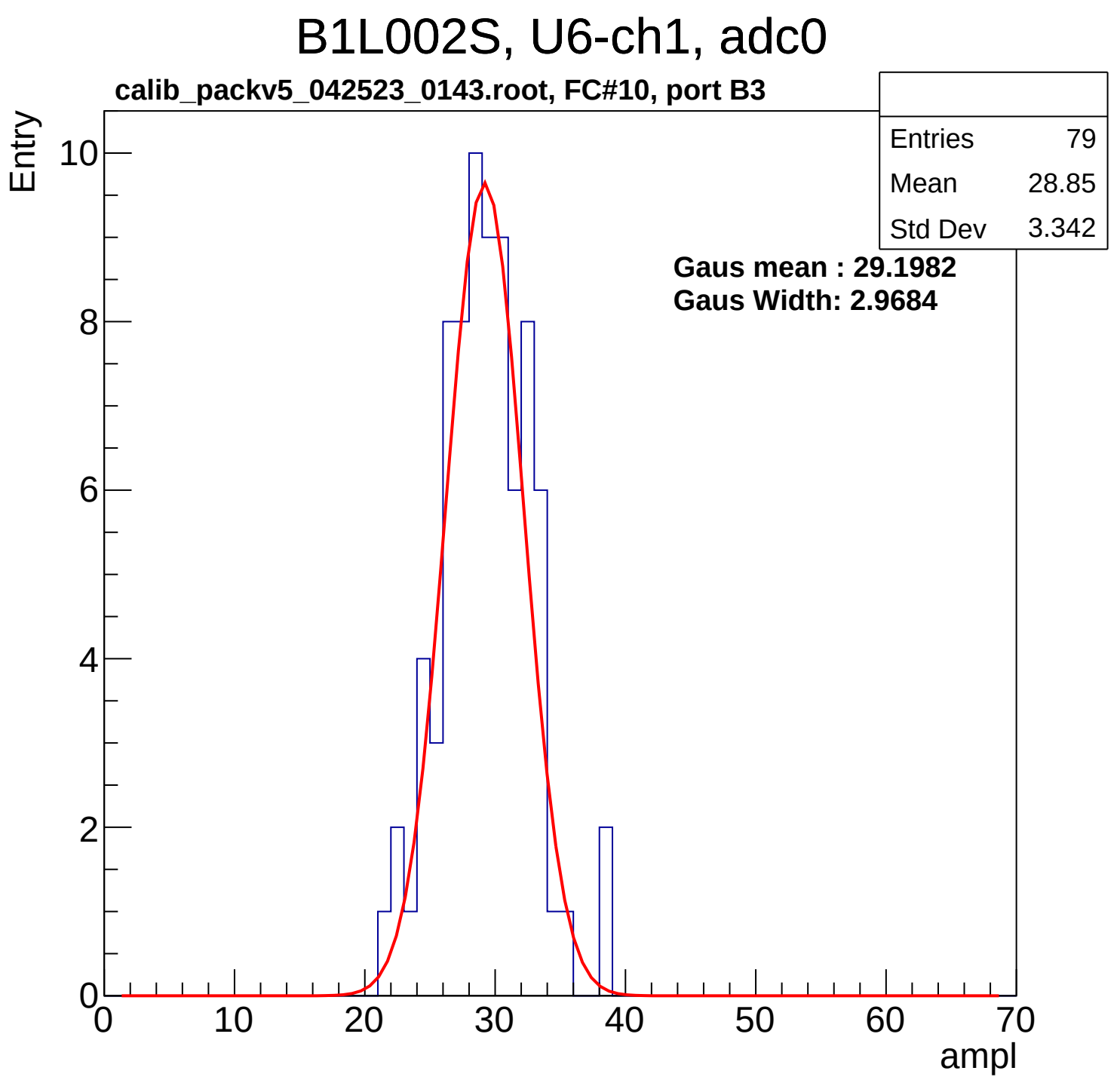

B1L002S, U6-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.09
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

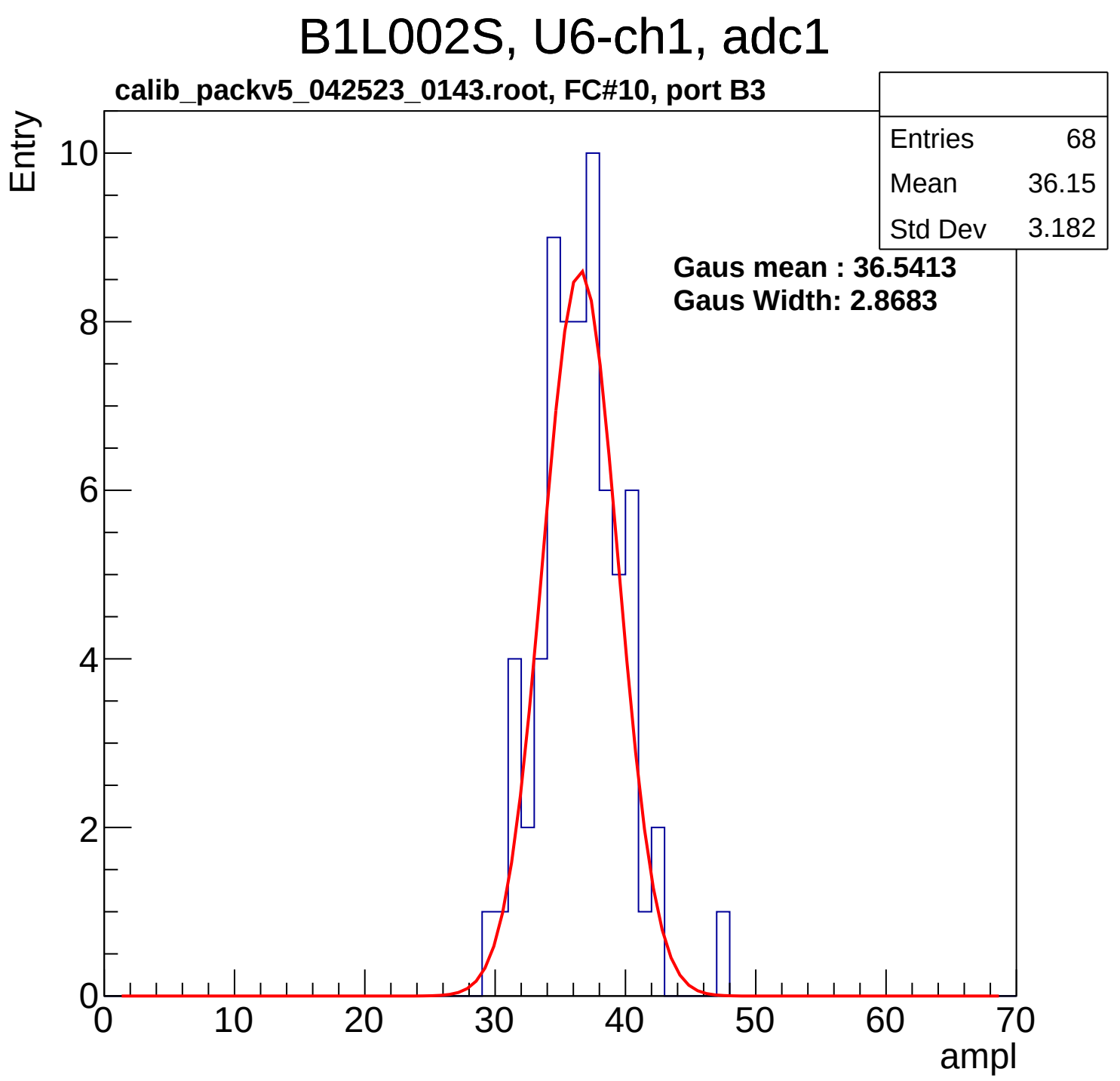

B1L002S, U6-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 54.25
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

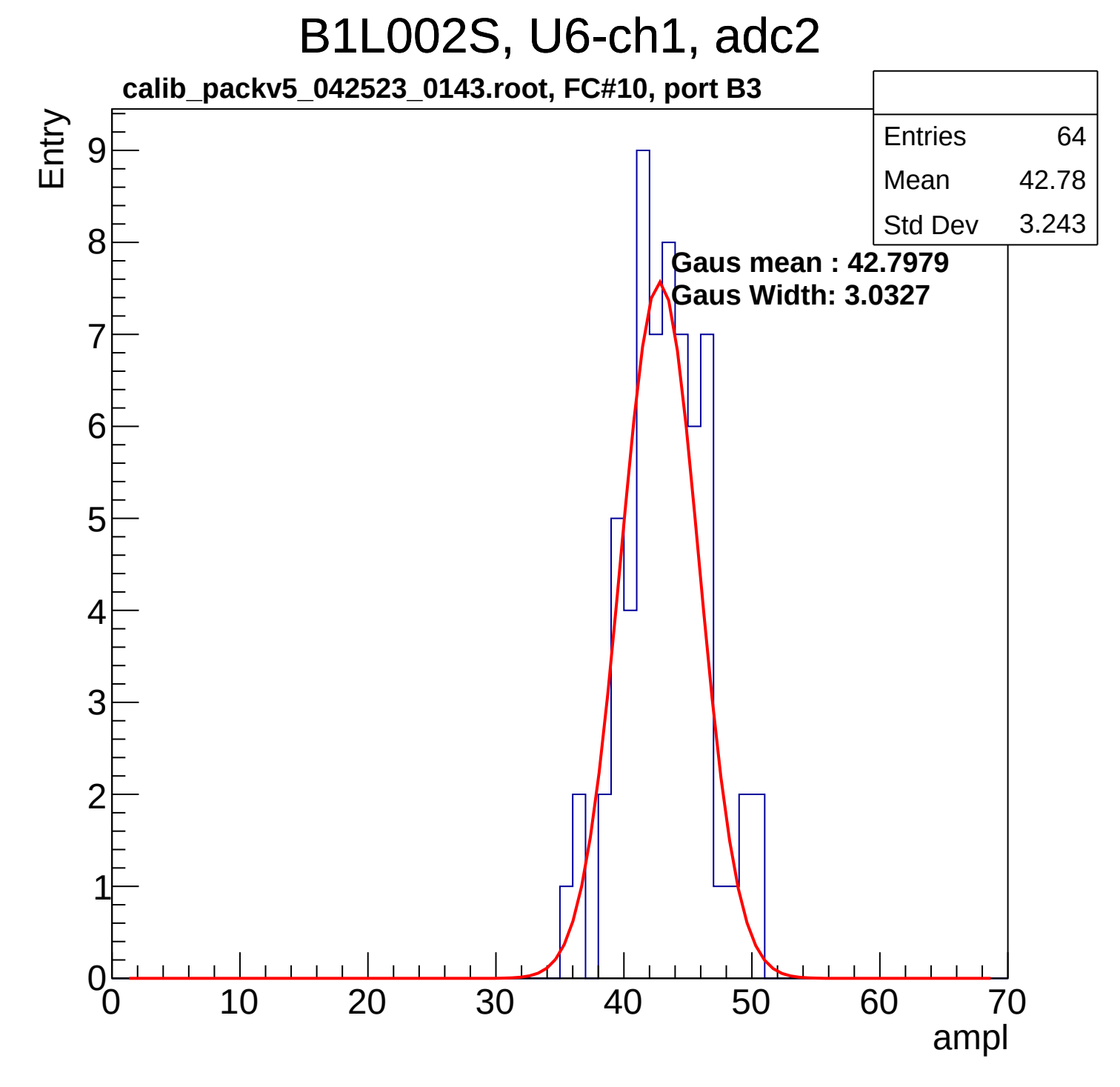

B1L002S, U6-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 58.38
Std Dev 3.029
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

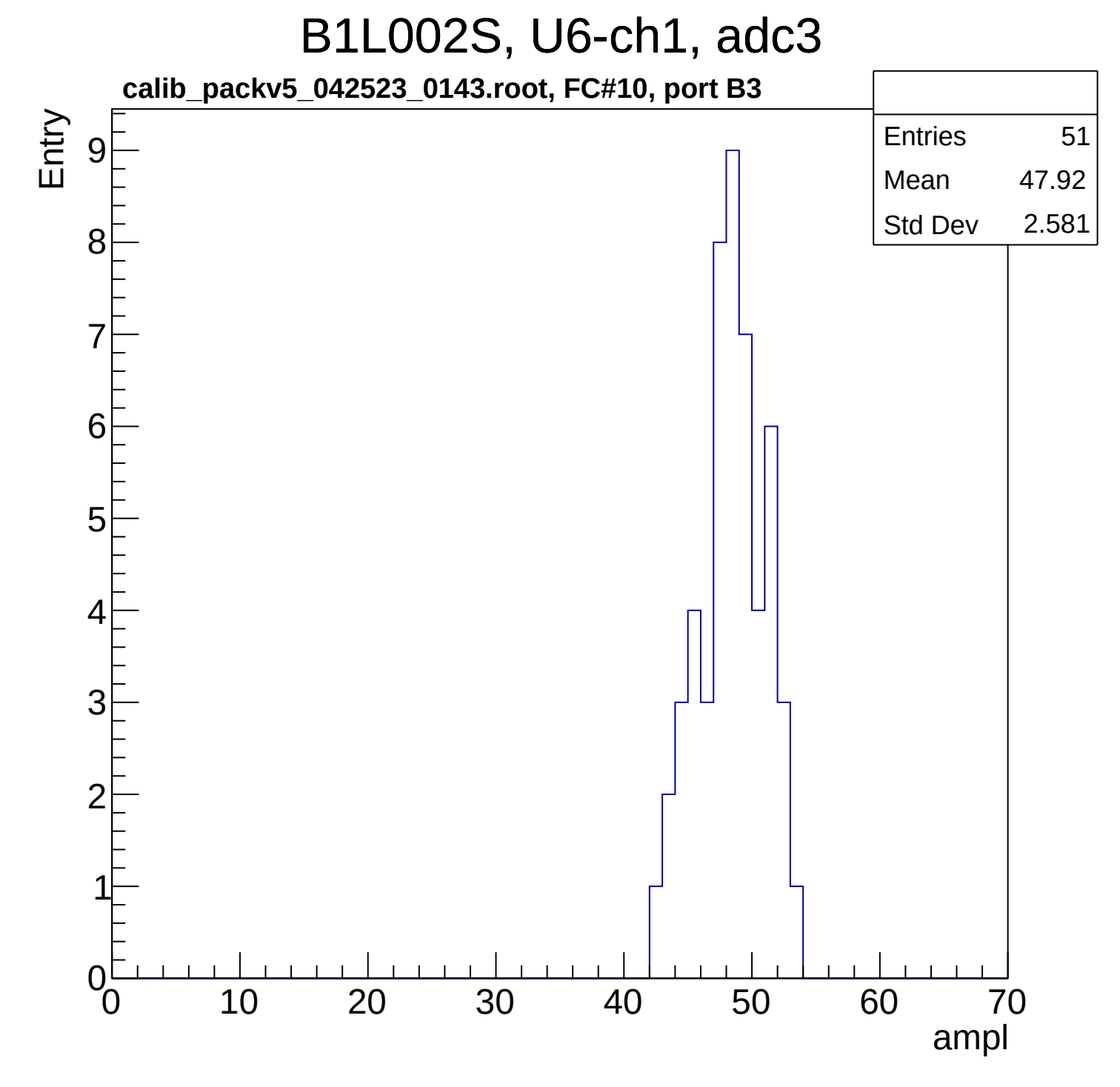

B1L002S, U6-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 57.17
Std Dev 17.25
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

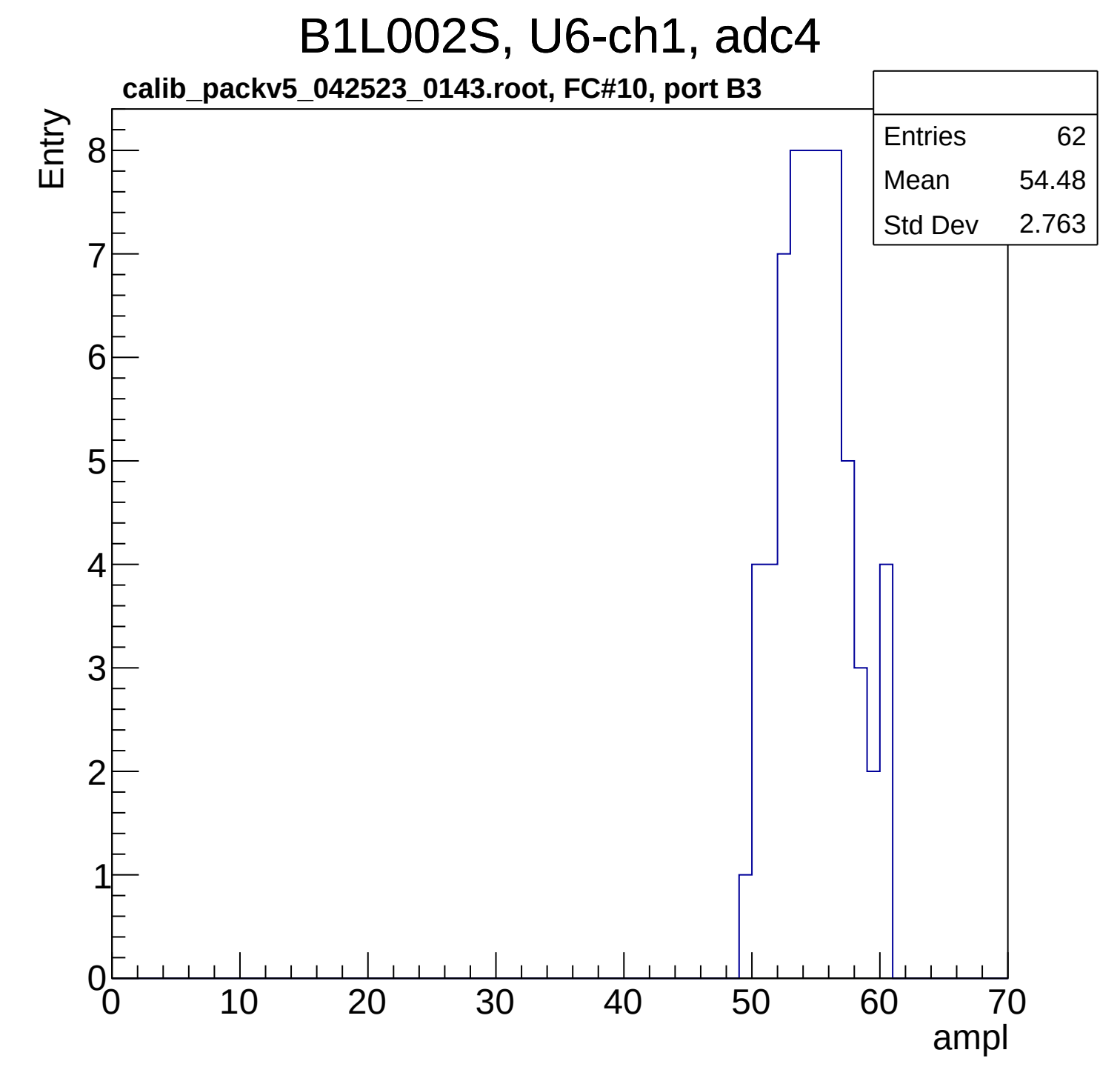

B1L002S, U6-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

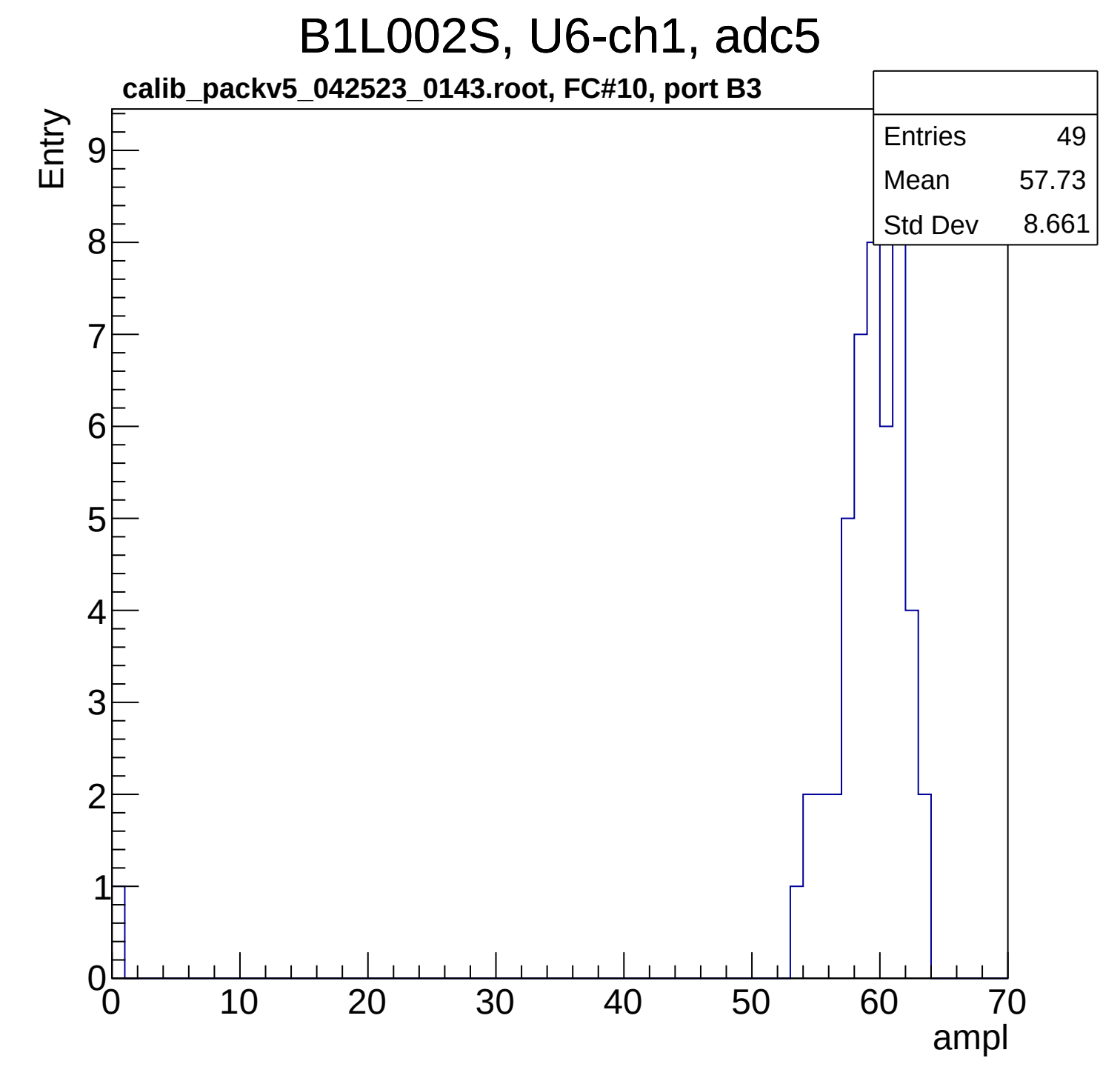

B1L002S, U6-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 28.85
Std Dev 3.342
Gaus mean : 29.1982
Gaus Width: 2.9684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

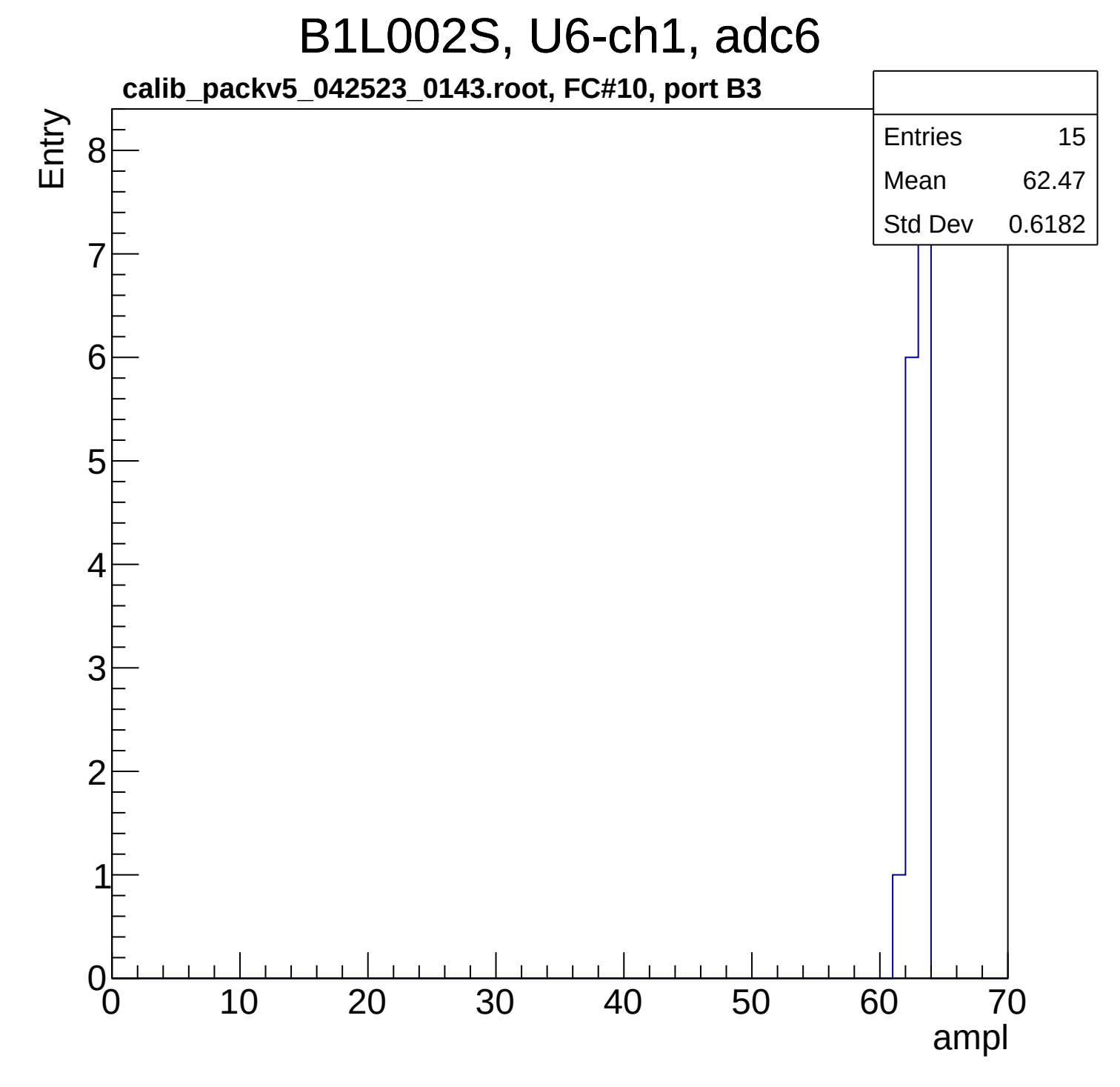

B1L002S, U6-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.15
Std Dev 3.182
Gaus mean : 36.5413
Gaus Width: 2.8683
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

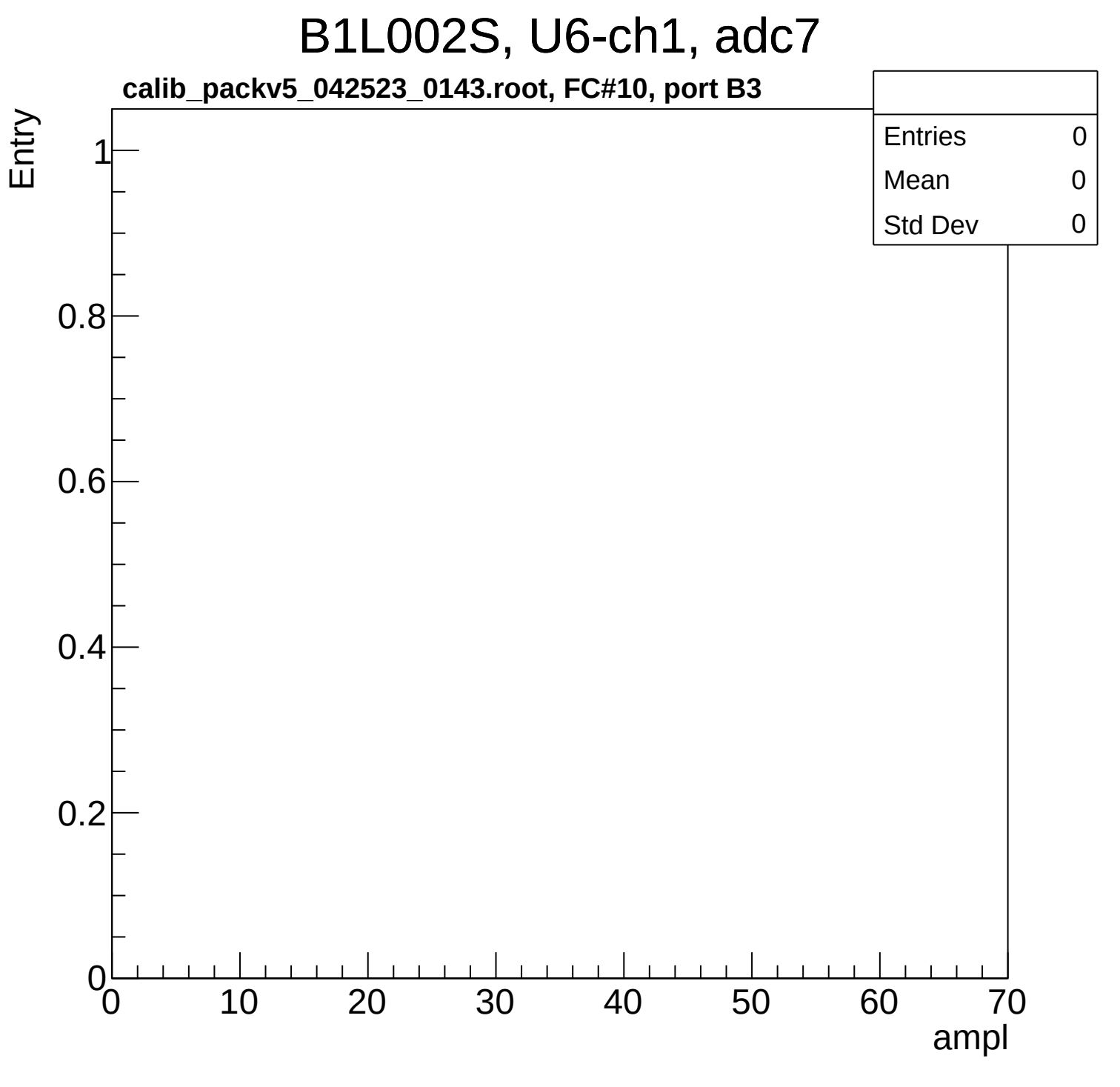

B1L002S, U6-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.78
Std Dev 3.243
Gaus mean : 42.7979
Gaus Width: 3.0327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

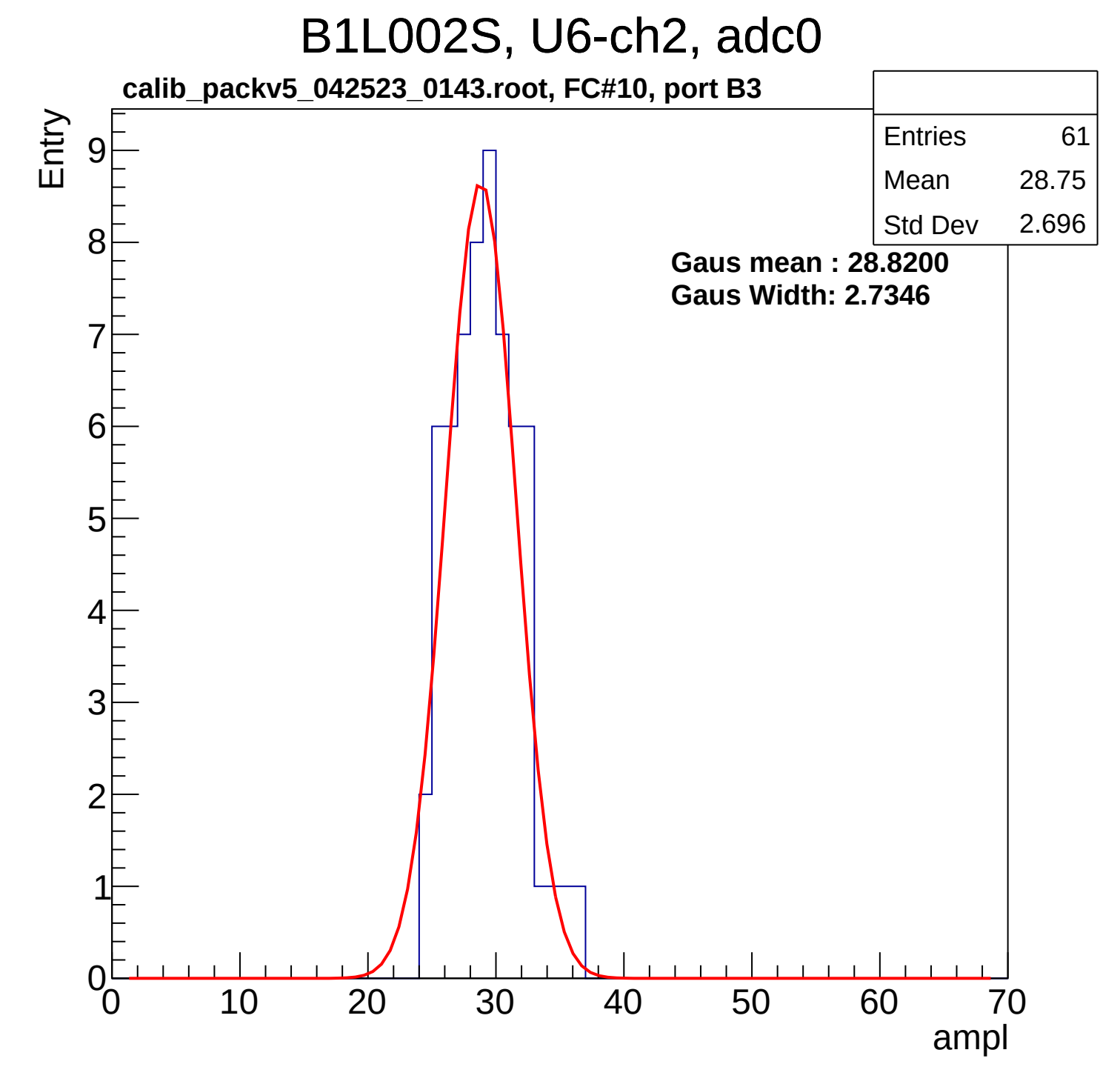

B1L002S, U6-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.92
Std Dev 2.581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

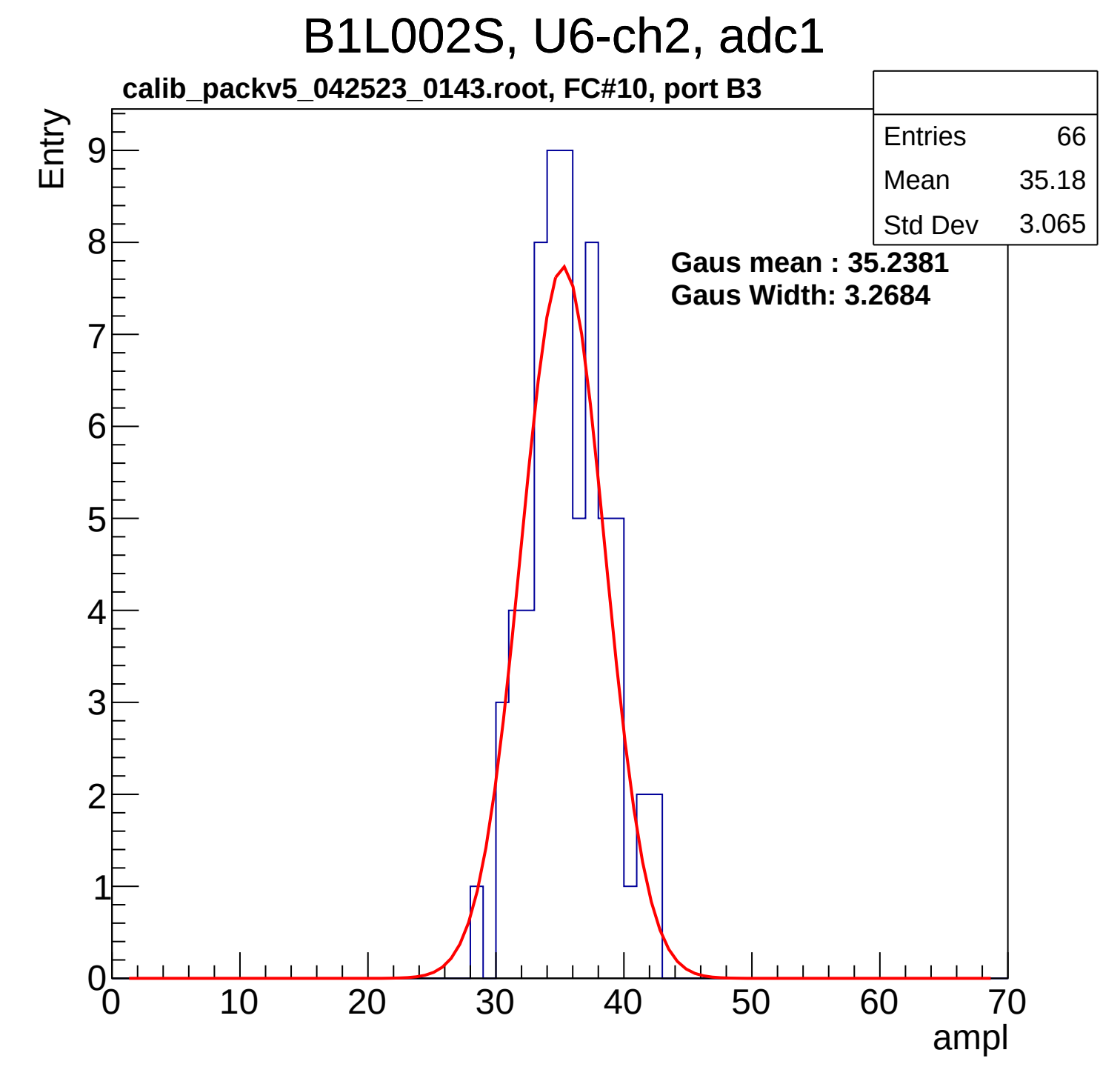

B1L002S, U6-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.48
Std Dev 2.763
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

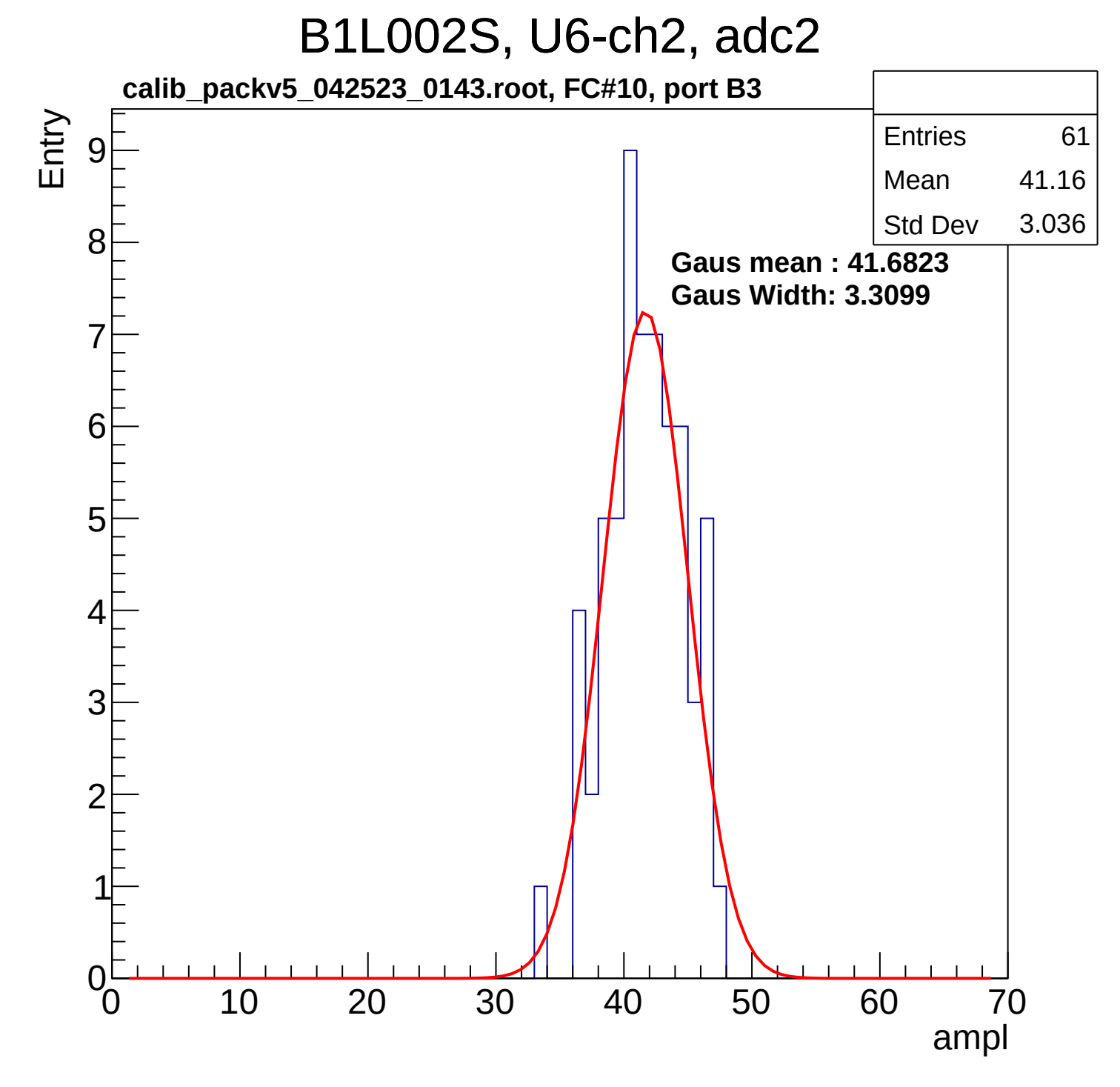

B1L002S, U6-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.73
Std Dev 8.661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

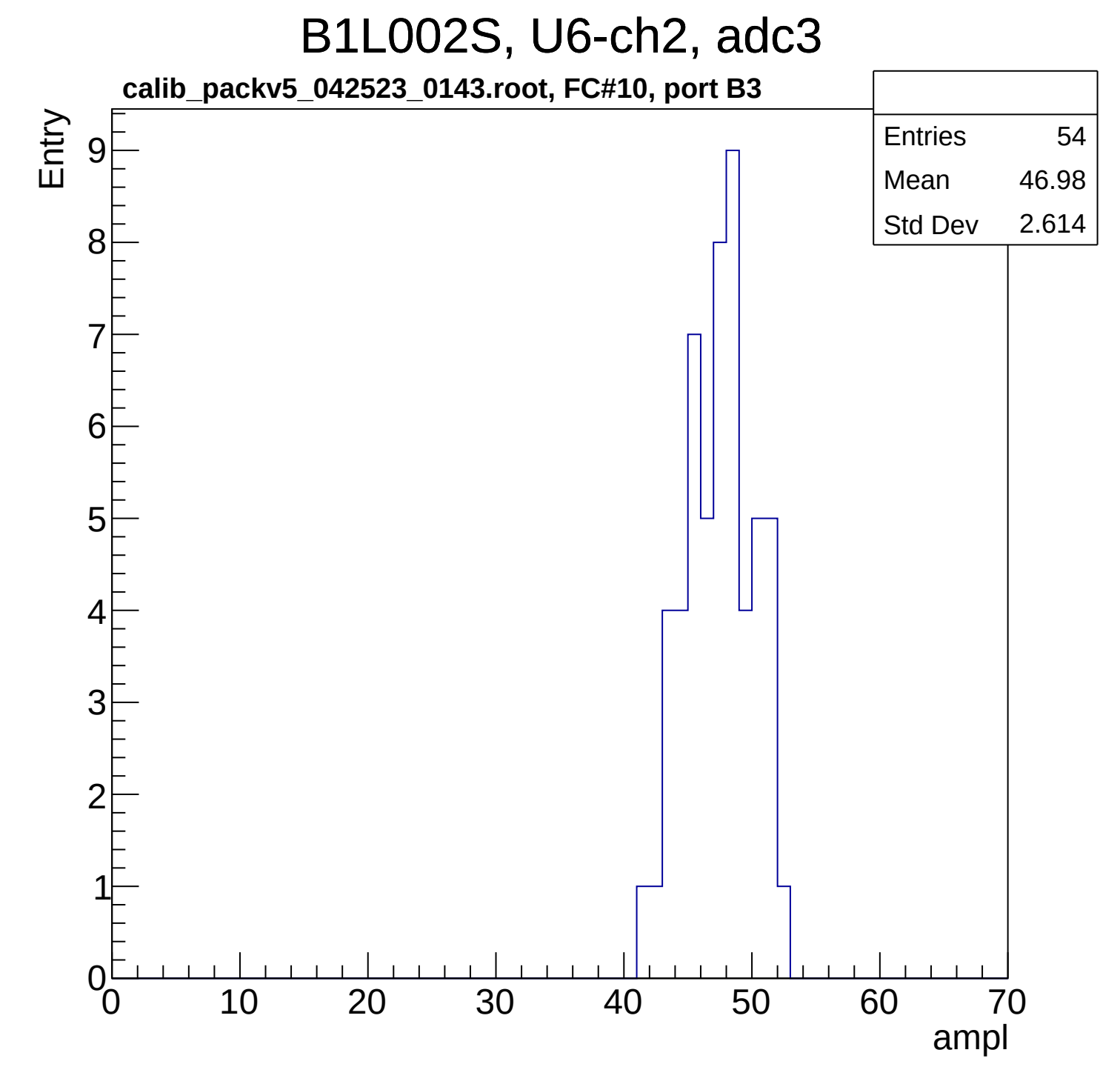

B1L002S, U6-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 62.47
Std Dev 0.6182
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

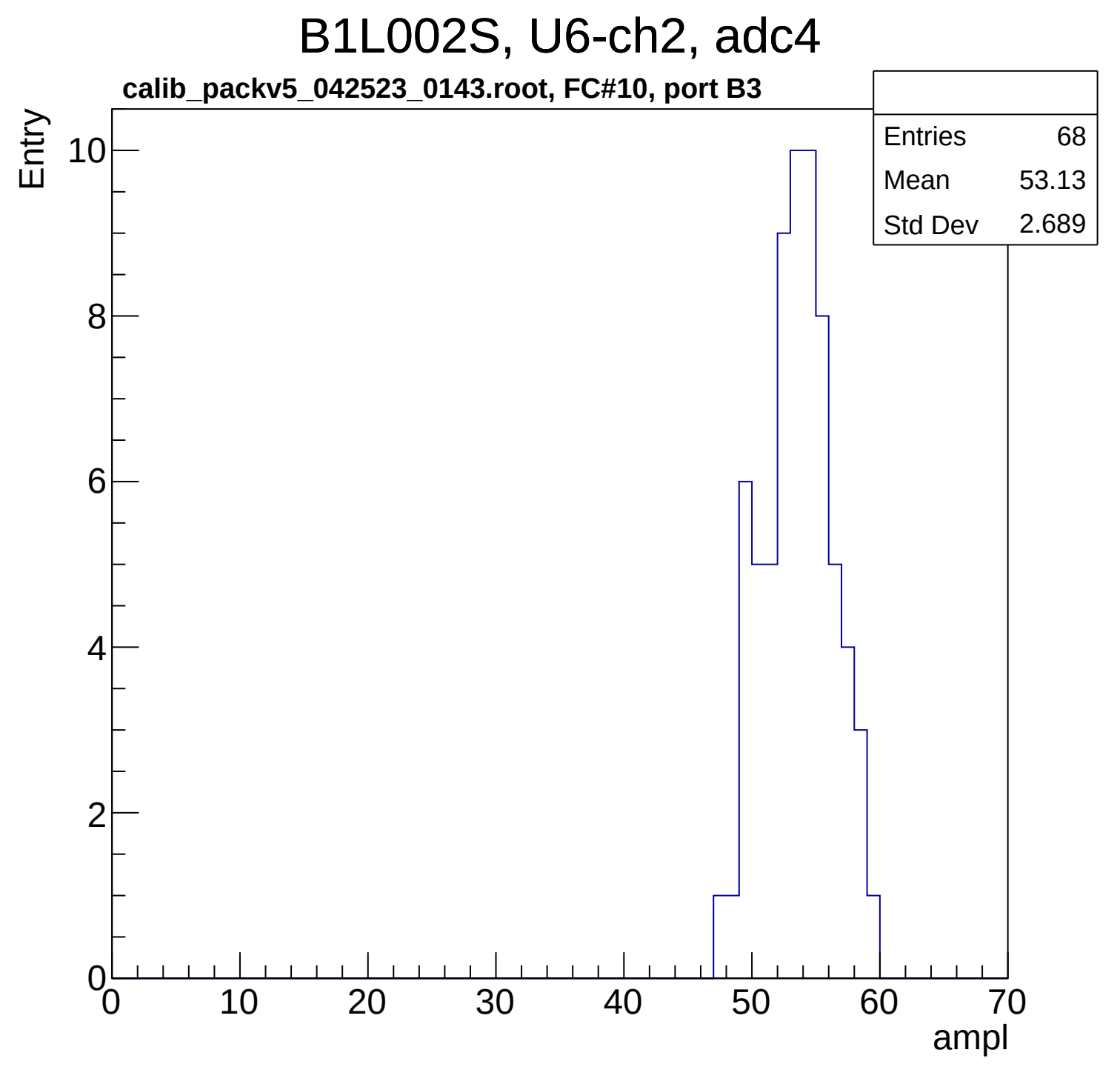

B1L002S, U6-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

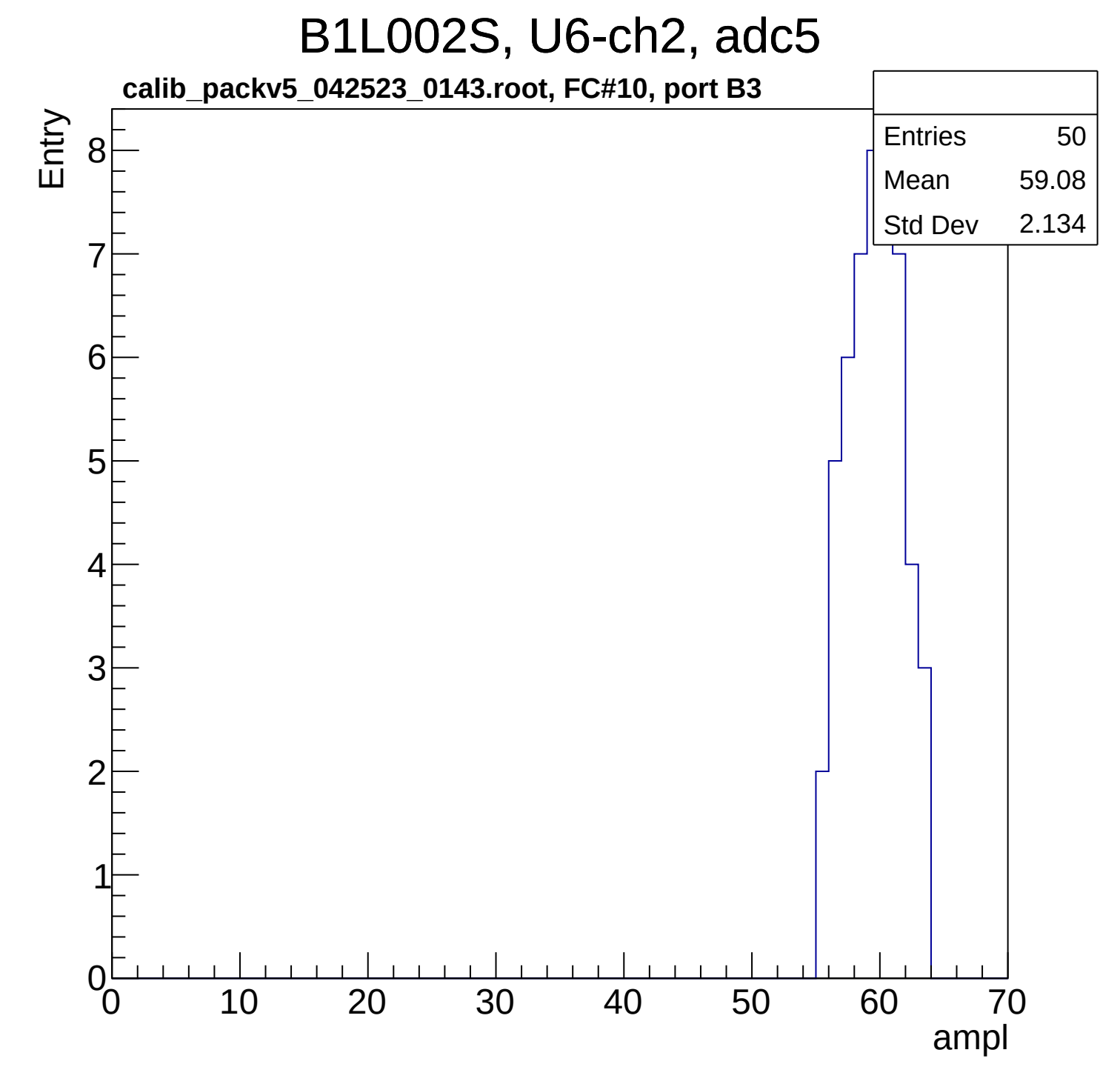

B1L002S, U6-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.75
Std Dev 2.696
Gaus mean : 28.8200
Gaus Width: 2.7346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

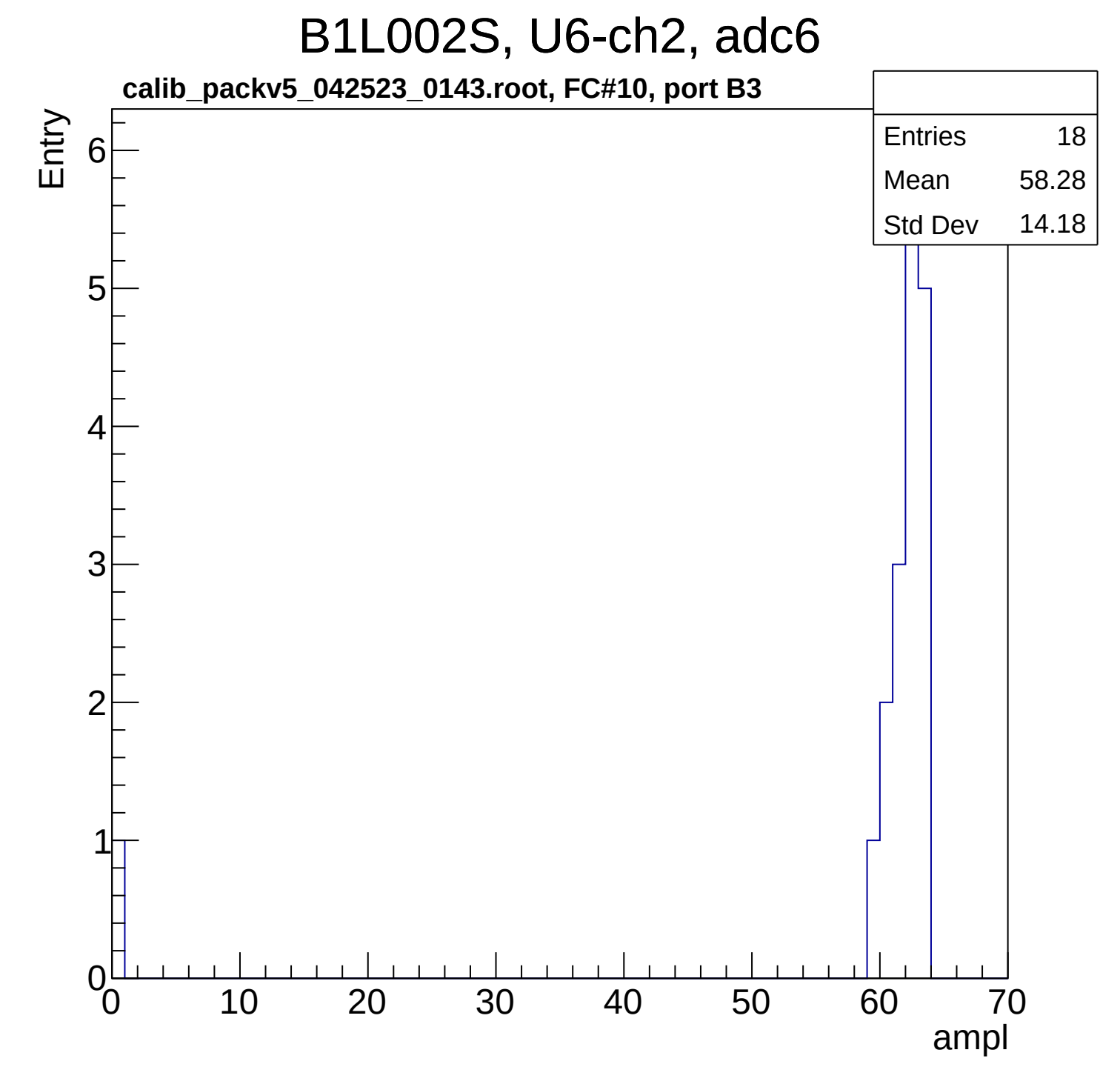

B1L002S, U6-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.18
Std Dev 3.065
Gaus mean : 35.2381
Gaus Width: 3.2684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

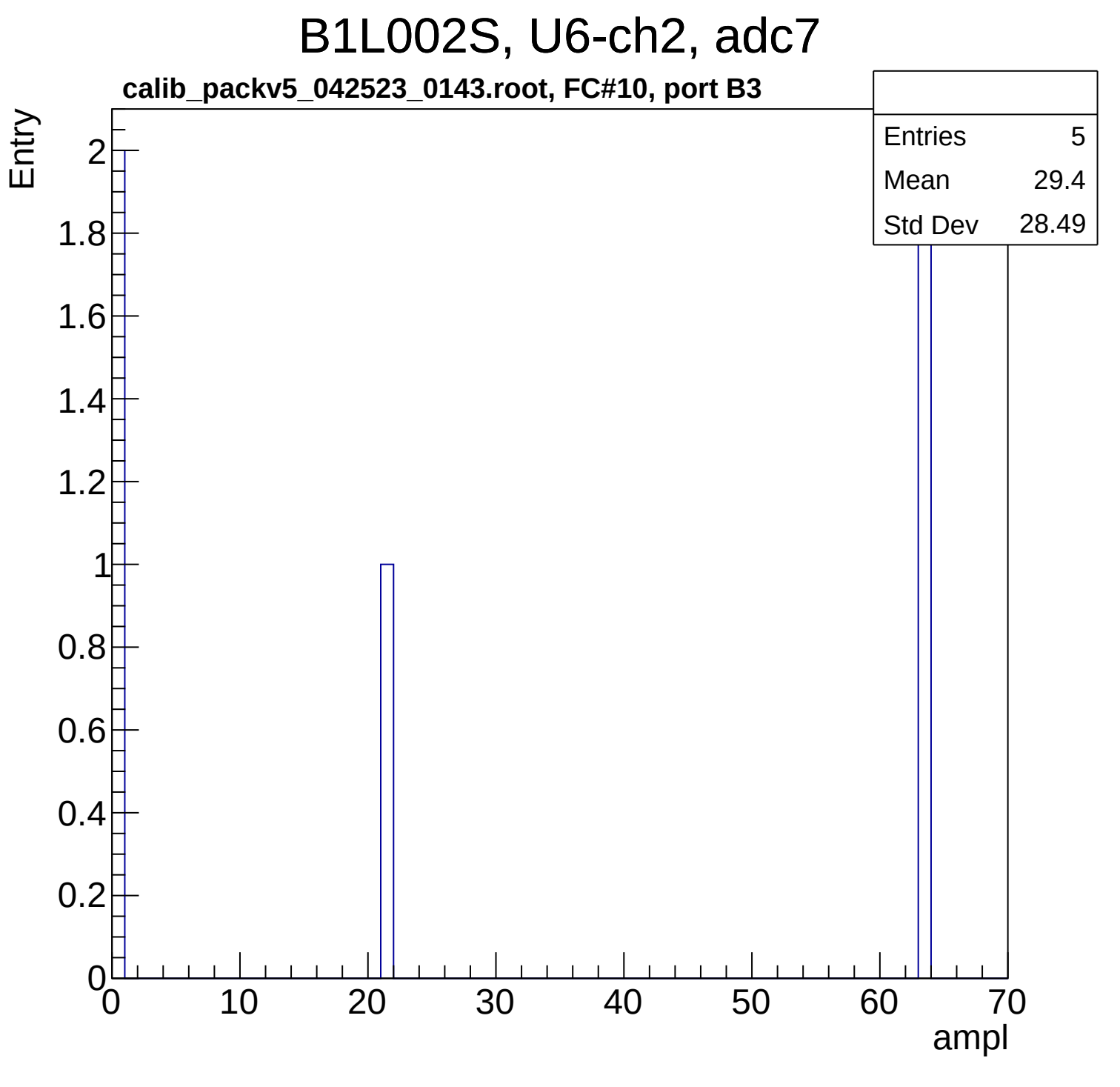

B1L002S, U6-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.16
Std Dev 3.036
Gaus mean : 41.6823
Gaus Width: 3.3099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

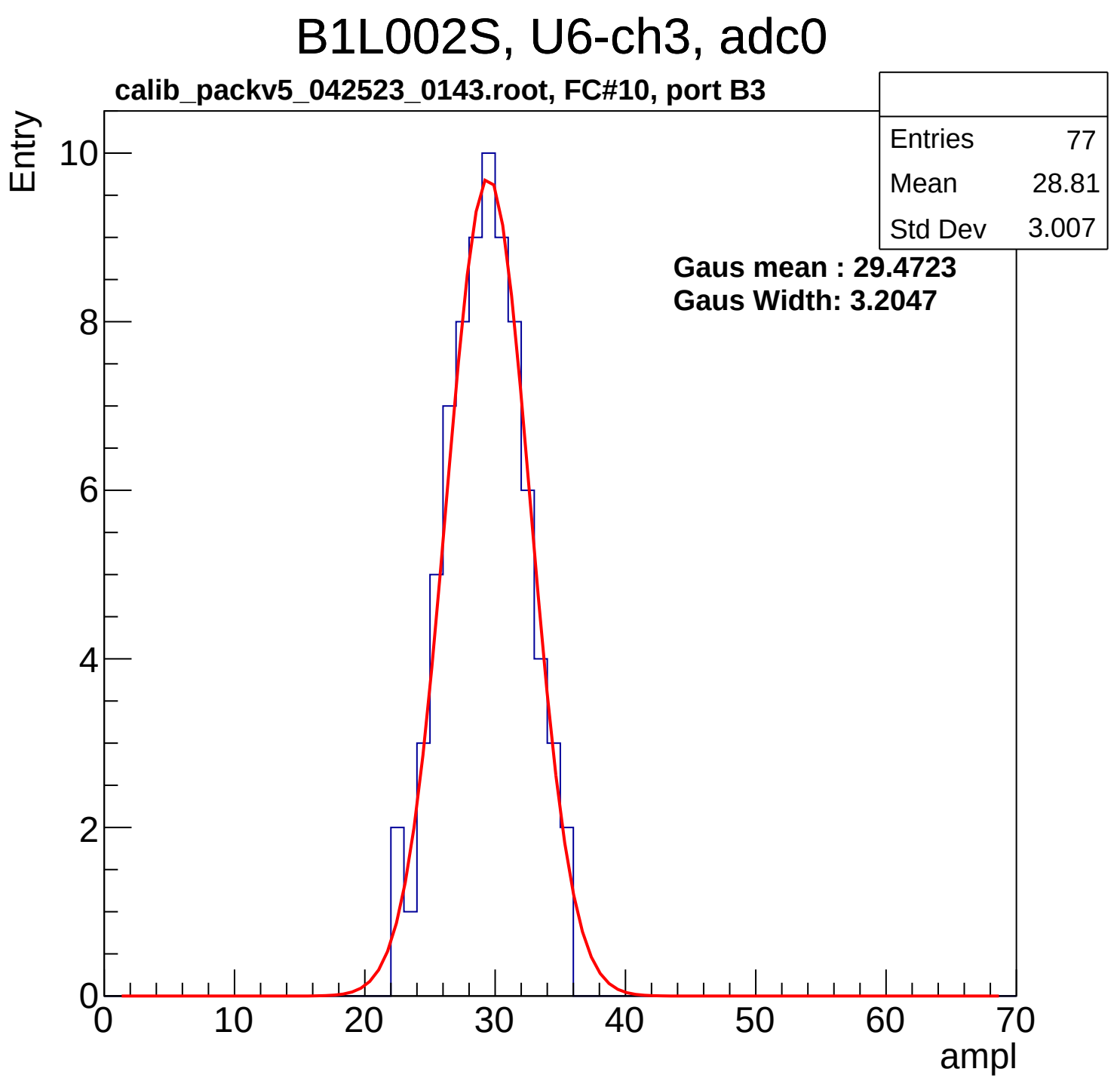

B1L002S, U6-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.98
Std Dev 2.614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

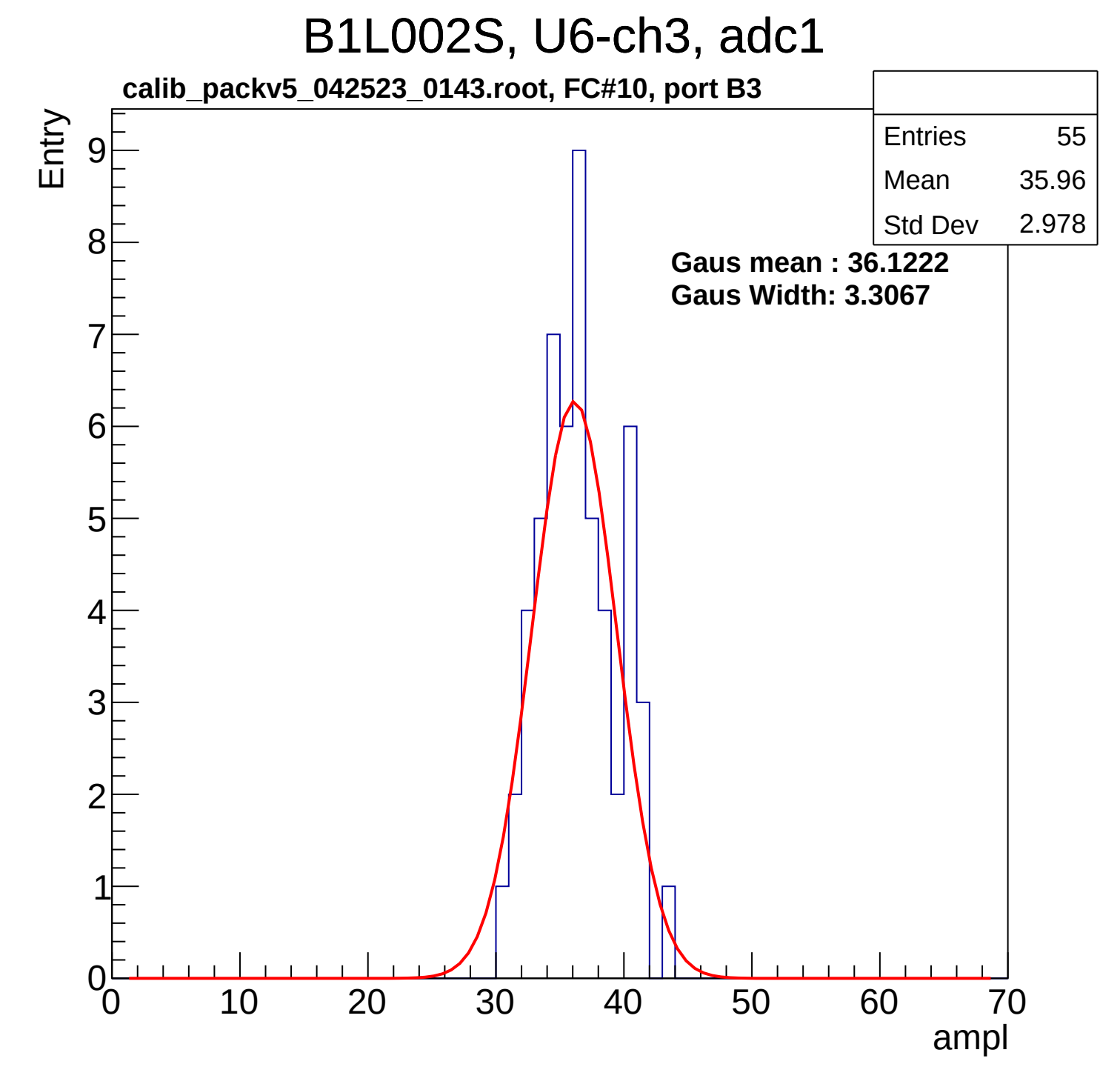

B1L002S, U6-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.13
Std Dev 2.689
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

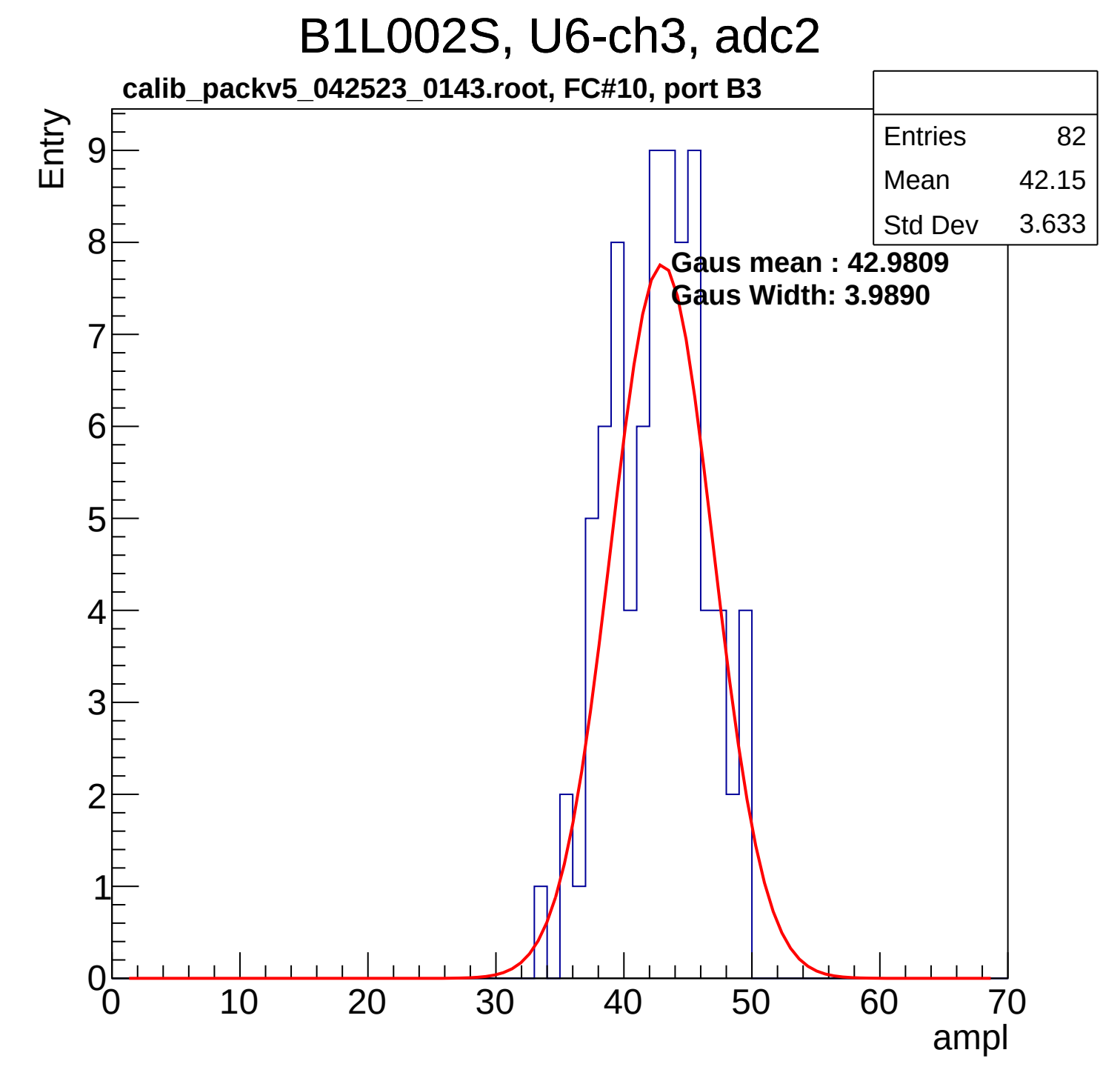

B1L002S, U6-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.08
Std Dev 2.134
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

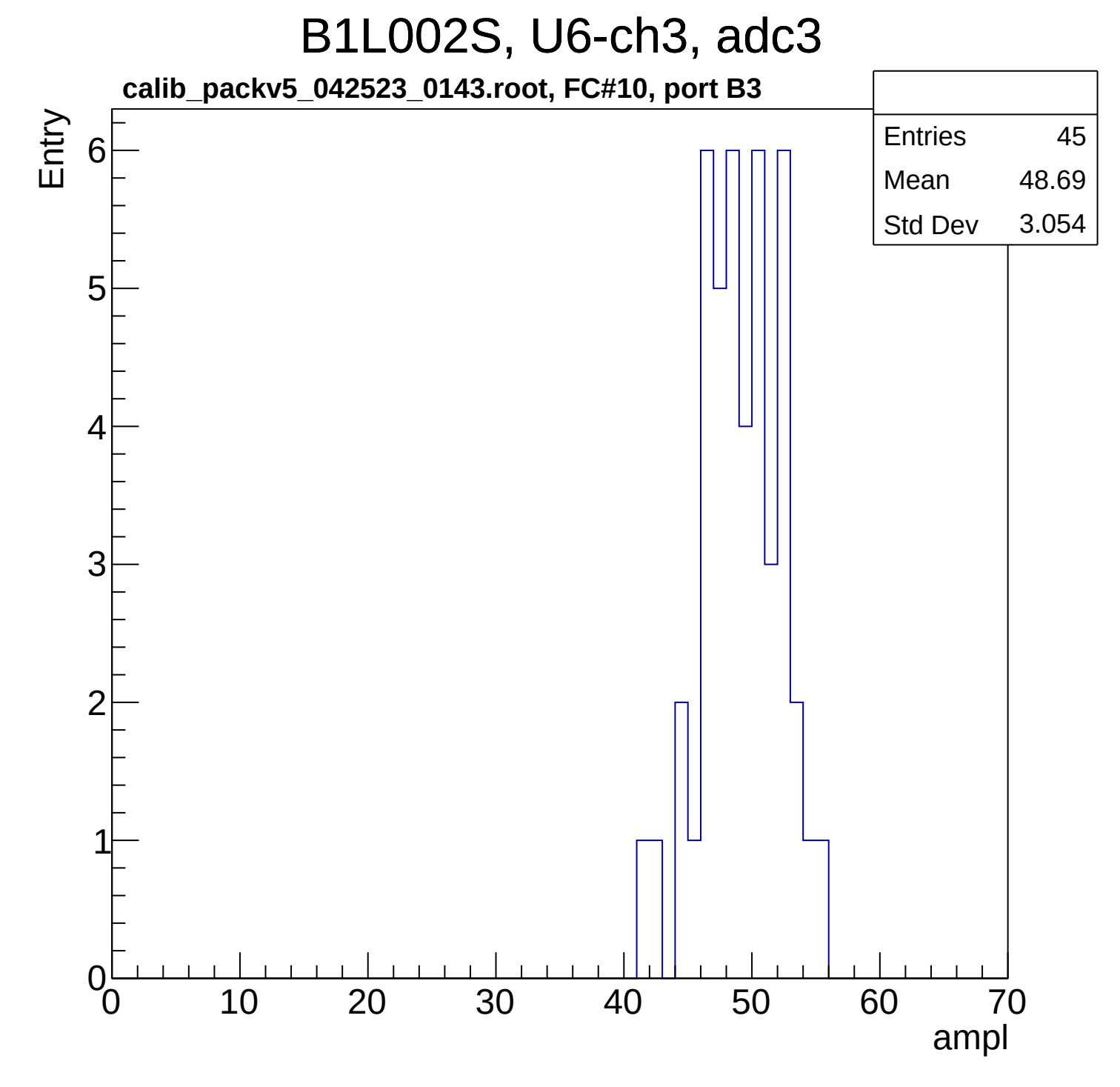

B1L002S, U6-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.28
Std Dev 14.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

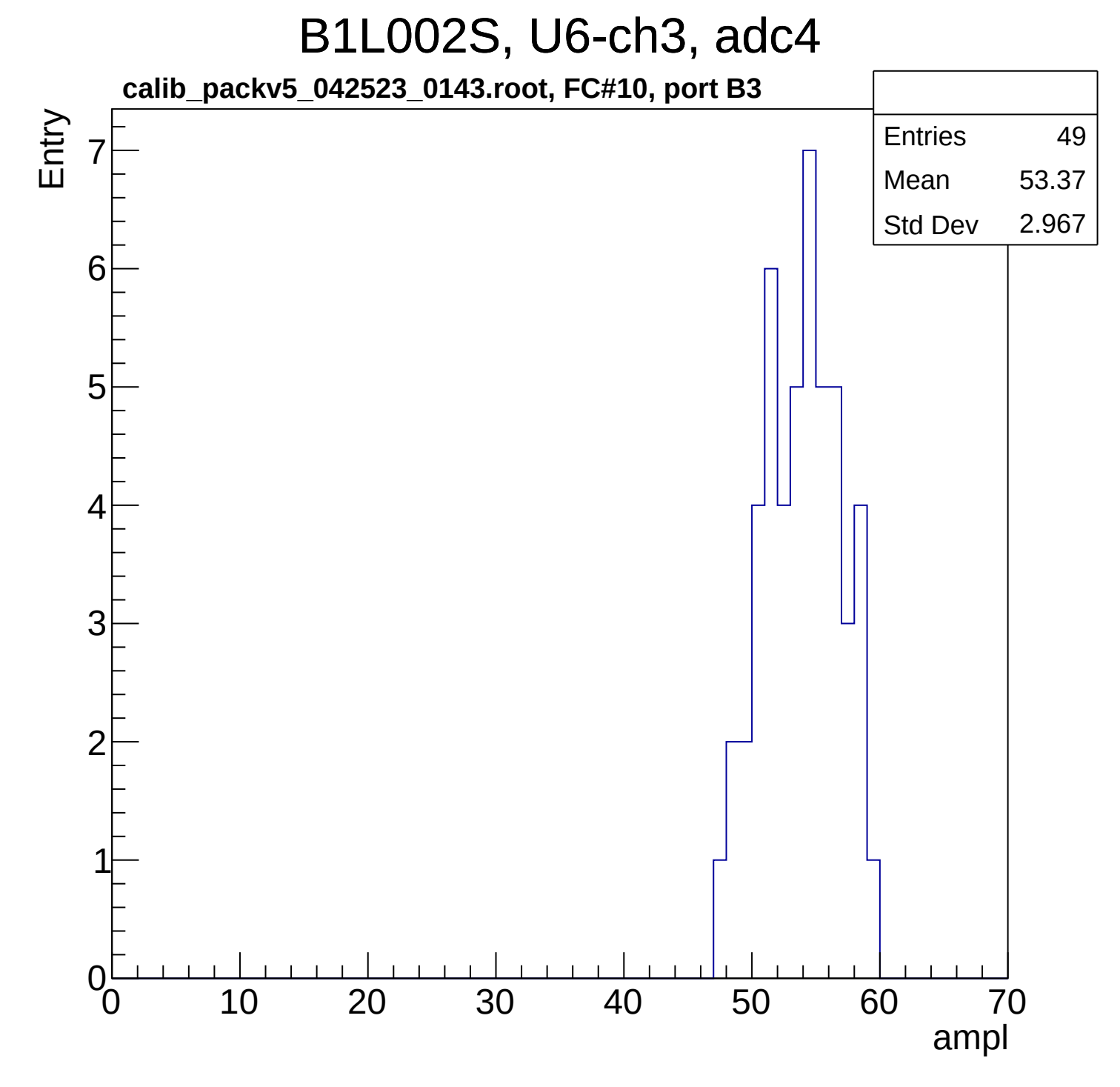

B1L002S, U6-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 29.4
Std Dev 28.49
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

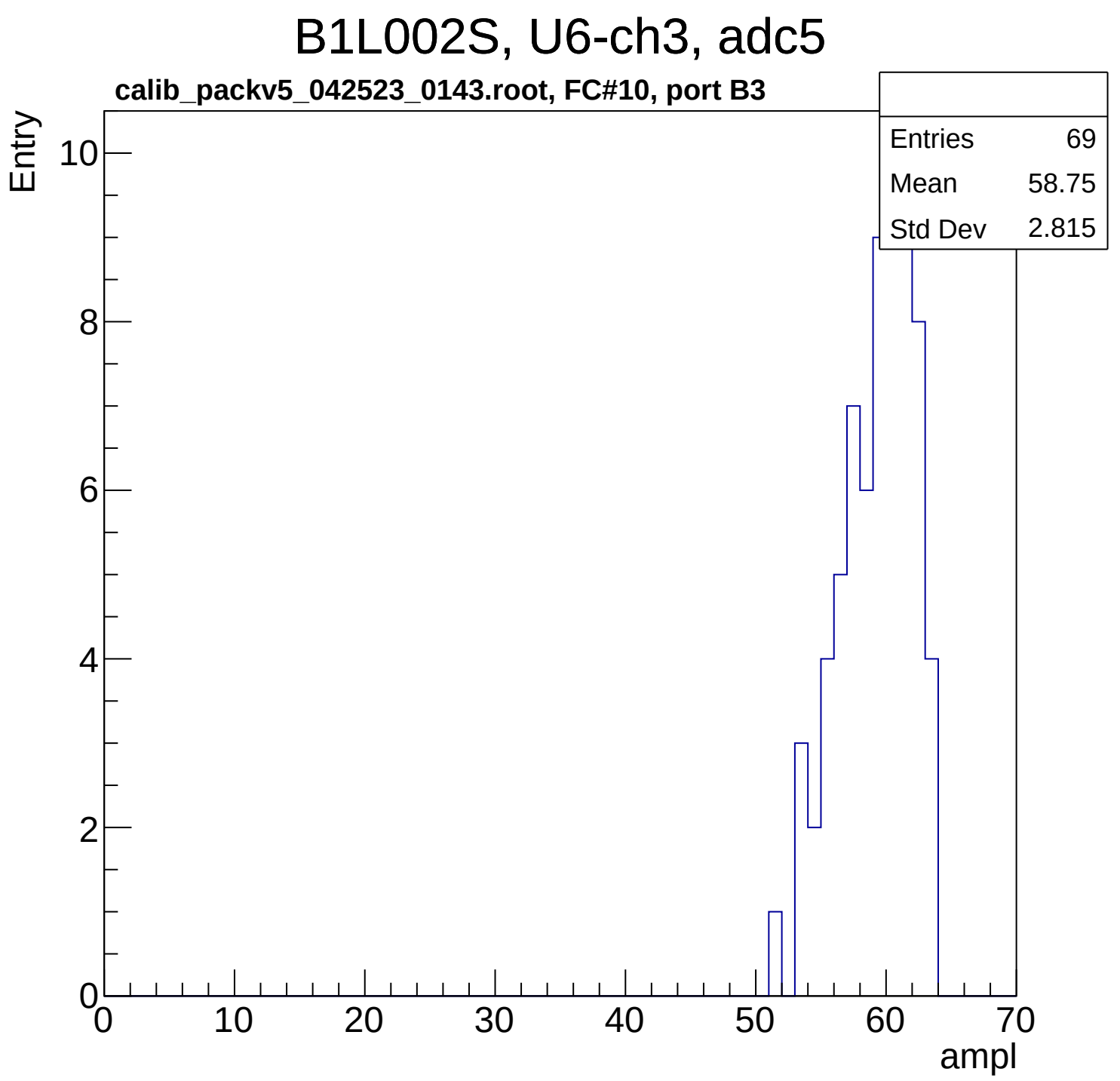

B1L002S, U6-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.81
Std Dev 3.007
Gaus mean : 29.4723
Gaus Width: 3.2047
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

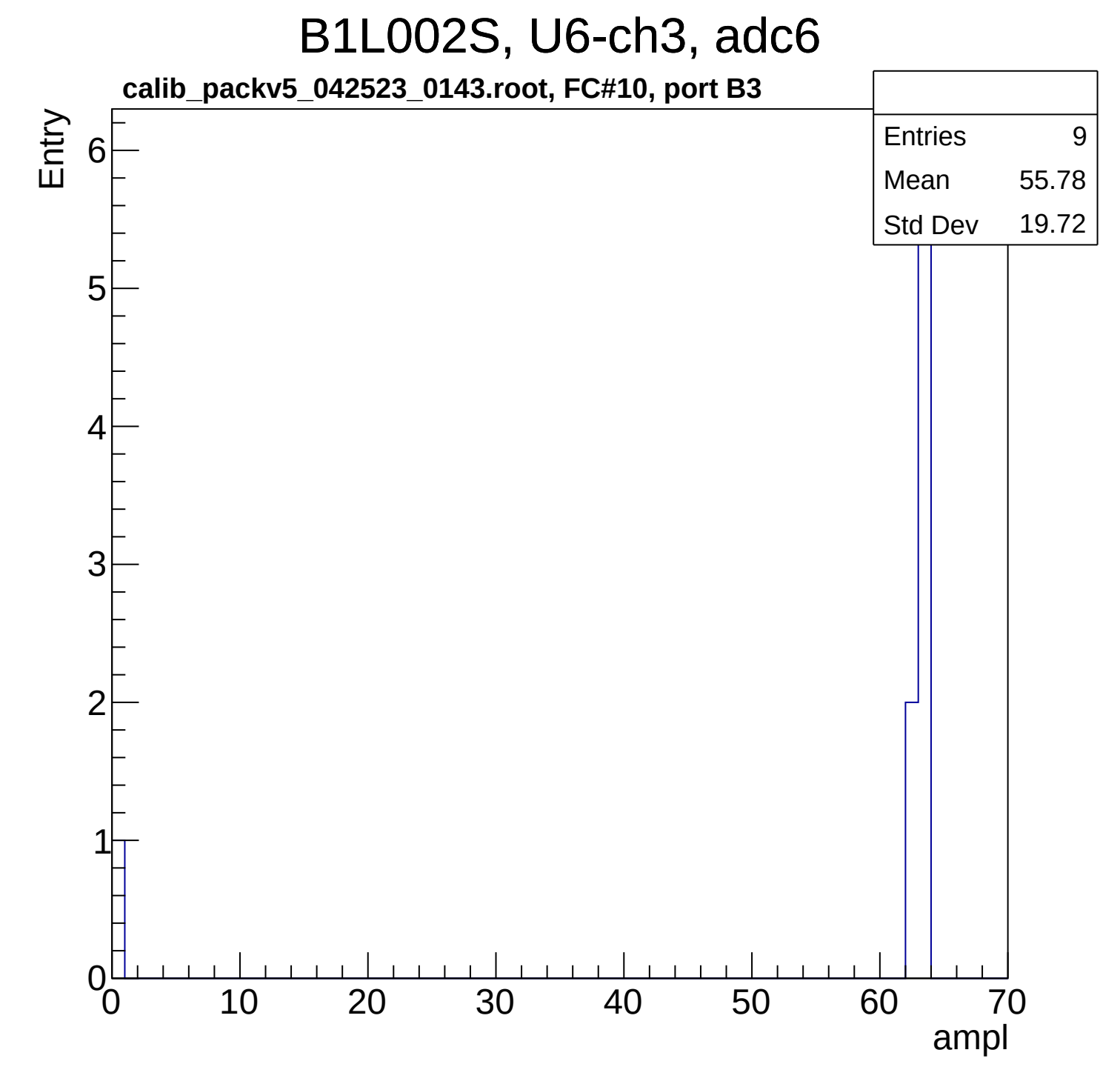

B1L002S, U6-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.96
Std Dev 2.978
Gaus mean : 36.1222
Gaus Width: 3.3067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

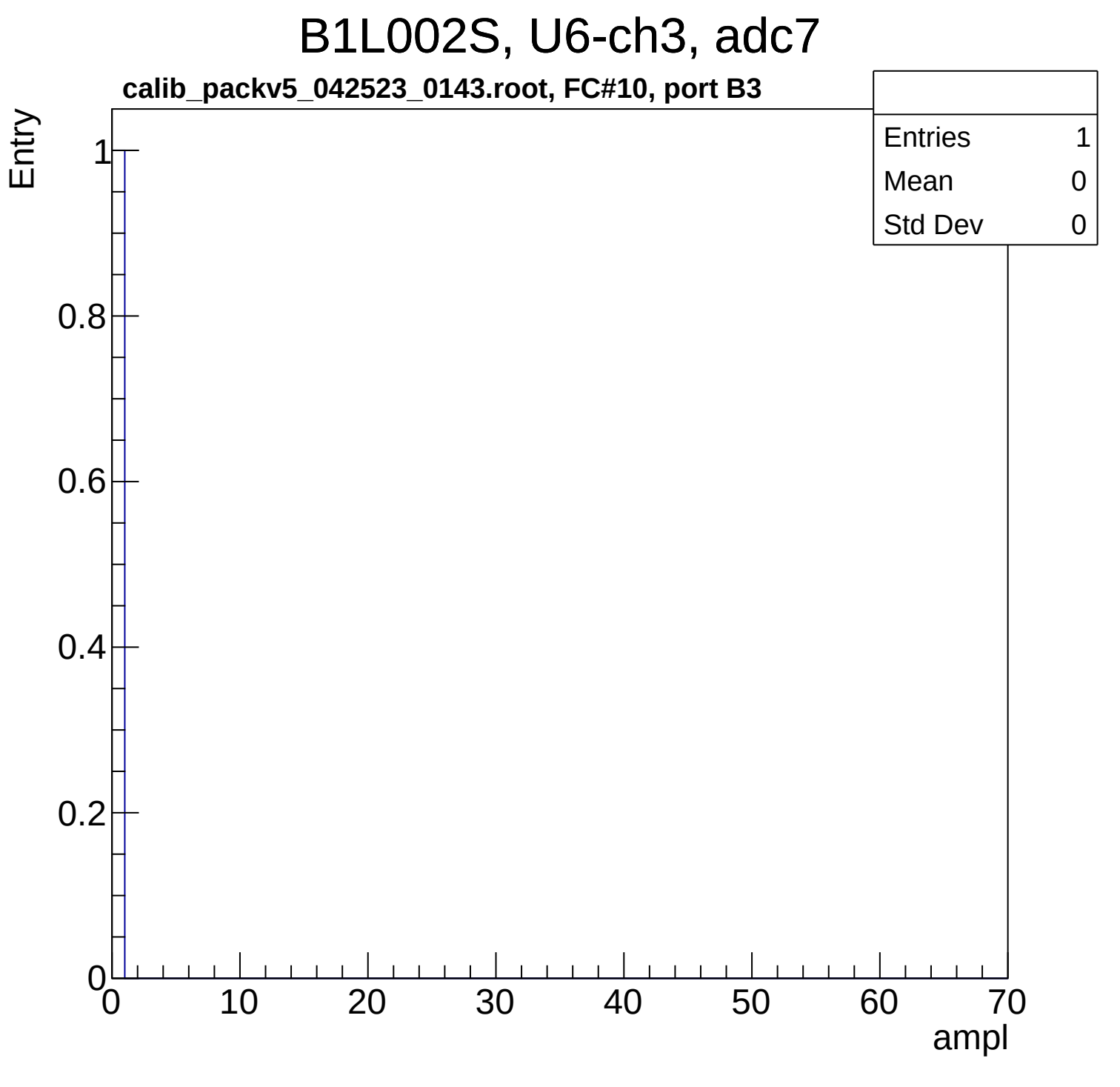

B1L002S, U6-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 42.15
Std Dev 3.633
Gaus mean : 42.9809
Gaus Width: 3.9890
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

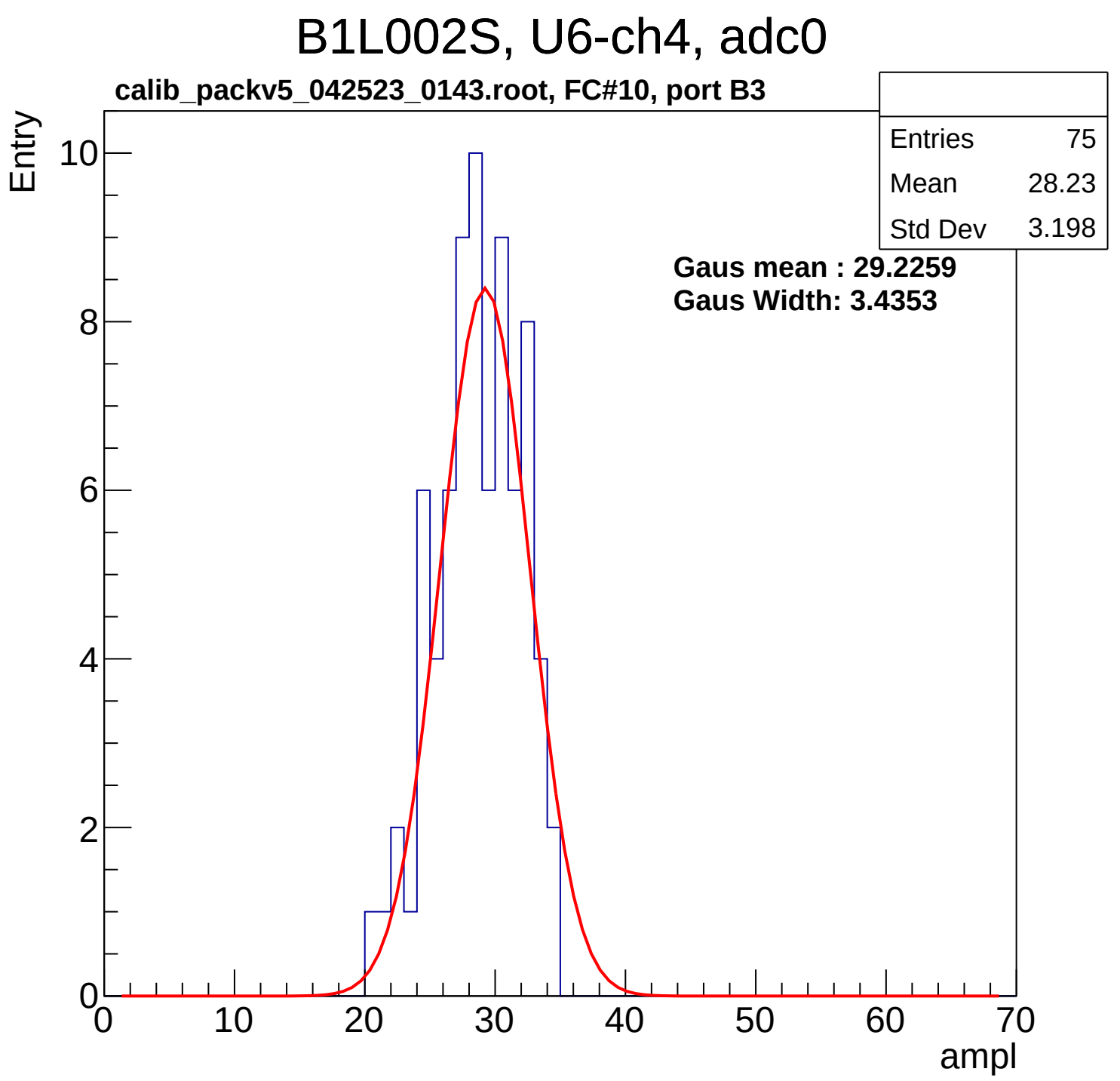

B1L002S, U6-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 48.69
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

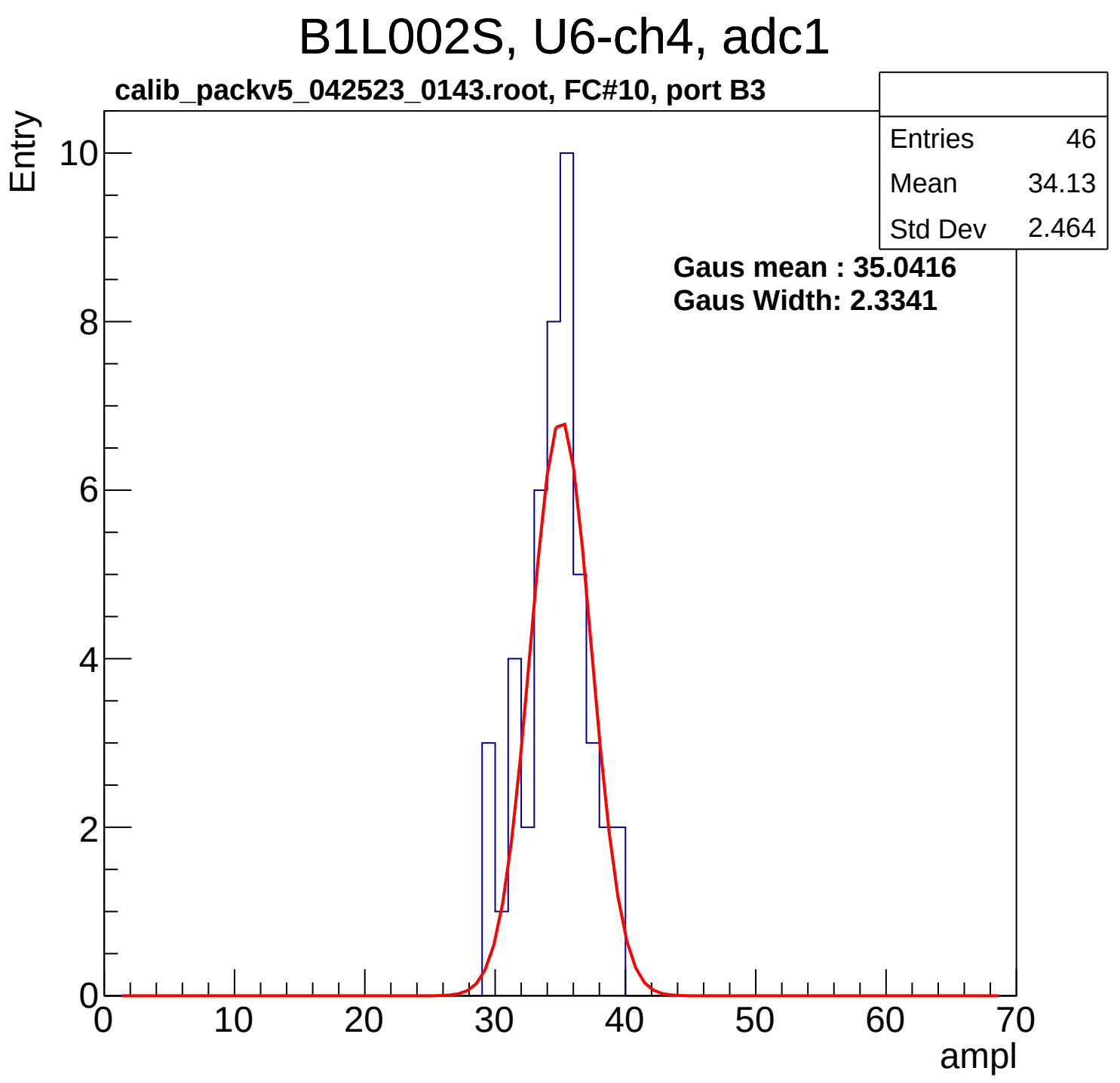

B1L002S, U6-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.37
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

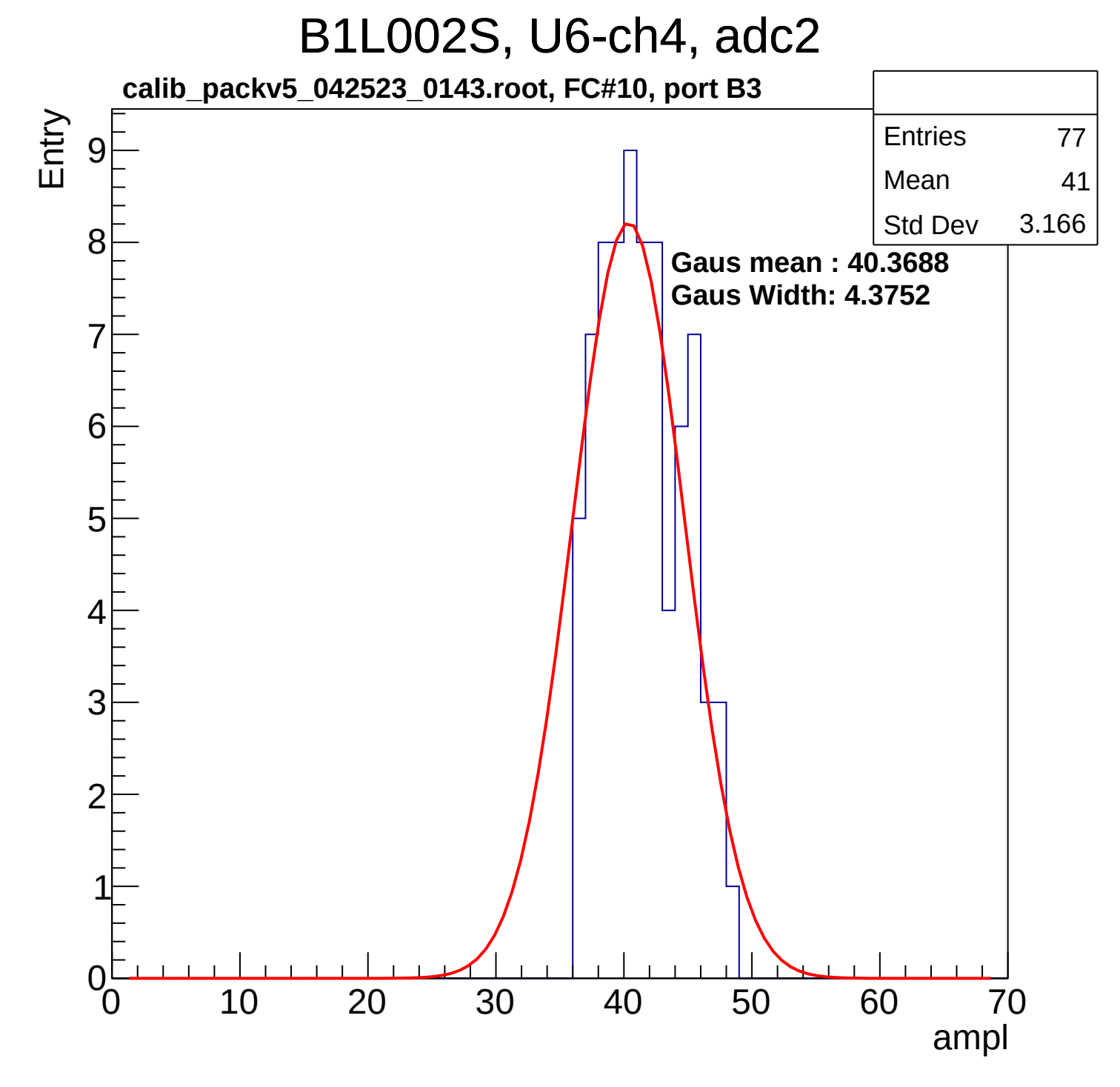

B1L002S, U6-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 58.75
Std Dev 2.815
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

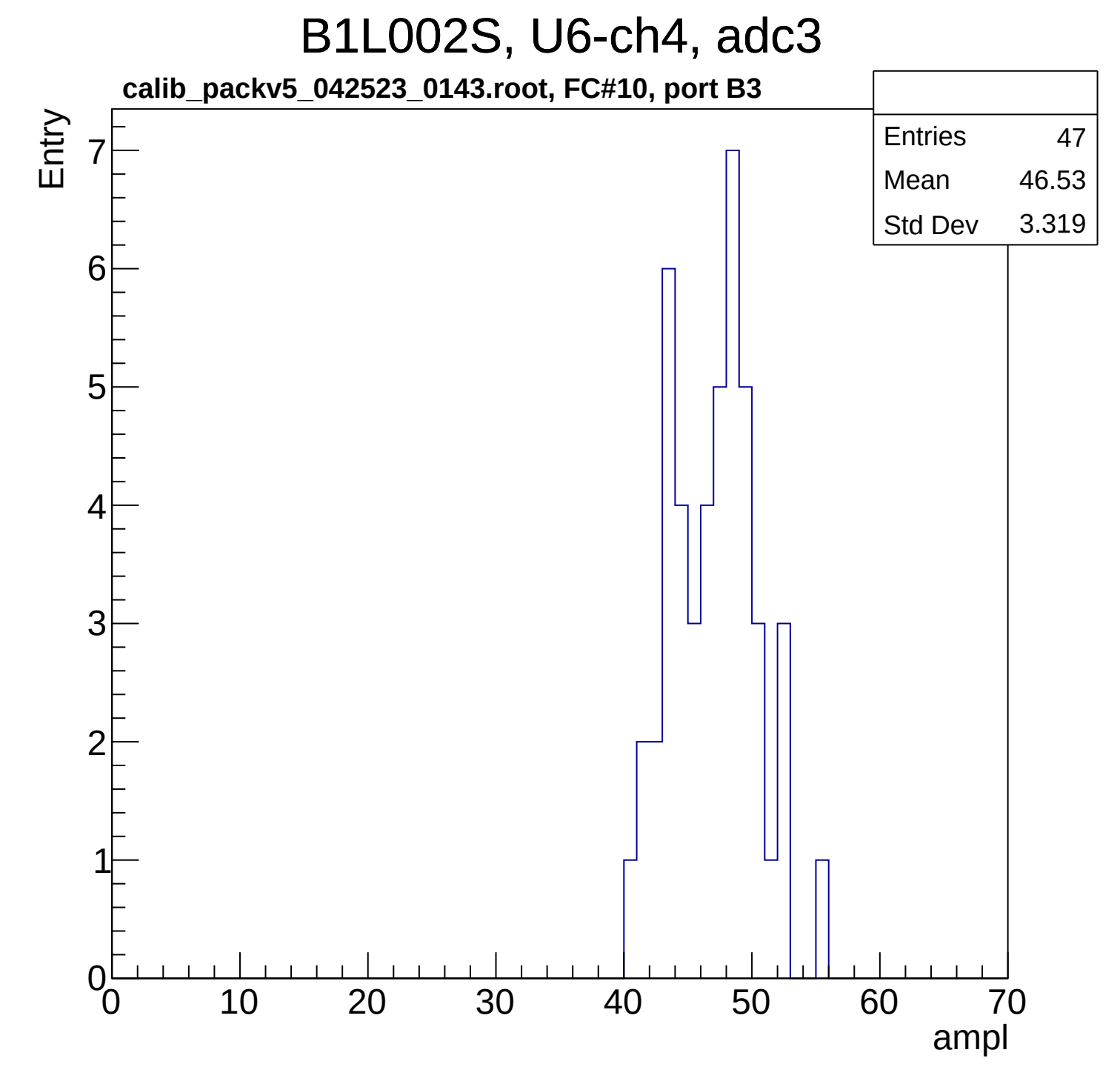

B1L002S, U6-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.78
Std Dev 19.72
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

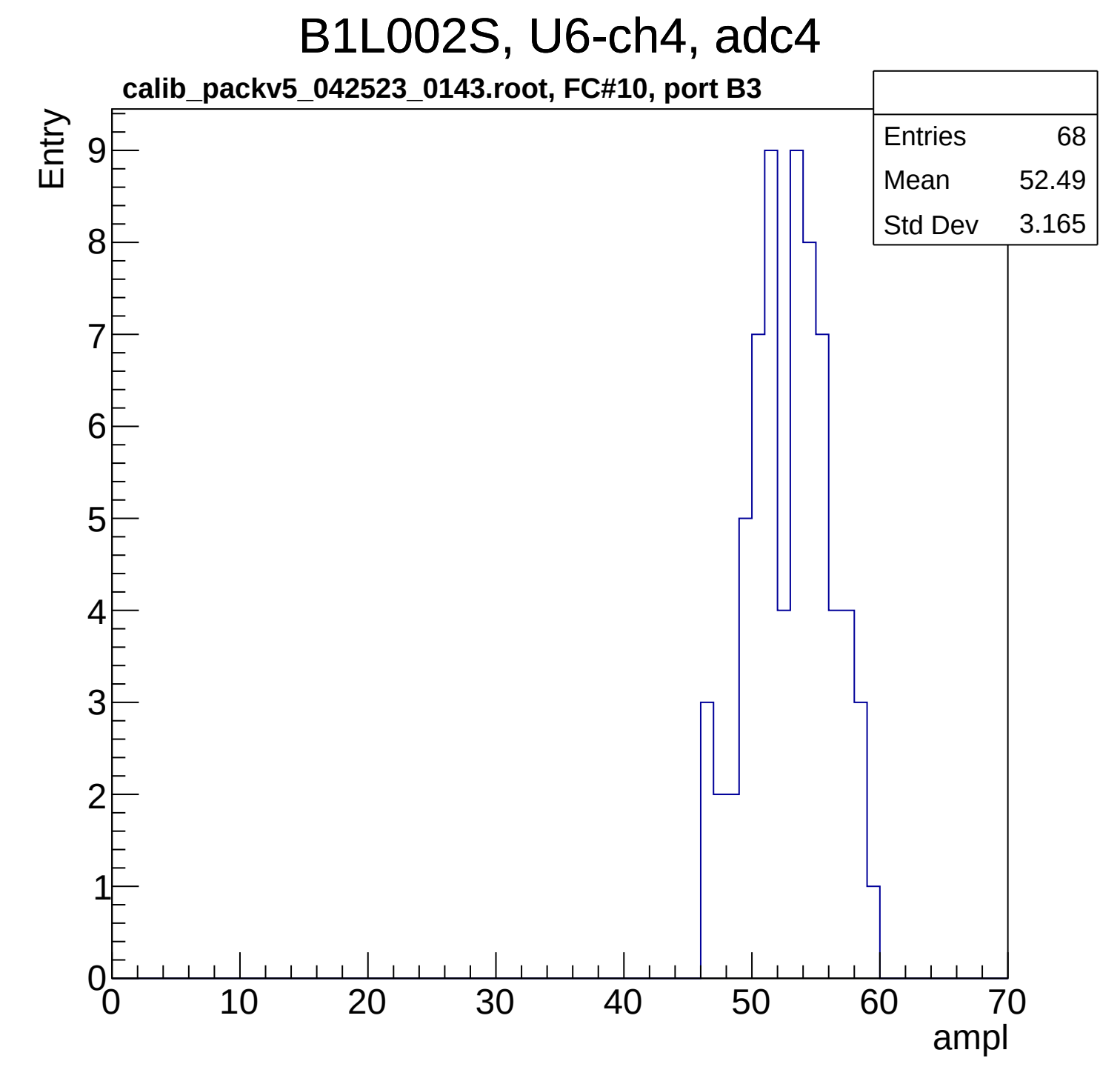

B1L002S, U6-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

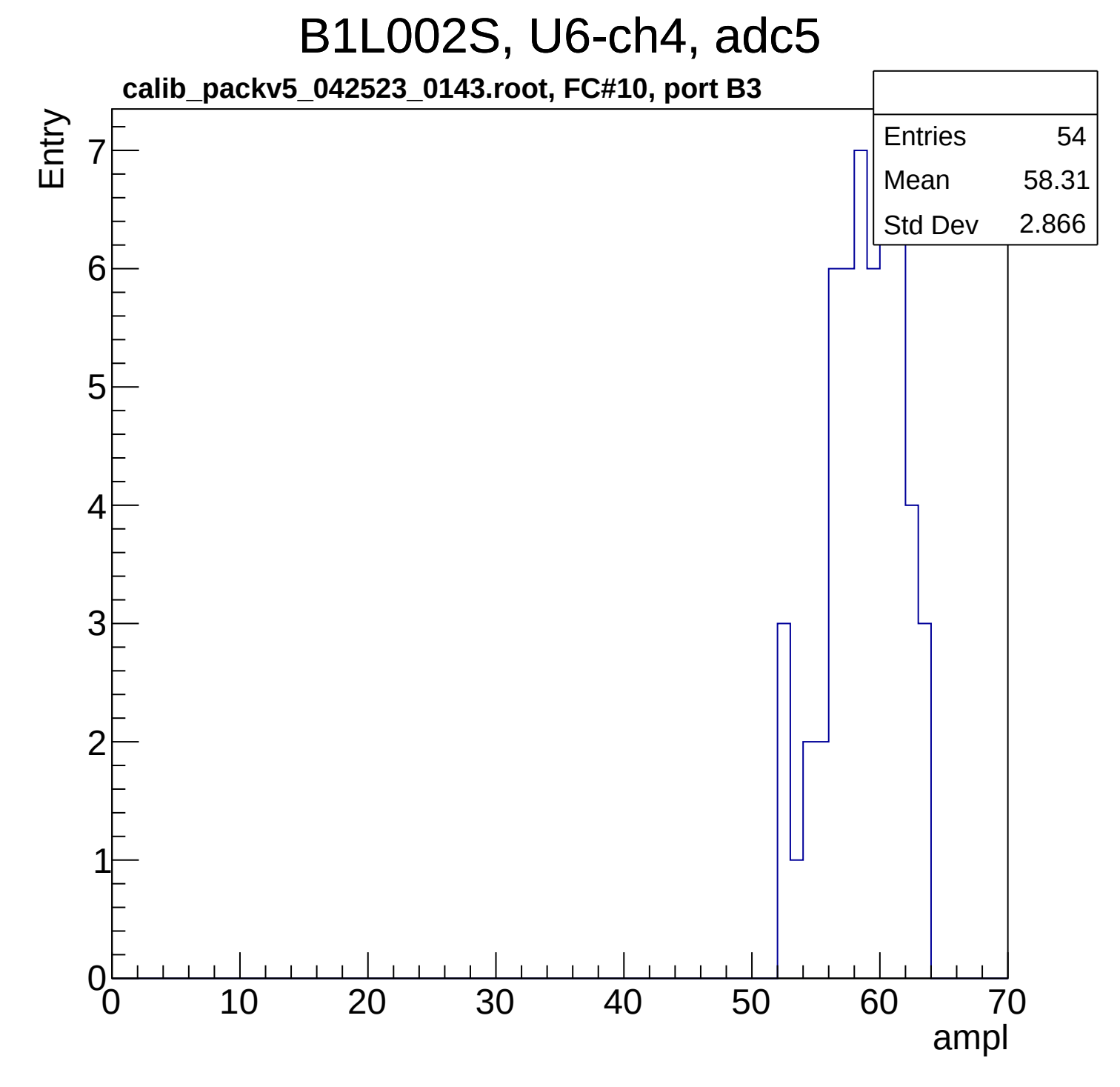

B1L002S, U6-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.23
Std Dev 3.198
Gaus mean : 29.2259
Gaus Width: 3.4353
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

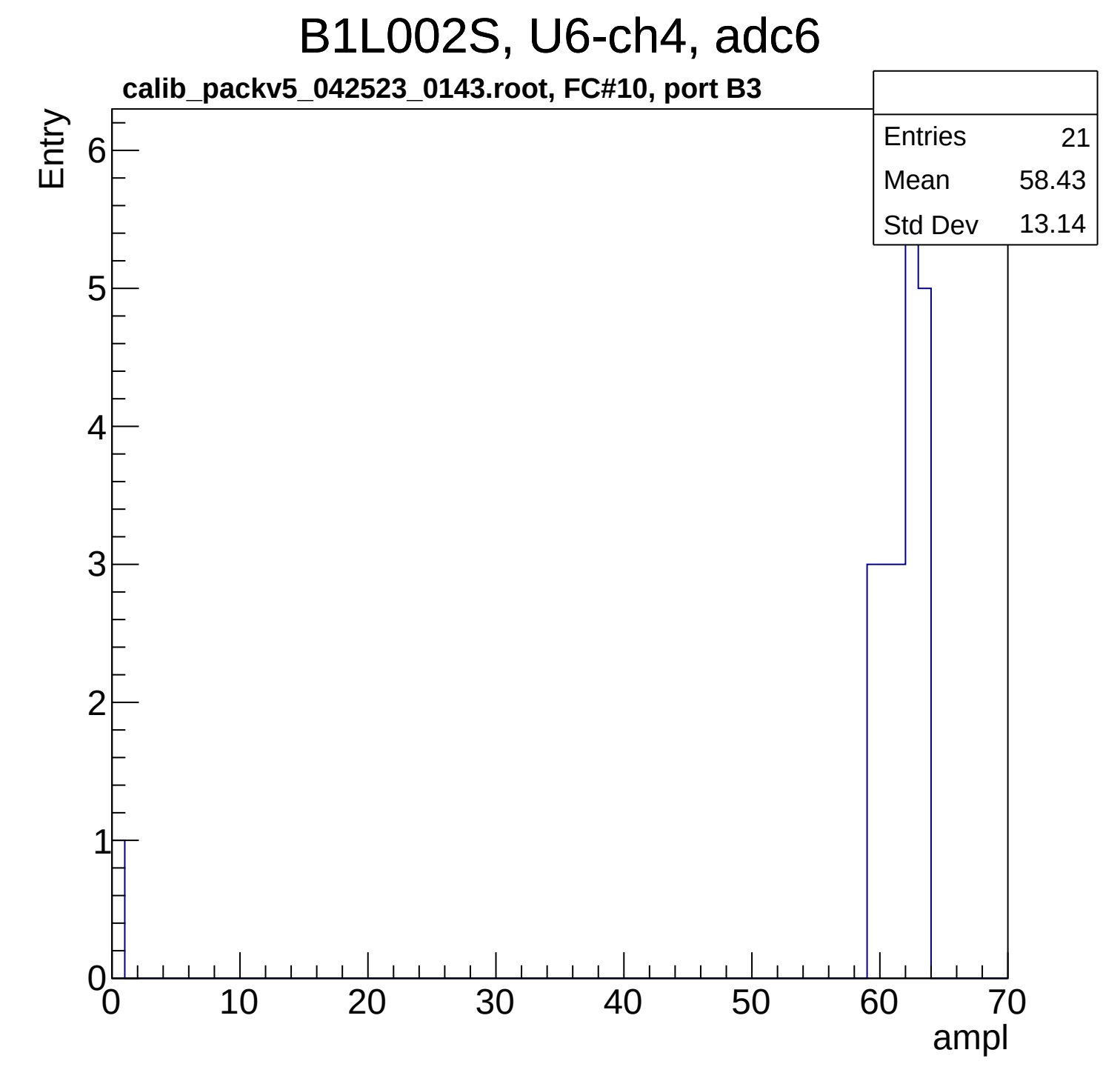

B1L002S, U6-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 34.13
Std Dev 2.464
Gaus mean : 35.0416
Gaus Width: 2.3341
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

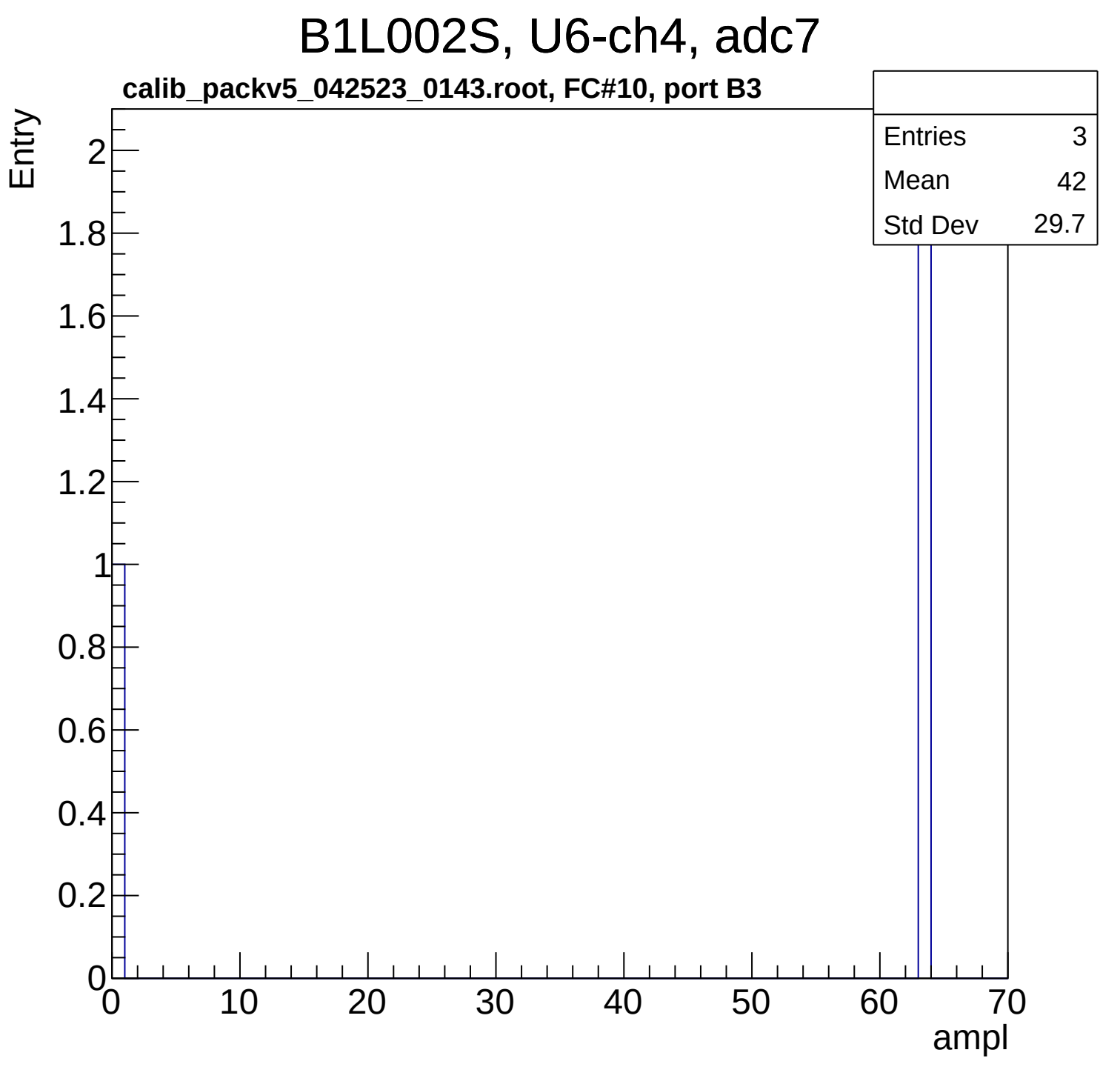

B1L002S, U6-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41
Std Dev 3.166
Gaus mean : 40.3688
Gaus Width: 4.3752
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

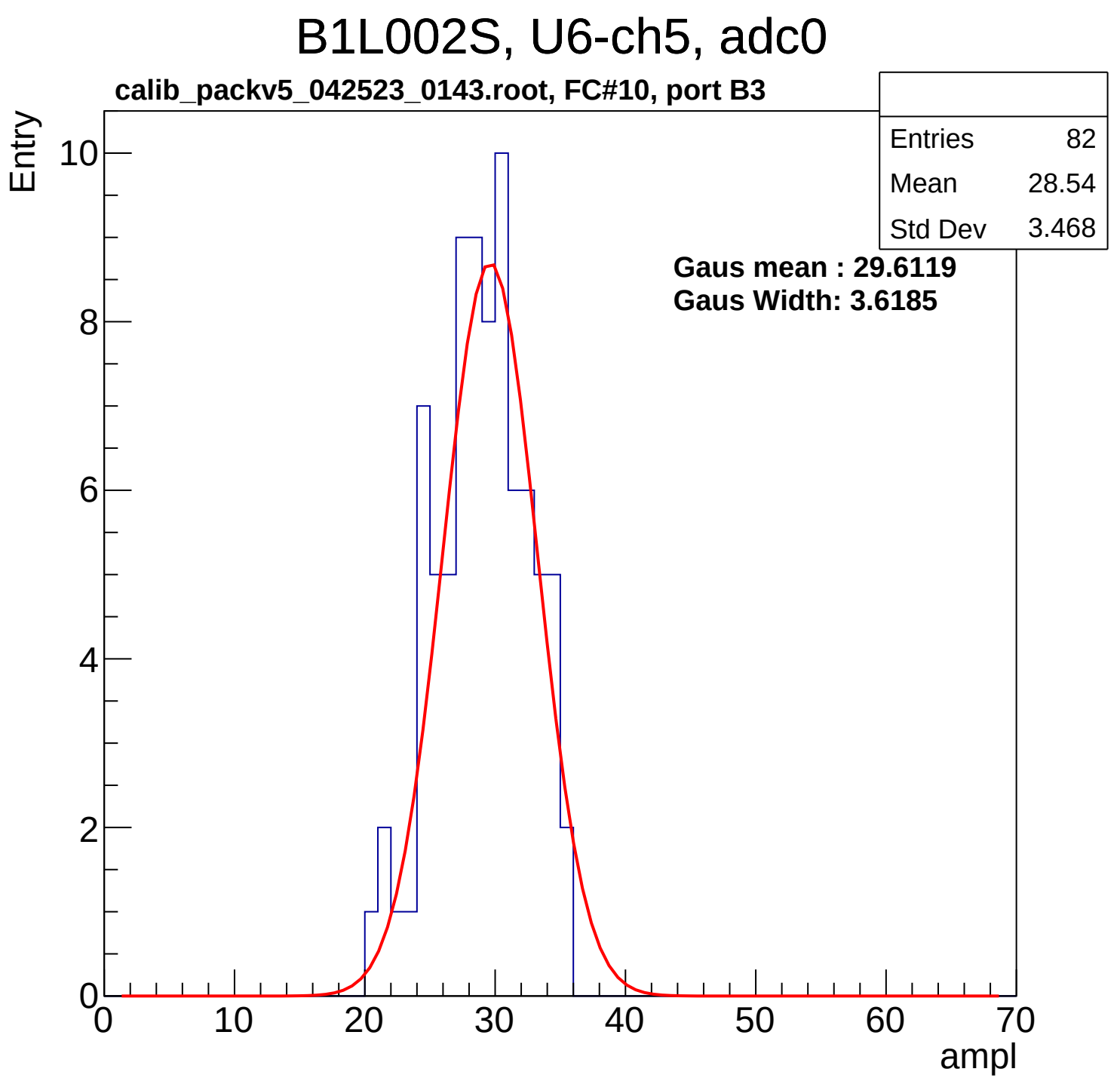

B1L002S, U6-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 46.53
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

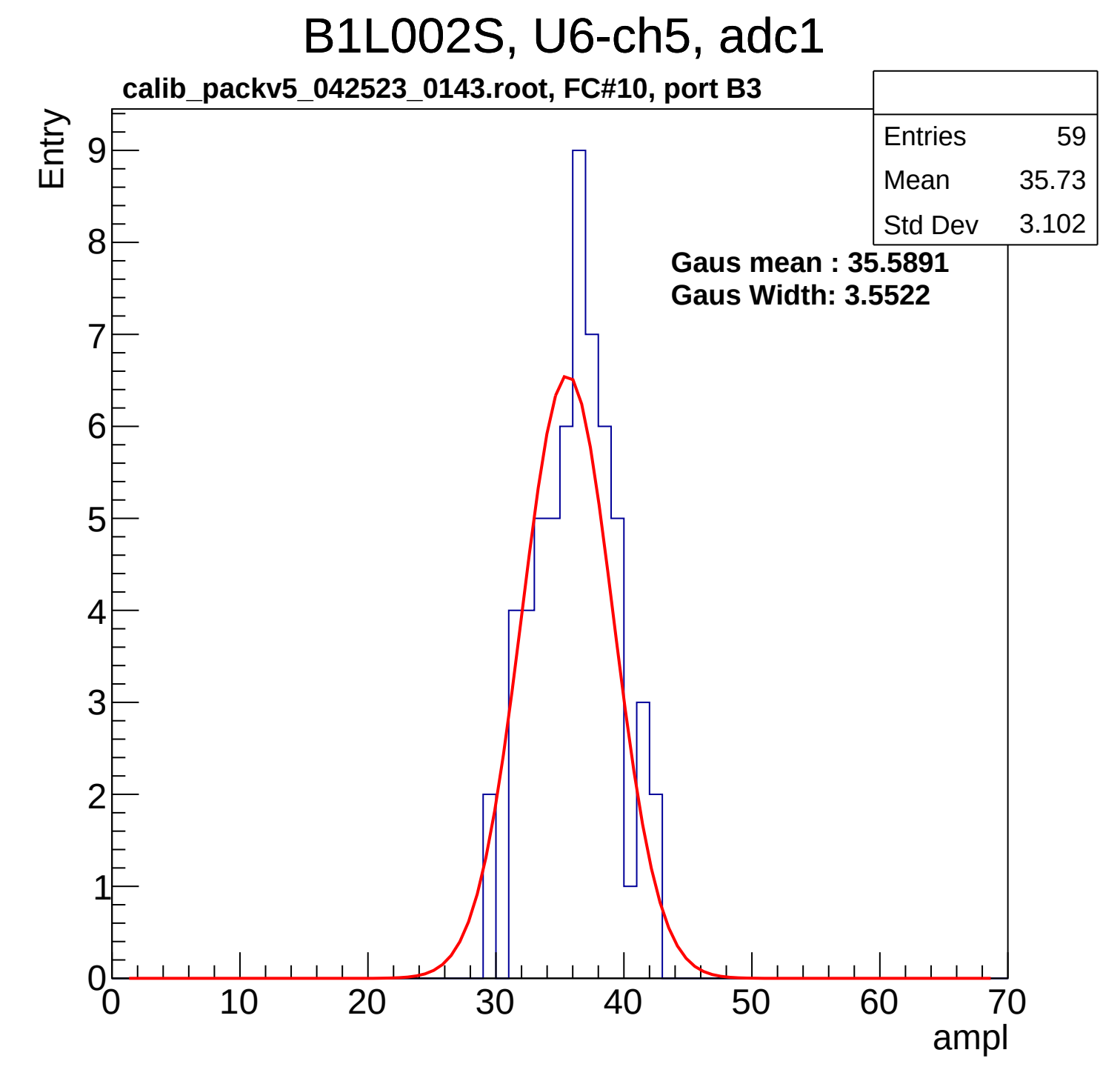

B1L002S, U6-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52.49
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

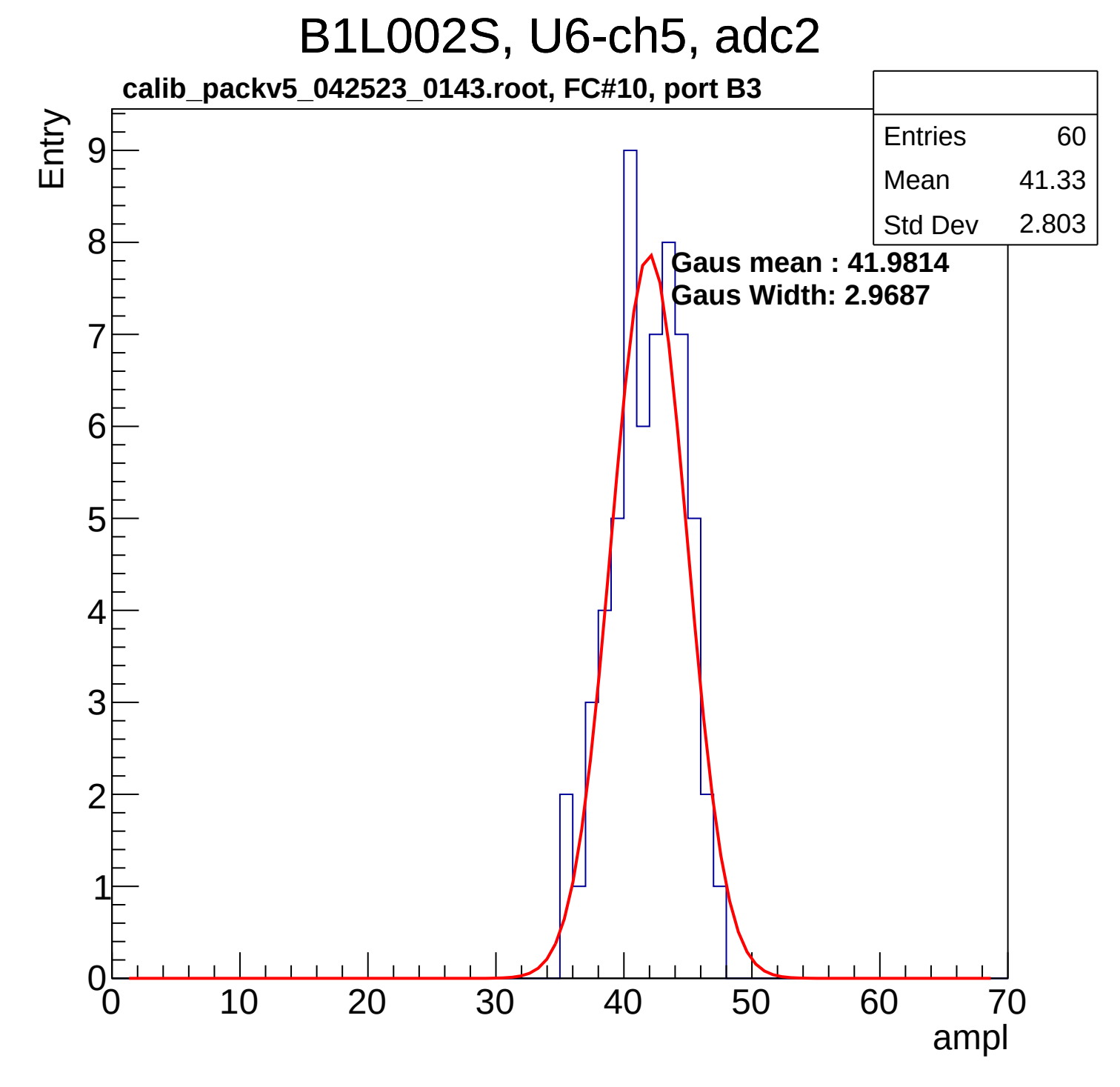

B1L002S, U6-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.31
Std Dev 2.866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

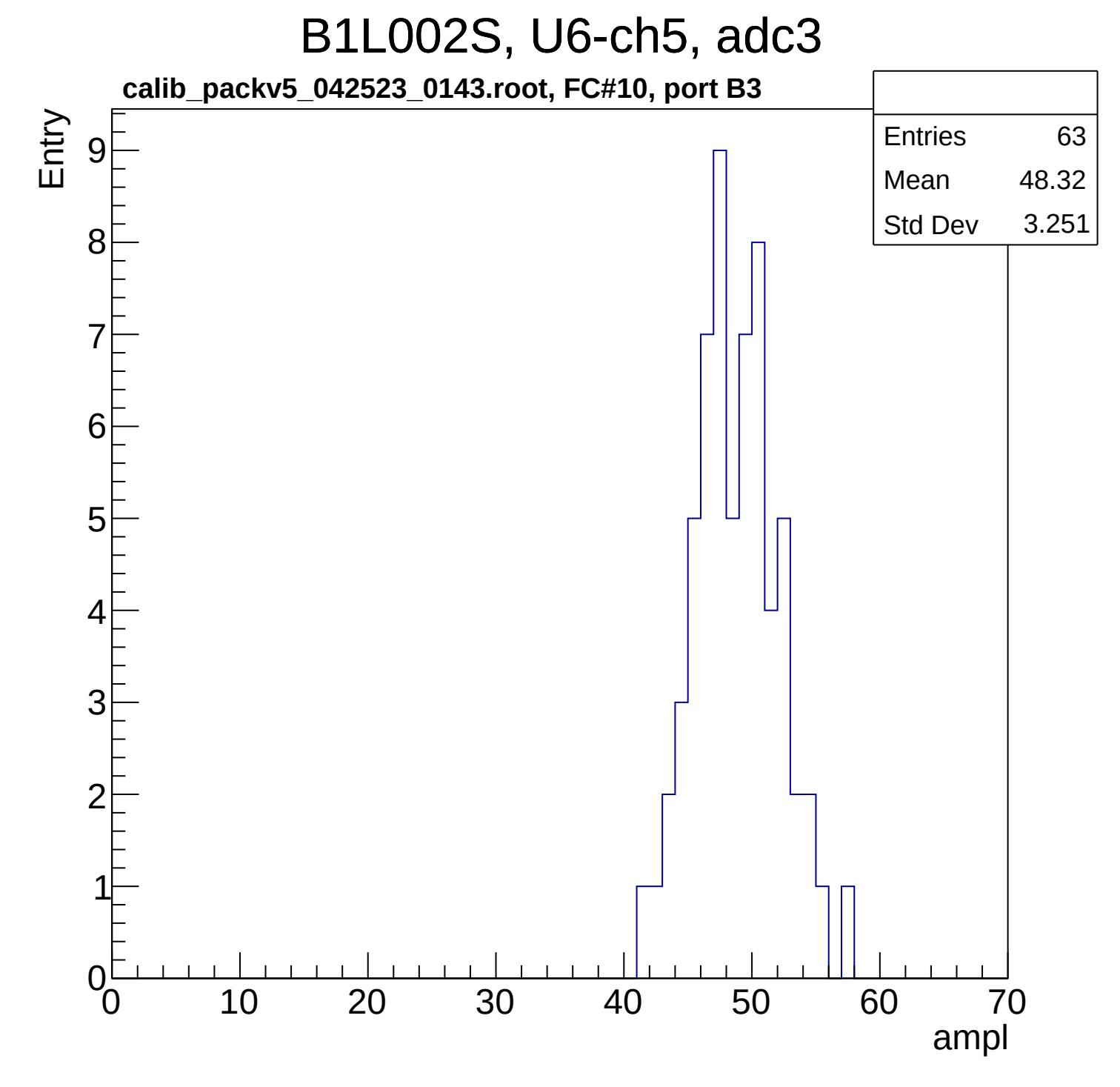

B1L002S, U6-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.43
Std Dev 13.14
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

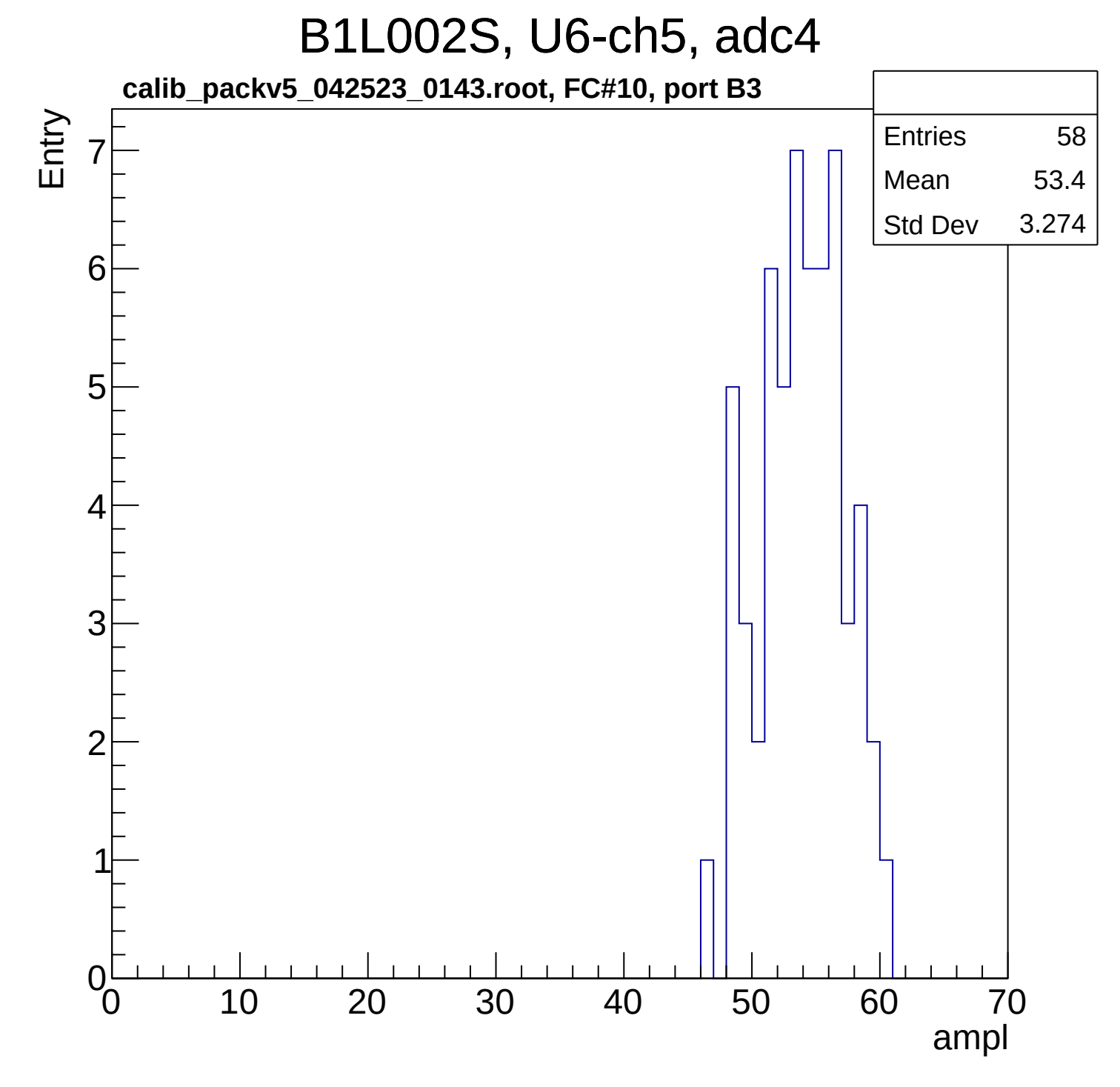

B1L002S, U6-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

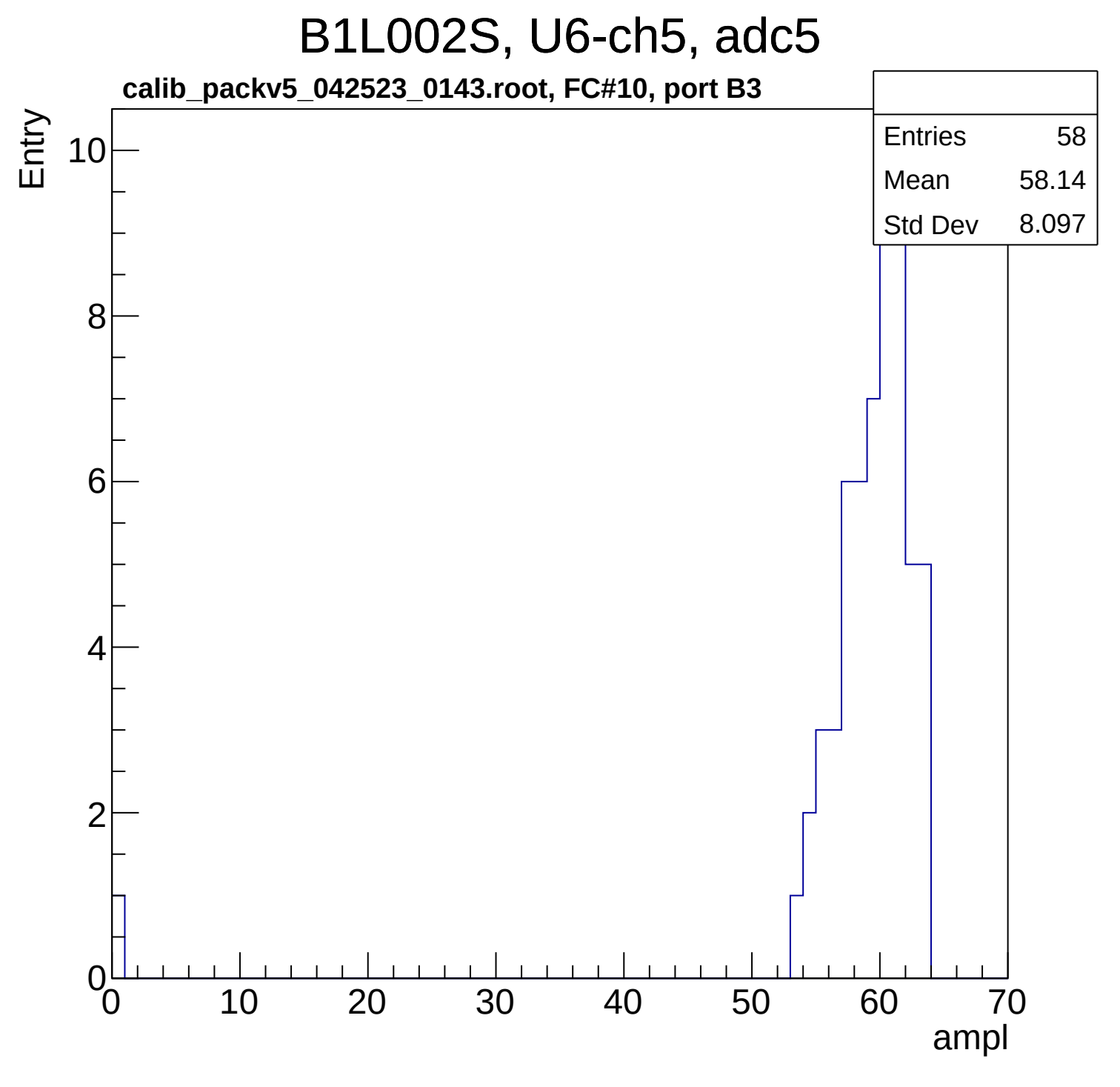

B1L002S, U6-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.54
Std Dev 3.468
Gaus mean : 29.6119
Gaus Width: 3.6185
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

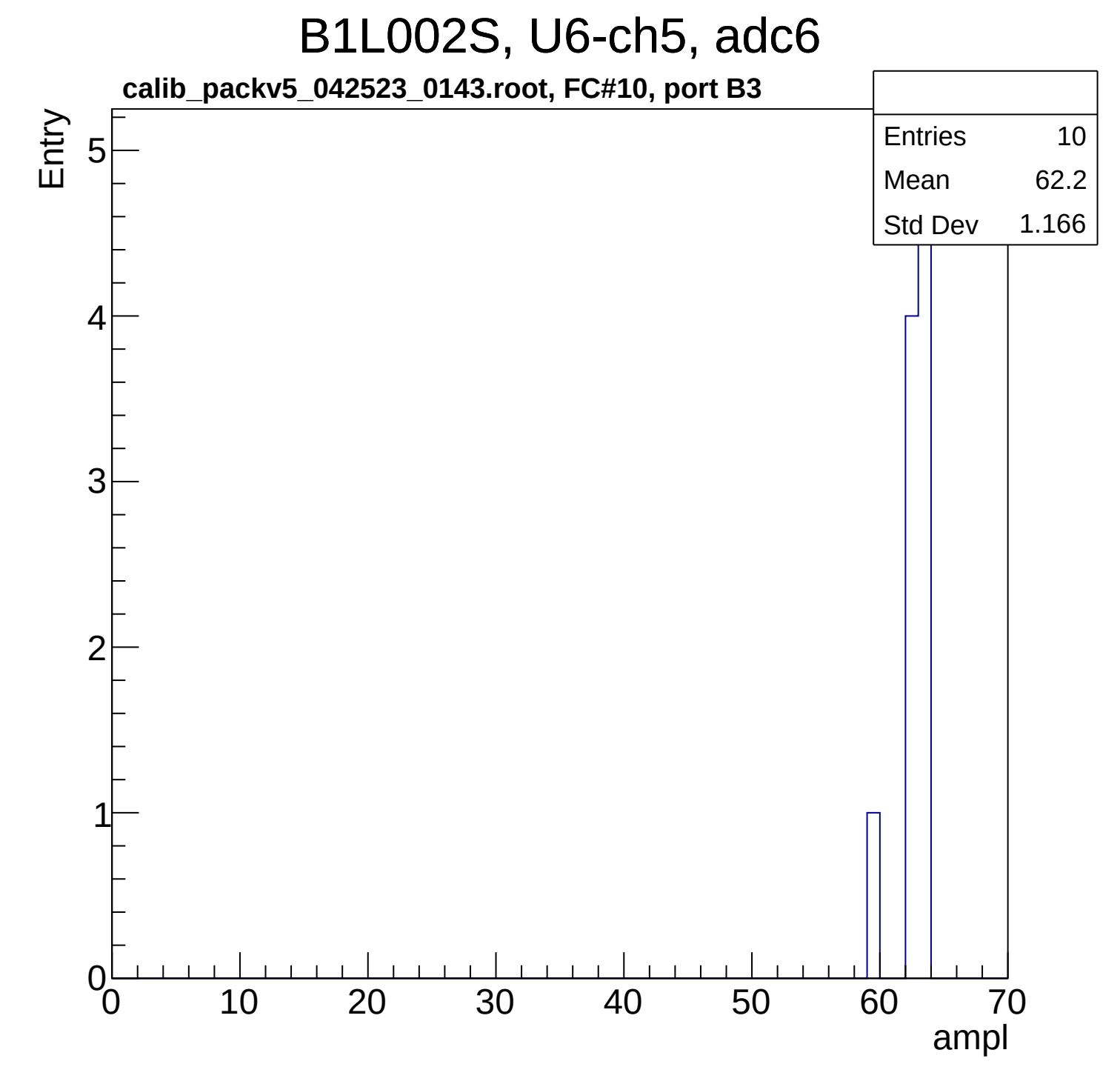

B1L002S, U6-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.73
Std Dev 3.102
Gaus mean : 35.5891
Gaus Width: 3.5522
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

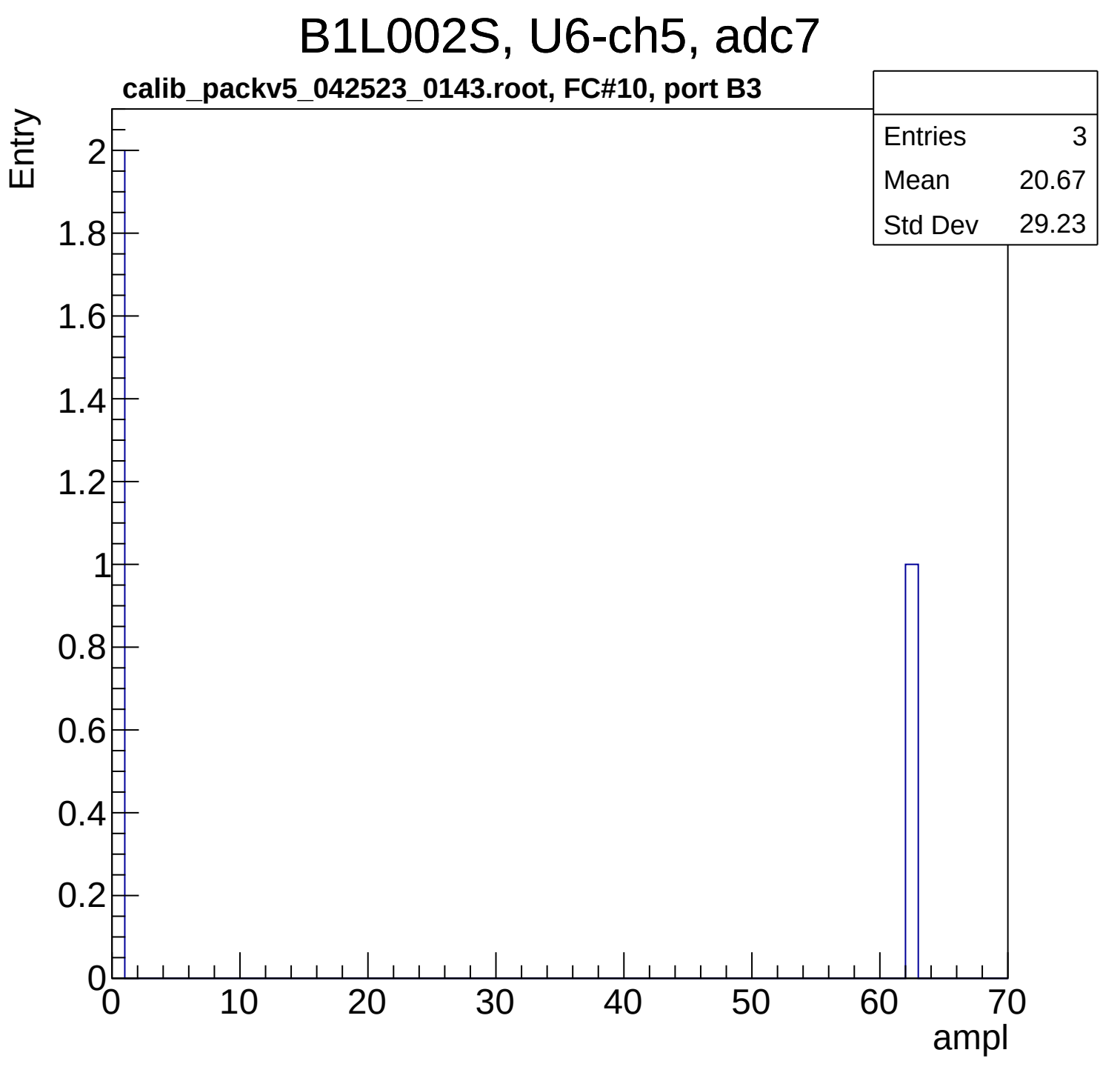

B1L002S, U6-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.33
Std Dev 2.803
Gaus mean : 41.9814
Gaus Width: 2.9687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

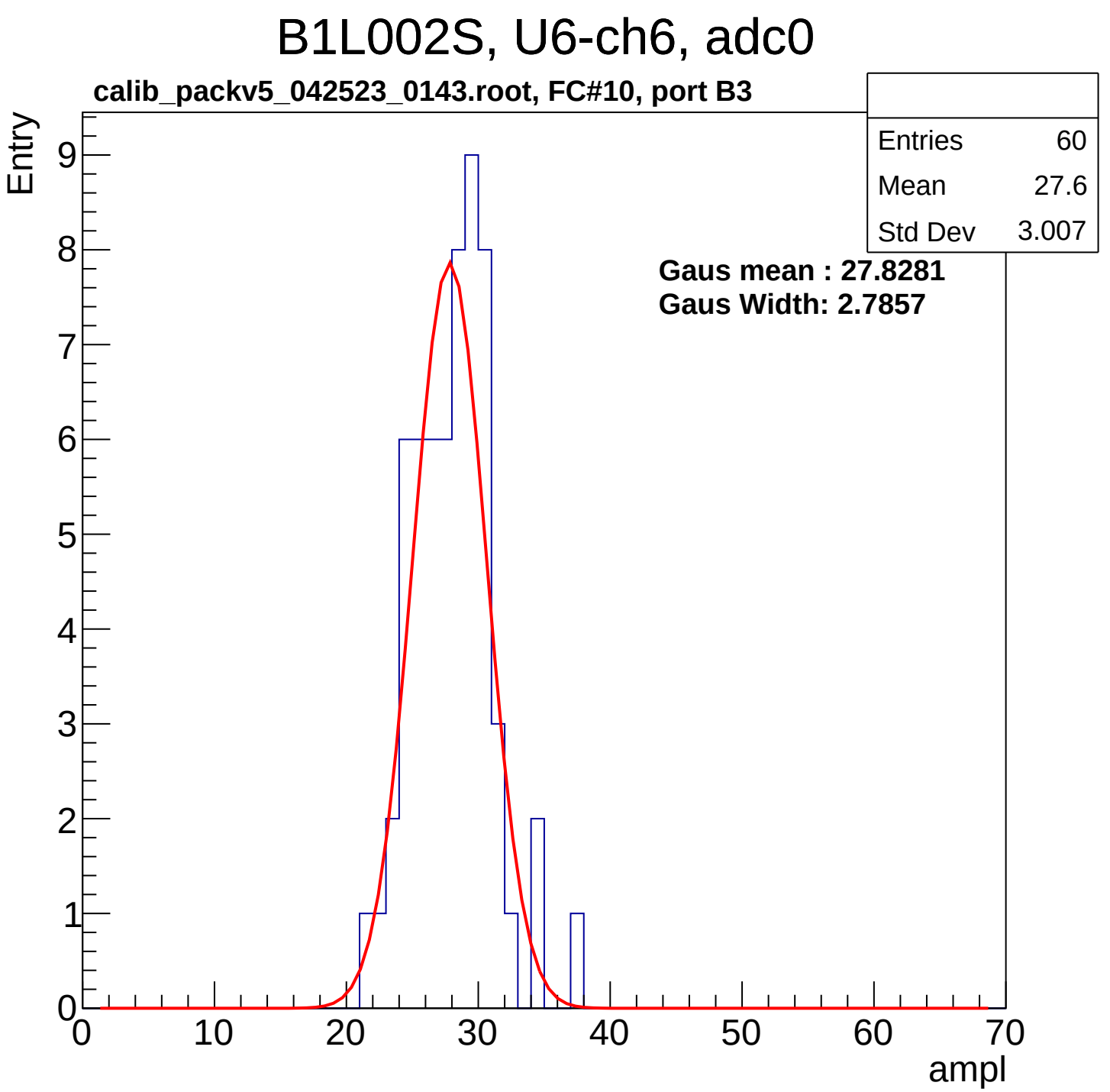

B1L002S, U6-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.32
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

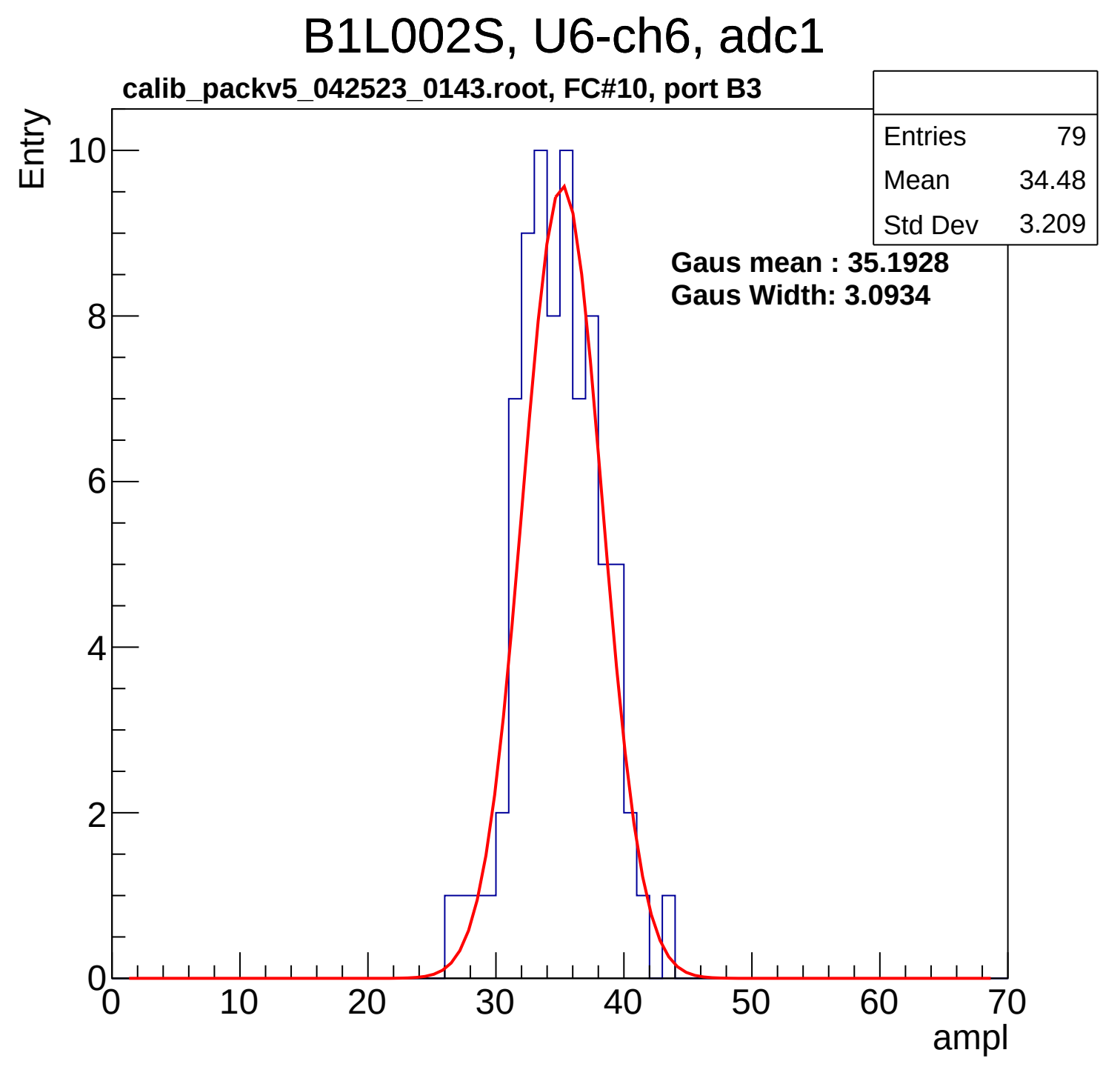

B1L002S, U6-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.4
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

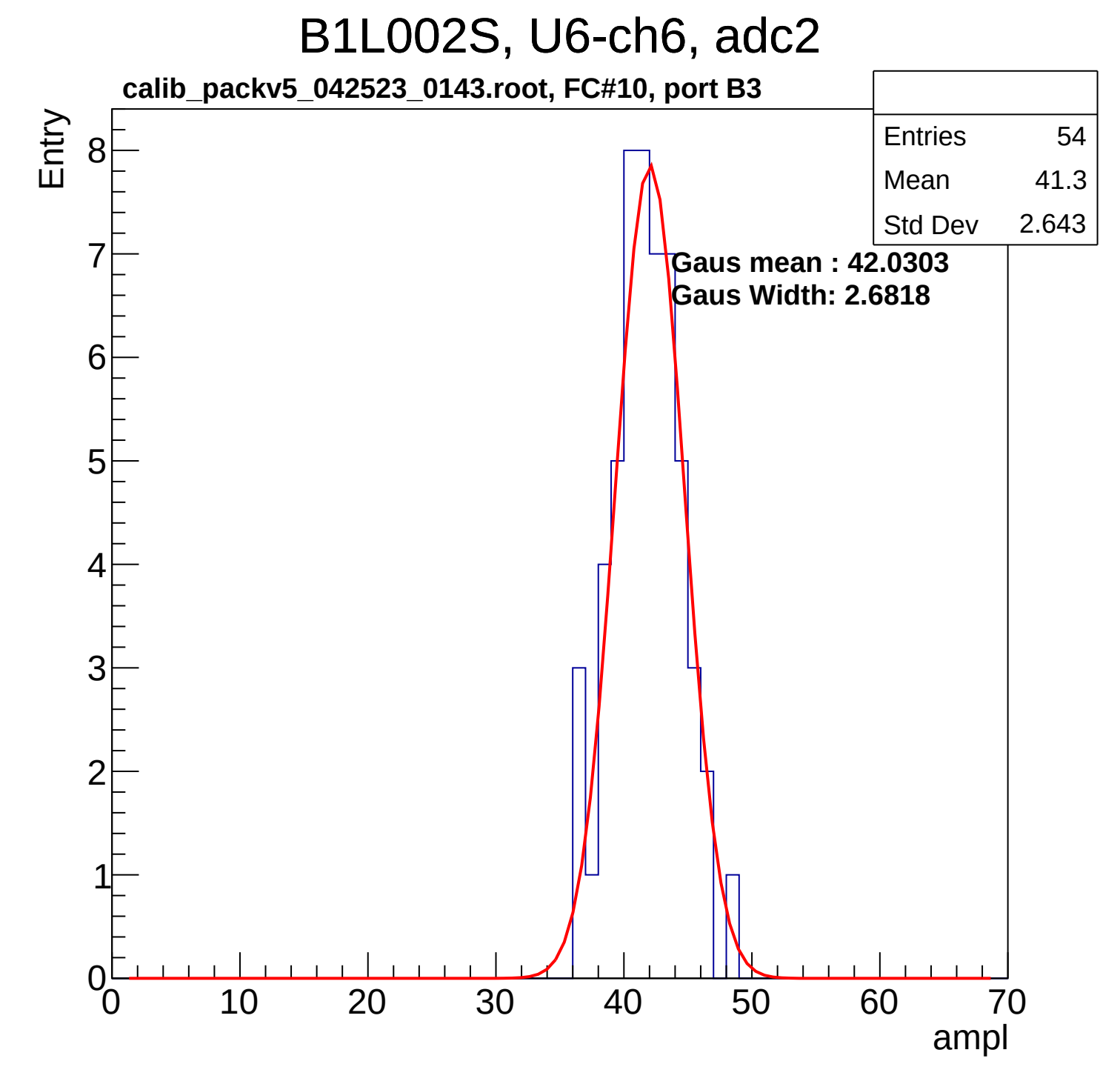

B1L002S, U6-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.14
Std Dev 8.097
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

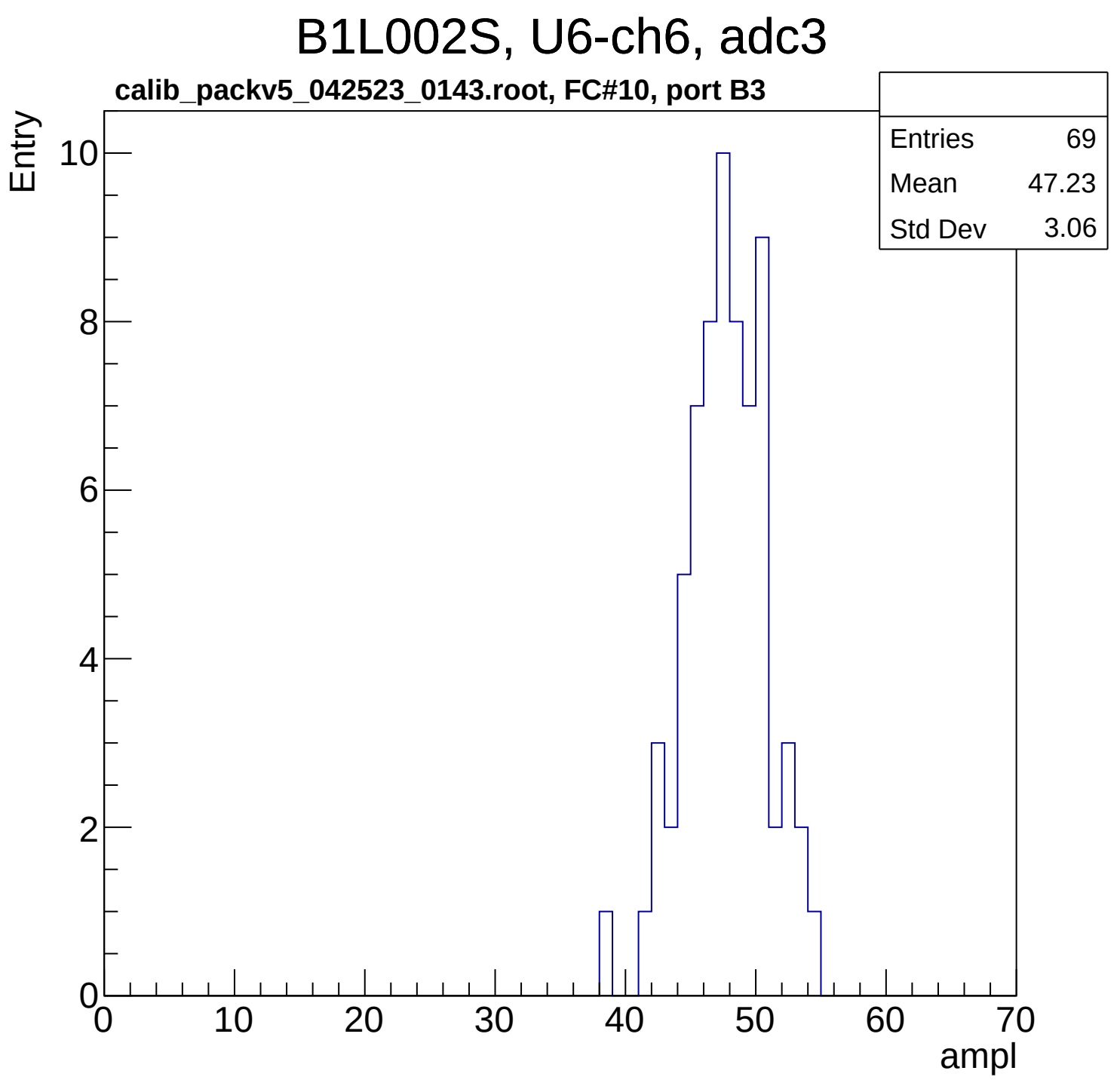

B1L002S, U6-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 62.2
Std Dev 1.166
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

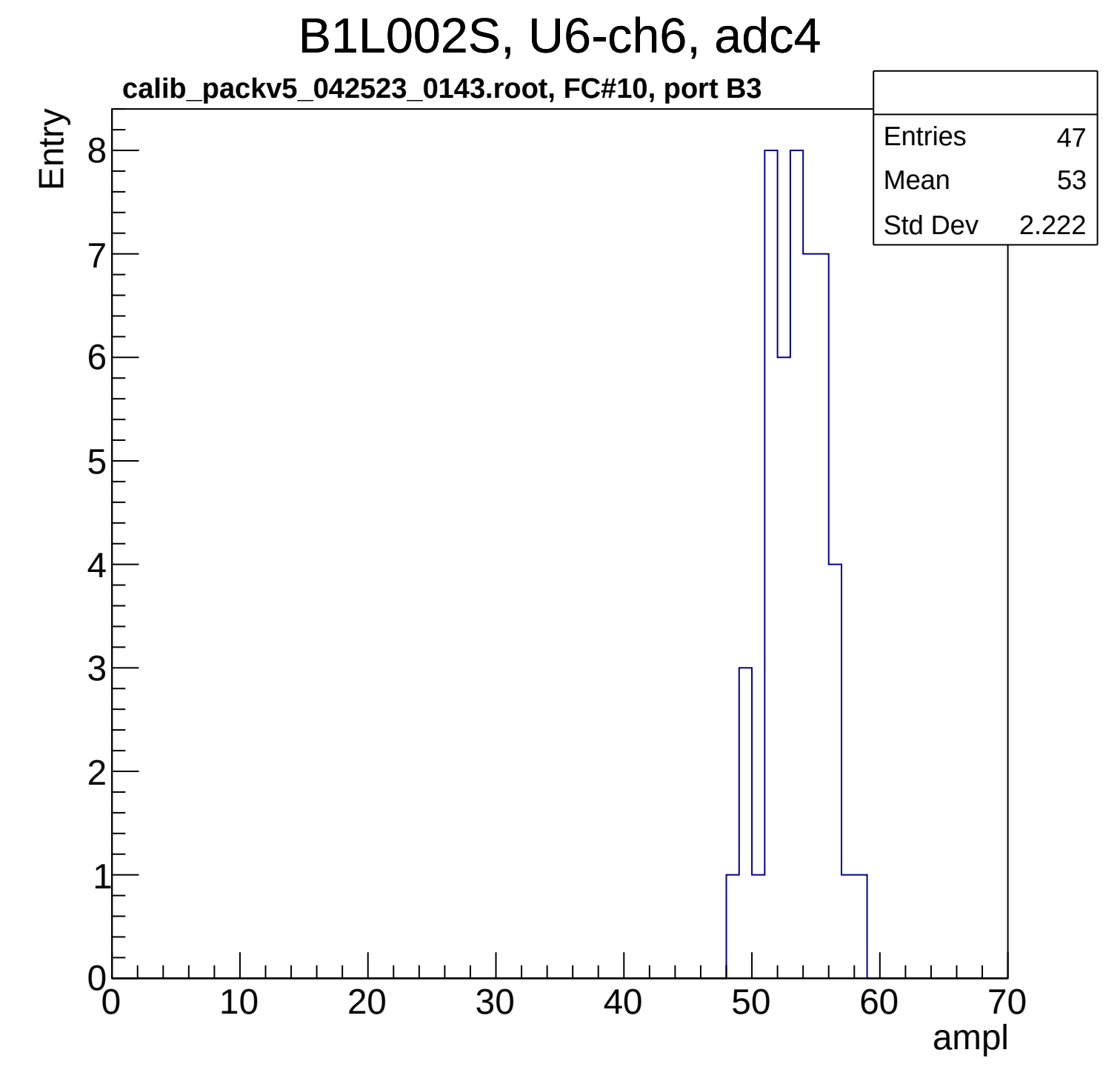

B1L002S, U6-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

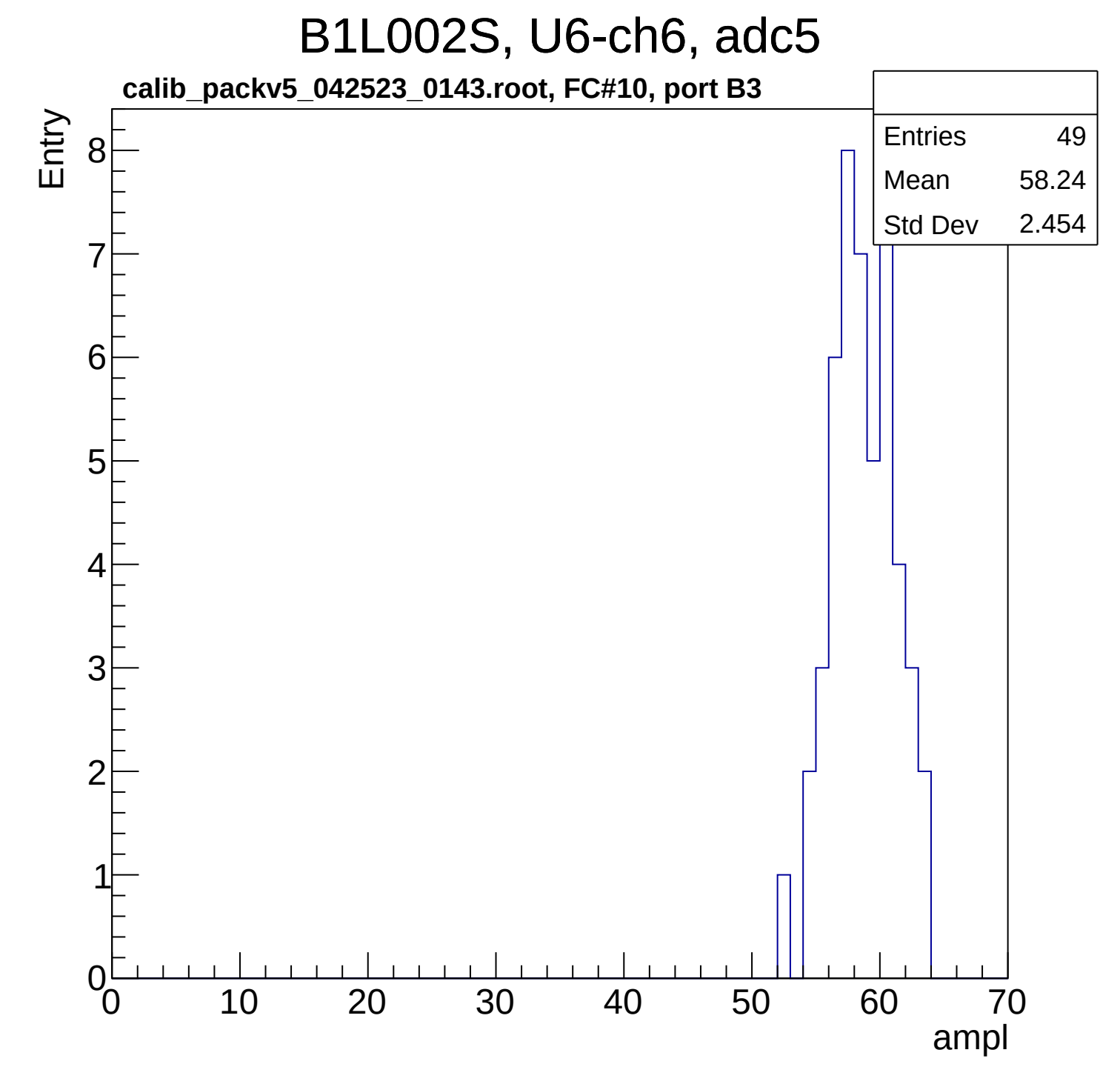

B1L002S, U6-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.6
Std Dev 3.007
Gaus mean : 27.8281
Gaus Width: 2.7857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

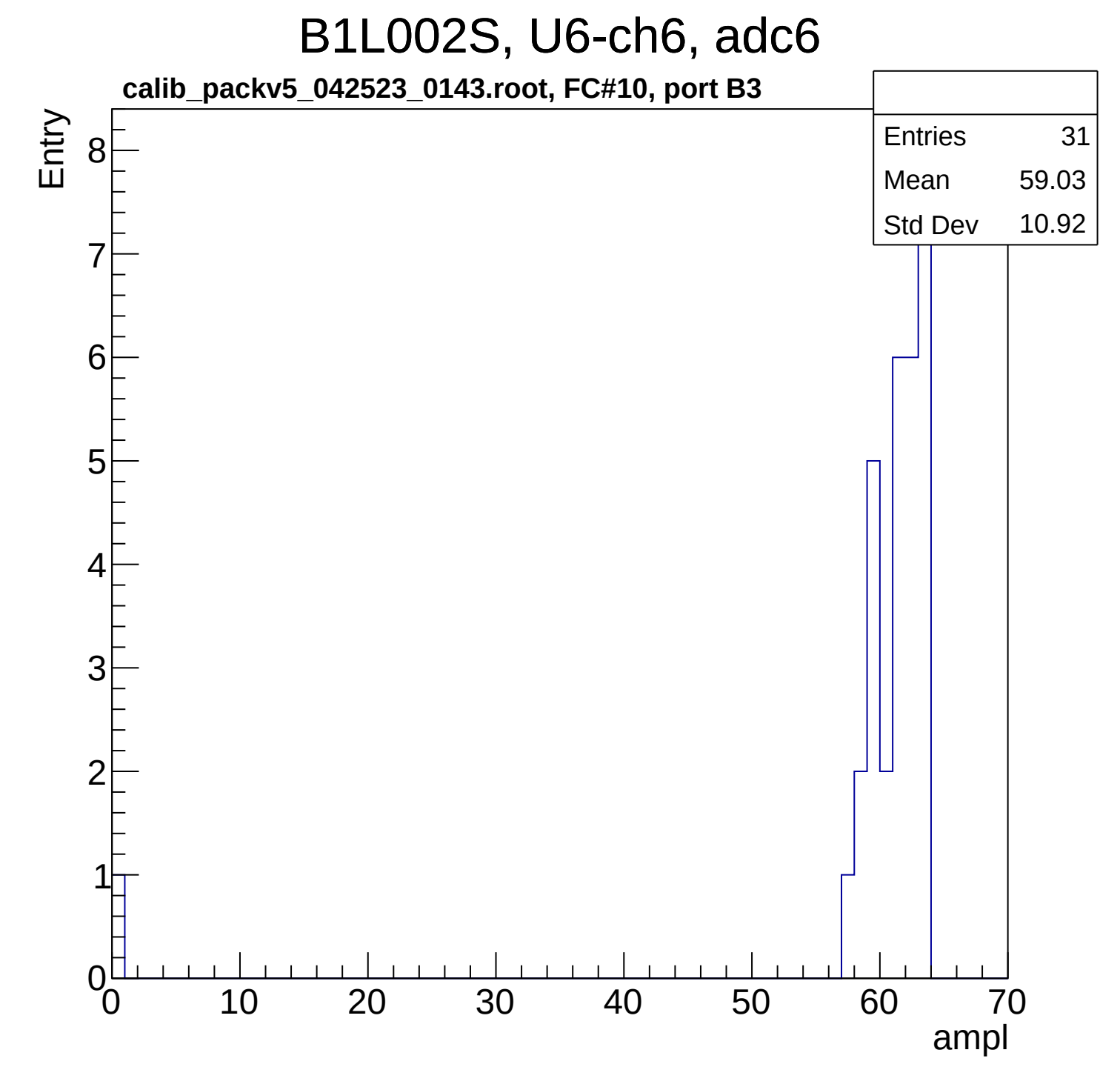

B1L002S, U6-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.48
Std Dev 3.209
Gaus mean : 35.1928
Gaus Width: 3.0934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

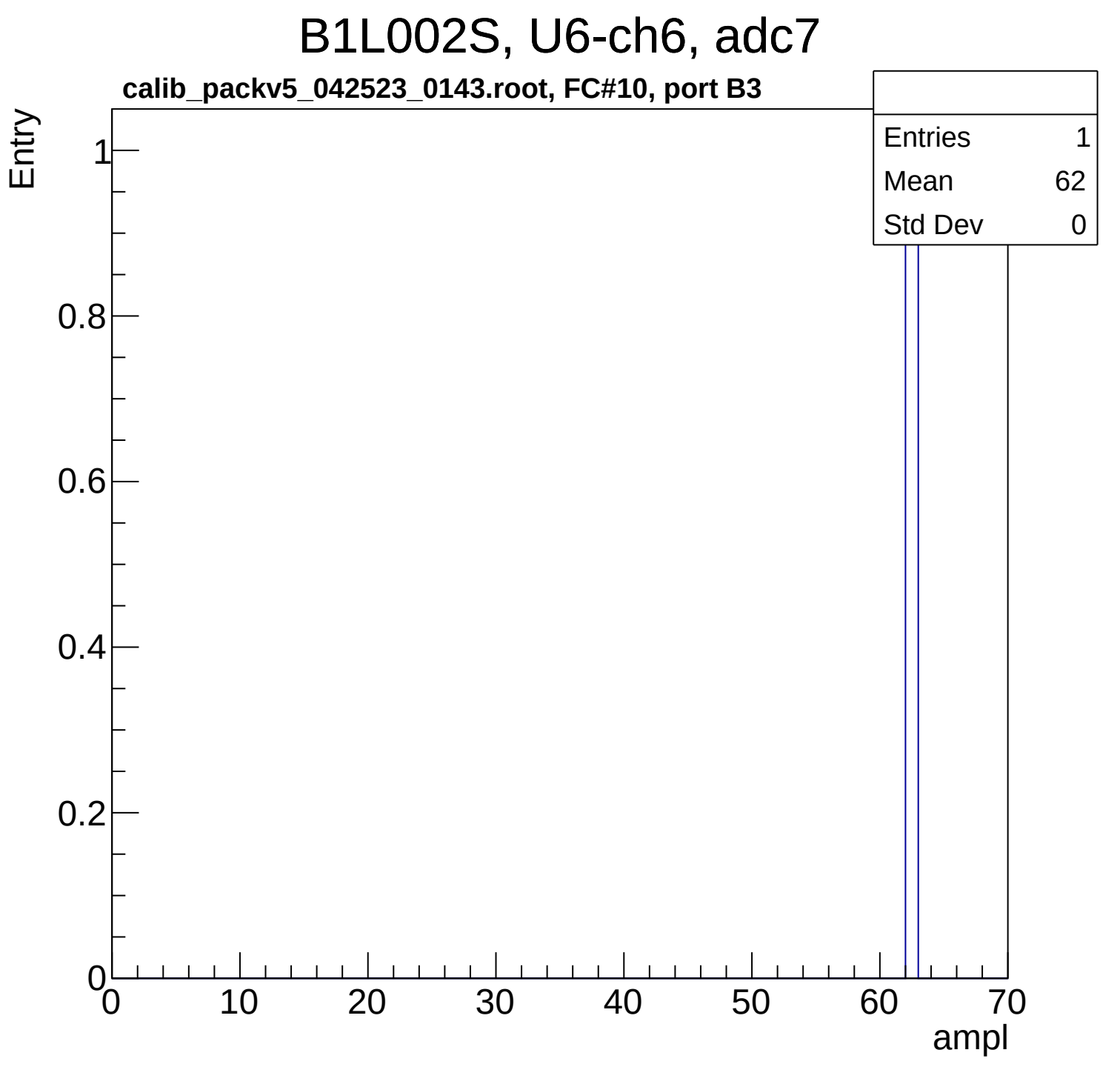

B1L002S, U6-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 41.3
Std Dev 2.643
Gaus mean : 42.0303
Gaus Width: 2.6818
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

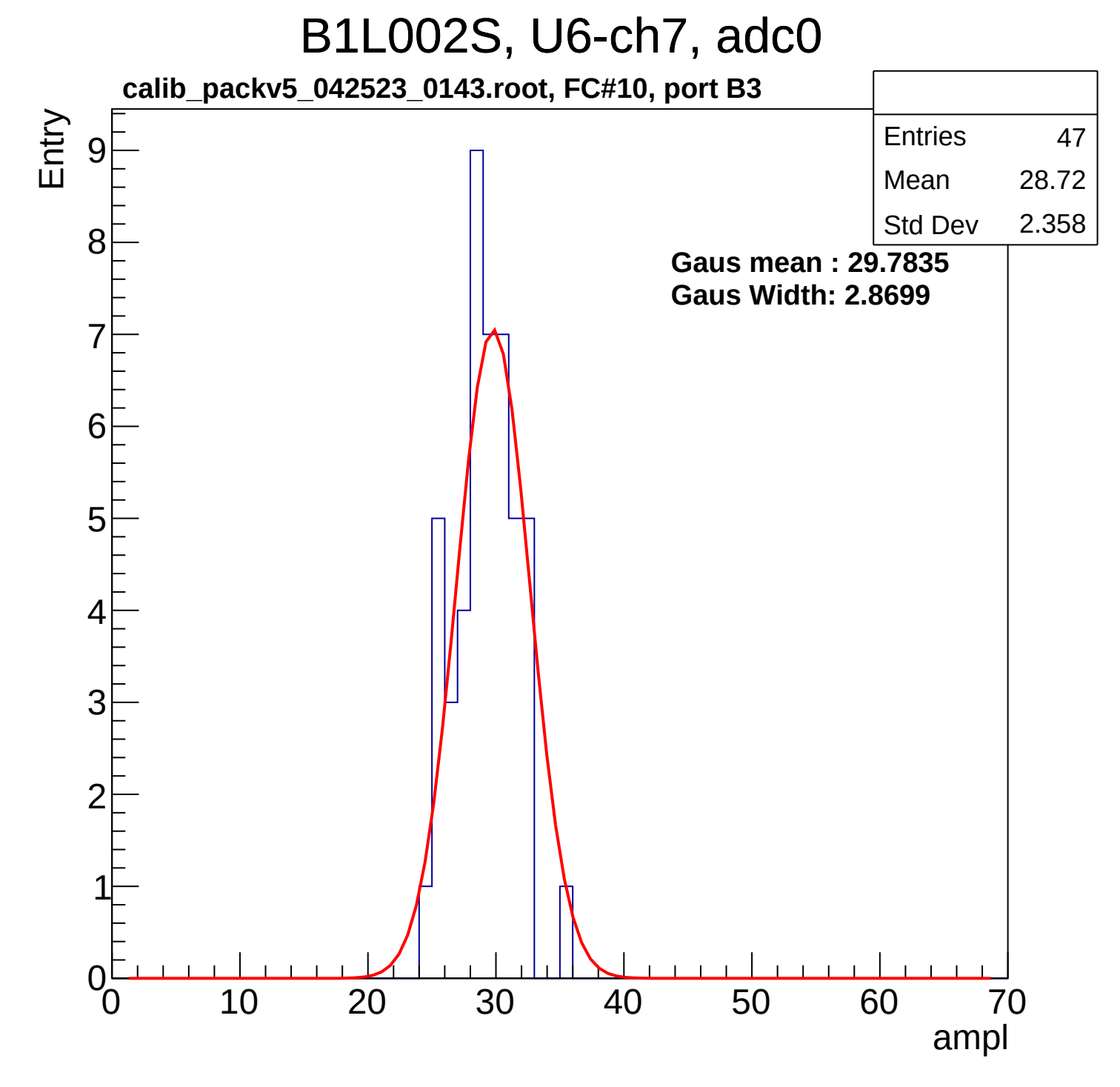

B1L002S, U6-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.23
Std Dev 3.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

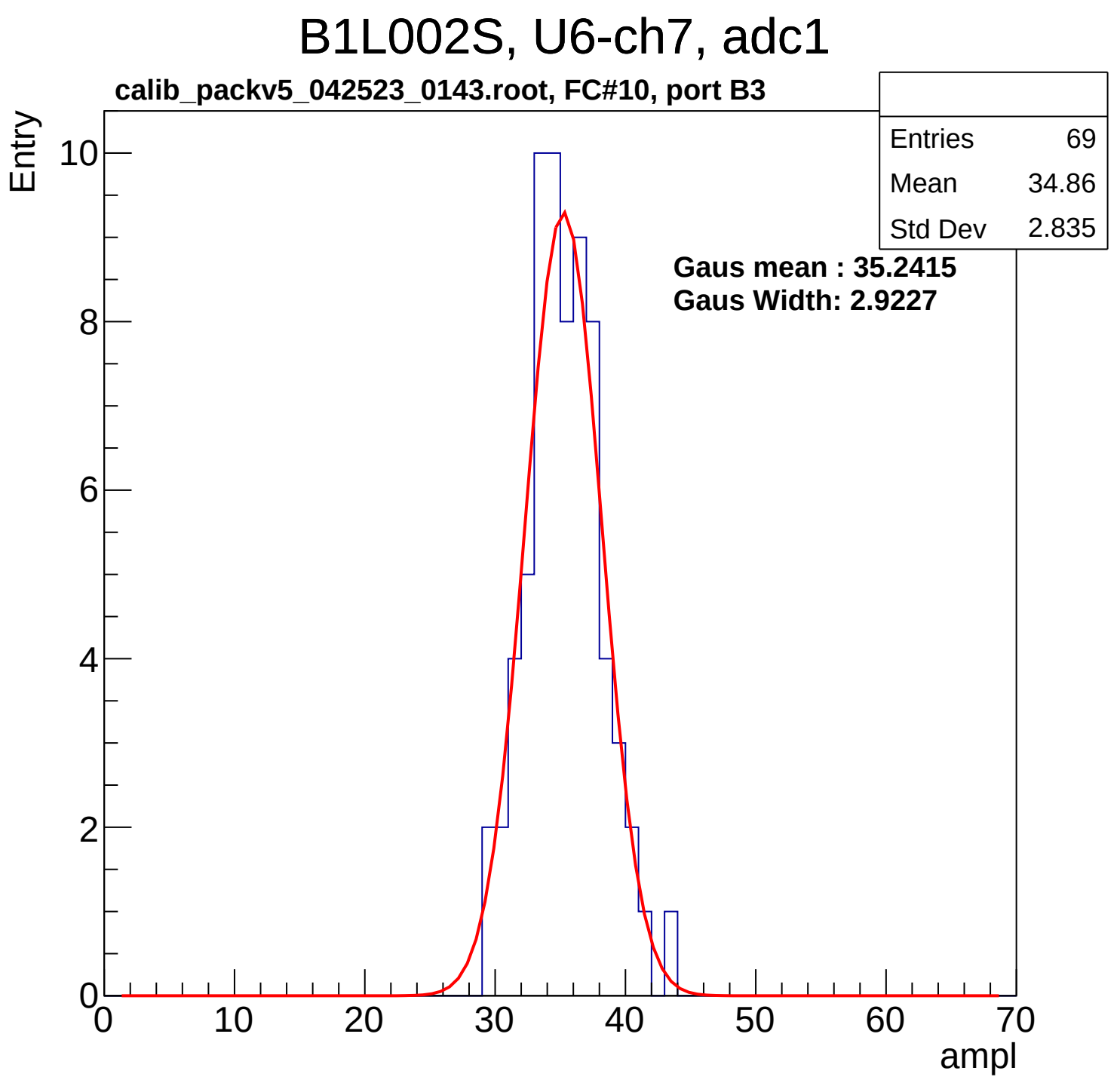

B1L002S, U6-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53
Std Dev 2.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

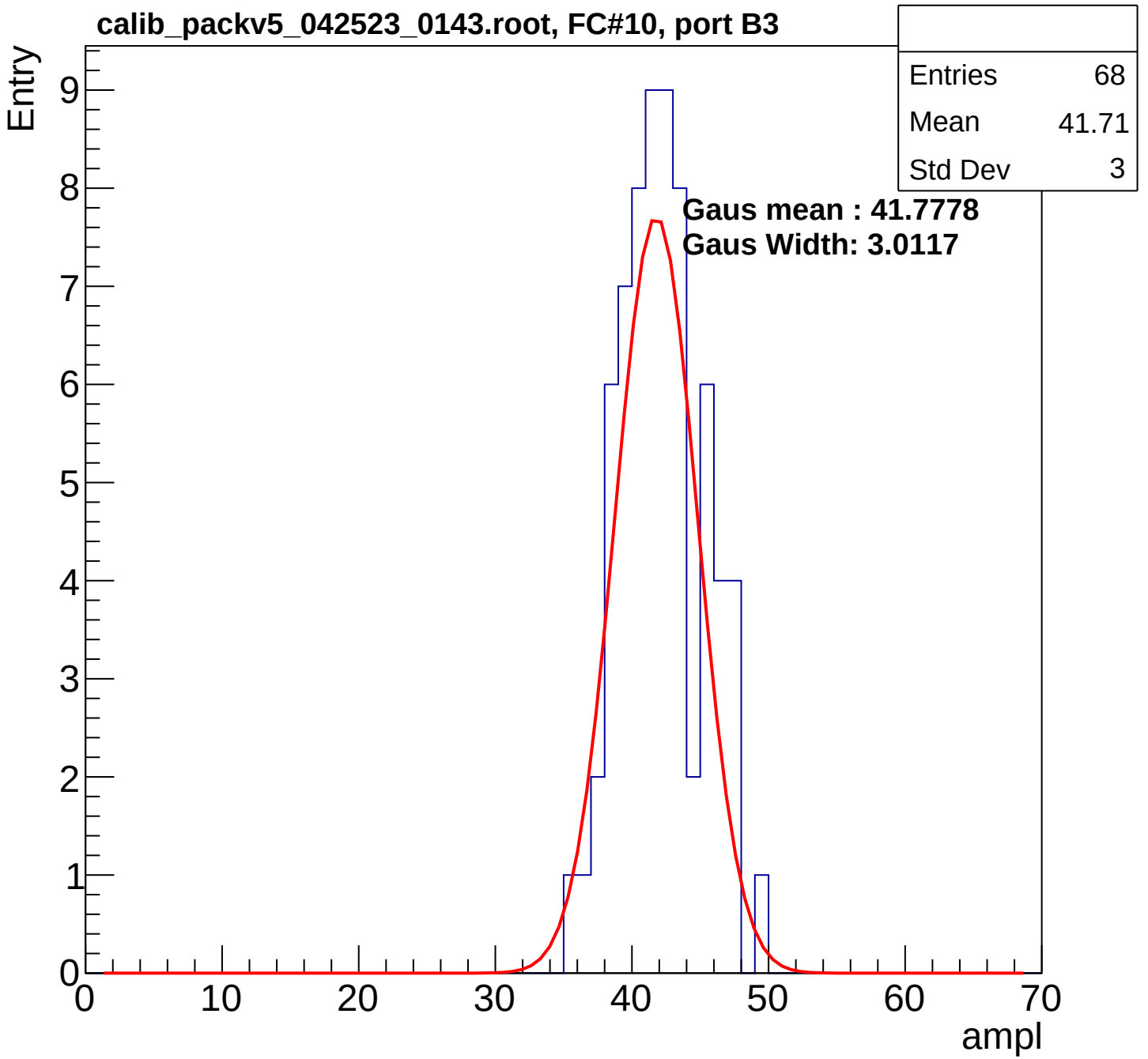

B1L002S, U6-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.24
Std Dev 2.454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

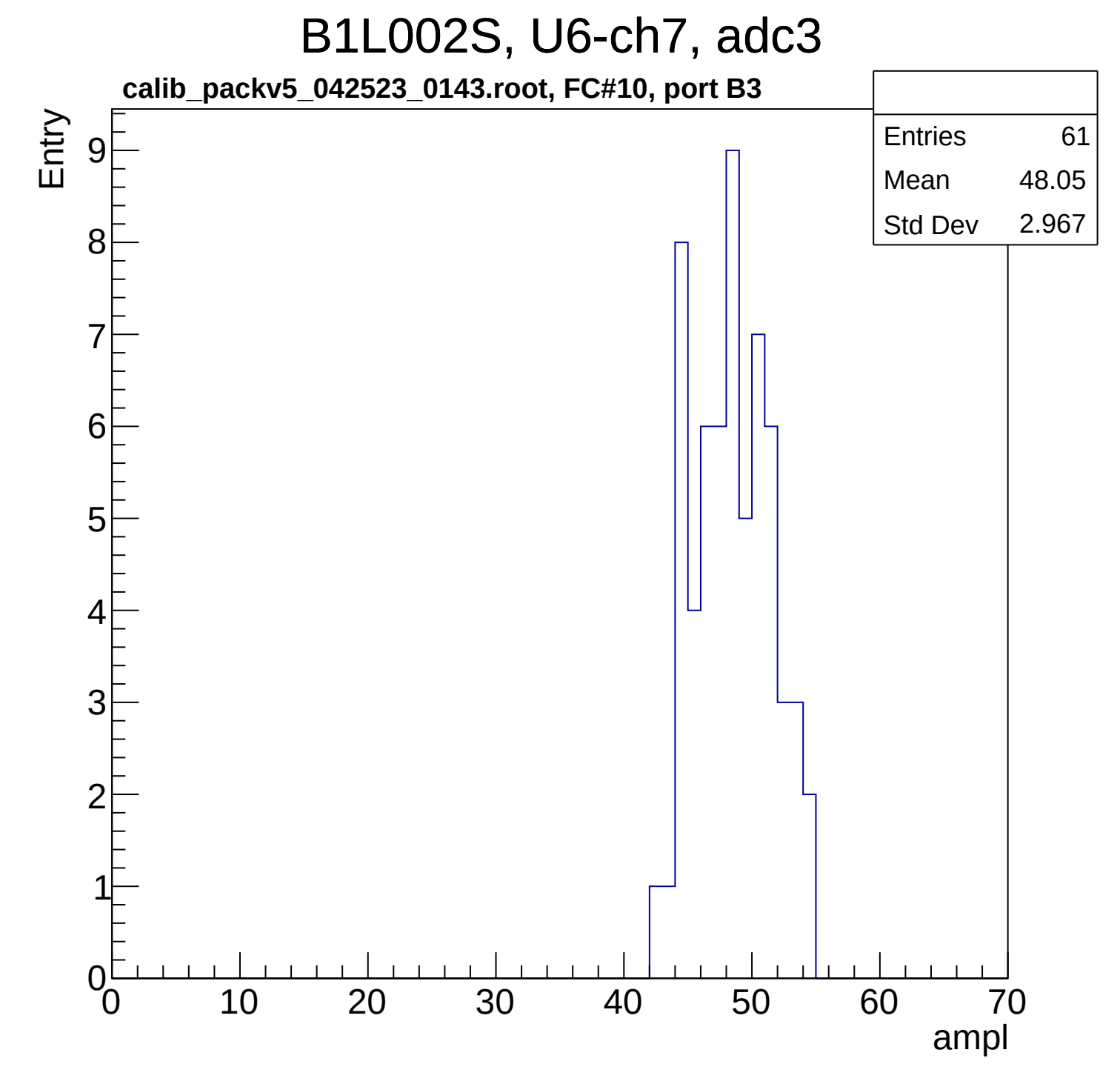

B1L002S, U6-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.03
Std Dev 10.92
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

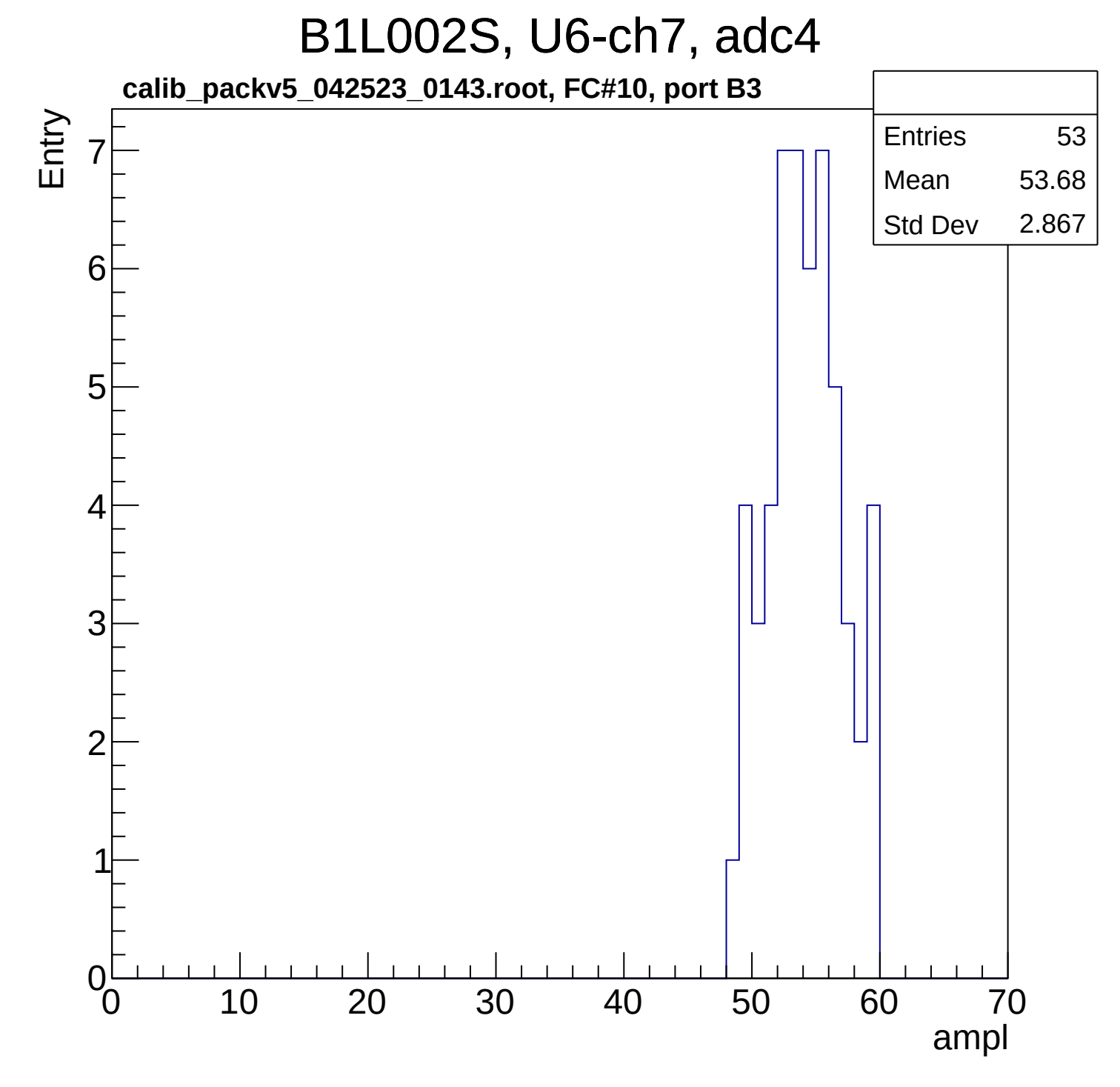

B1L002S, U6-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

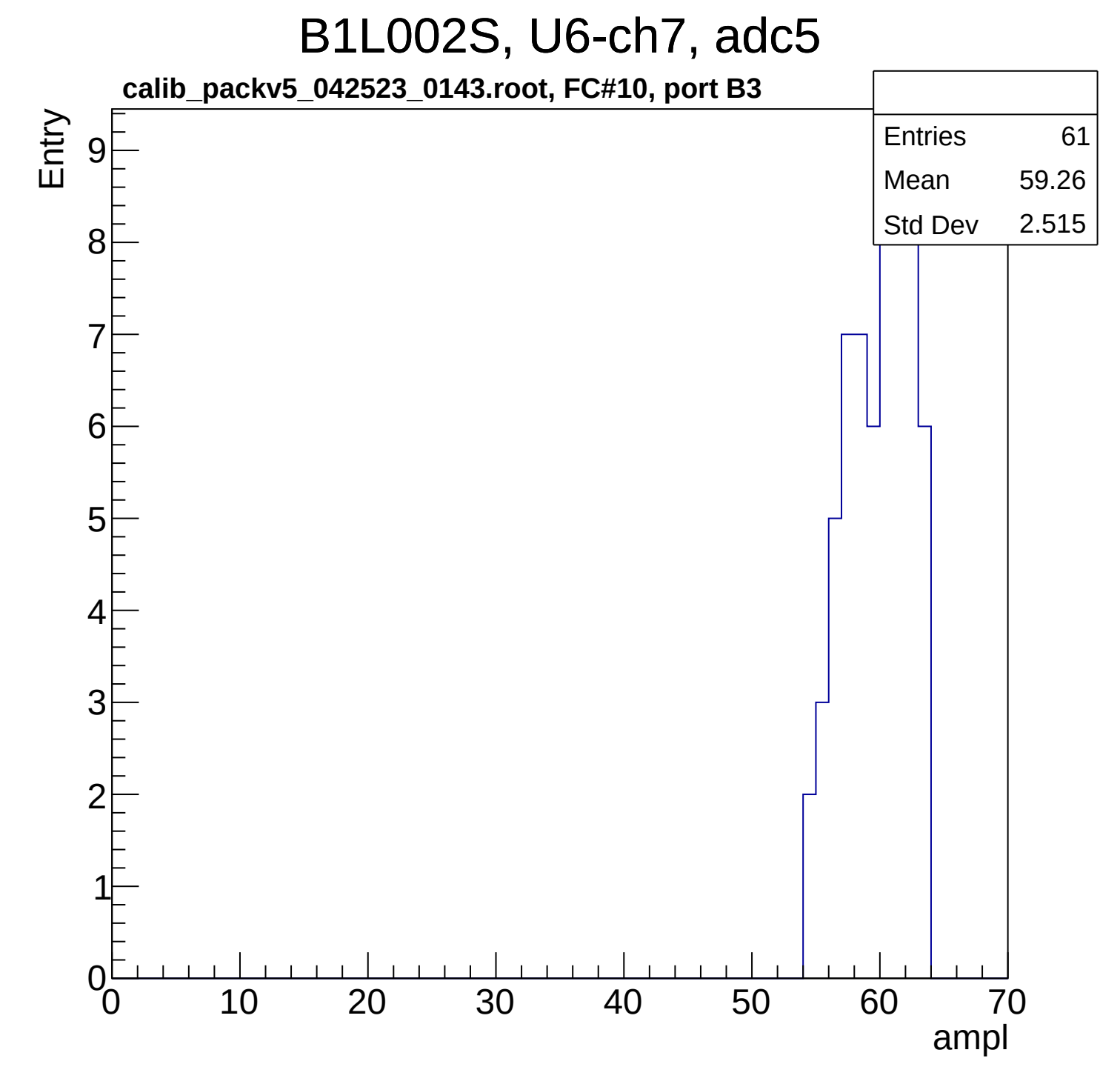

B1L002S, U6-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 28.72
Std Dev 2.358
Gaus mean : 29.7835
Gaus Width: 2.8699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

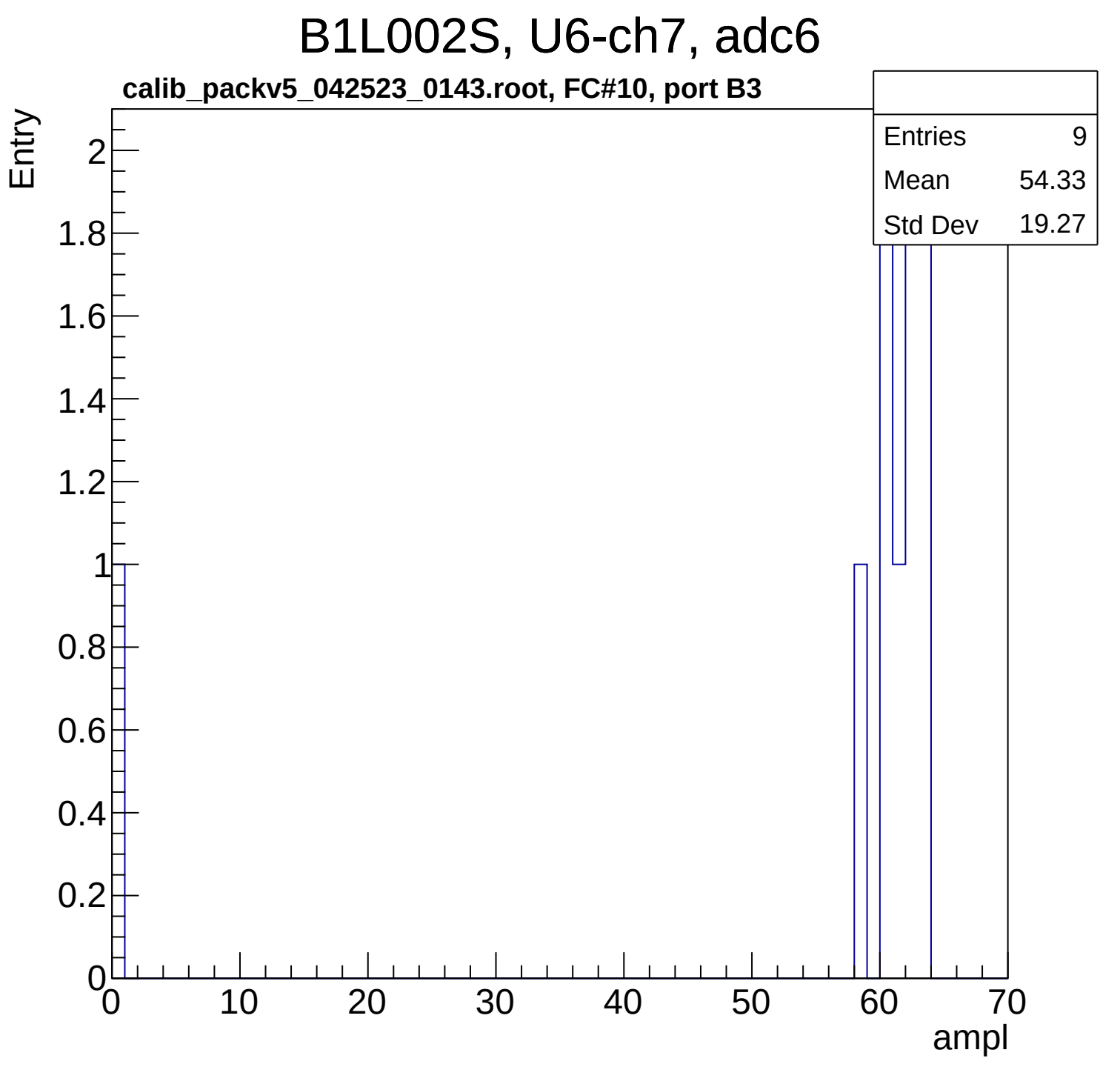

B1L002S, U6-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.86
Std Dev 2.835
Gaus mean : 35.2415
Gaus Width: 2.9227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

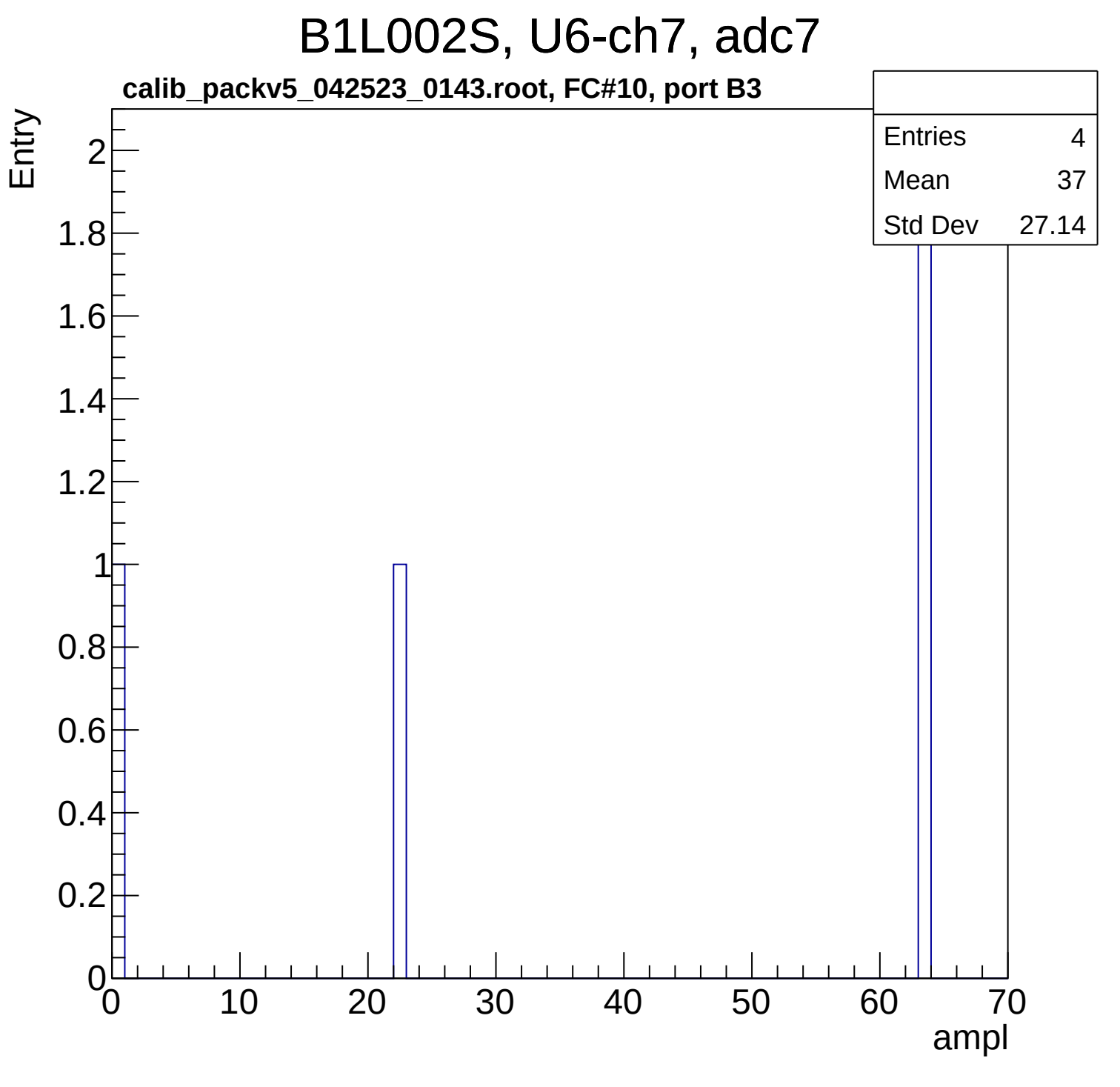

B1L002S, U6-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.71
Std Dev 3
Gaus mean : 41.7778
Gaus Width: 3.0117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

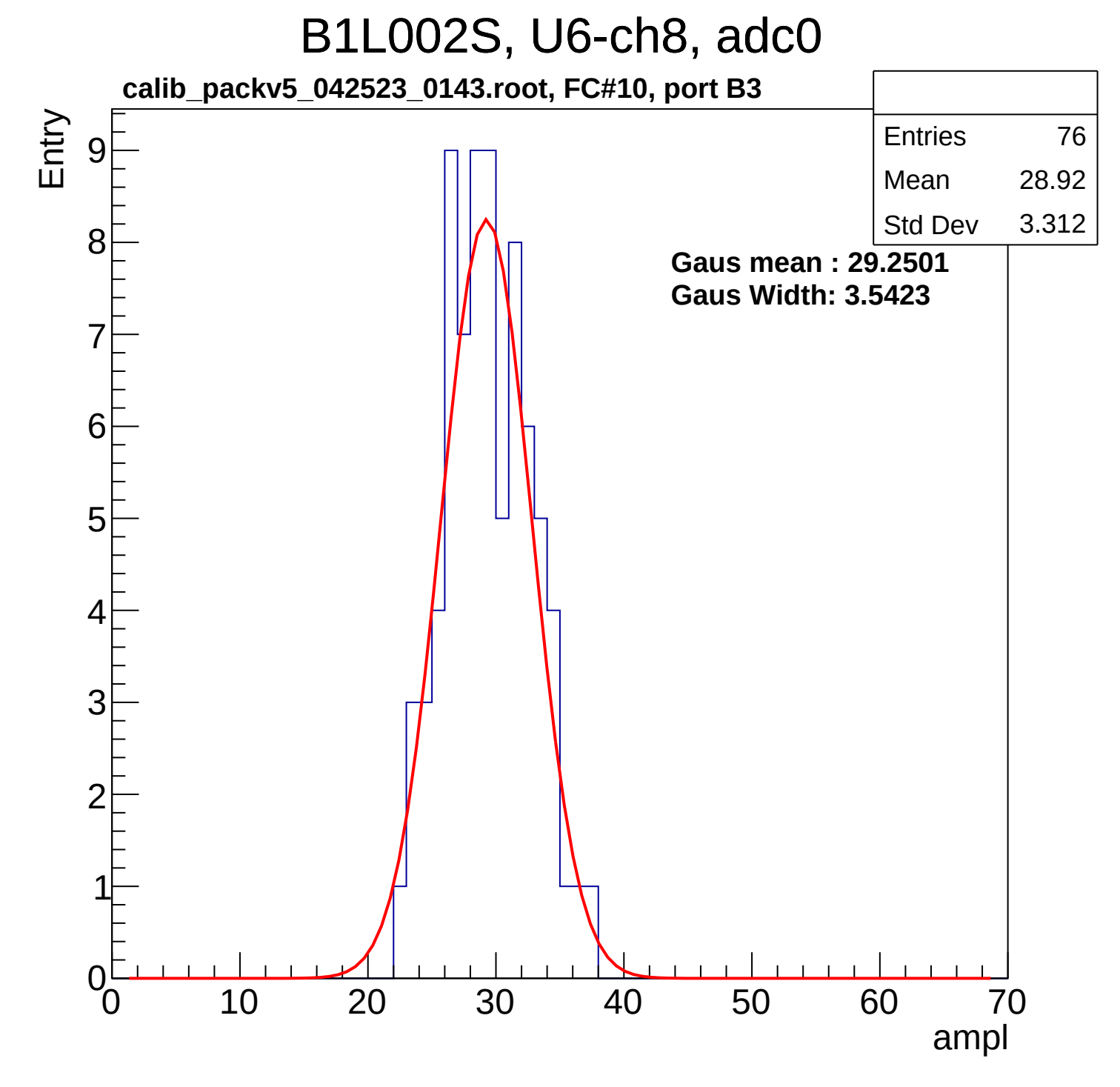

B1L002S, U6-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.05
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

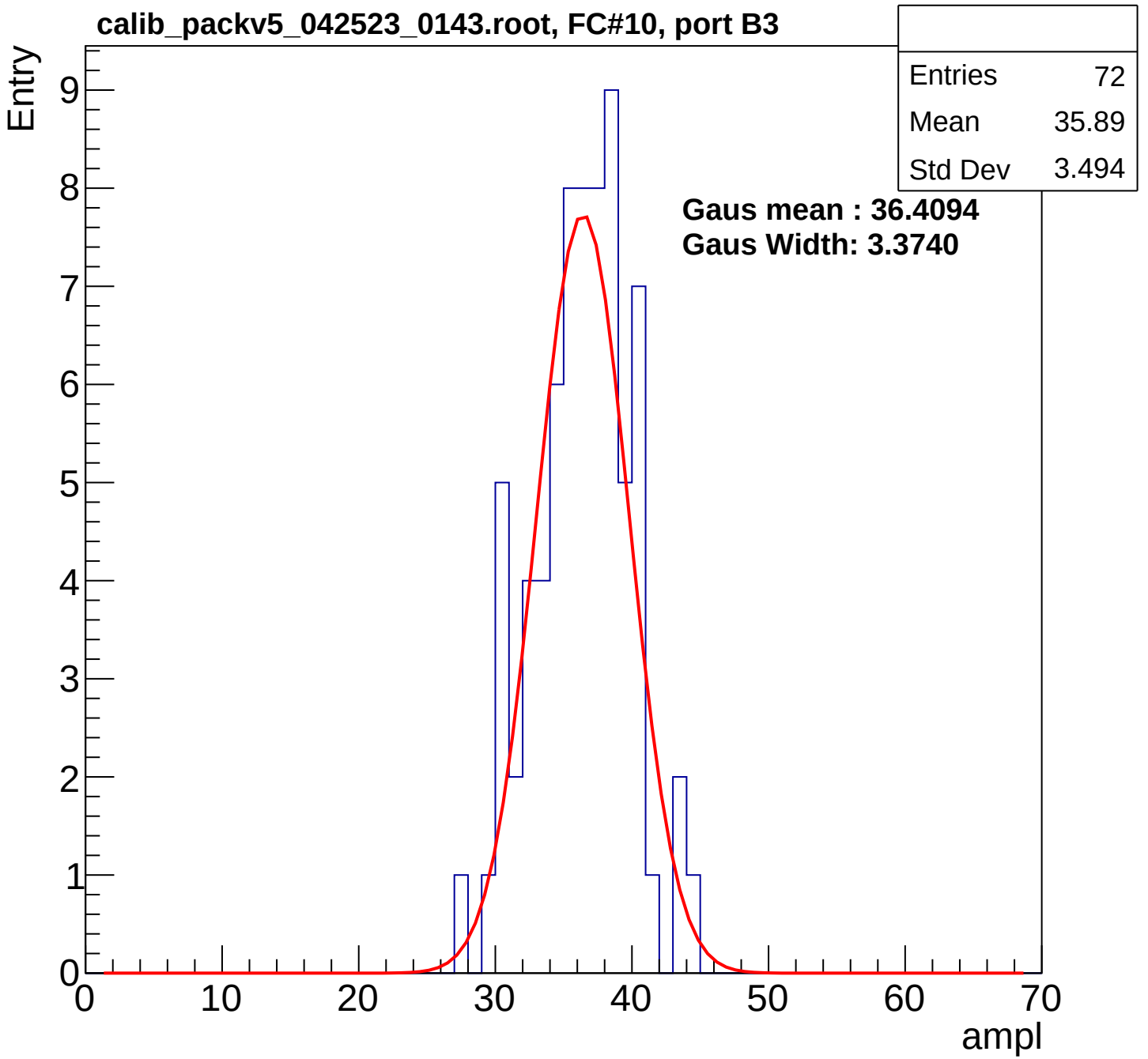

B1L002S, U6-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.68
Std Dev 2.867
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

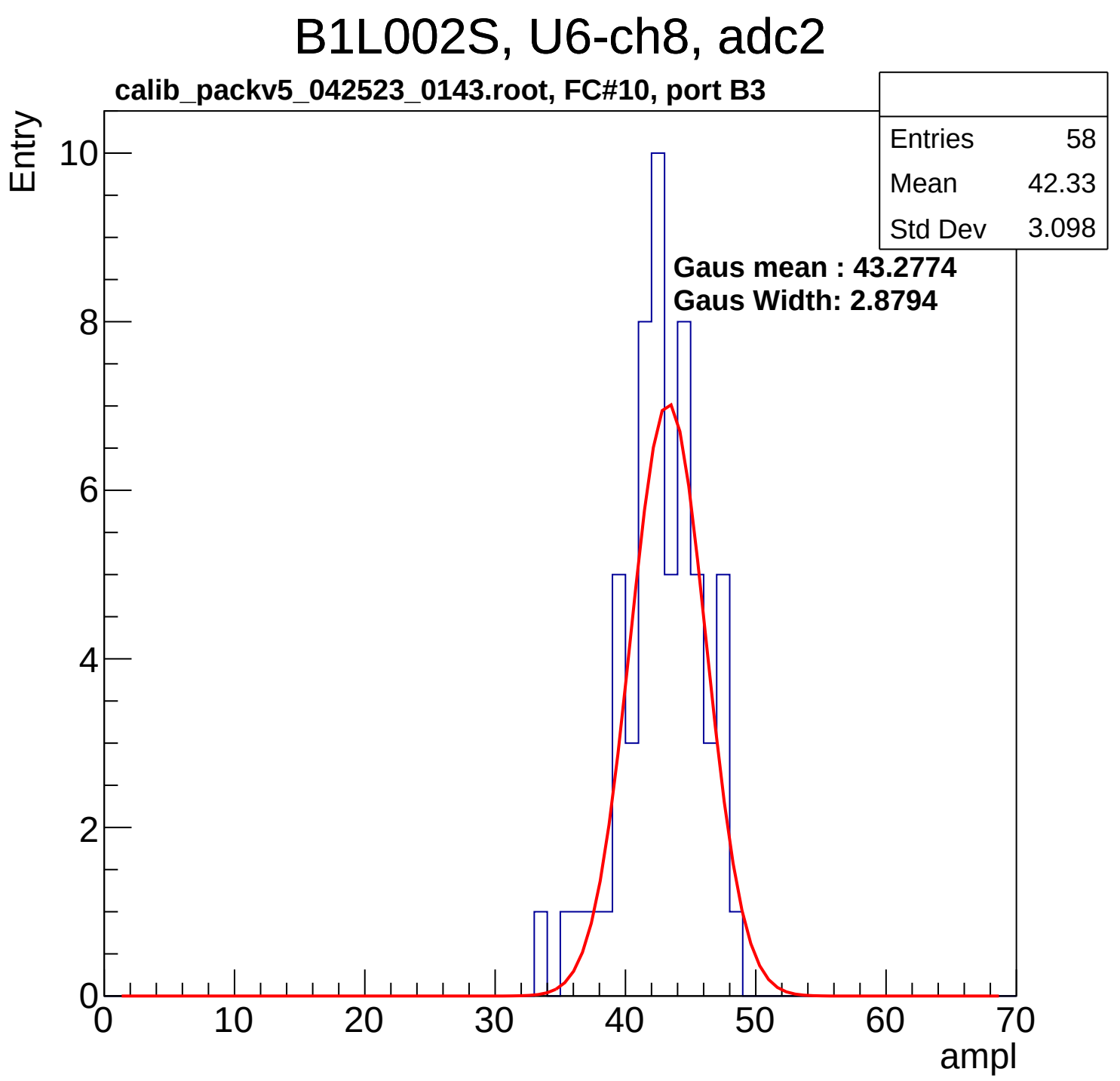

B1L002S, U6-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 59.26
Std Dev 2.515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

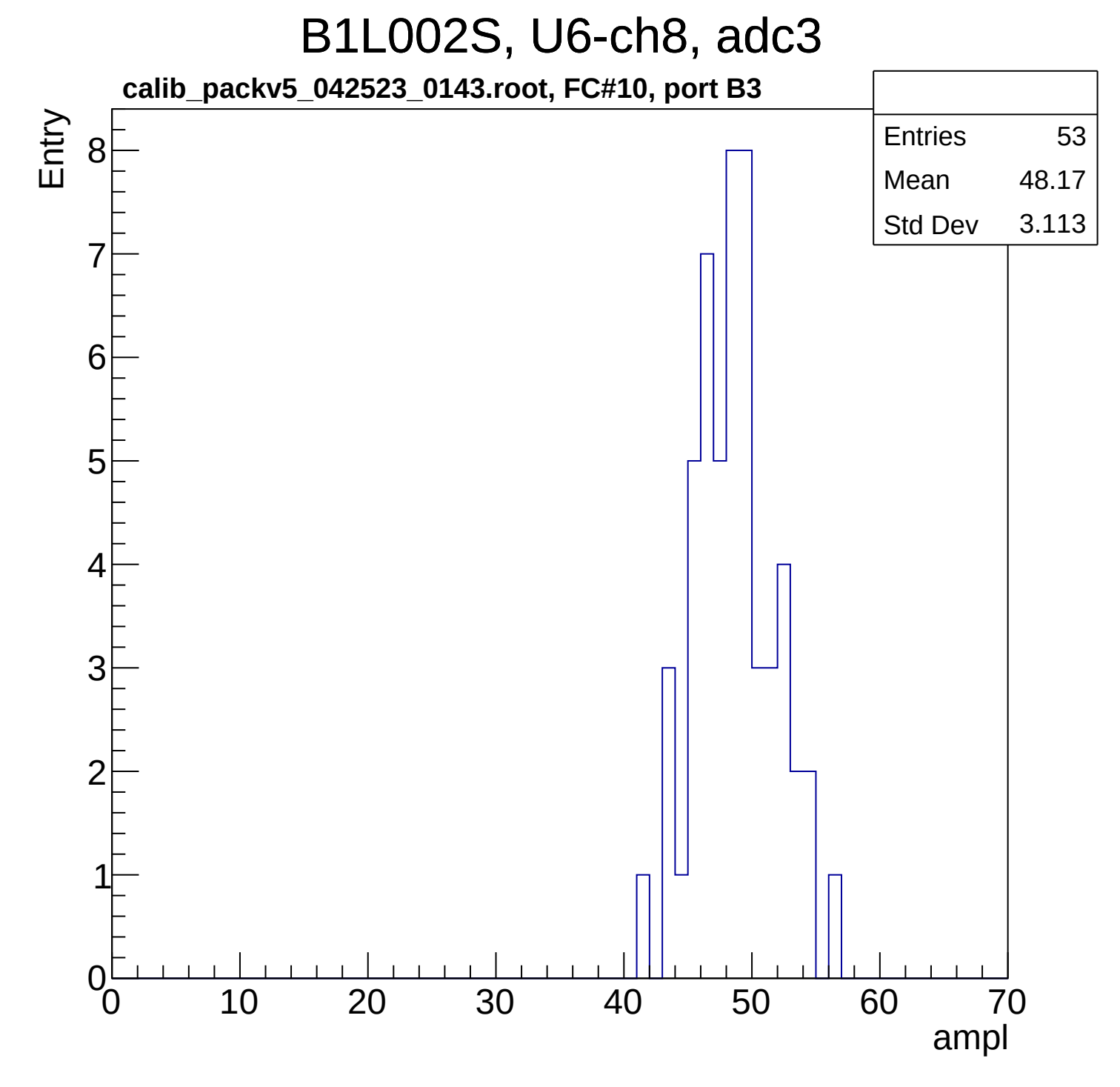

B1L002S, U6-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 54.33
Std Dev 19.27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

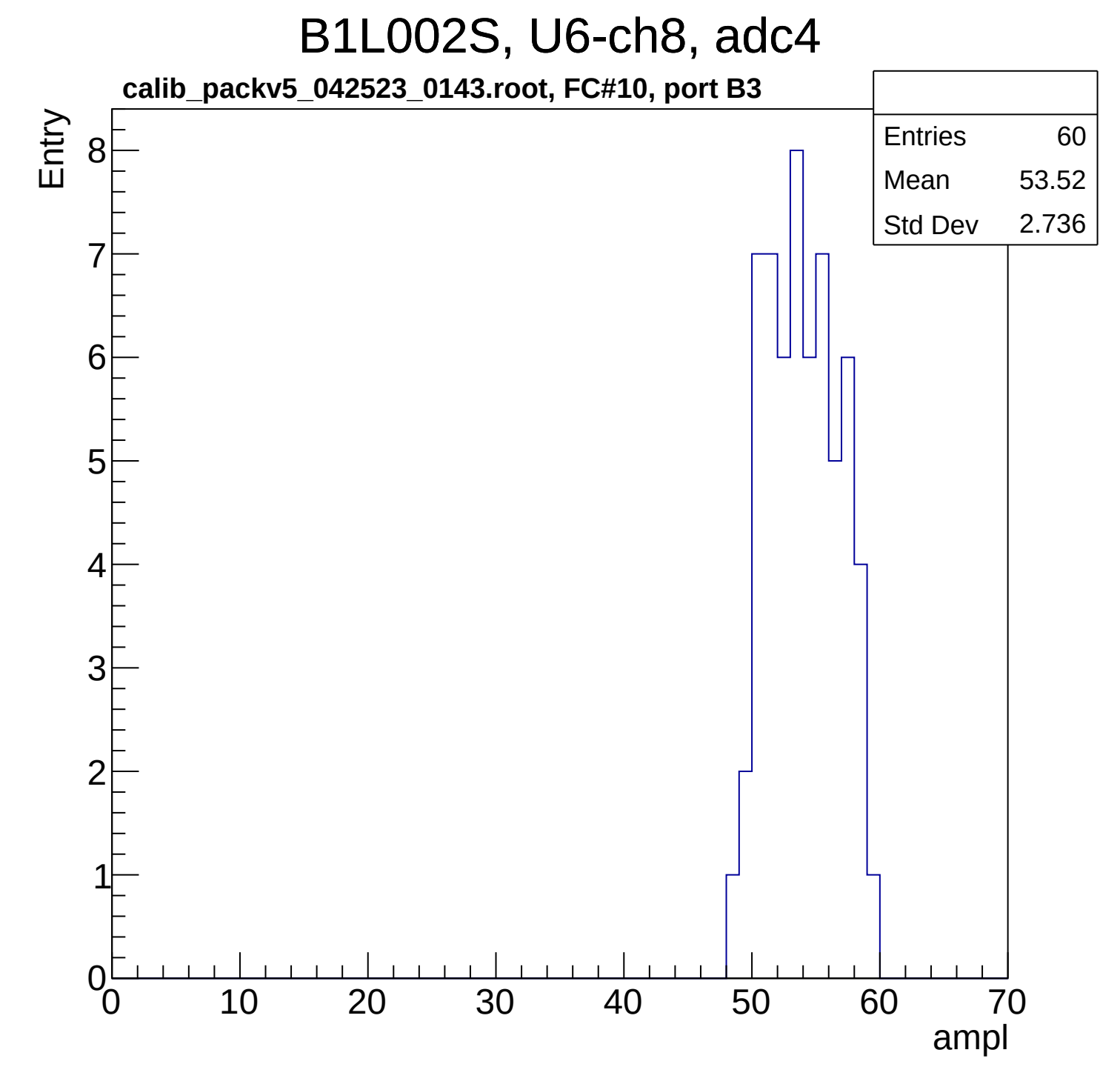

B1L002S, U6-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 37
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

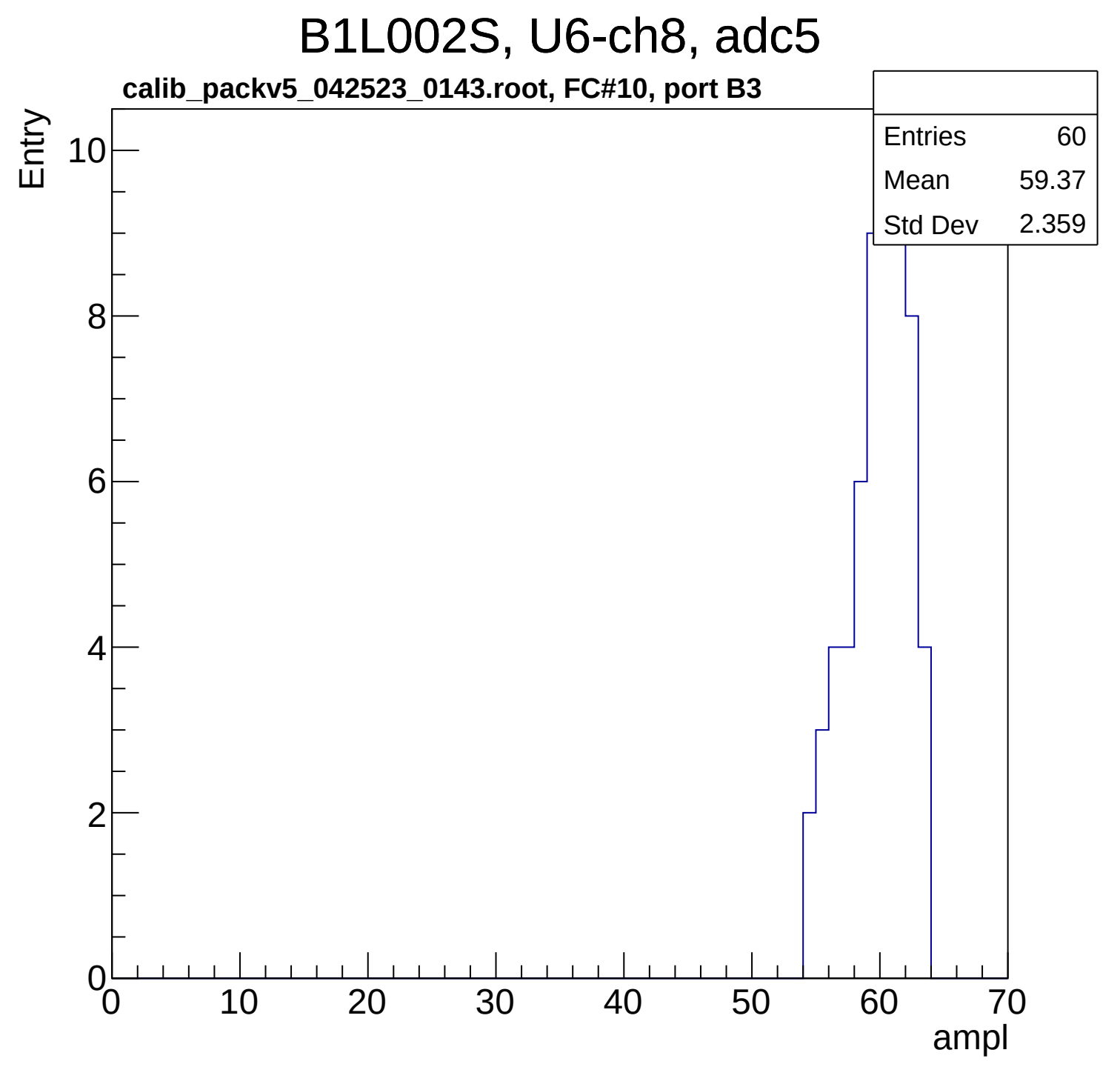

B1L002S, U6-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.92
Std Dev 3.312
Gaus mean : 29.2501
Gaus Width: 3.5423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

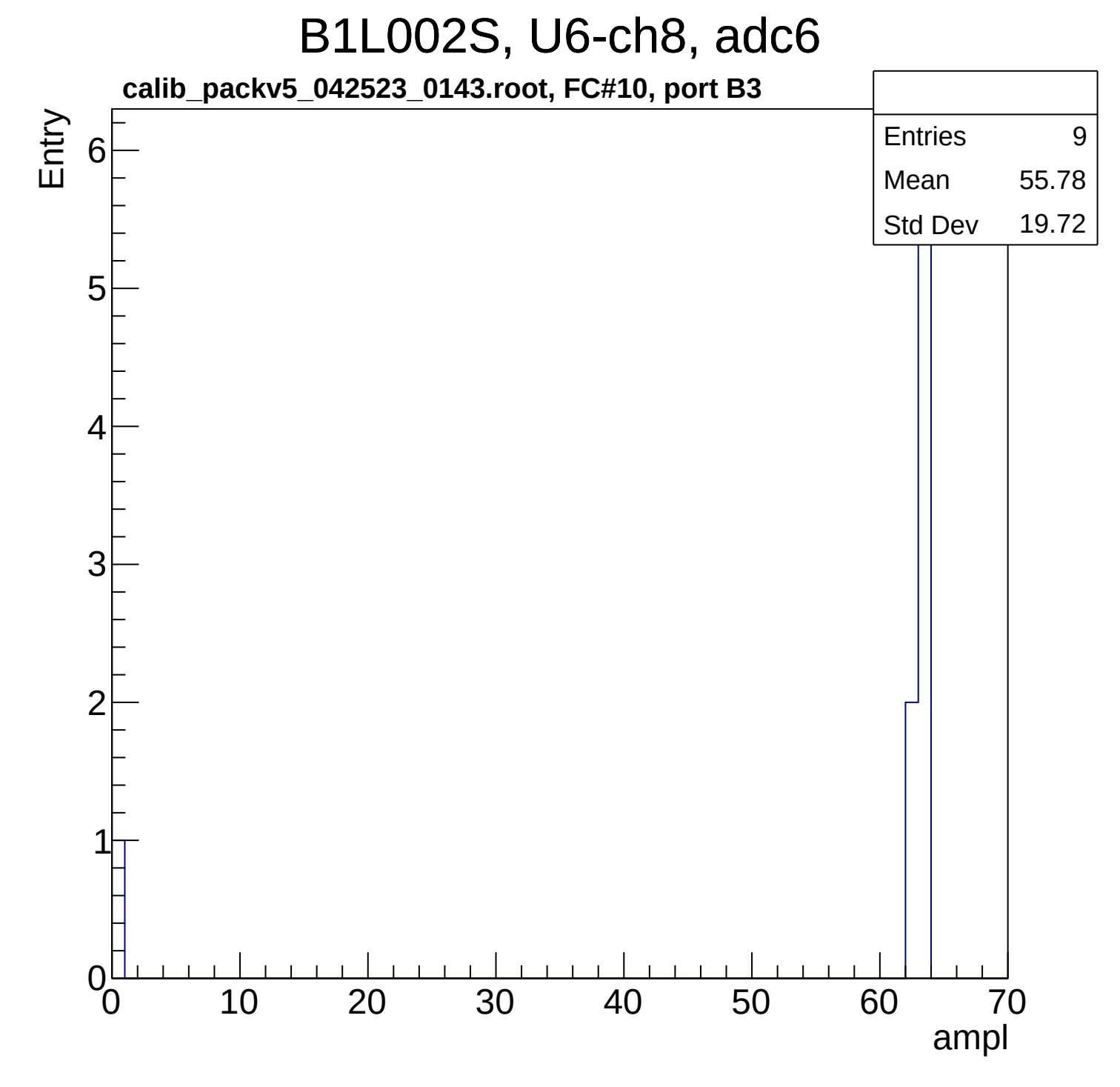

B1L002S, U6-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.89
Std Dev 3.494
Gaus mean : 36.4094
Gaus Width: 3.3740
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

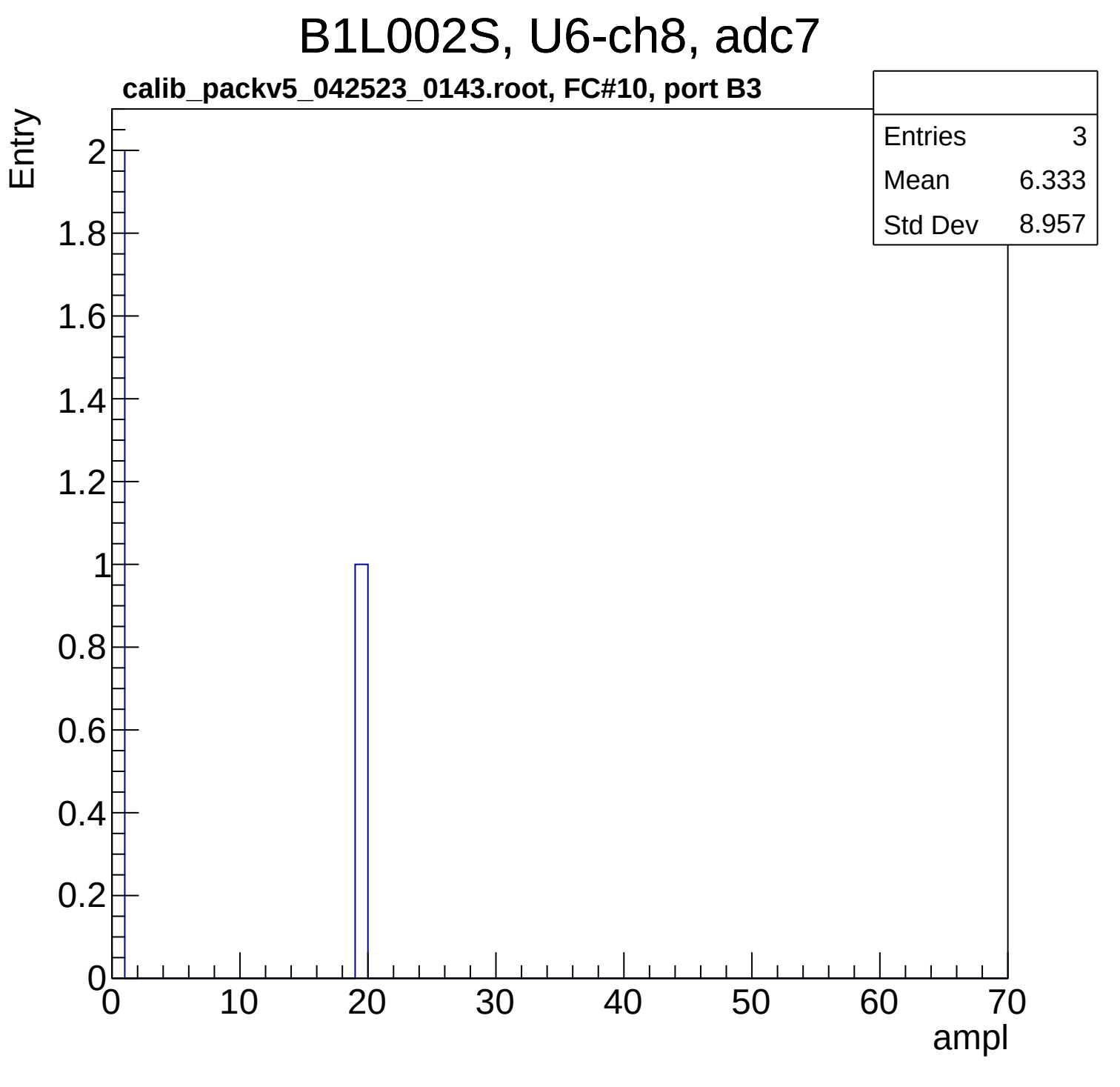

B1L002S, U6-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.33
Std Dev 3.098
Gaus mean : 43.2774
Gaus Width: 2.8794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

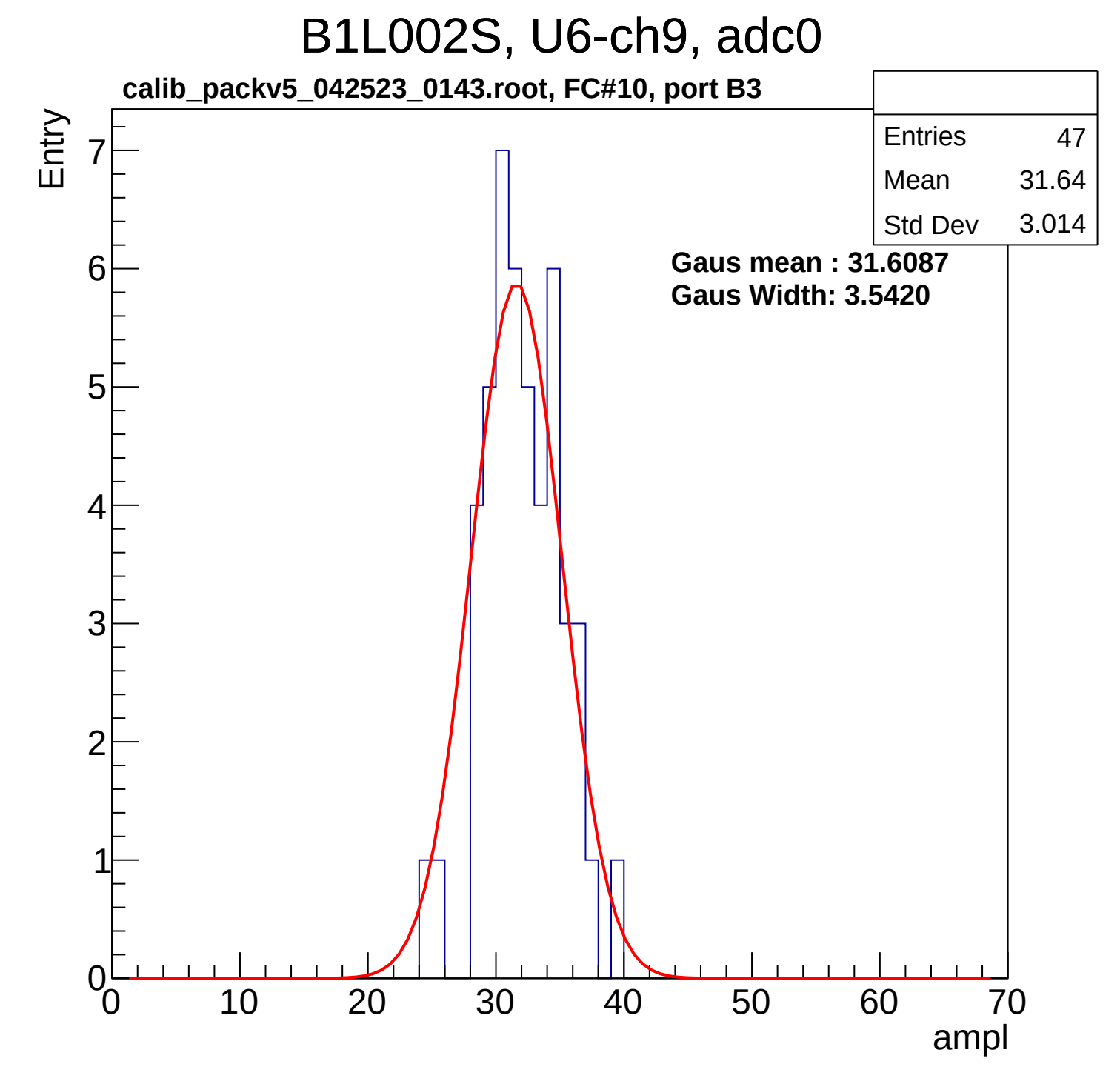

B1L002S, U6-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.17
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

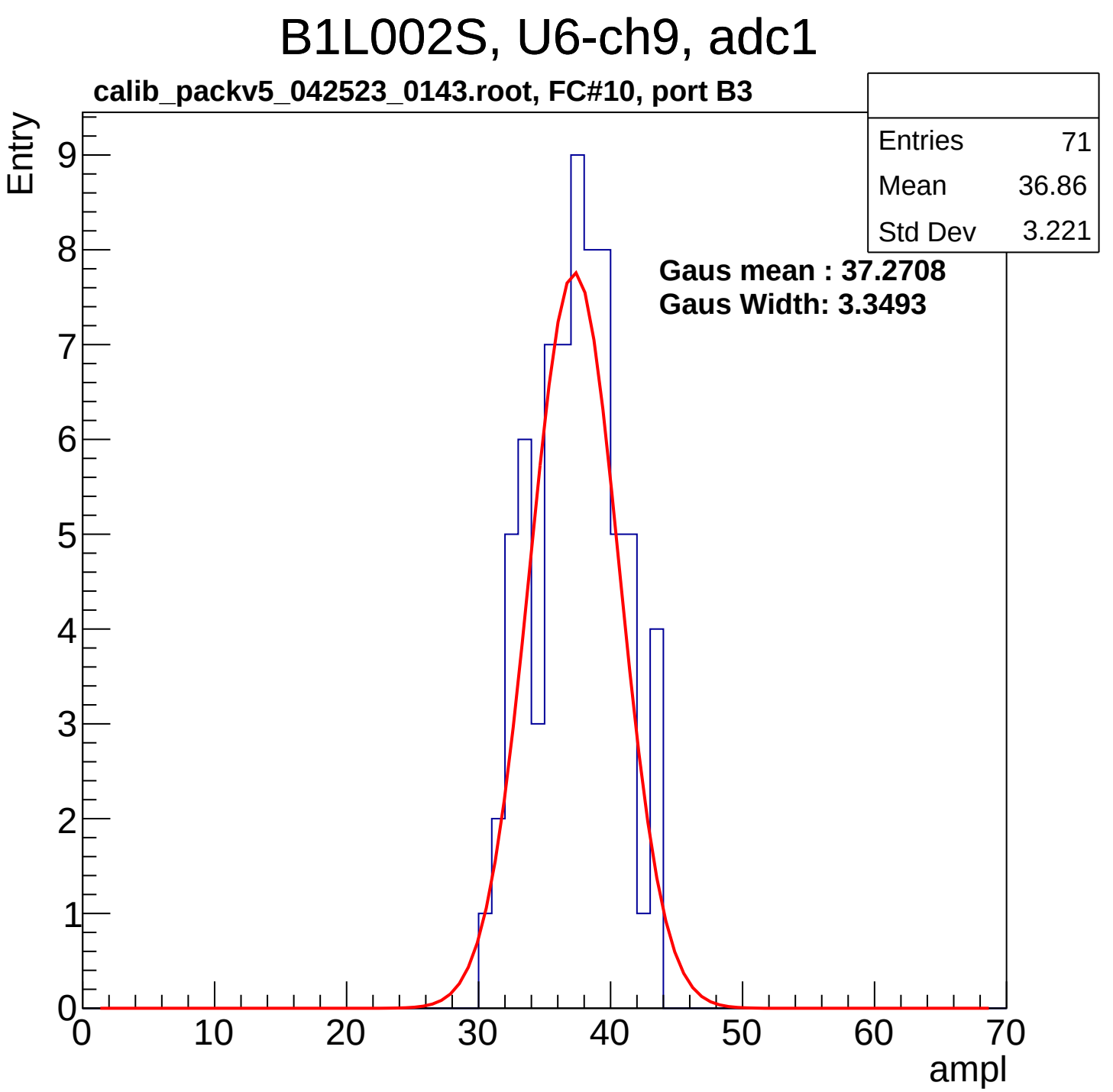

B1L002S, U6-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.52
Std Dev 2.736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

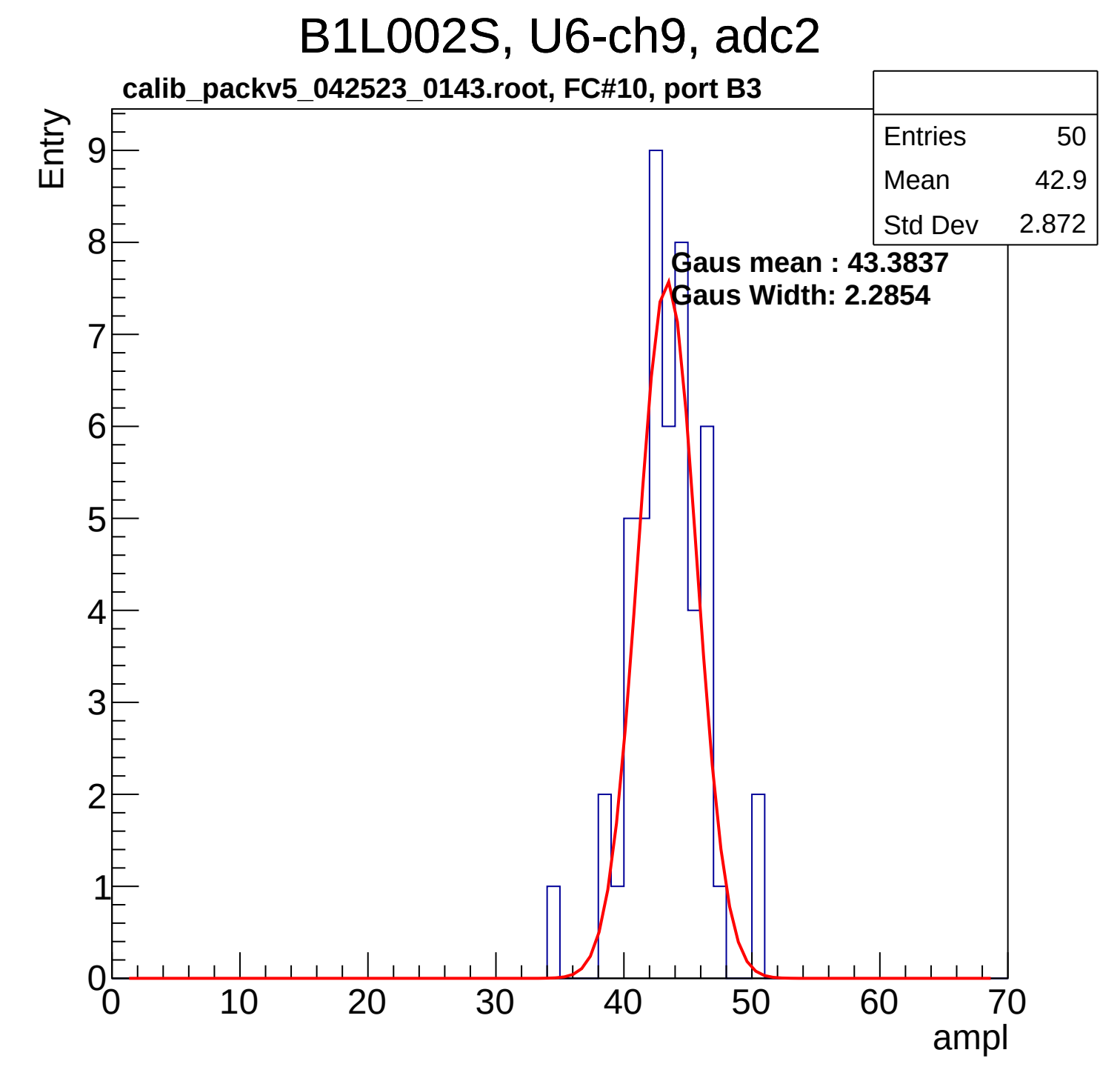

B1L002S, U6-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 59.37
Std Dev 2.359
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

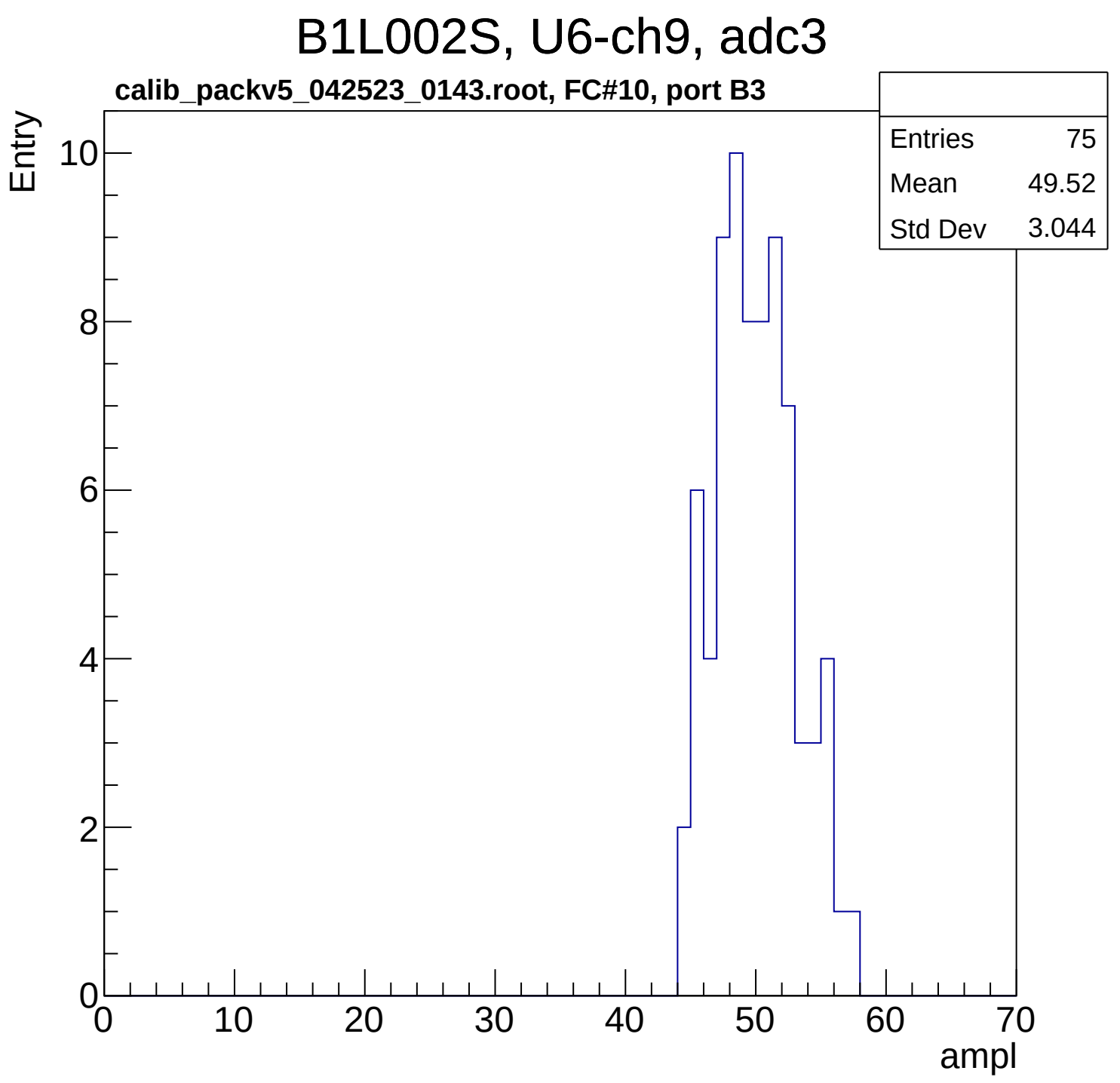

B1L002S, U6-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.78
Std Dev 19.72
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

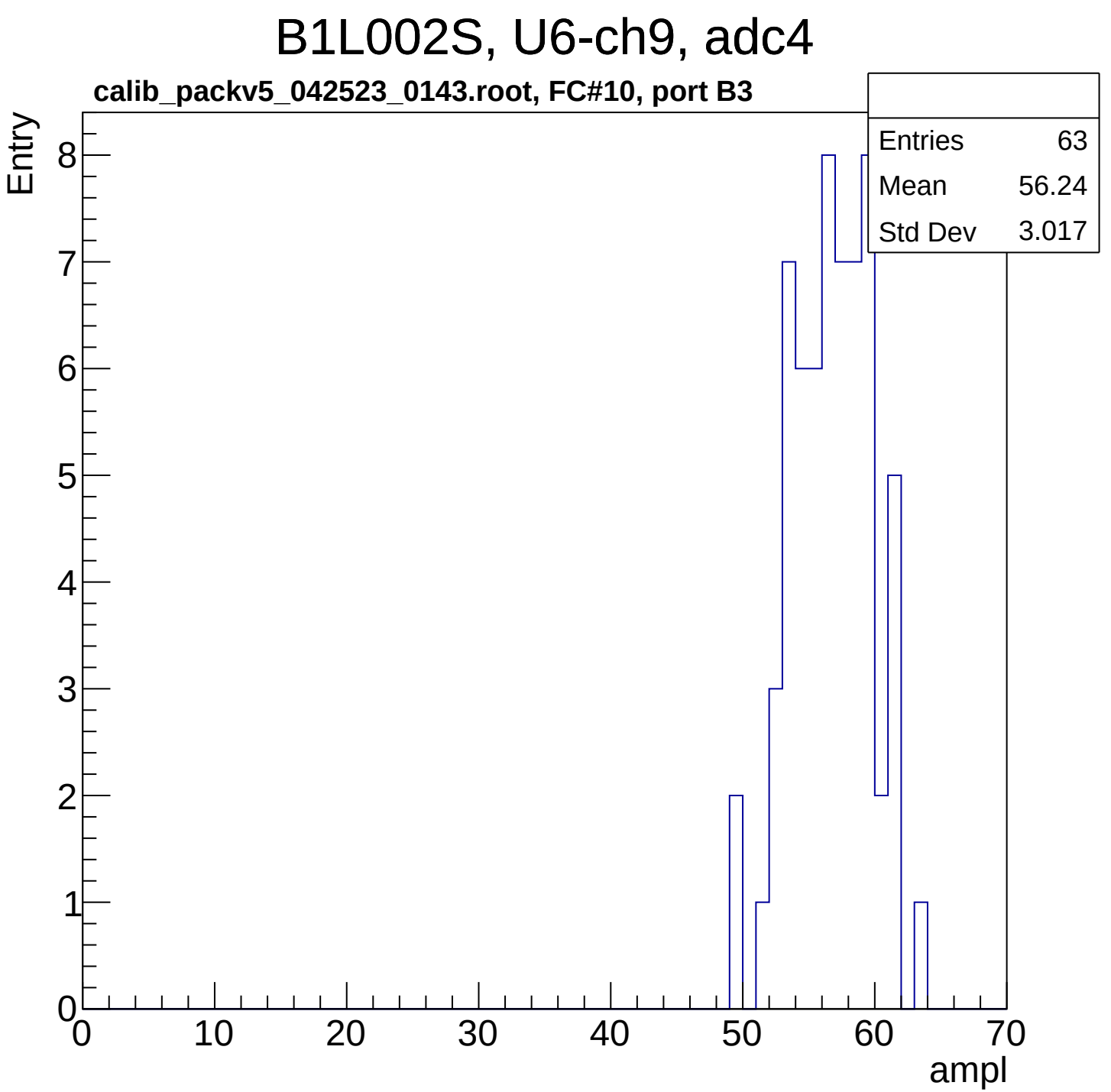

B1L002S, U6-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

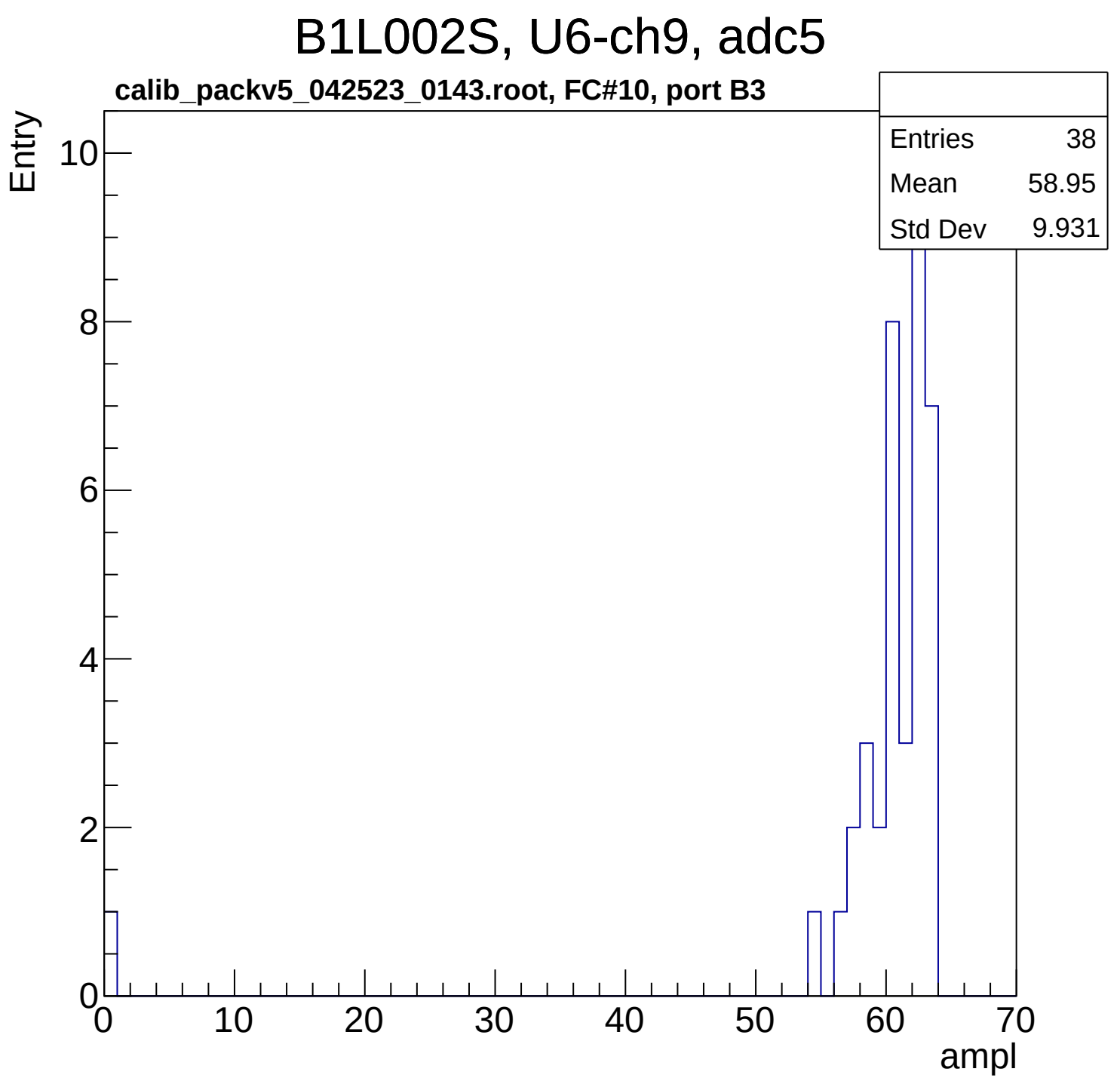

B1L002S, U6-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 31.64
Std Dev 3.014
Gaus mean : 31.6087
Gaus Width: 3.5420
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

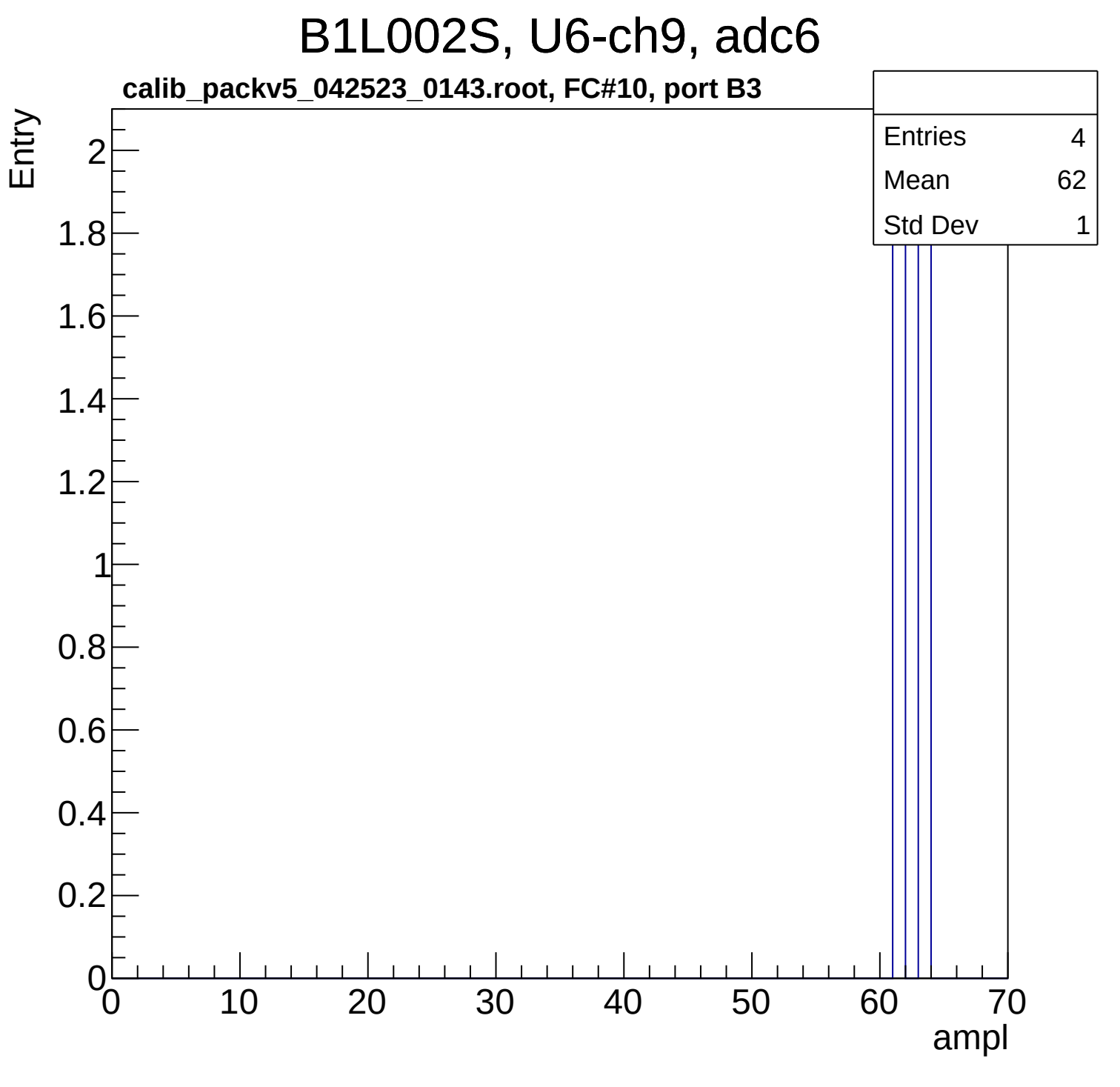

B1L002S, U6-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 36.86
Std Dev 3.221
Gaus mean : 37.2708
Gaus Width: 3.3493
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

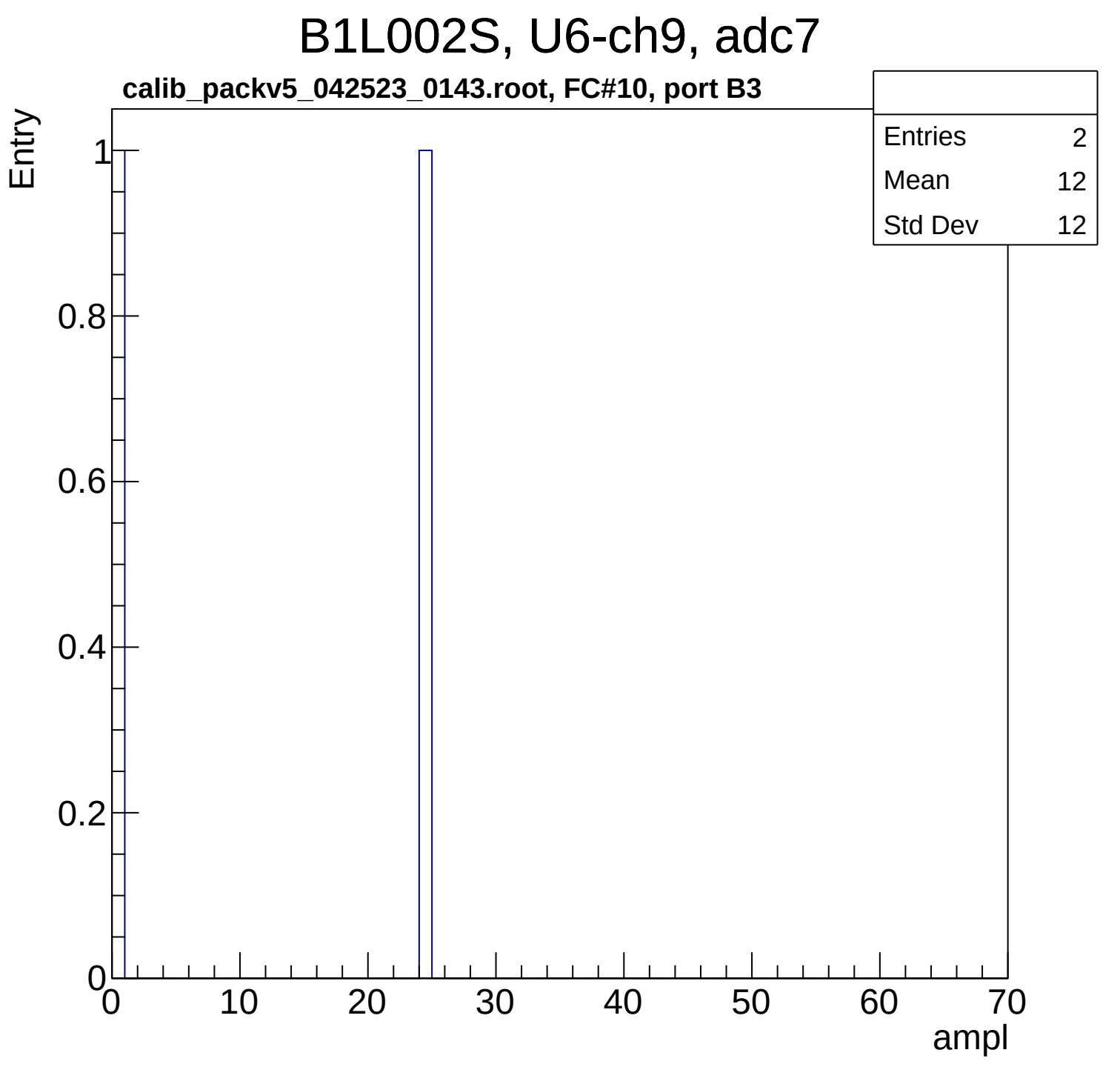

B1L002S, U6-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.9
Std Dev 2.872
Gaus mean : 43.3837
Gaus Width: 2.2854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

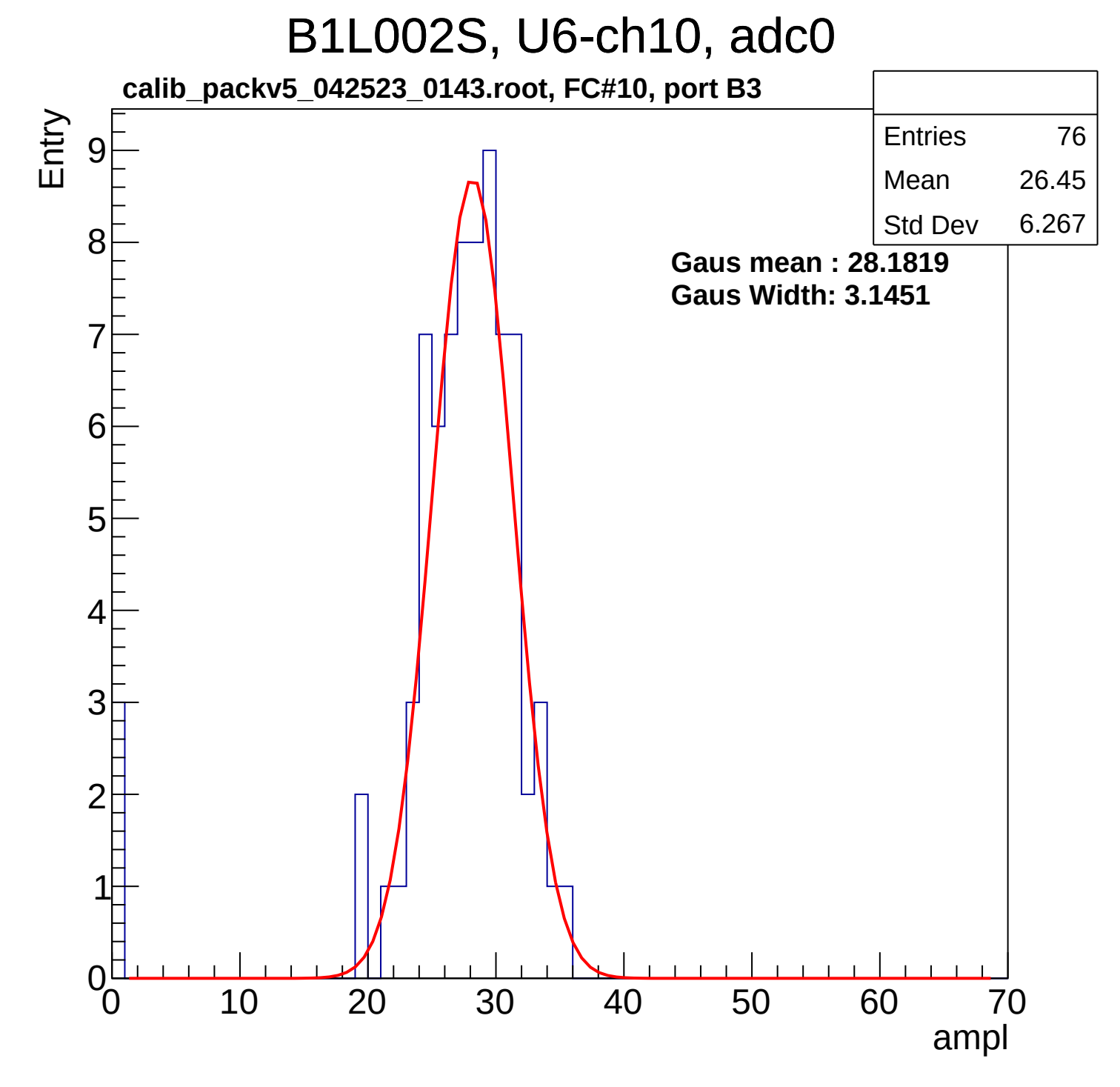

B1L002S, U6-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 49.52
Std Dev 3.044
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

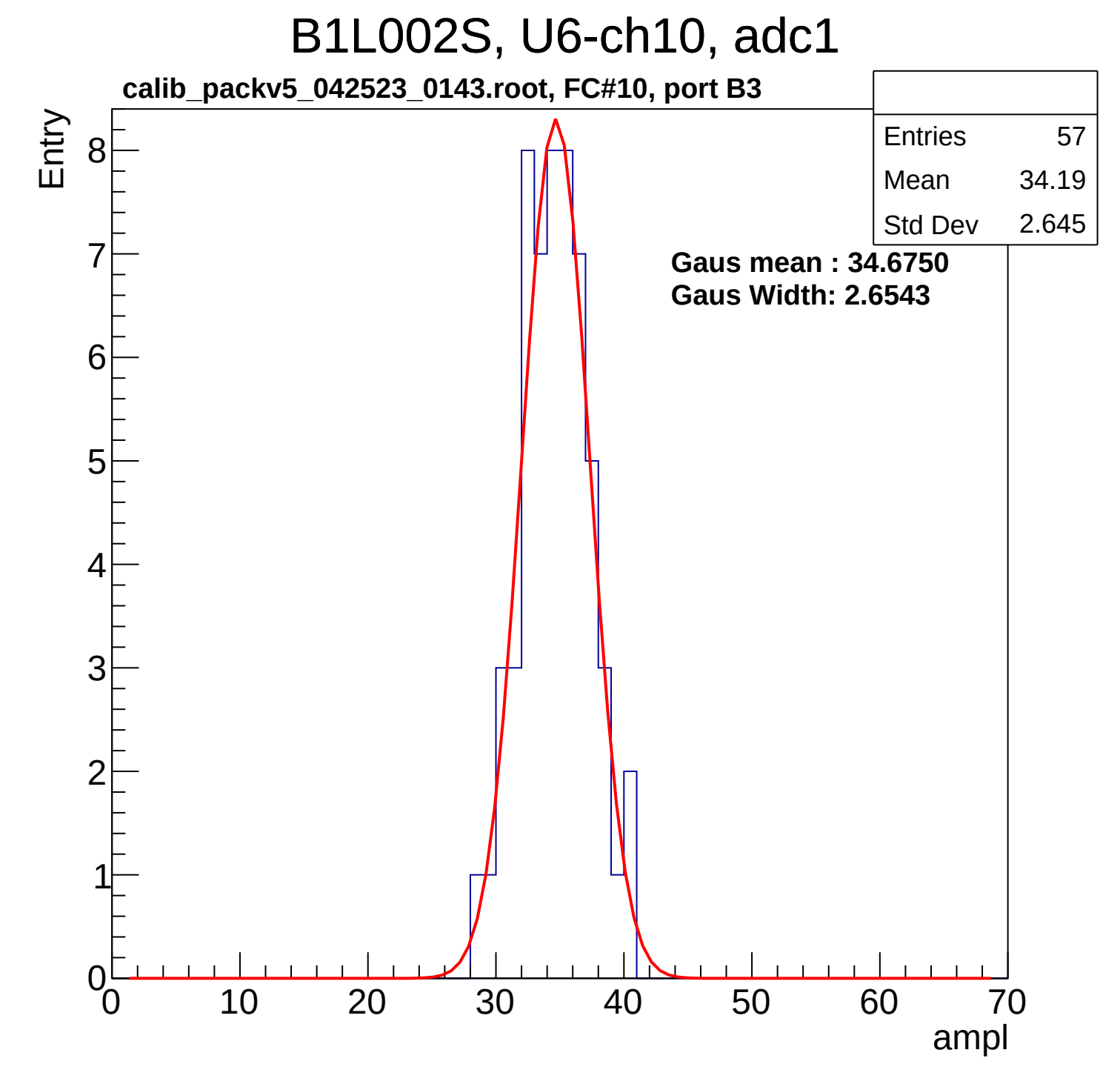

B1L002S, U6-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 56.24
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

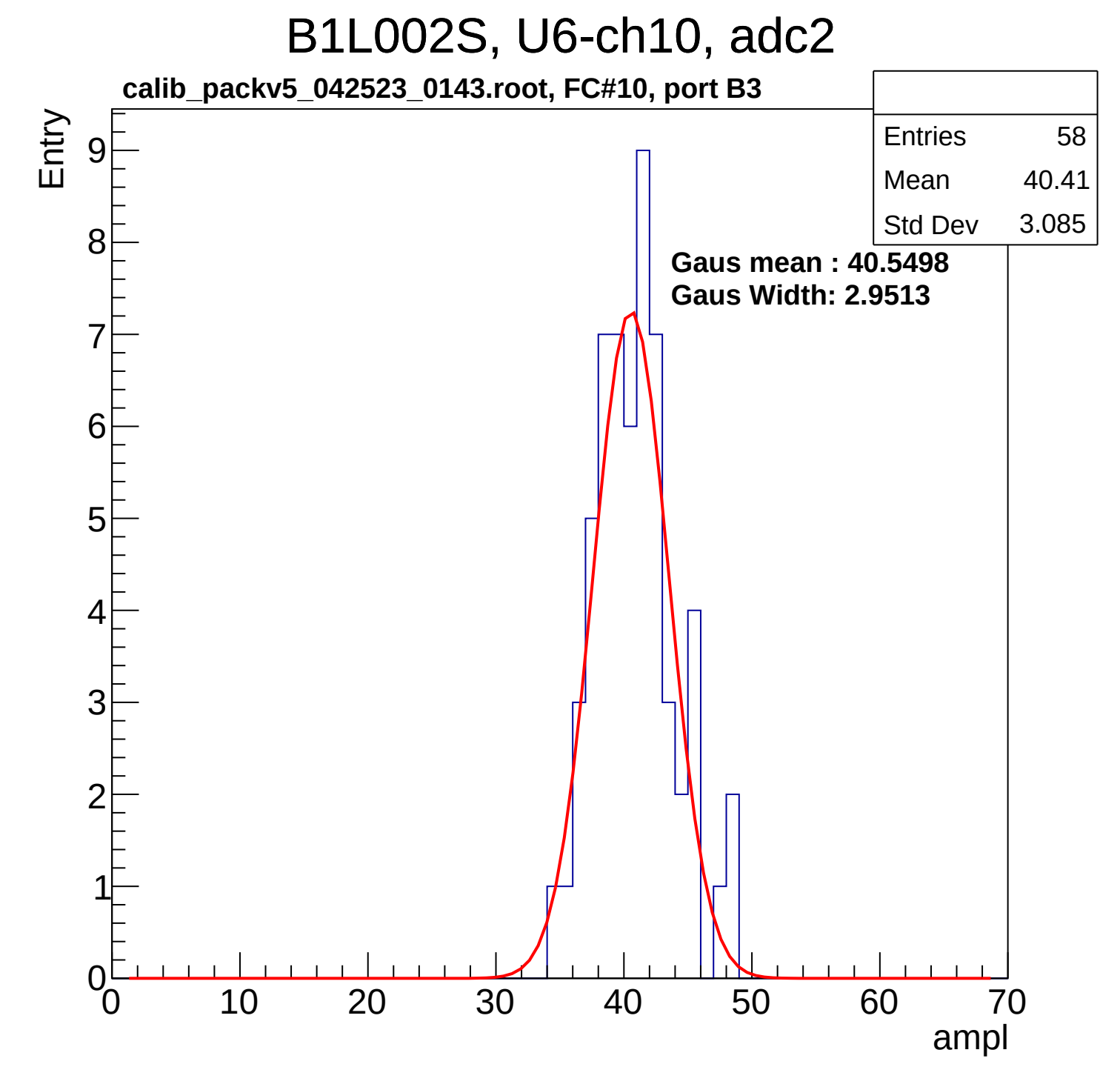

B1L002S, U6-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 58.95
Std Dev 9.931
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

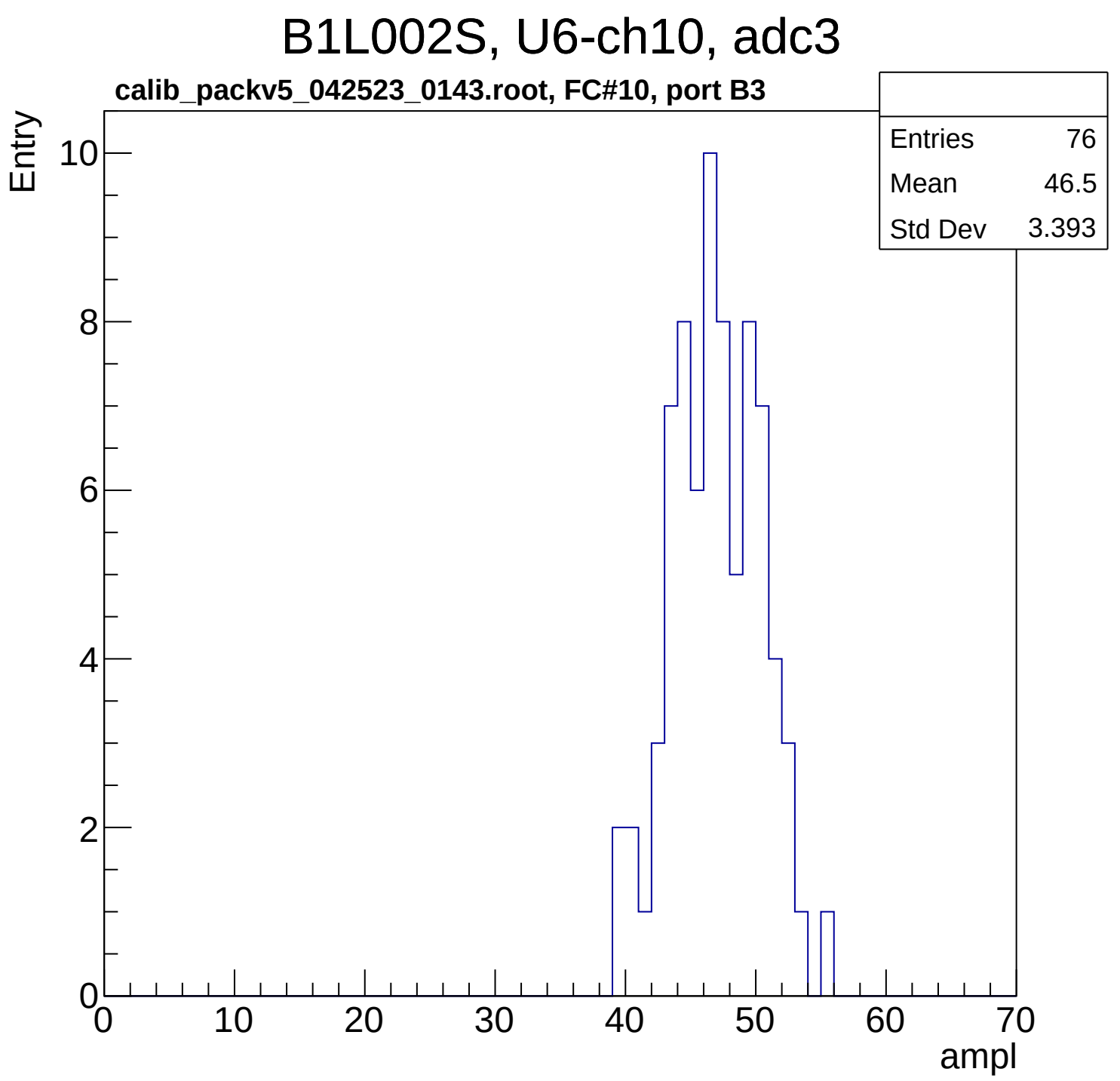

B1L002S, U6-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

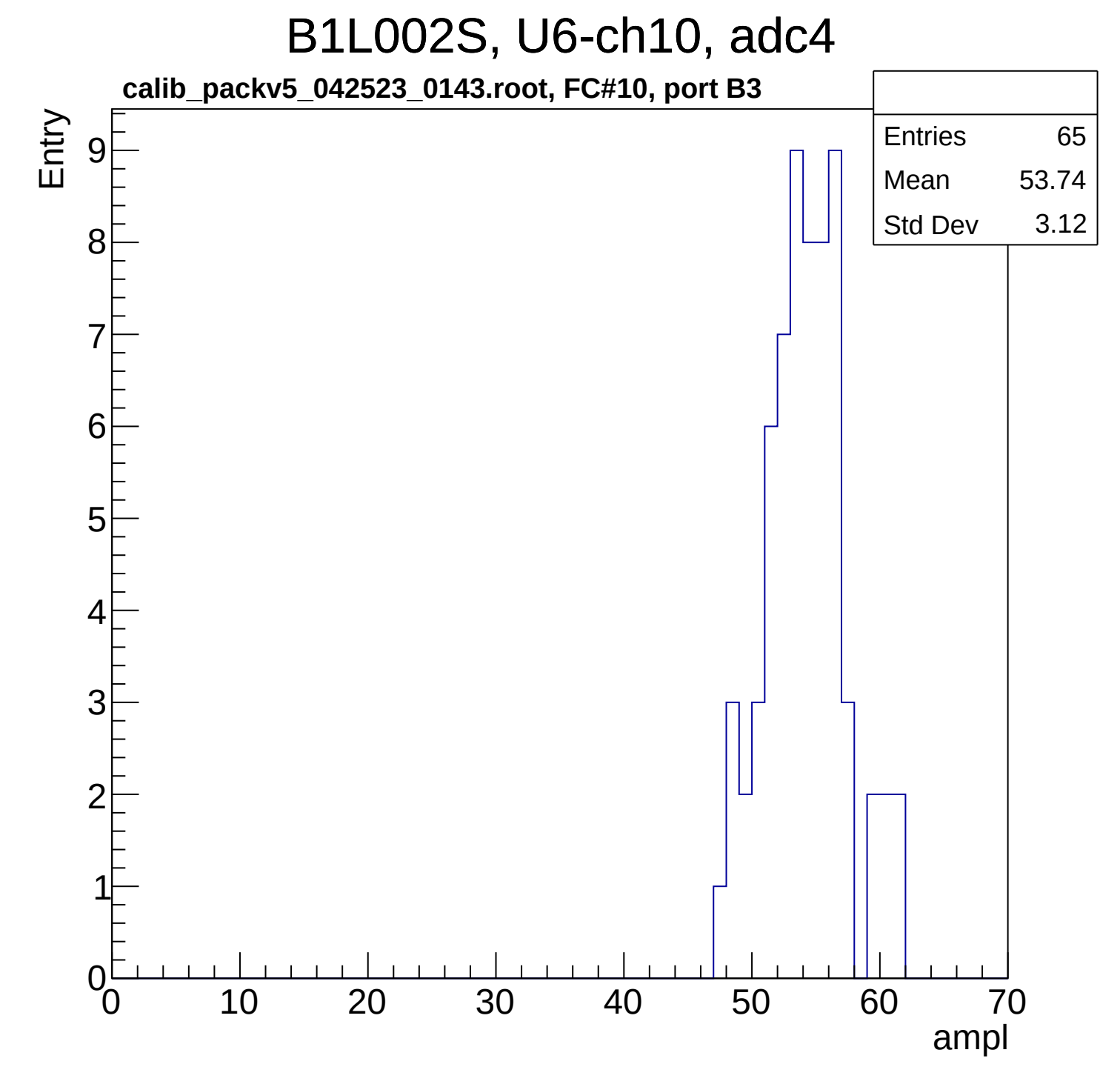

B1L002S, U6-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

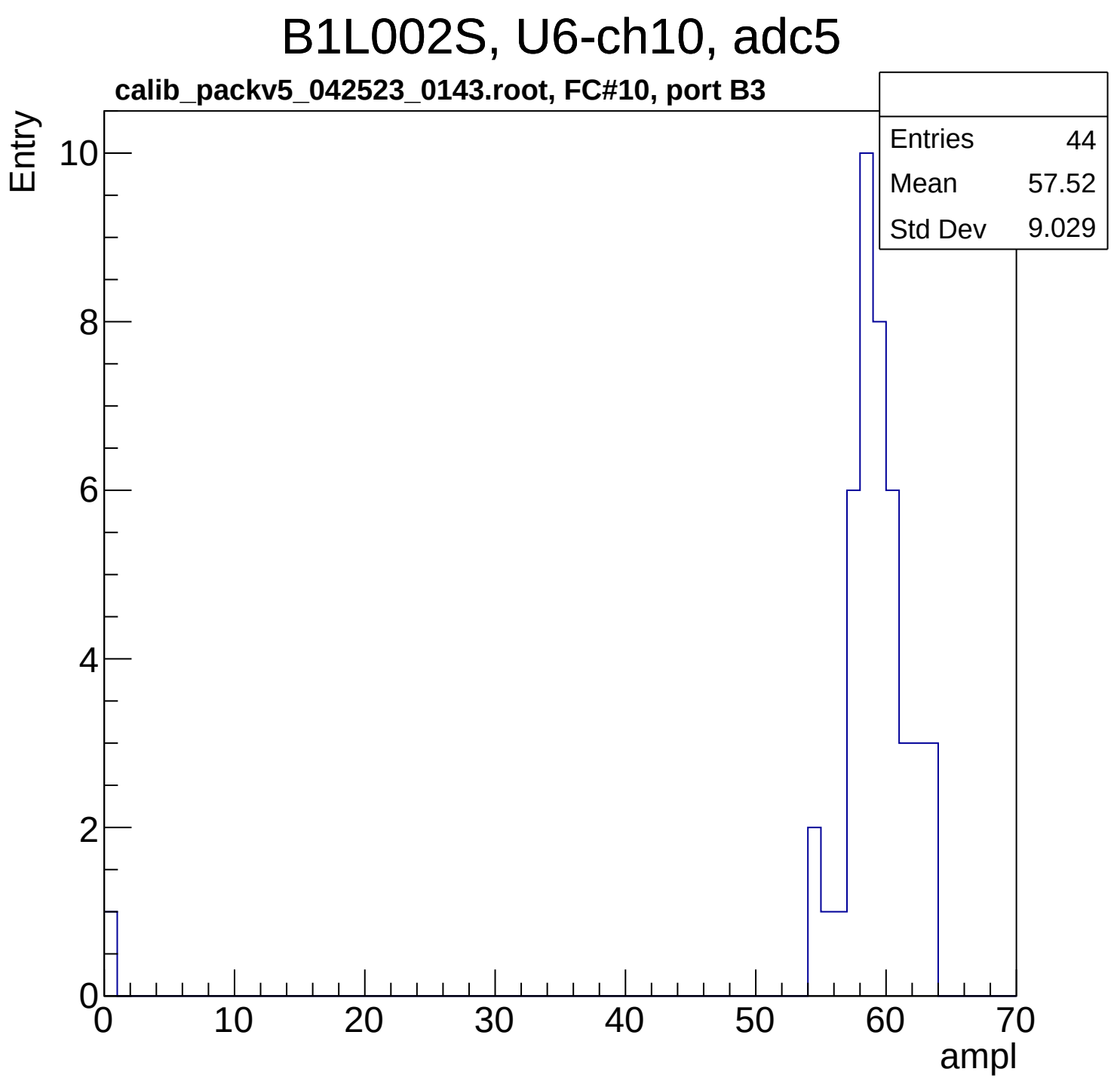

B1L002S, U6-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 26.45
Std Dev 6.267
Gaus mean : 28.1819
Gaus Width: 3.1451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

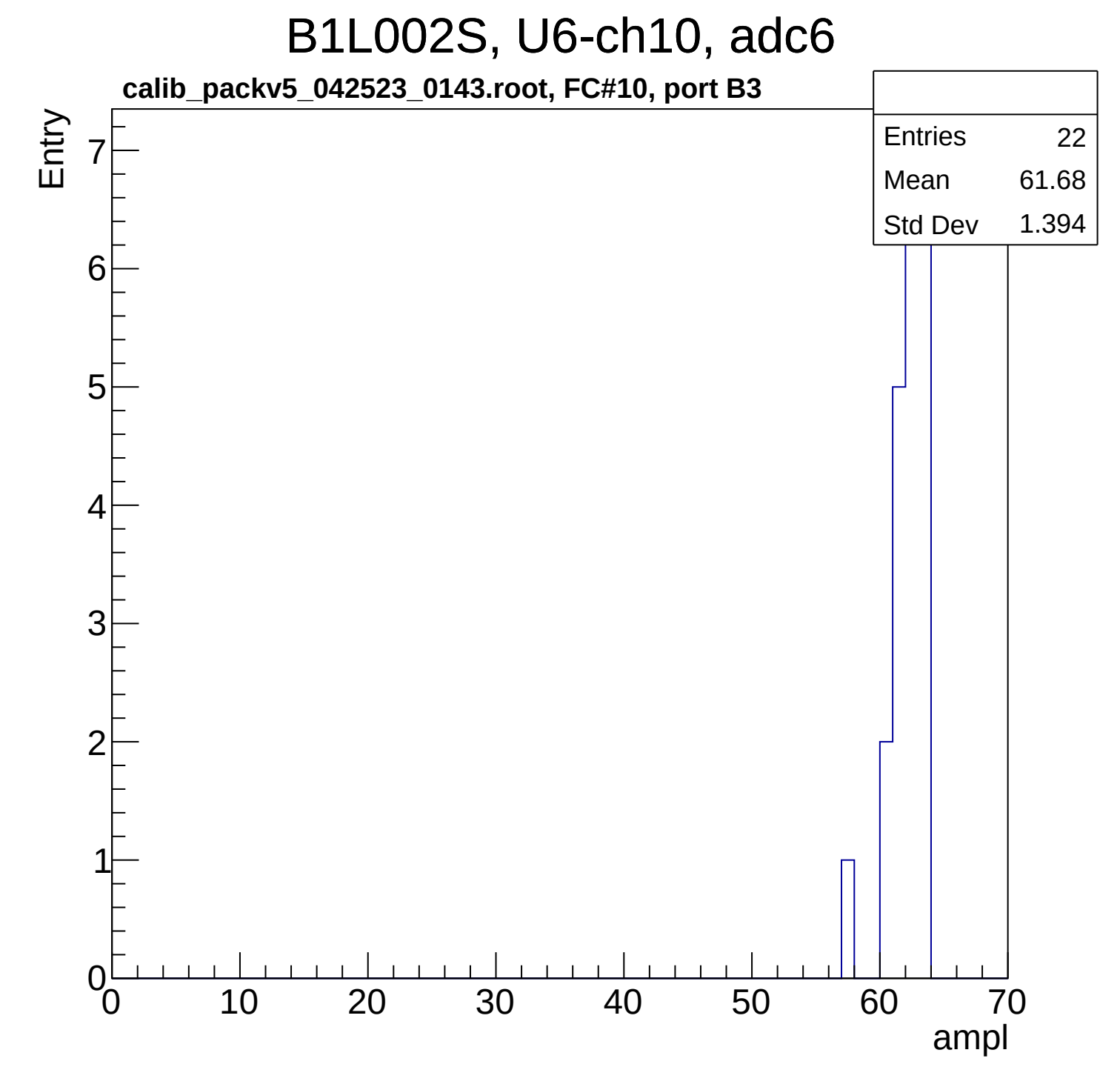

B1L002S, U6-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.19
Std Dev 2.645
Gaus mean : 34.6750
Gaus Width: 2.6543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

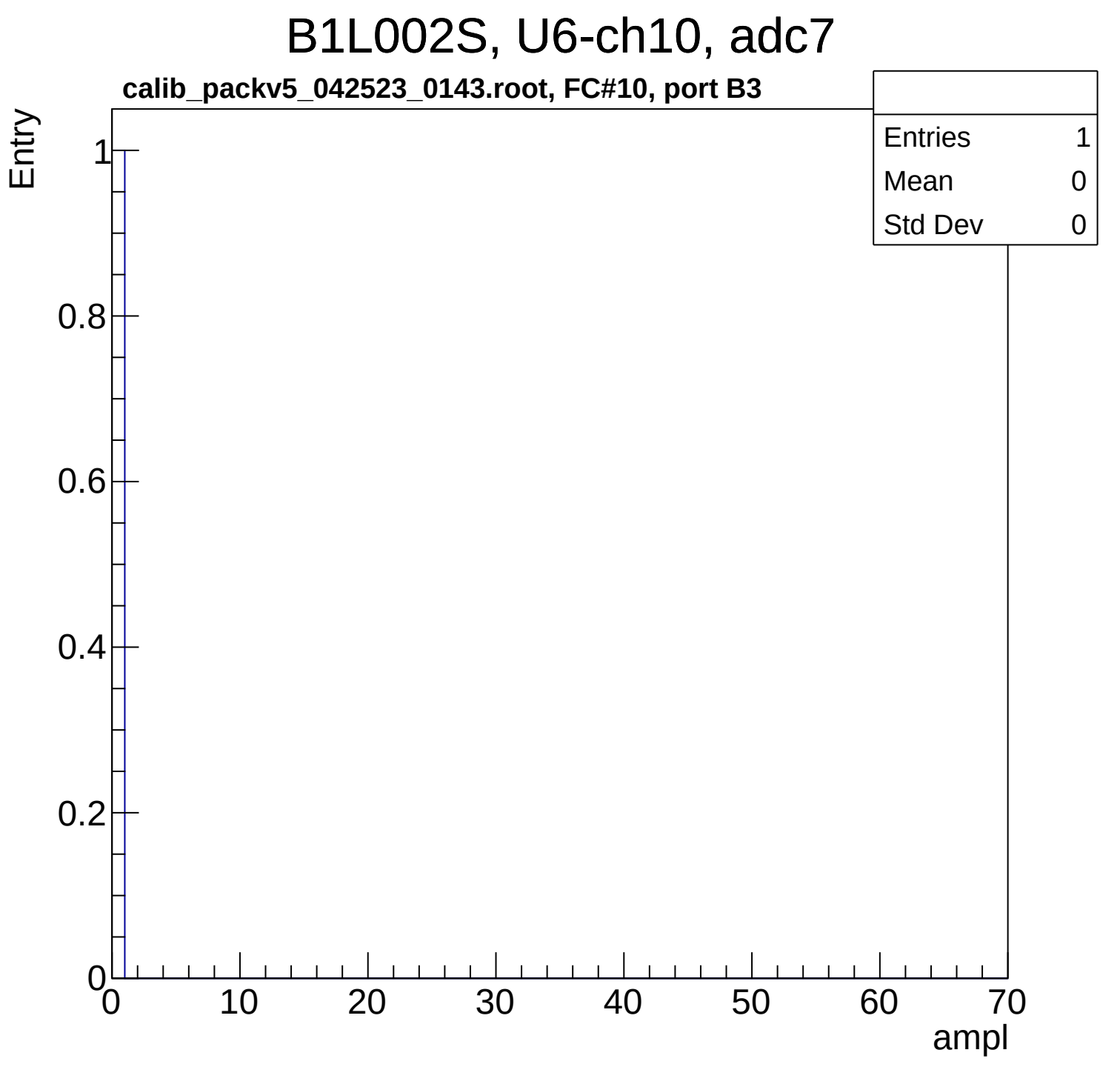

B1L002S, U6-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.41
Std Dev 3.085
Gaus mean : 40.5498
Gaus Width: 2.9513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

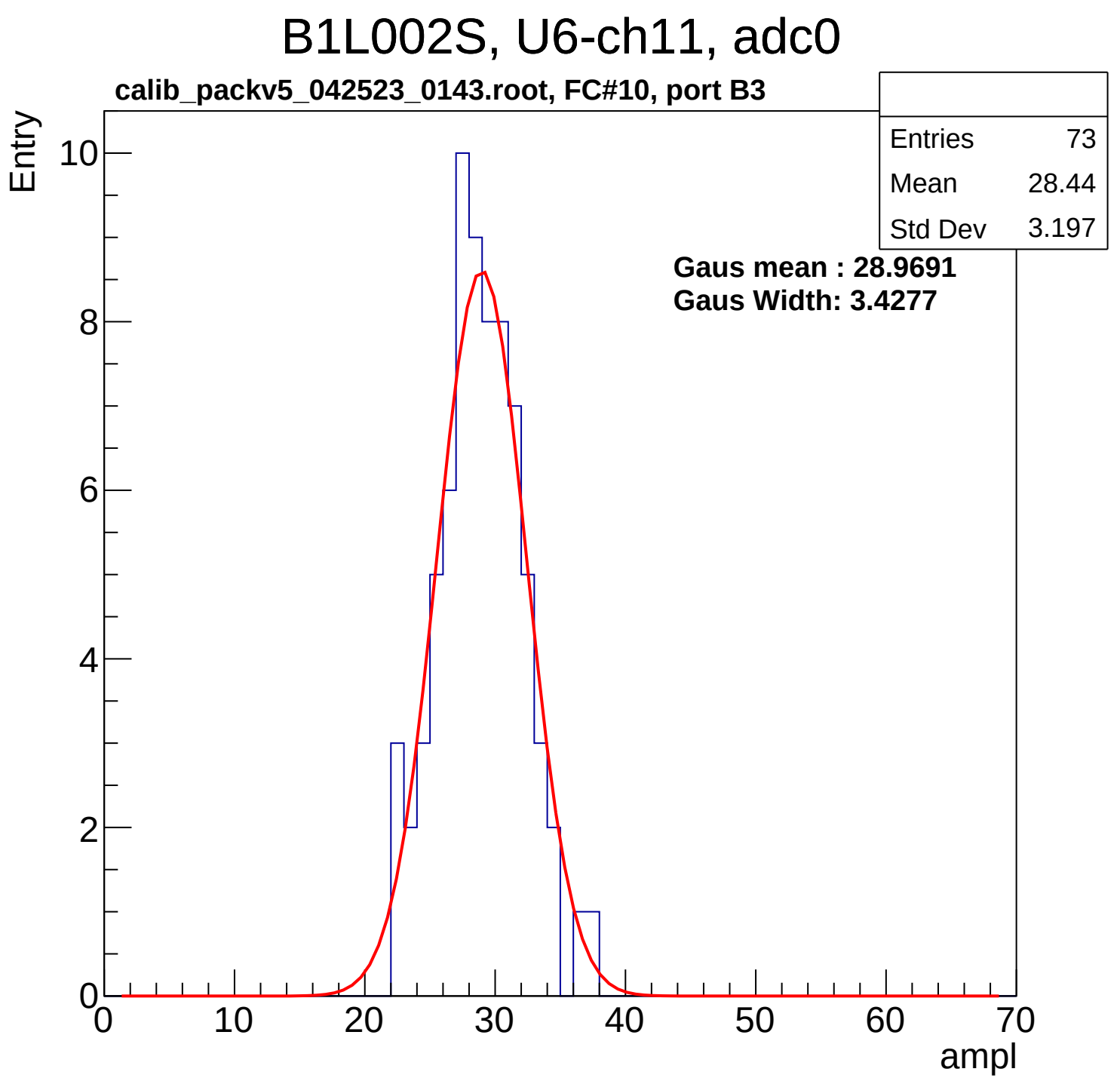

B1L002S, U6-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.5
Std Dev 3.393
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

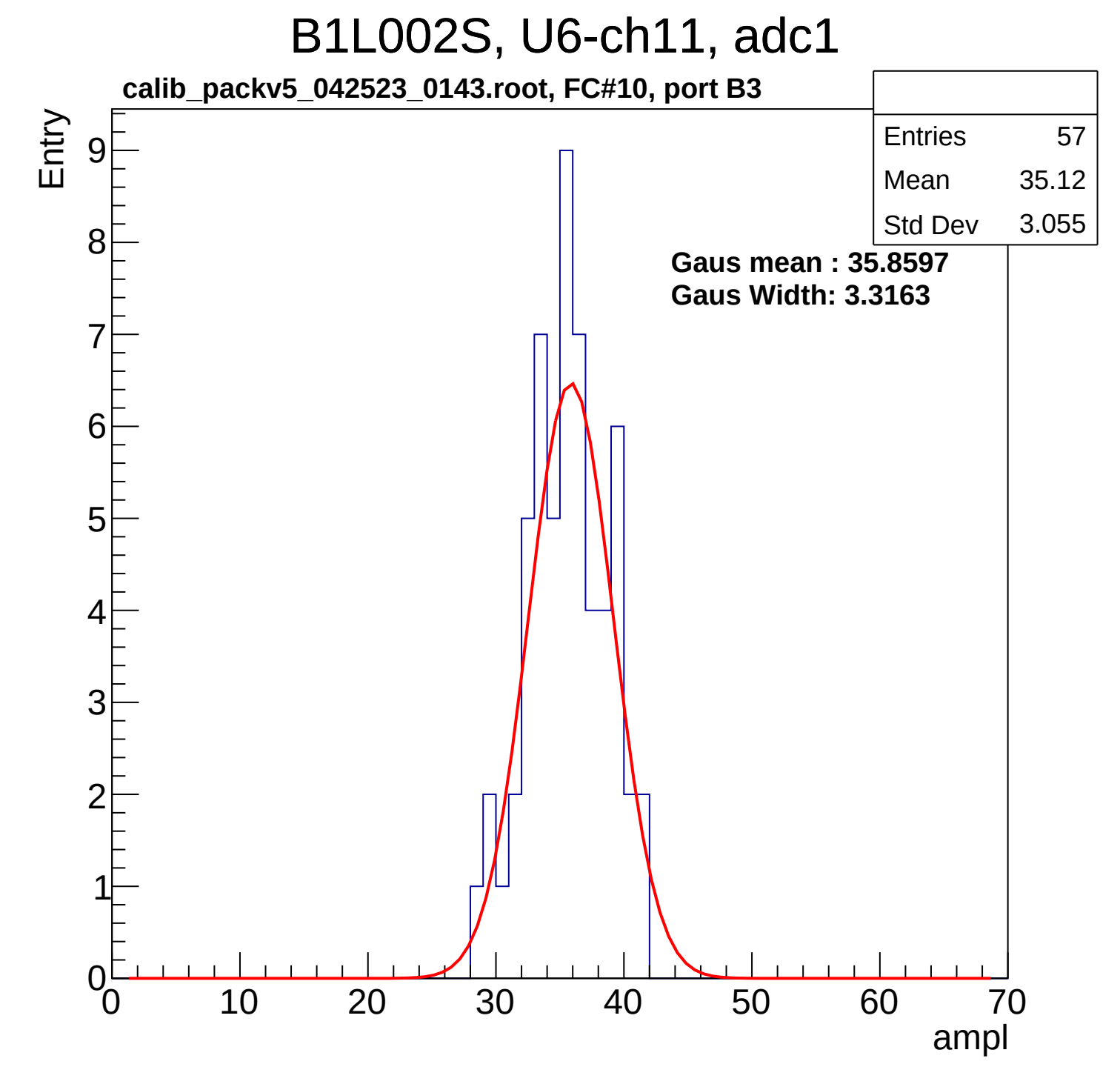

B1L002S, U6-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.74
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

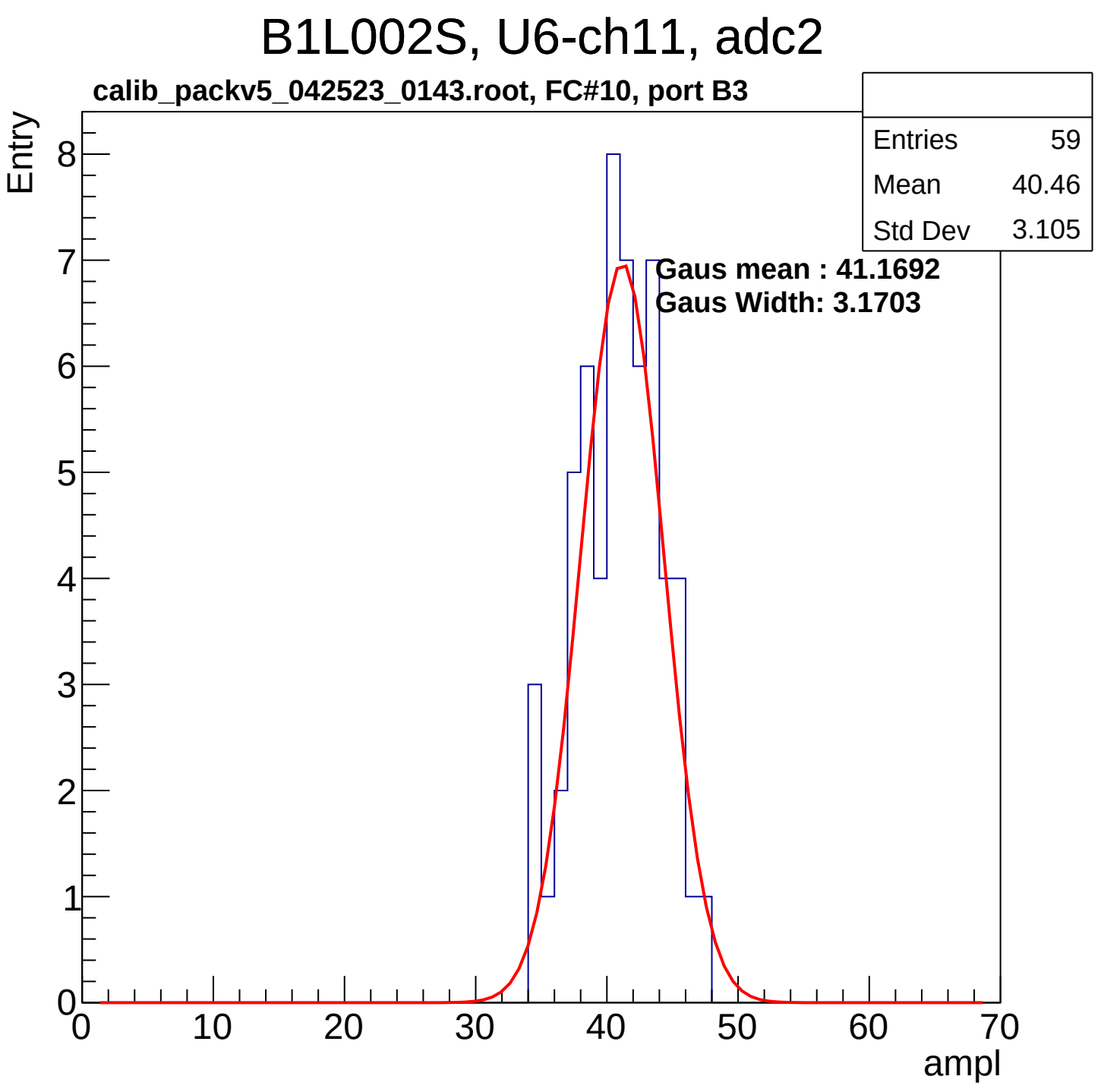

B1L002S, U6-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.52
Std Dev 9.029
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

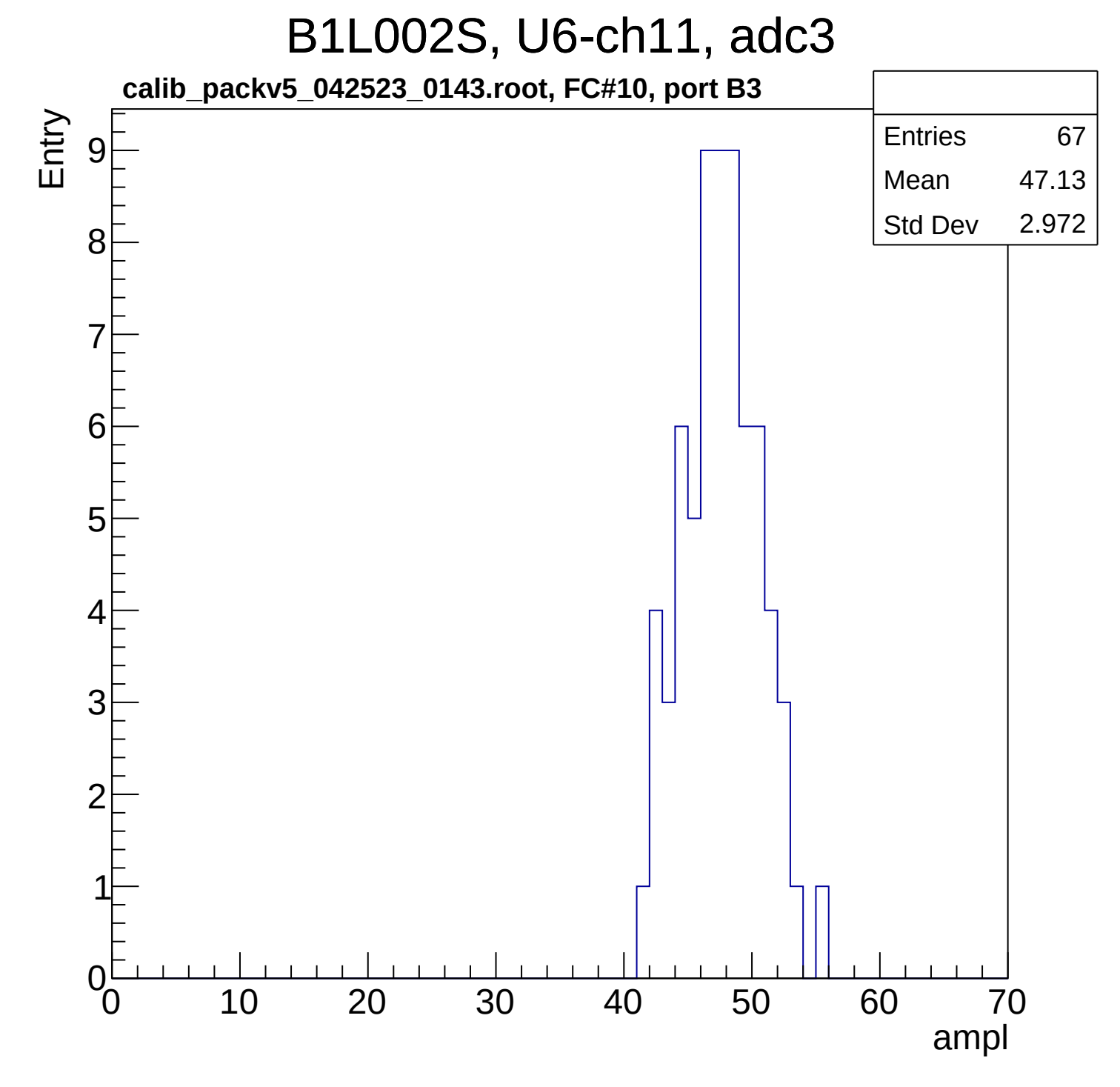

B1L002S, U6-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.68
Std Dev 1.394
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

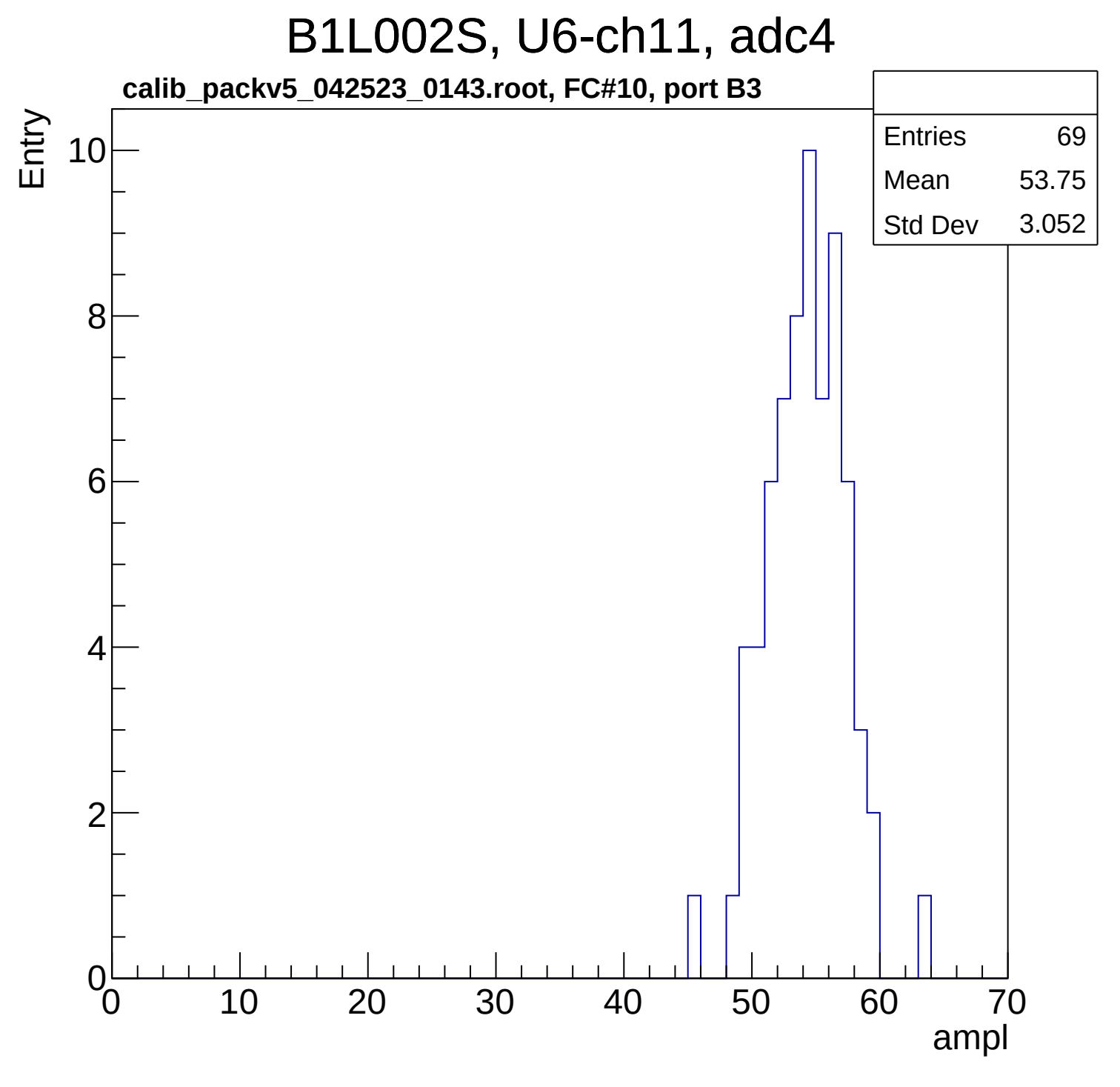

B1L002S, U6-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

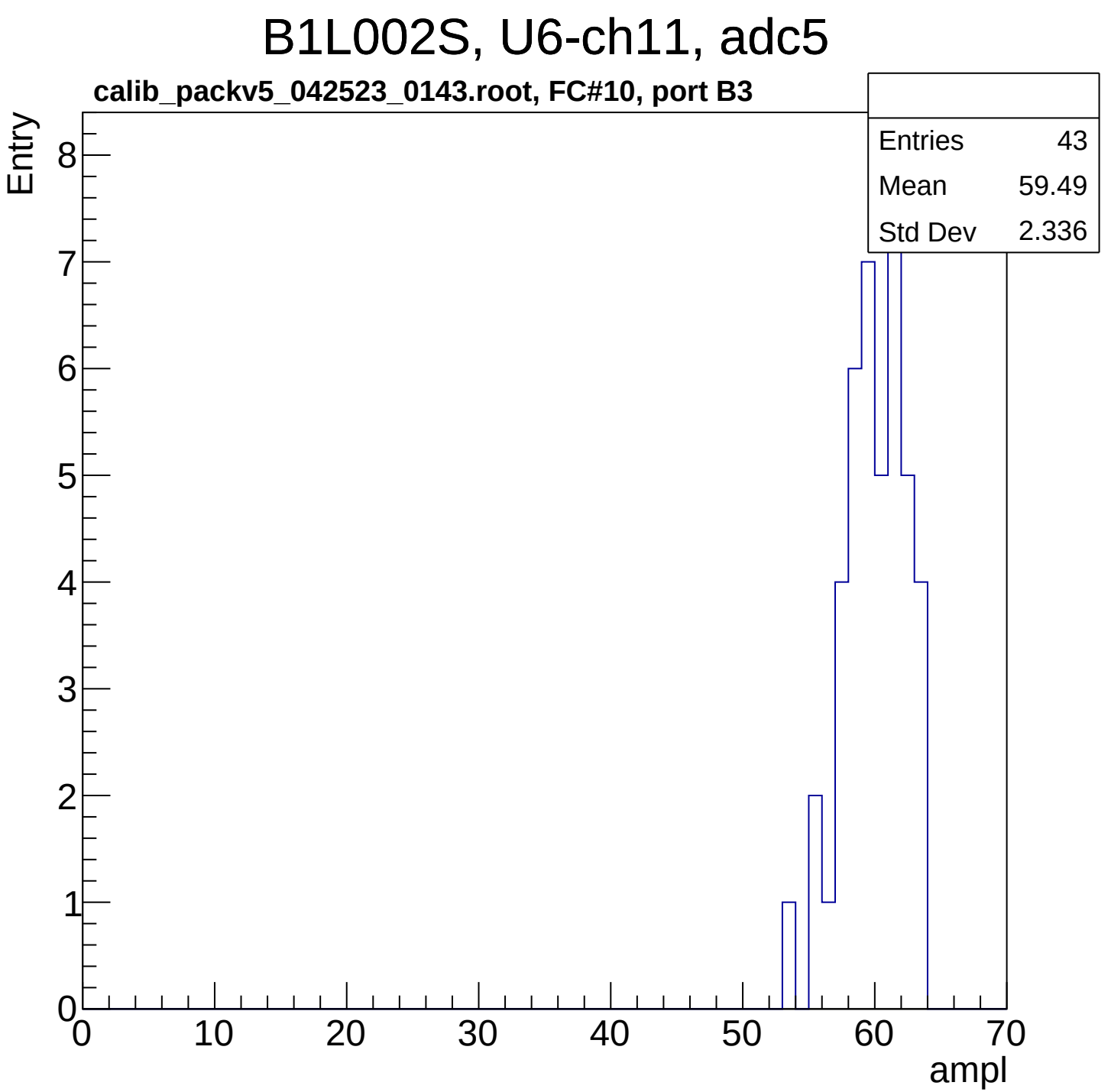

B1L002S, U6-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.44
Std Dev 3.197
Gaus mean : 28.9691
Gaus Width: 3.4277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

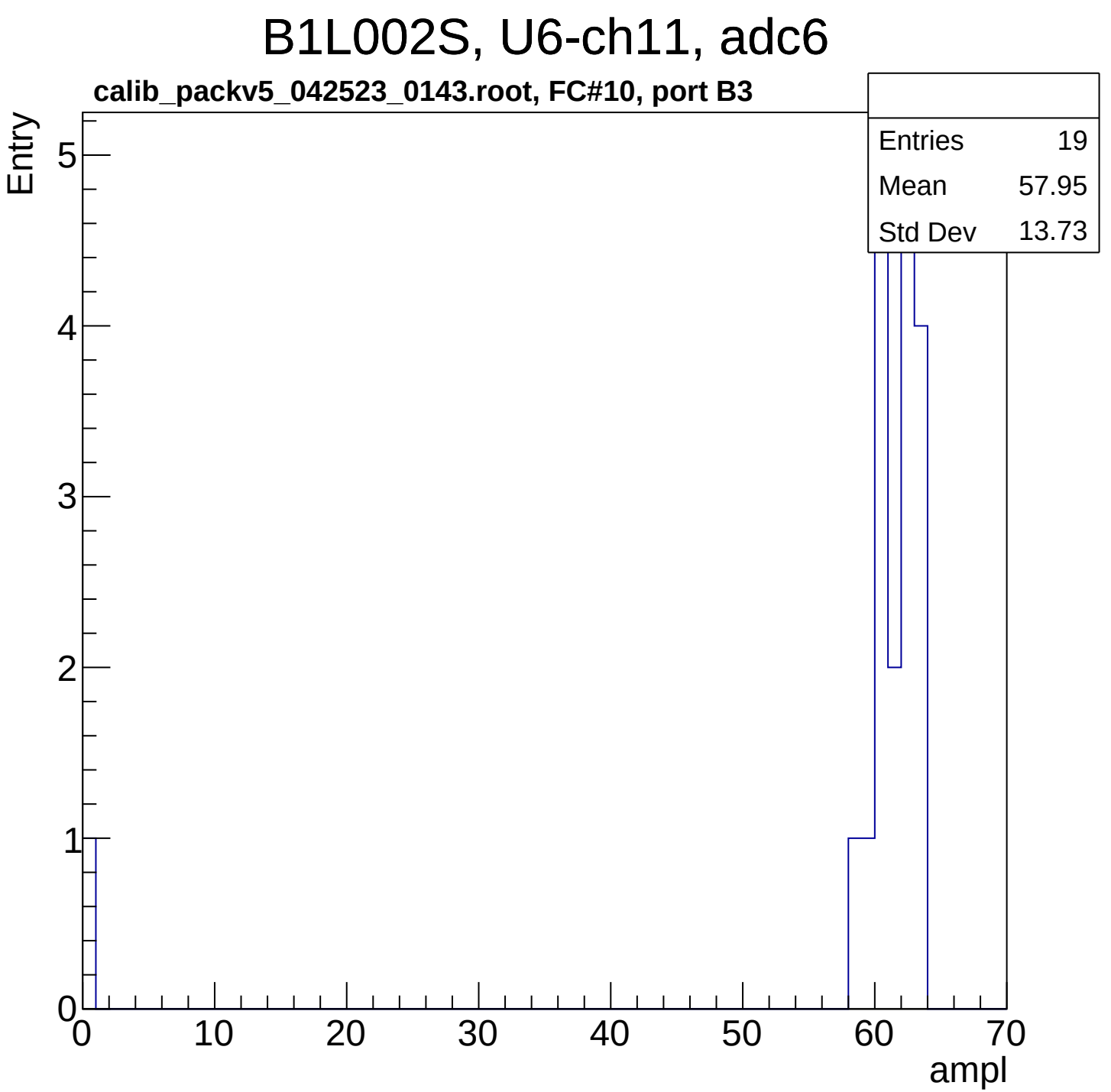

B1L002S, U6-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.12
Std Dev 3.055
Gaus mean : 35.8597
Gaus Width: 3.3163
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

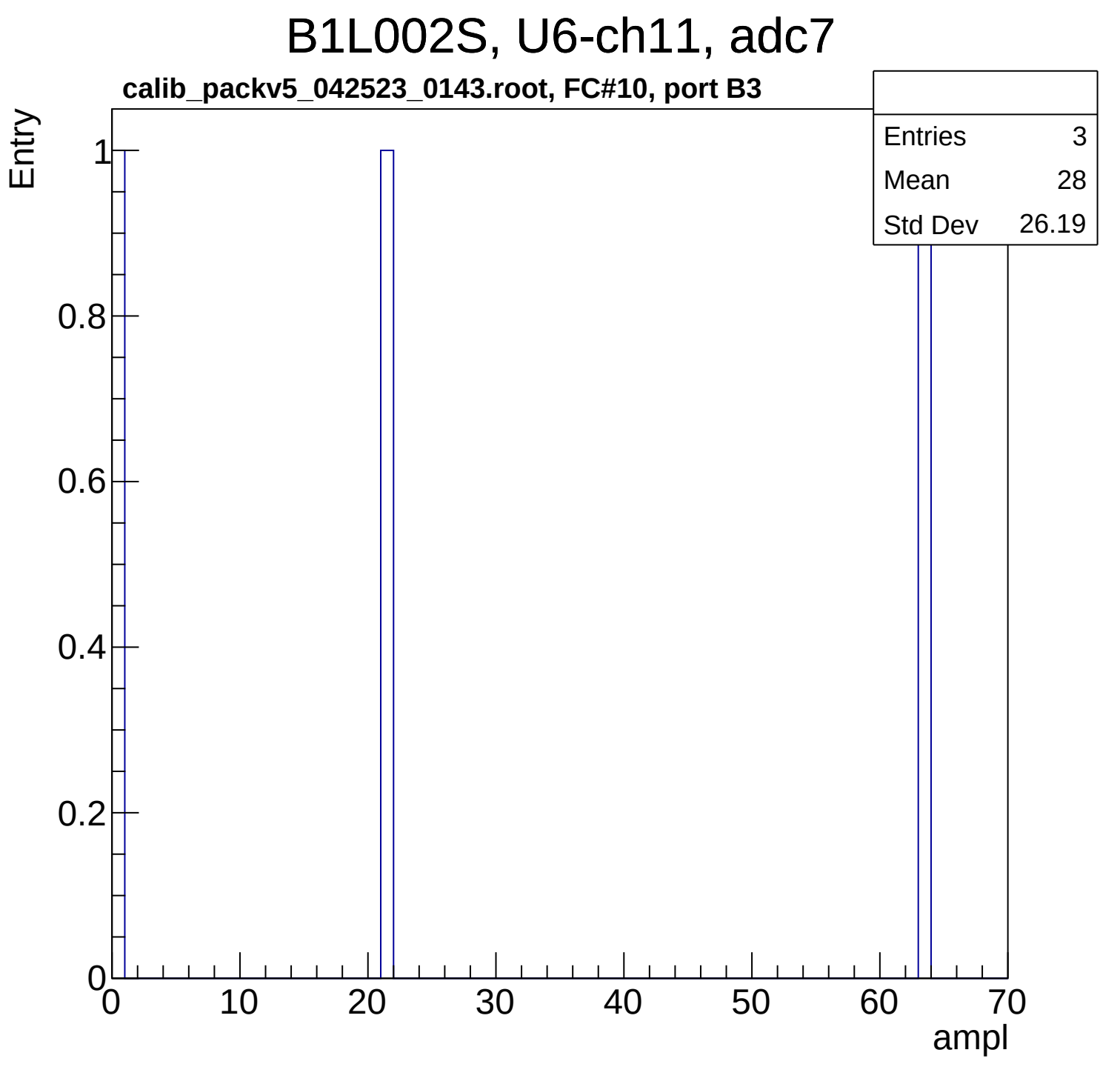

B1L002S, U6-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.46
Std Dev 3.105
Gaus mean : 41.1692
Gaus Width: 3.1703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

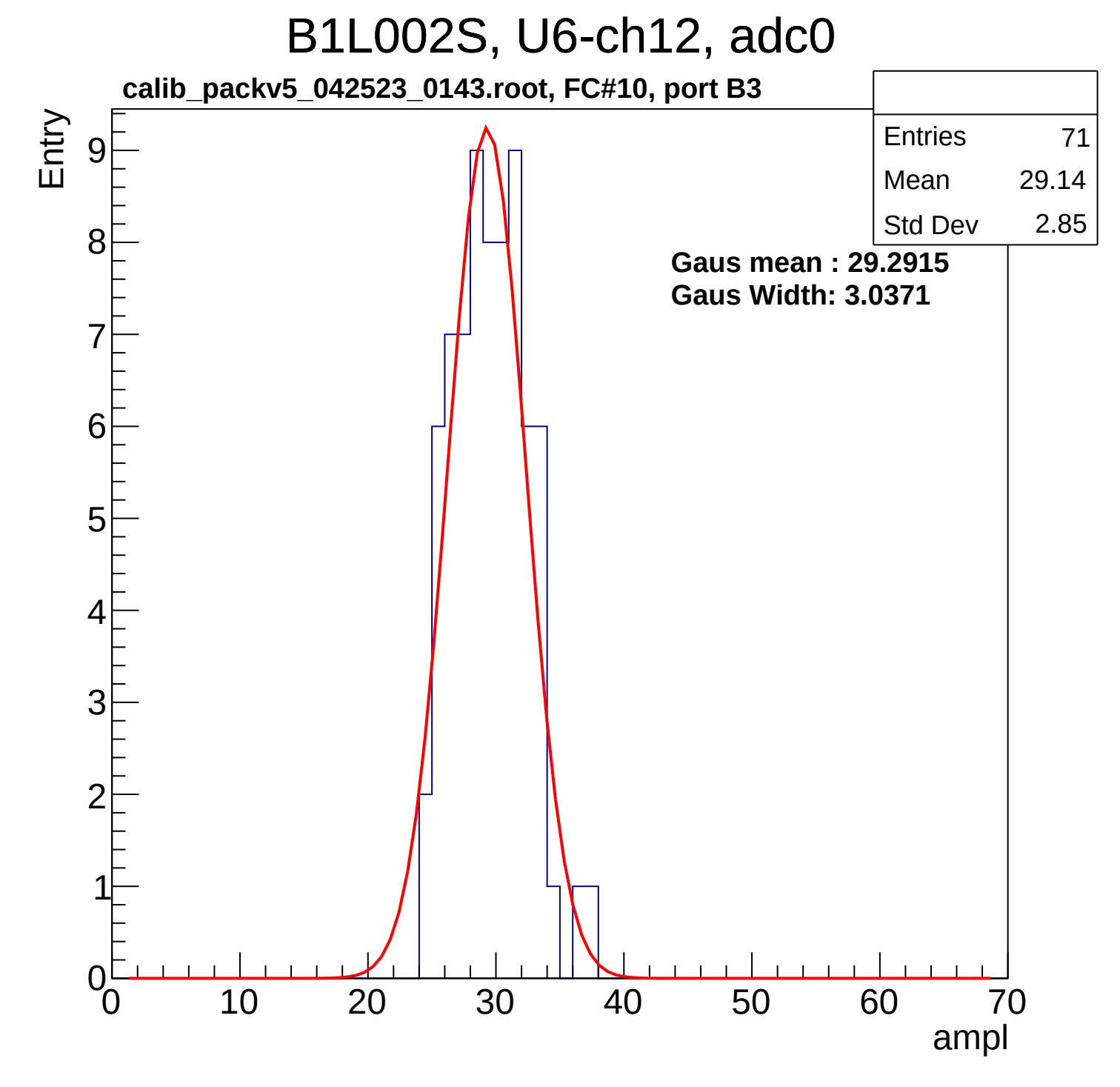

B1L002S, U6-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.13
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

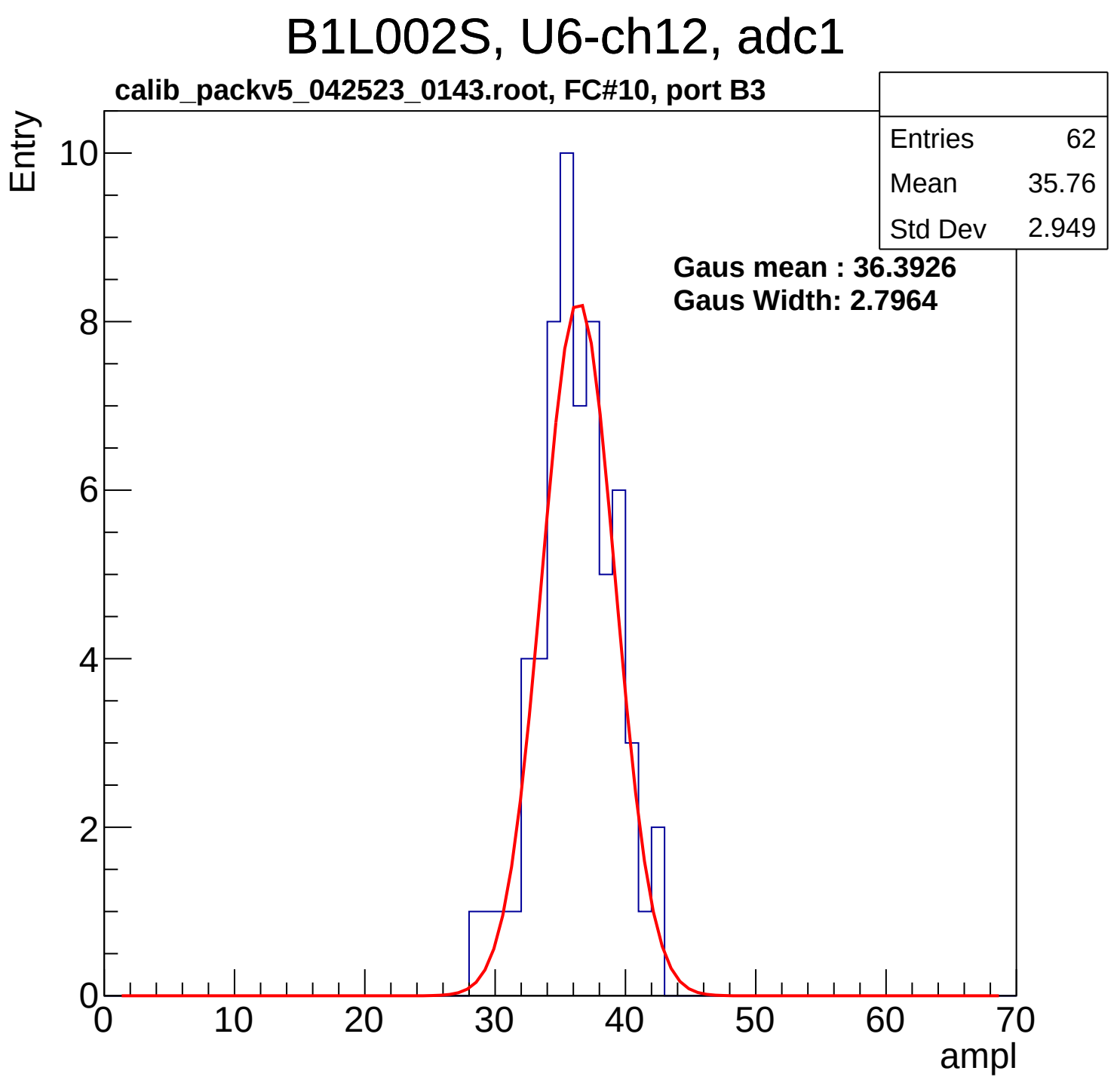

B1L002S, U6-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.75
Std Dev 3.052
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

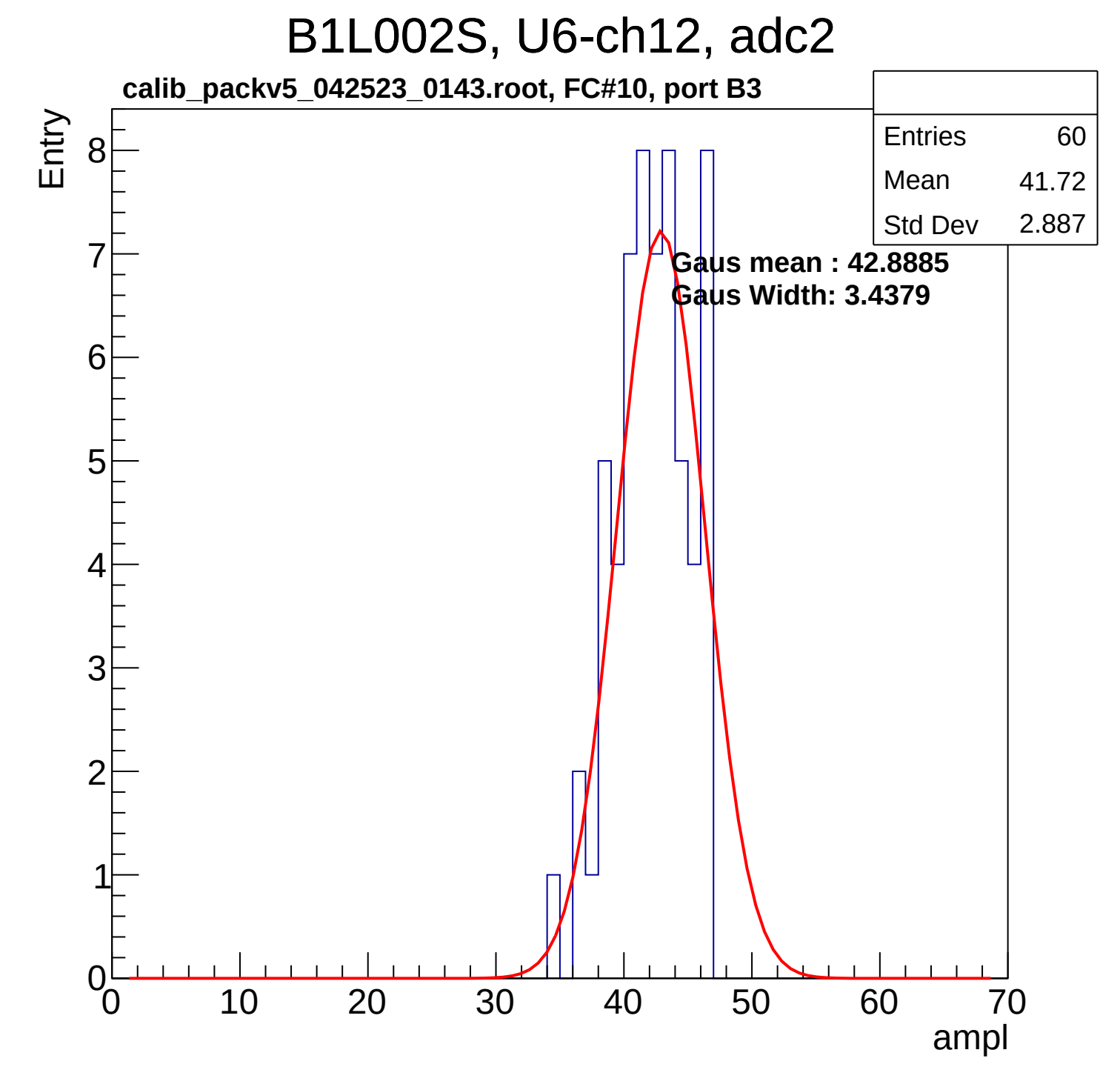

B1L002S, U6-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 59.49
Std Dev 2.336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

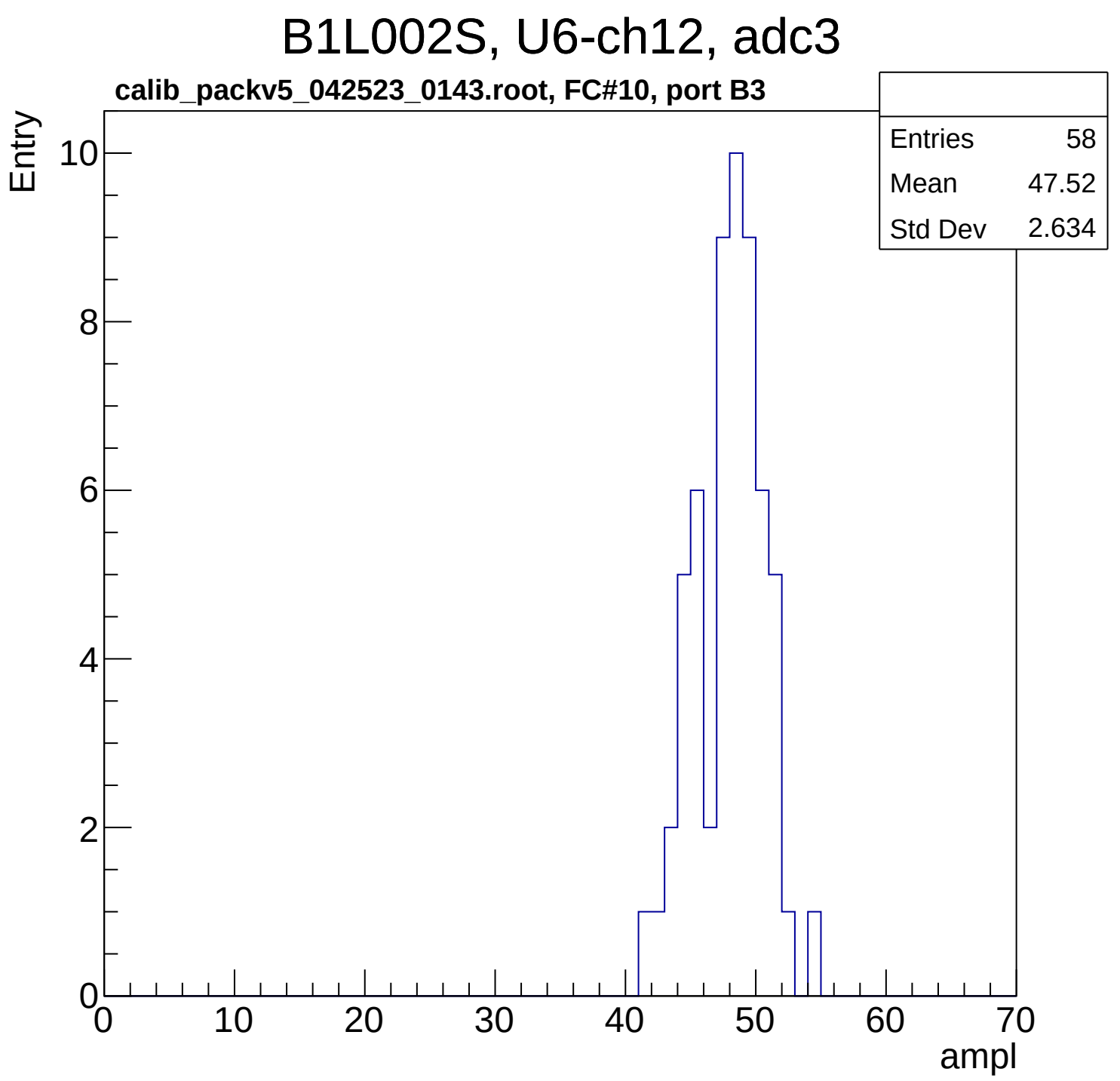

B1L002S, U6-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.95
Std Dev 13.73
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

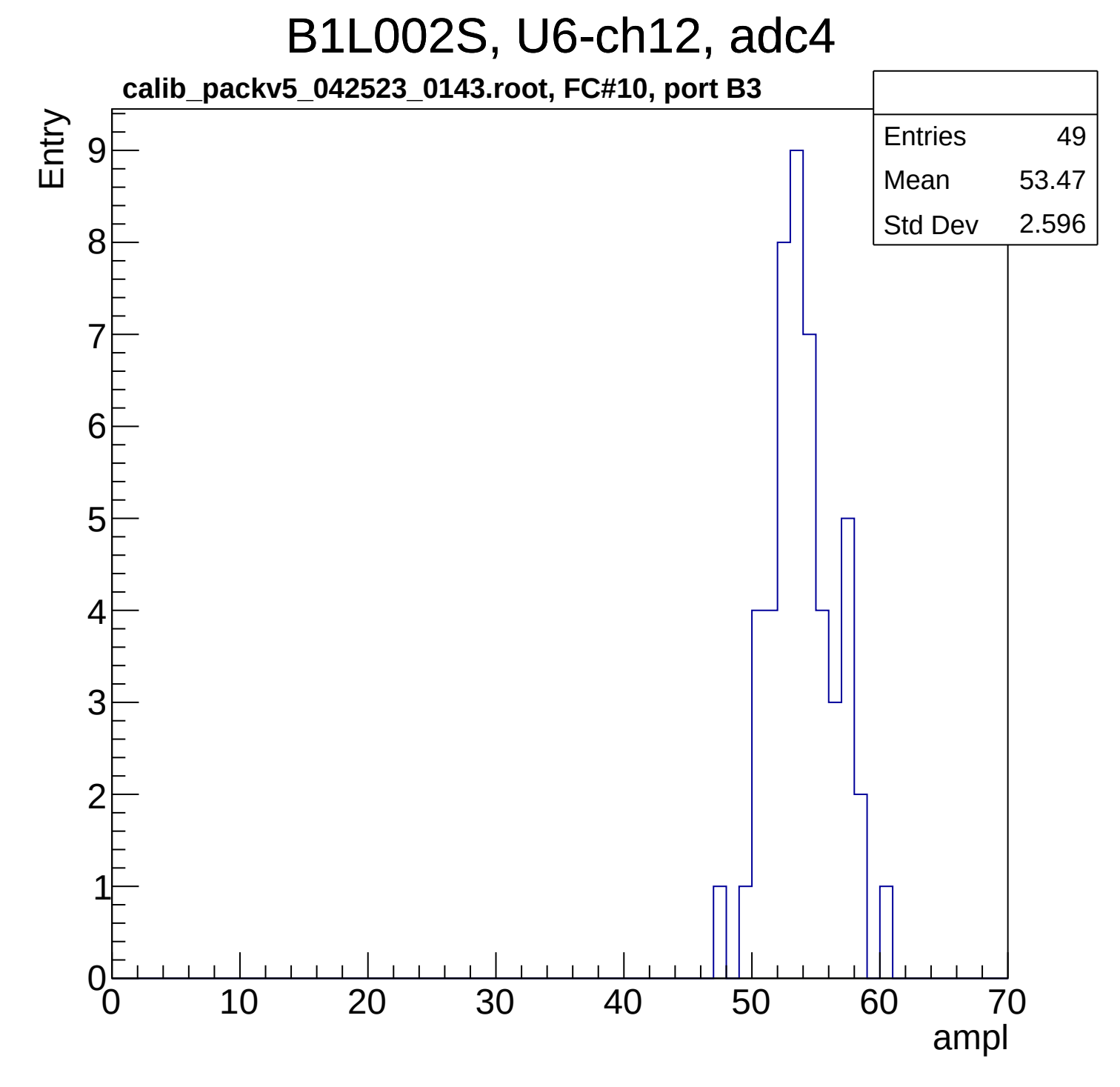

B1L002S, U6-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

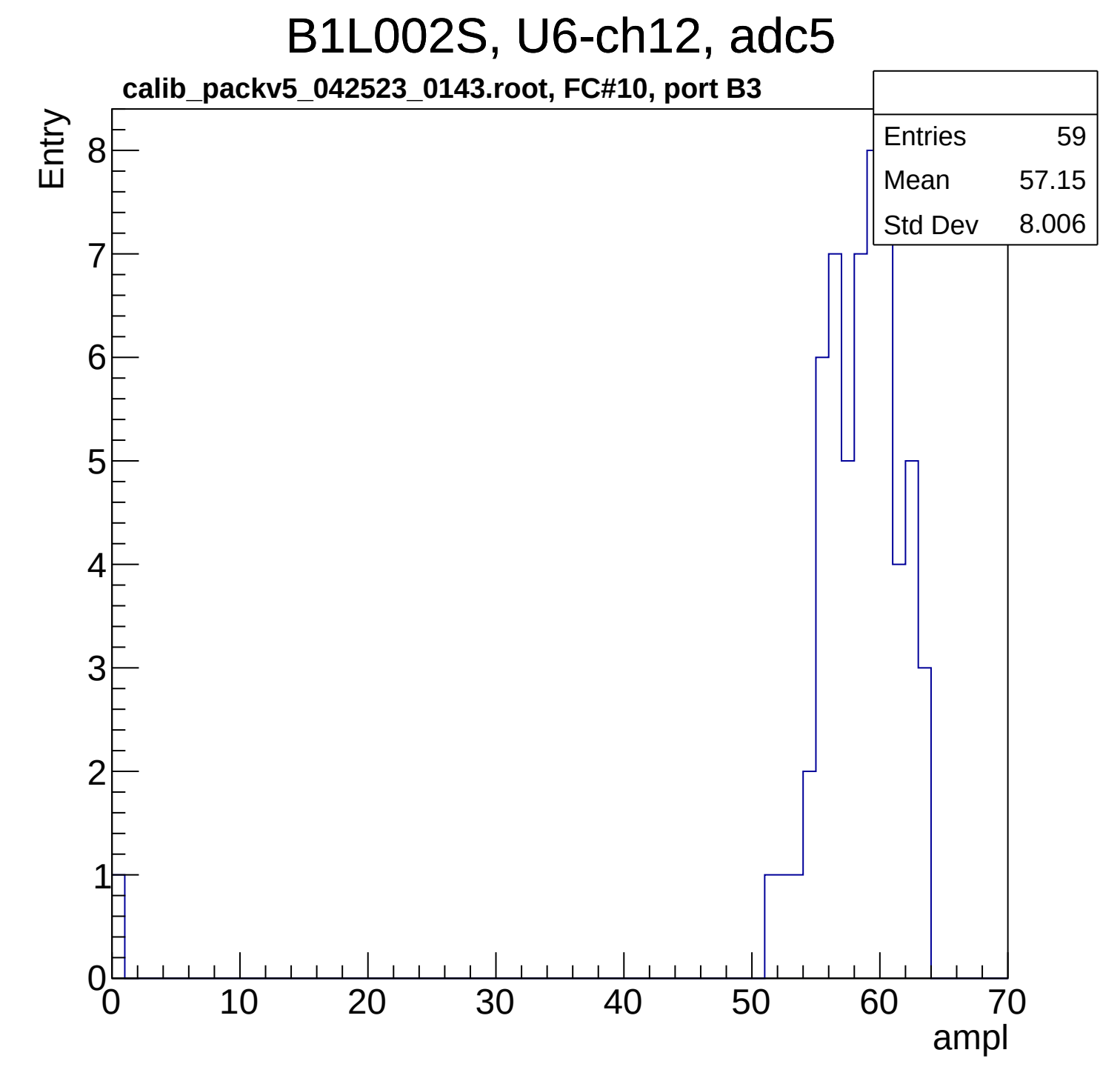

B1L002S, U6-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 29.14
Std Dev 2.85
Gaus mean : 29.2915
Gaus Width: 3.0371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

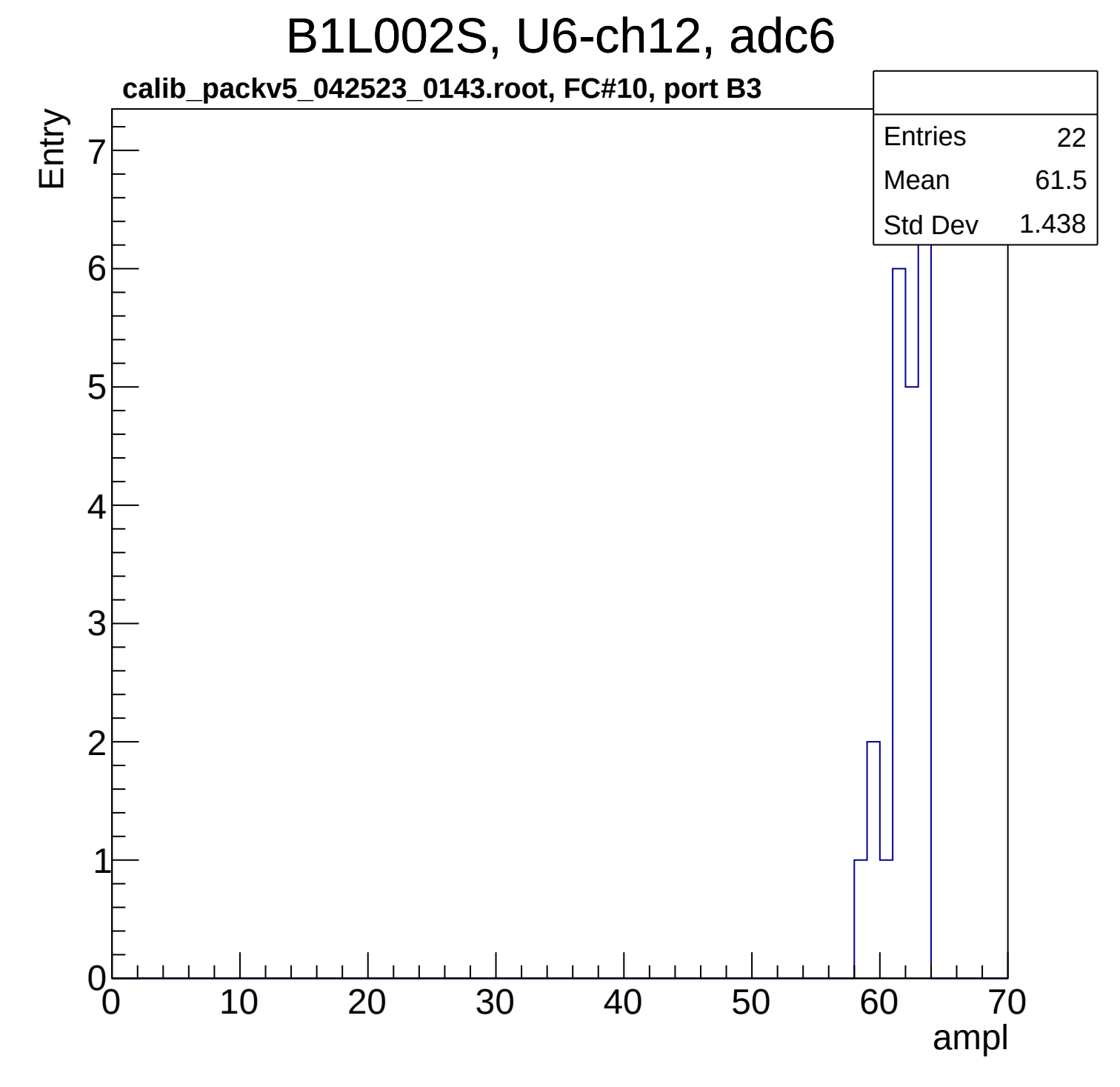

B1L002S, U6-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.76
Std Dev 2.949
Gaus mean : 36.3926
Gaus Width: 2.7964
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

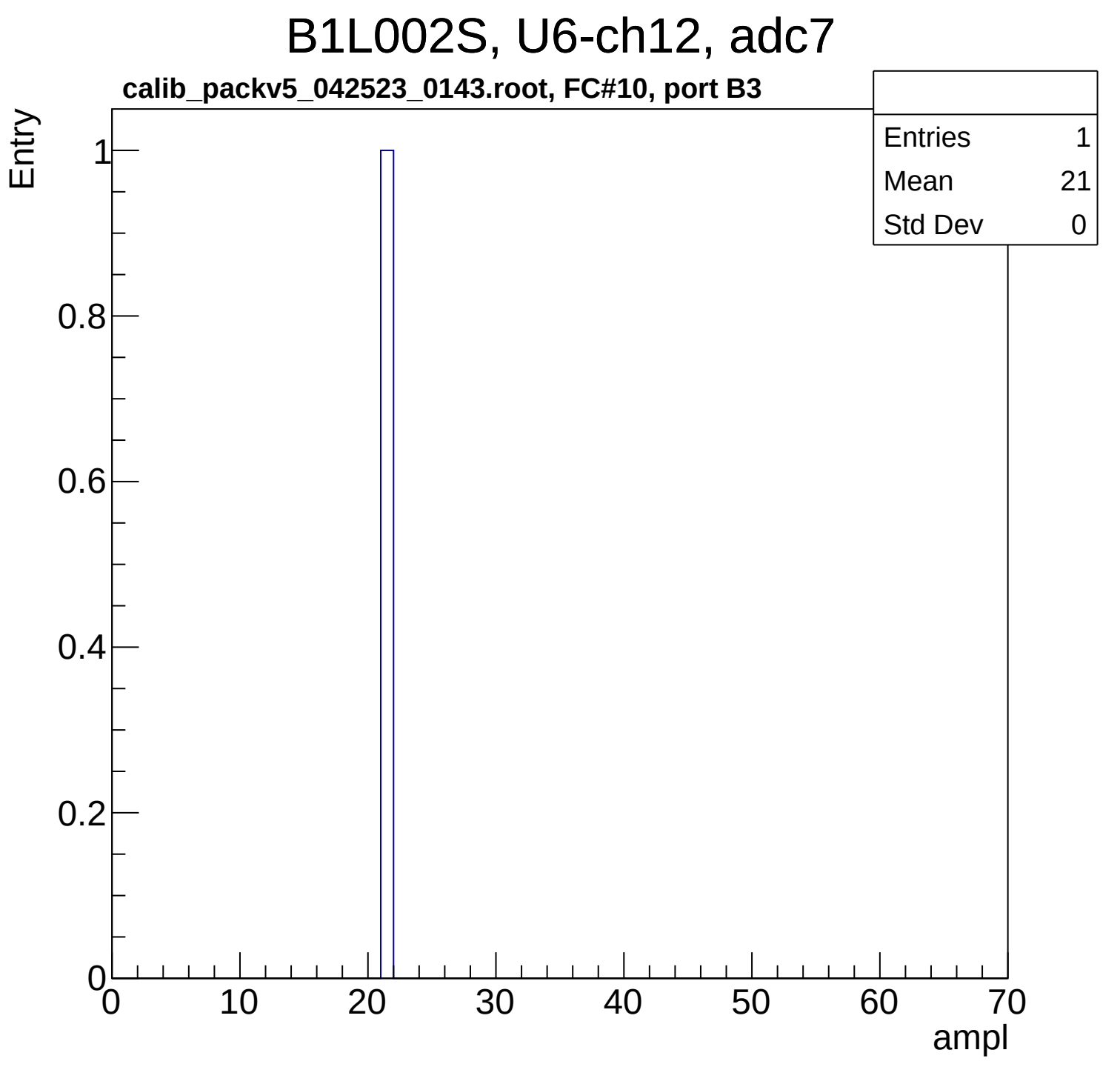

B1L002S, U6-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.72
Std Dev 2.887
Gaus mean : 42.8885
Gaus Width: 3.4379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

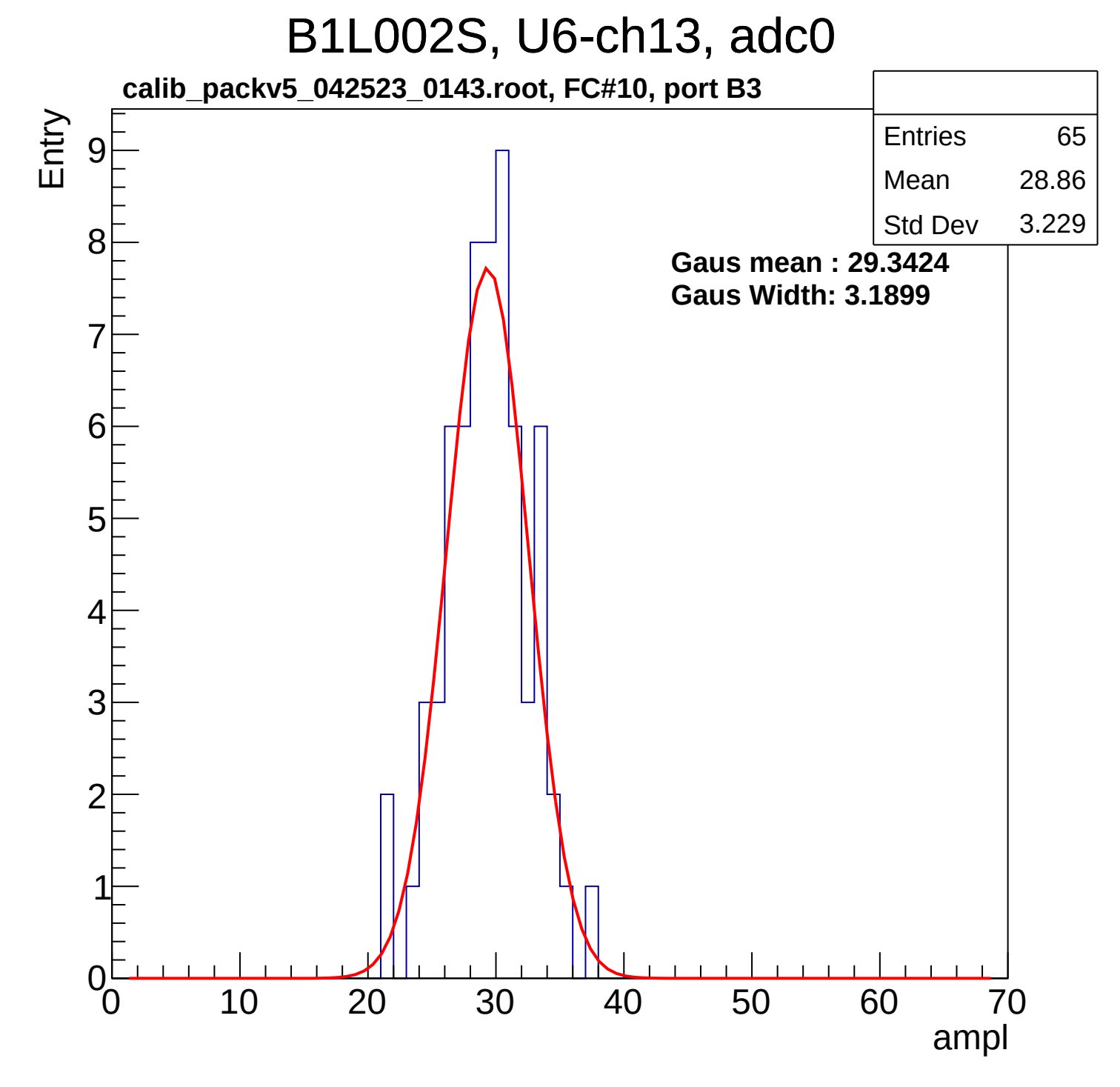

B1L002S, U6-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.52
Std Dev 2.634
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

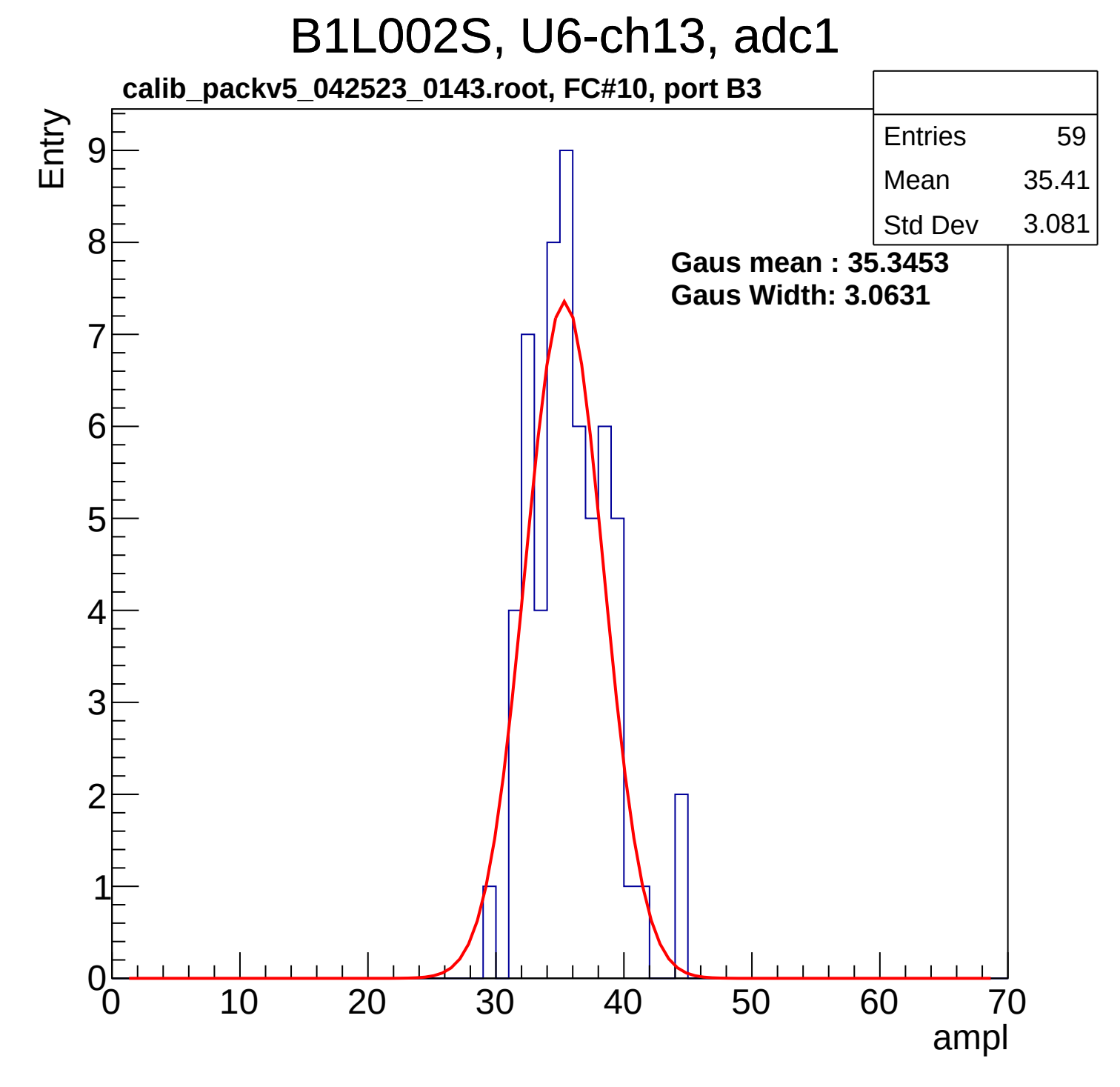

B1L002S, U6-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.47
Std Dev 2.596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

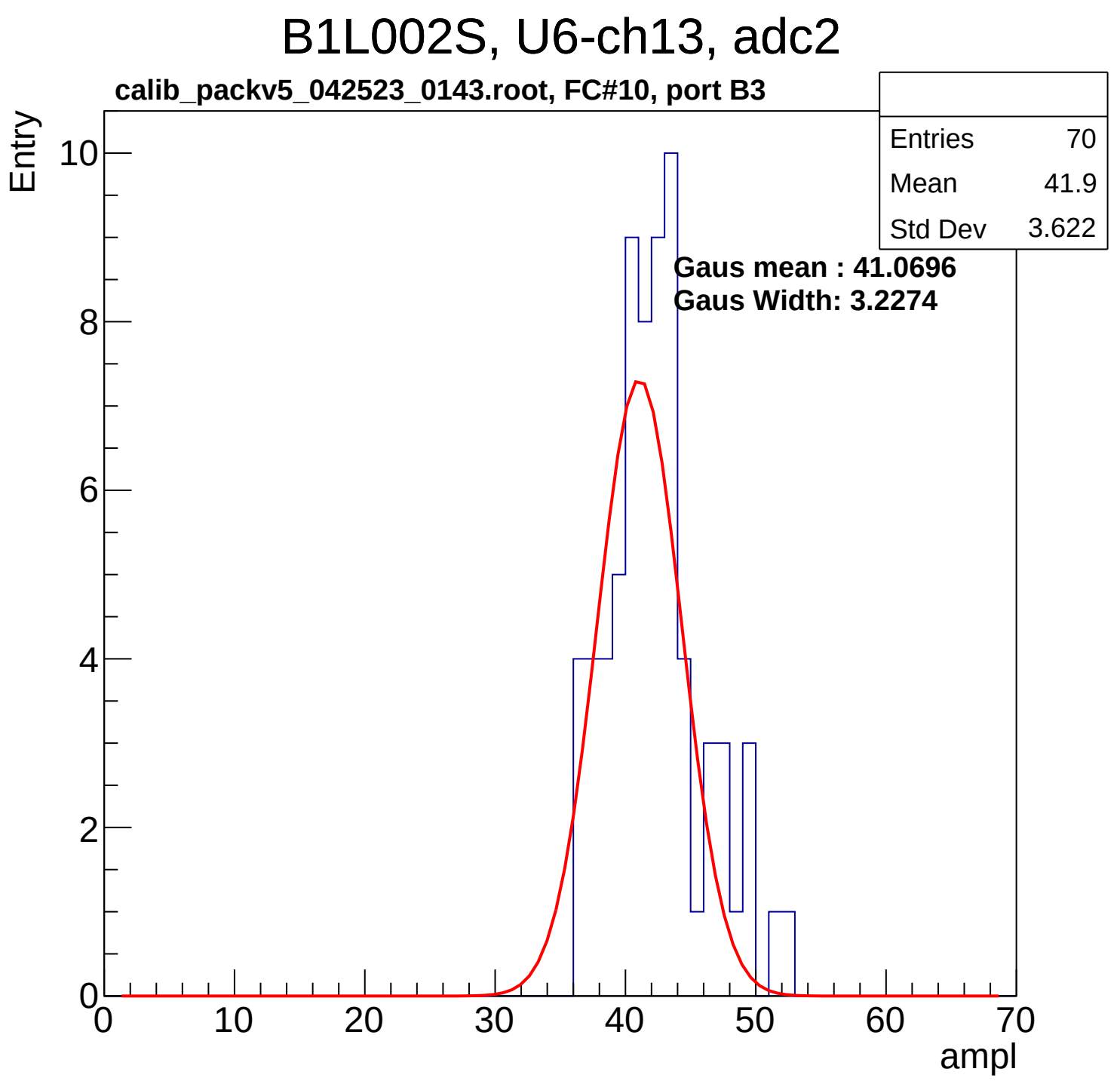

B1L002S, U6-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.15
Std Dev 8.006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

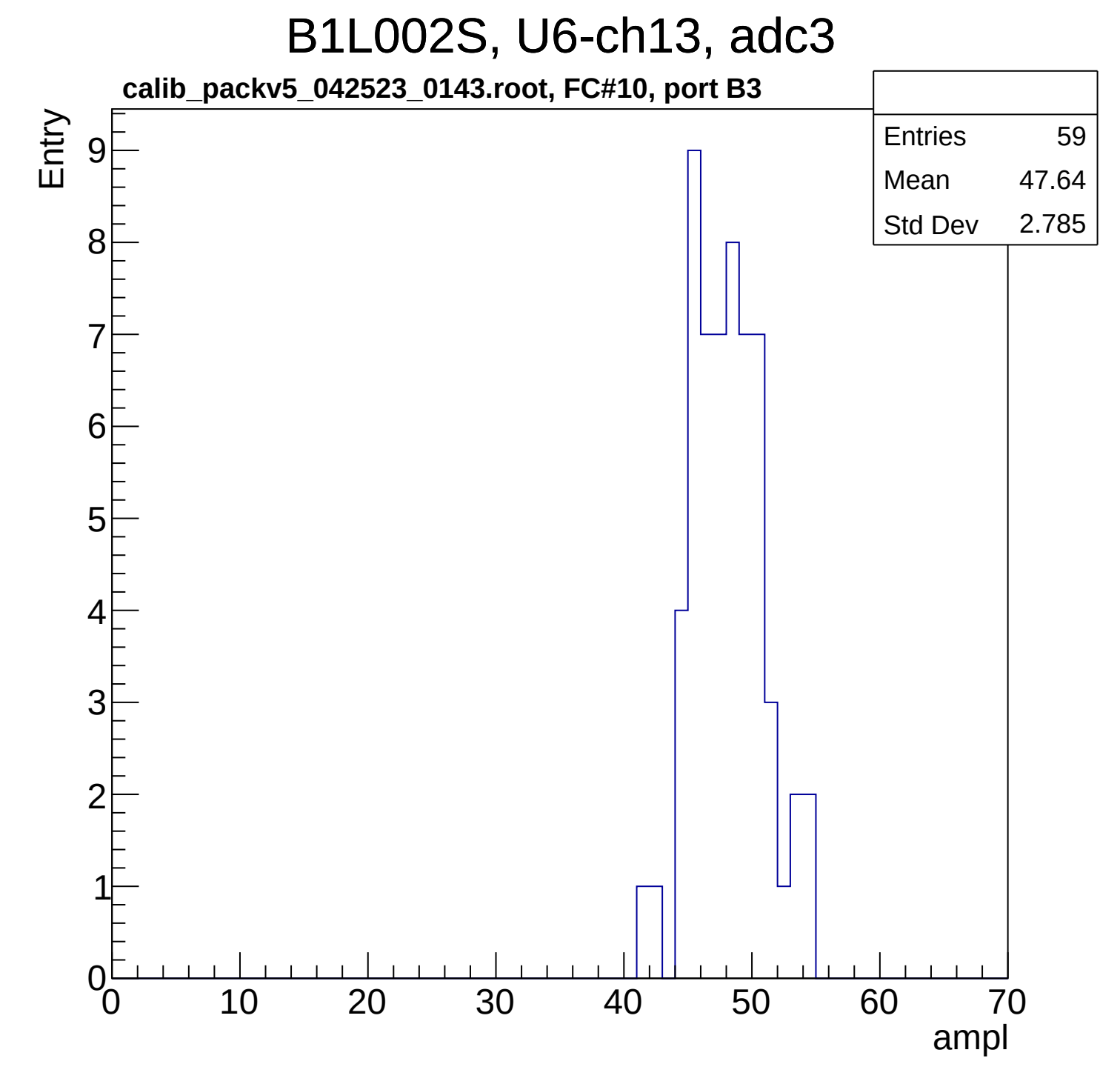

B1L002S, U6-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.5
Std Dev 1.438
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

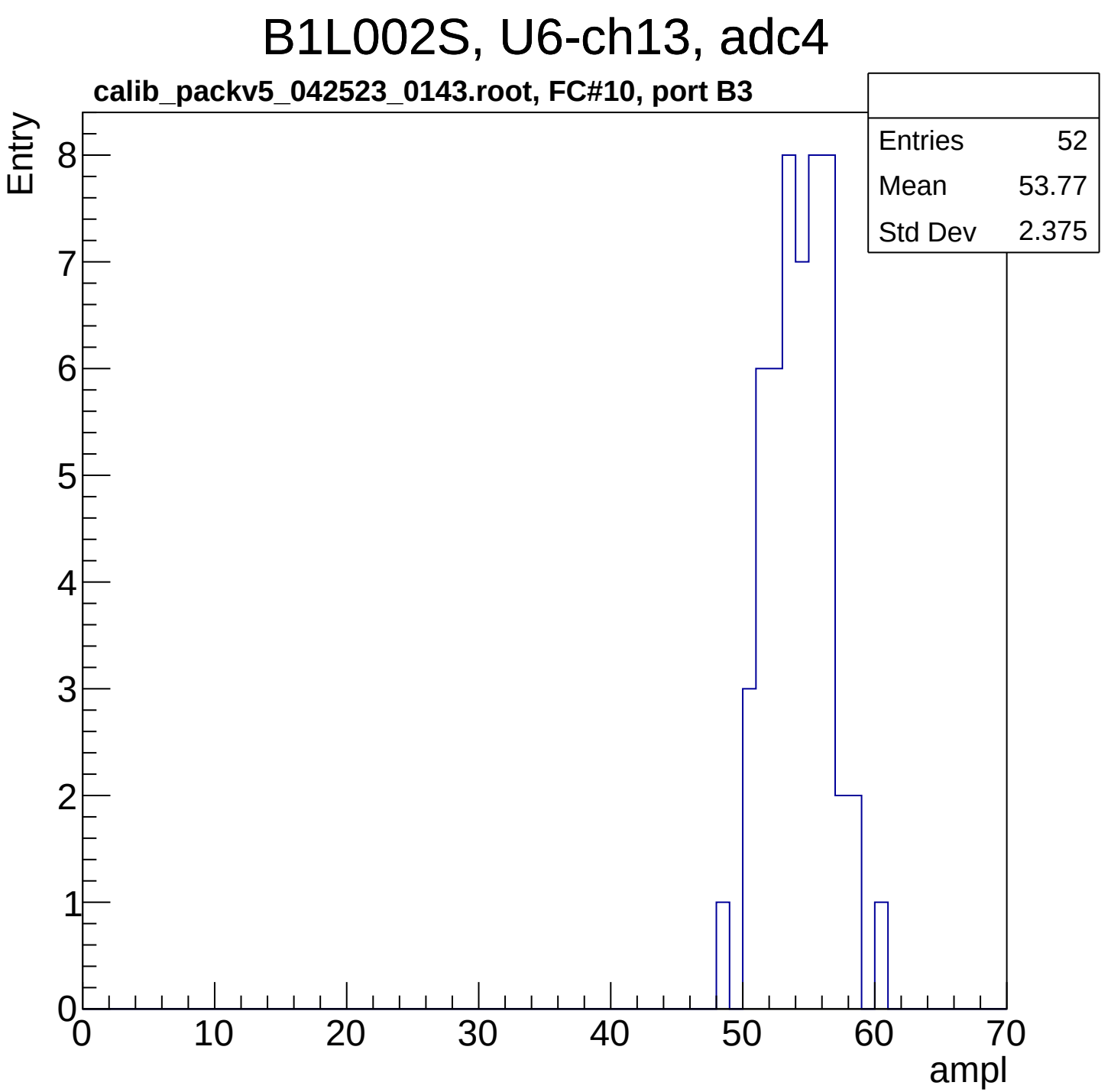

B1L002S, U6-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

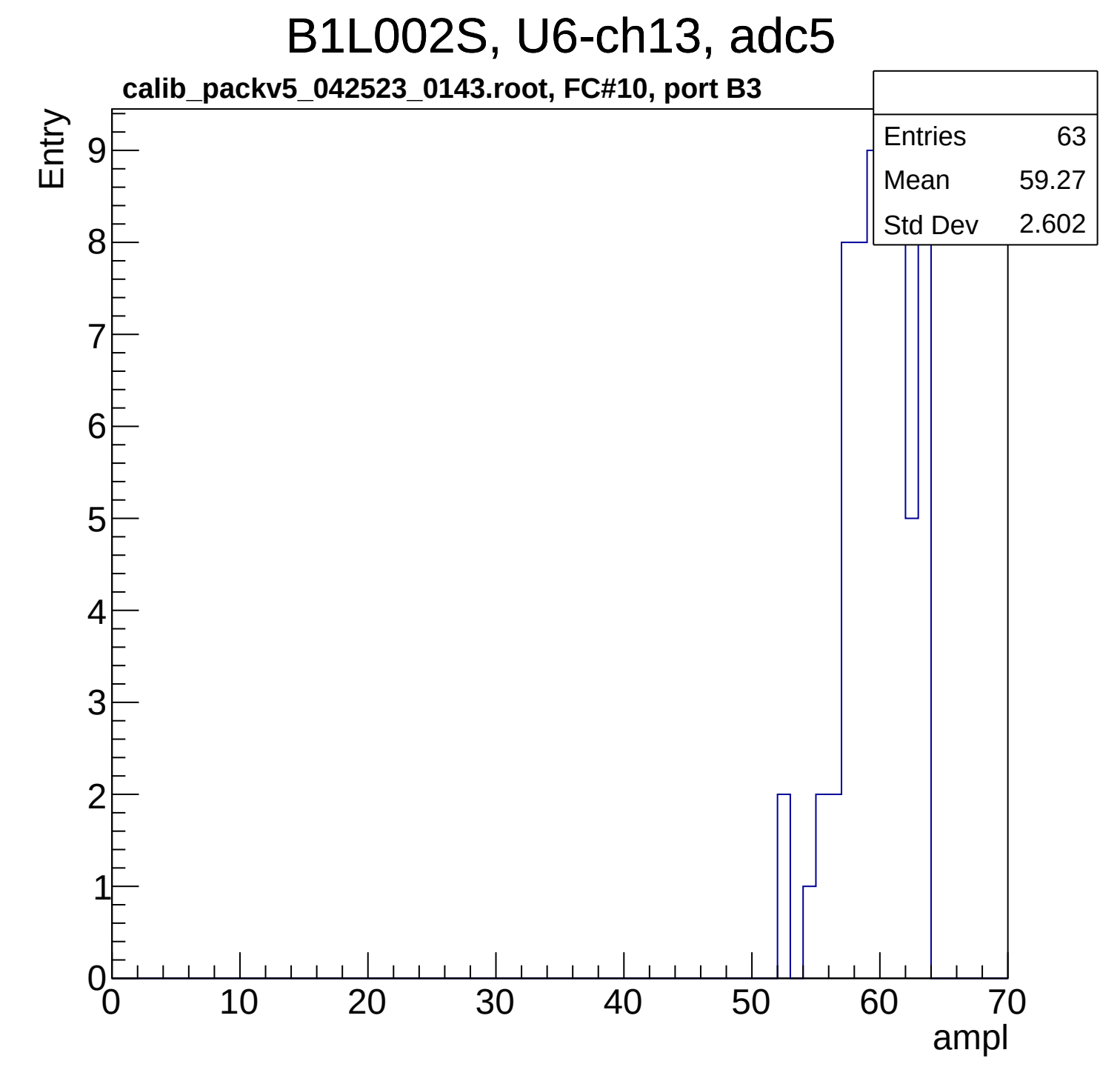

B1L002S, U6-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.86
Std Dev 3.229
Gaus mean : 29.3424
Gaus Width: 3.1899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

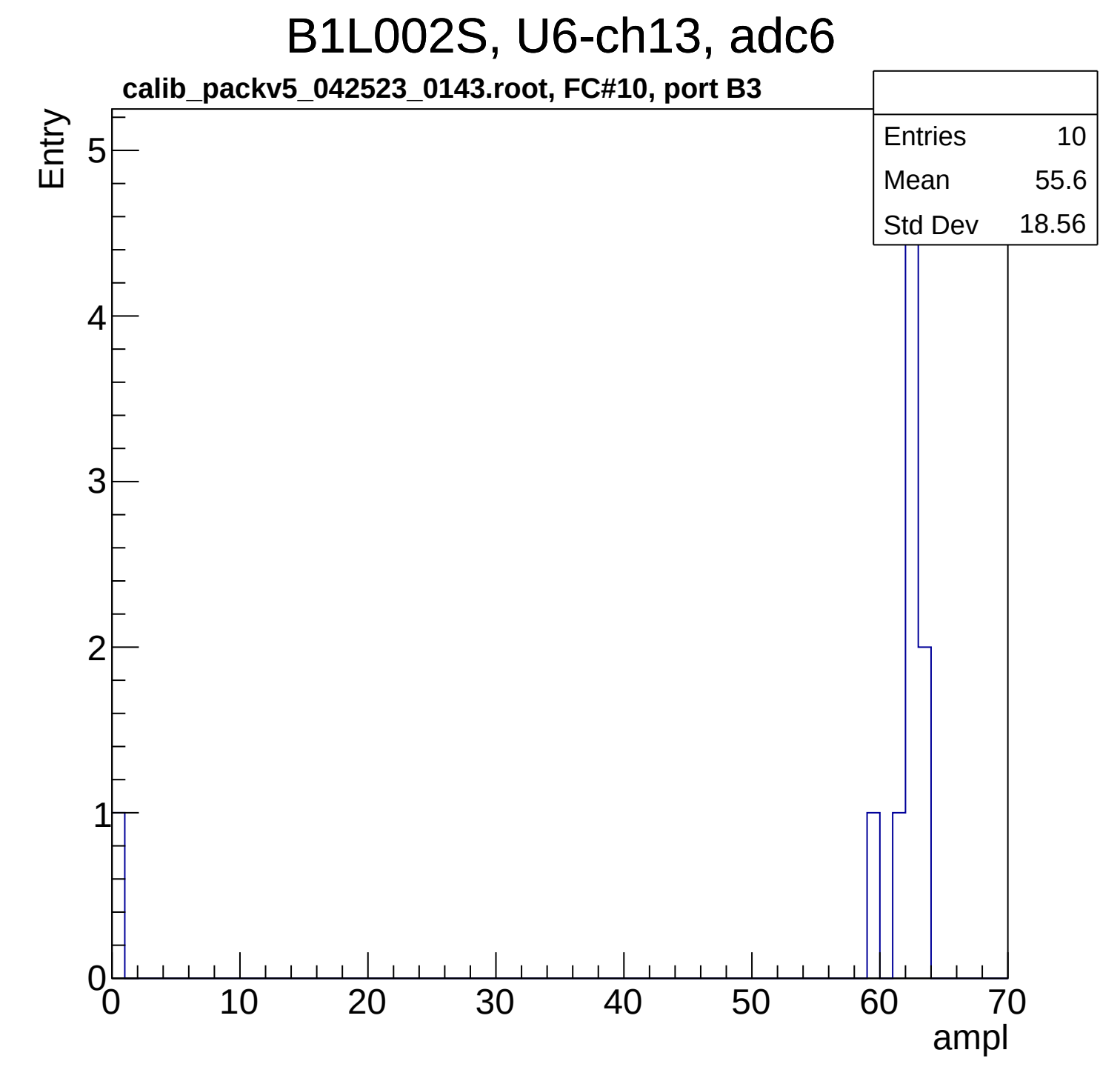

B1L002S, U6-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.41
Std Dev 3.081
Gaus mean : 35.3453
Gaus Width: 3.0631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

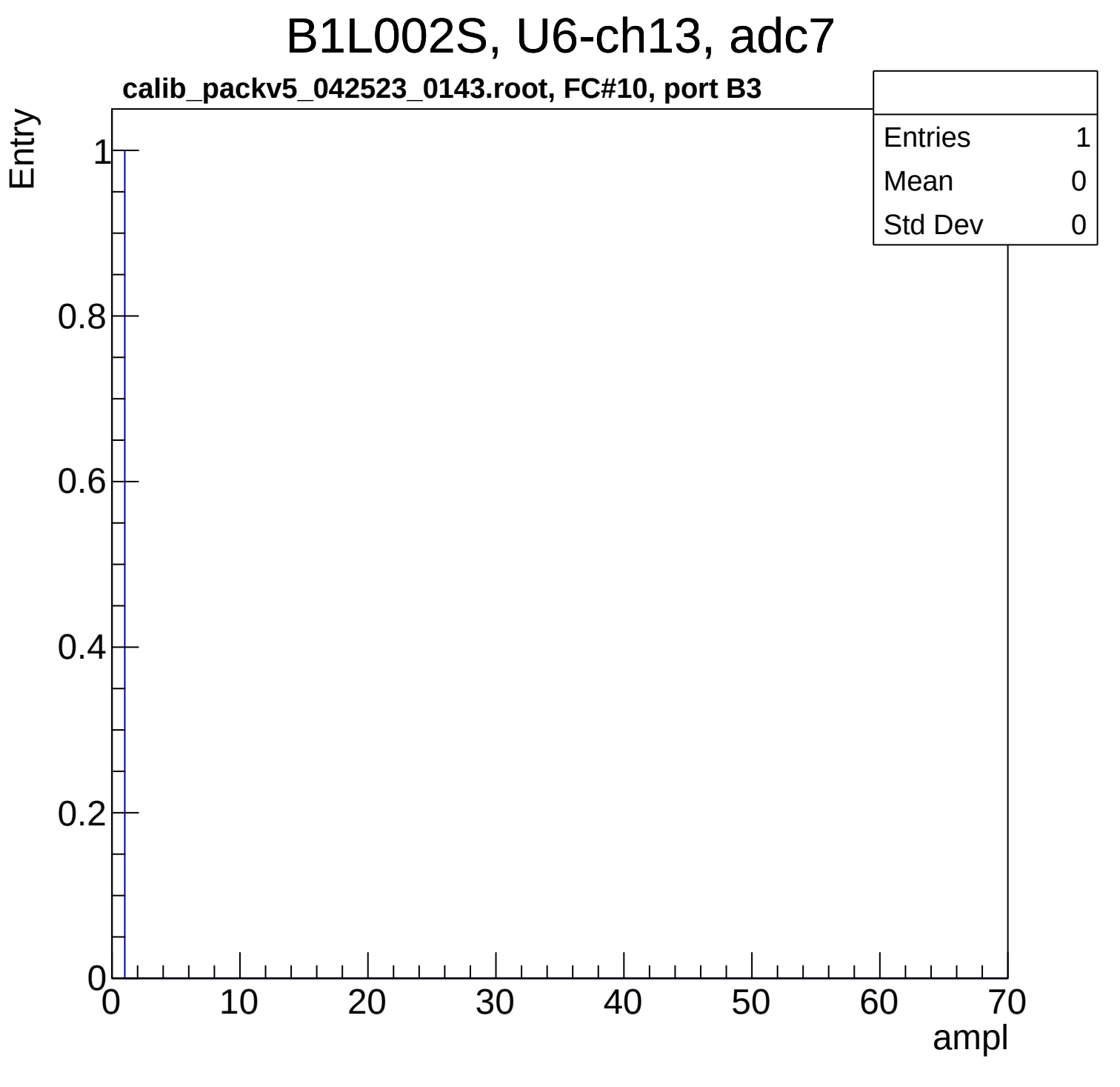

B1L002S, U6-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.9
Std Dev 3.622
Gaus mean : 41.0696
Gaus Width: 3.2274
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

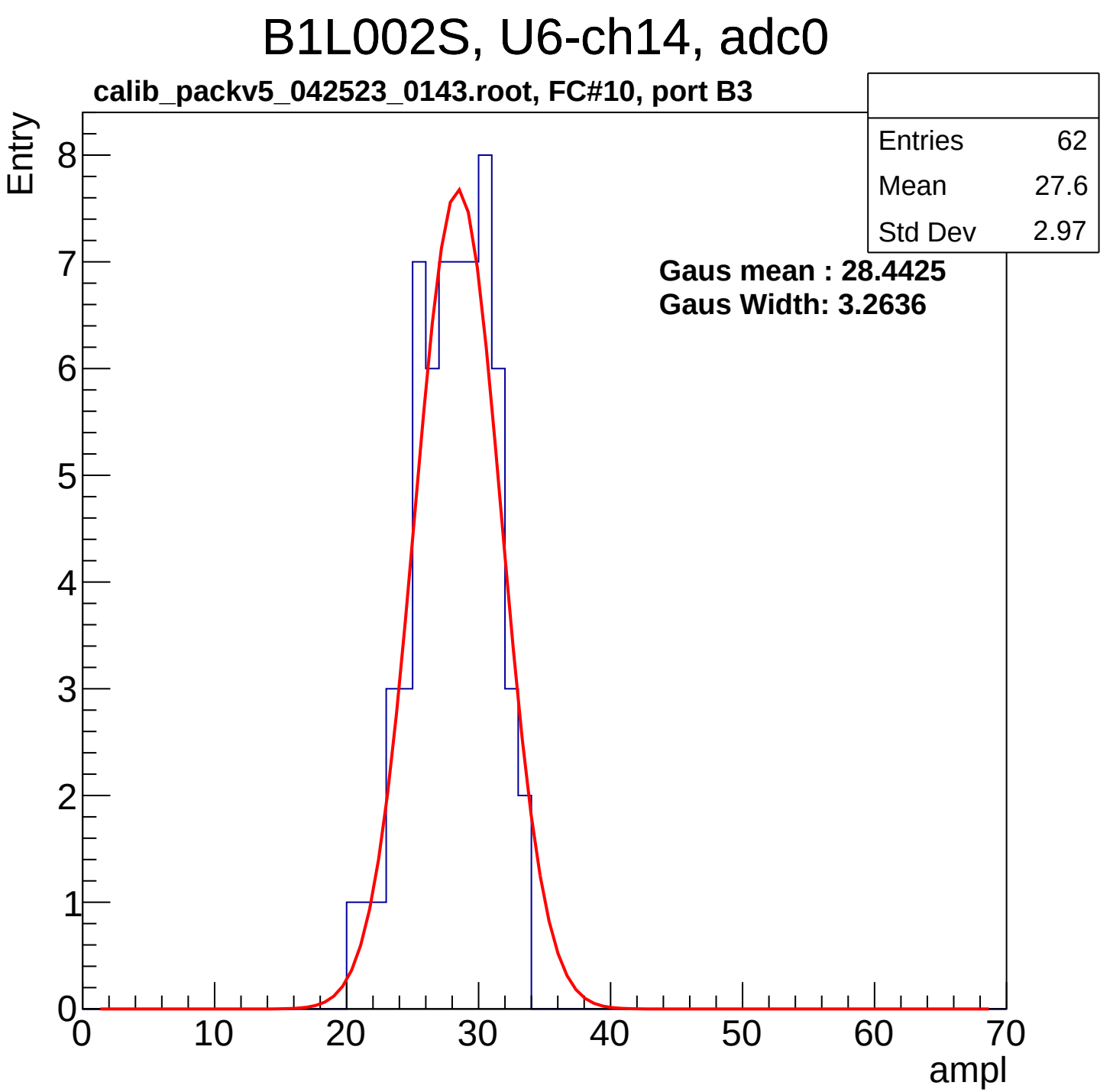

B1L002S, U6-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.64
Std Dev 2.785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

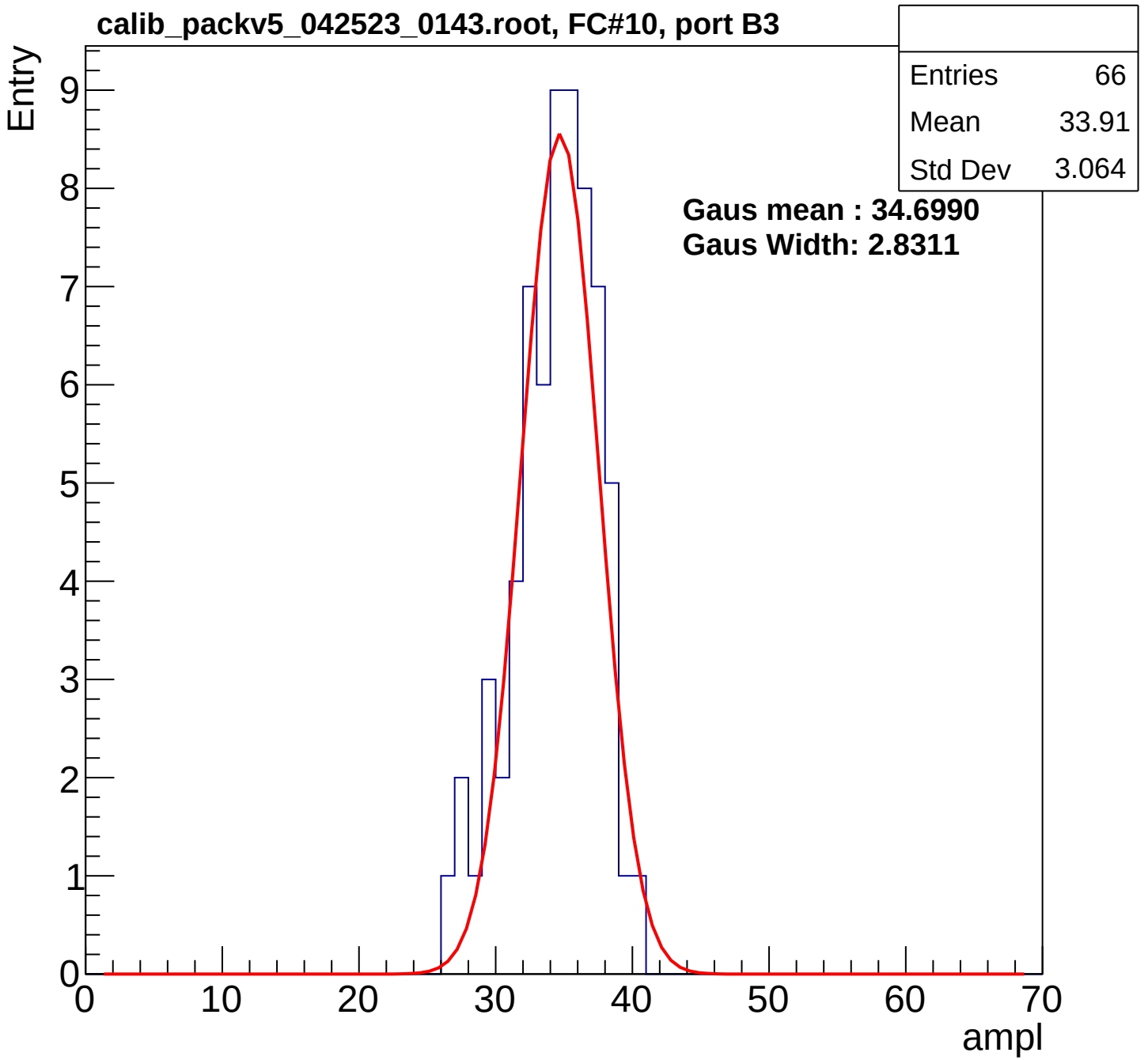

B1L002S, U6-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.77
Std Dev 2.375
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

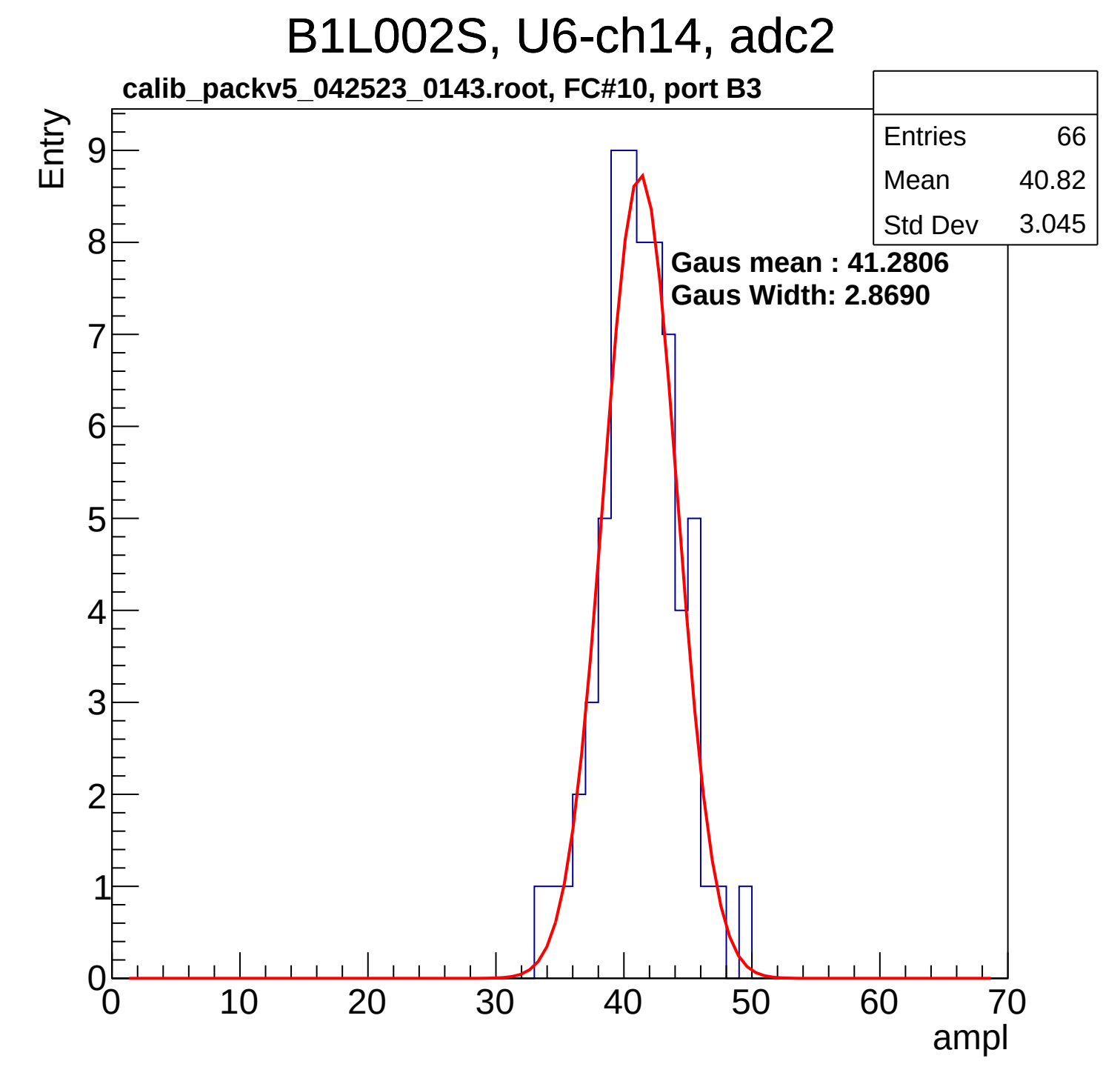

B1L002S, U6-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 59.27
Std Dev 2.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

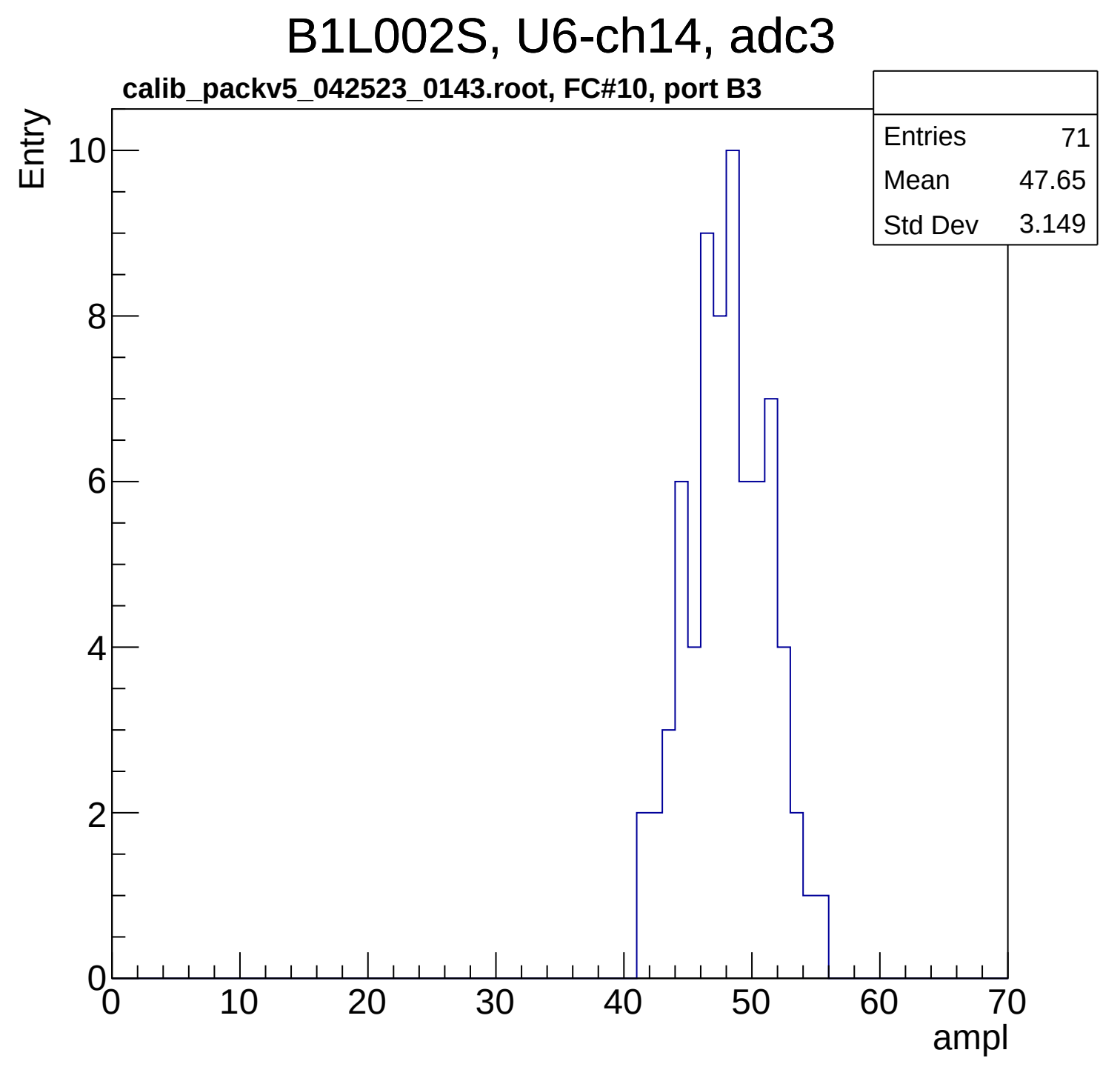

B1L002S, U6-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.6
Std Dev 18.56
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

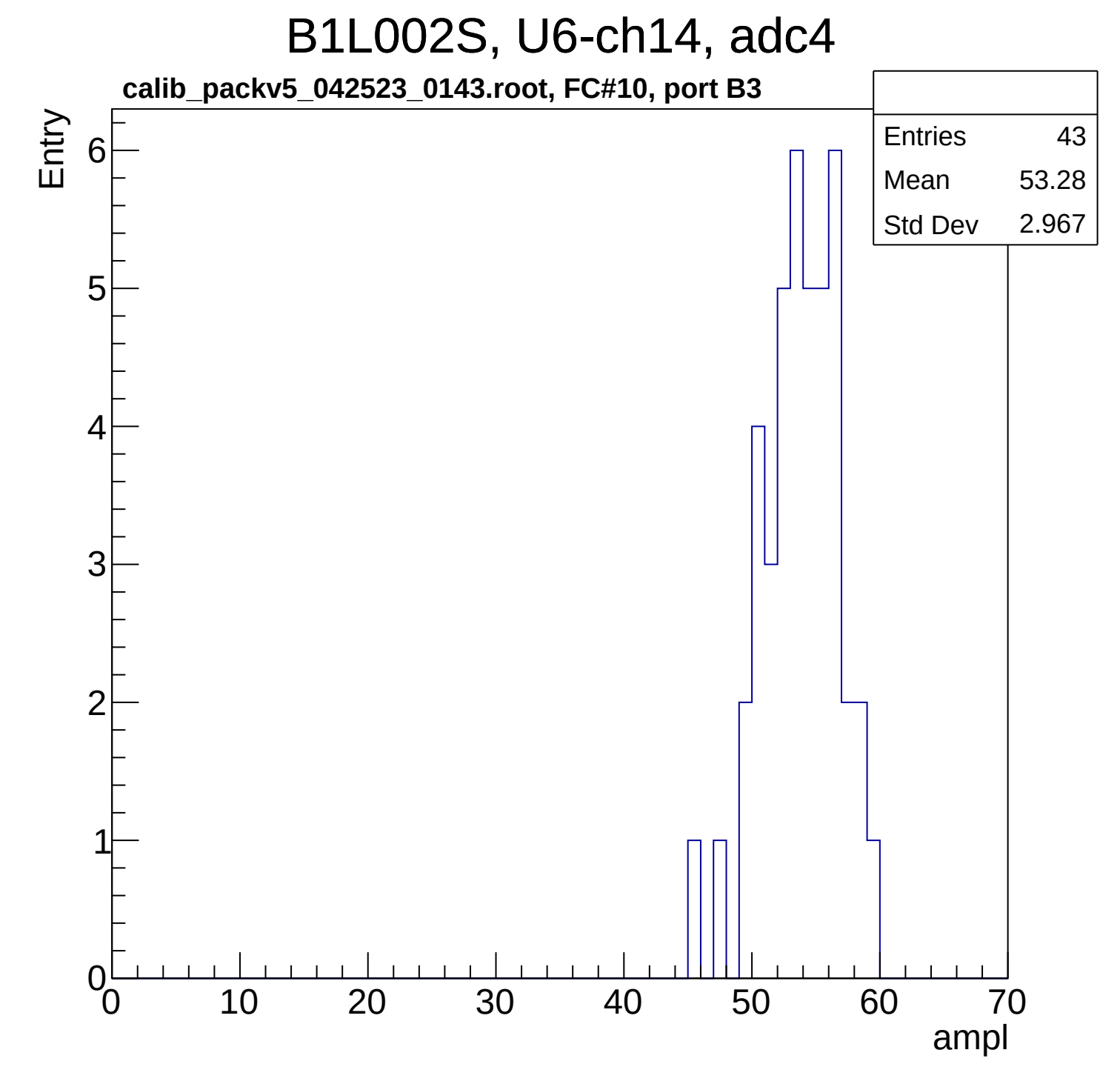

B1L002S, U6-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

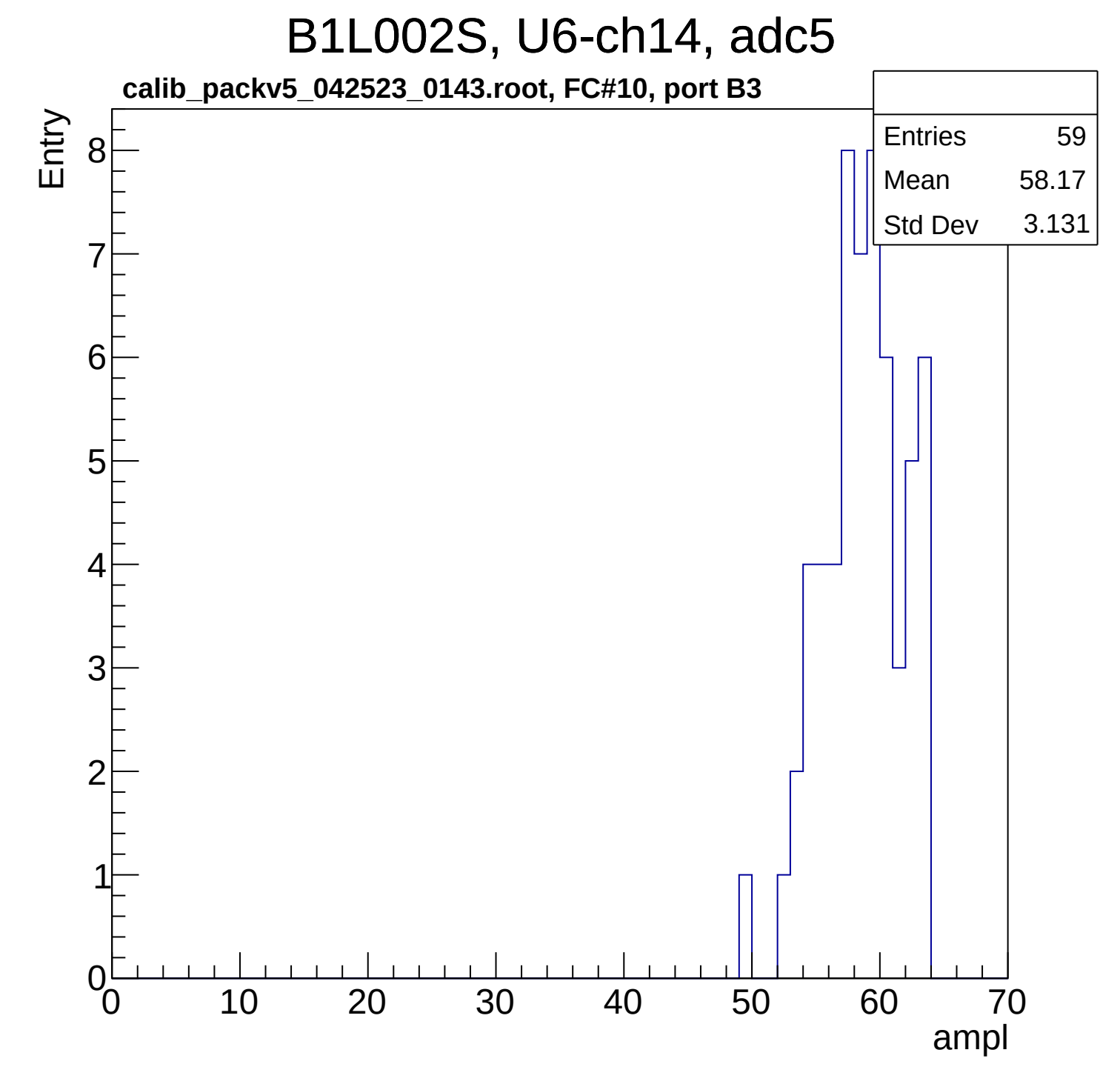

B1L002S, U6-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.6
Std Dev 2.97
Gaus mean : 28.4425
Gaus Width: 3.2636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

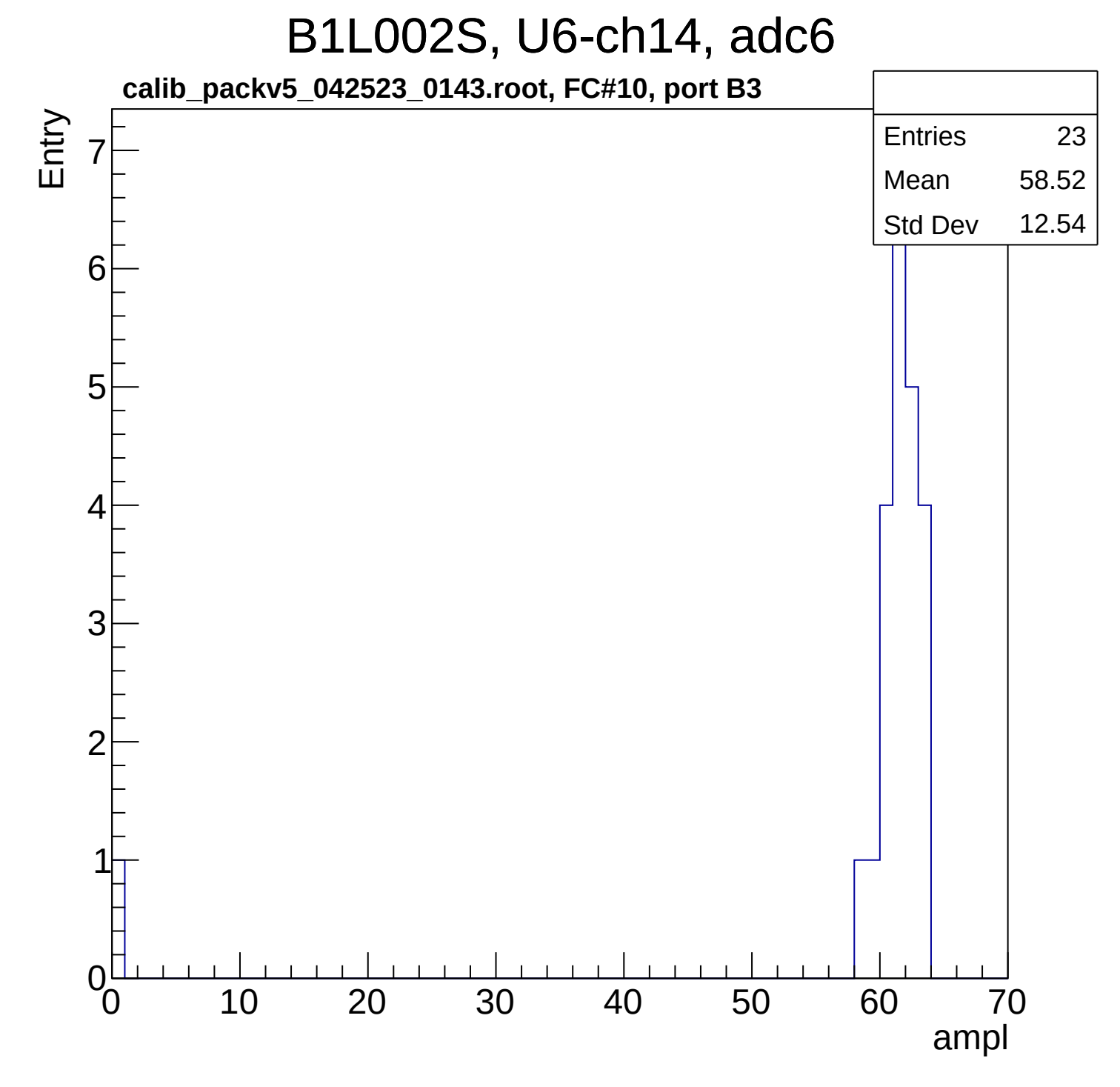

B1L002S, U6-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.91
Std Dev 3.064
Gaus mean : 34.6990
Gaus Width: 2.8311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

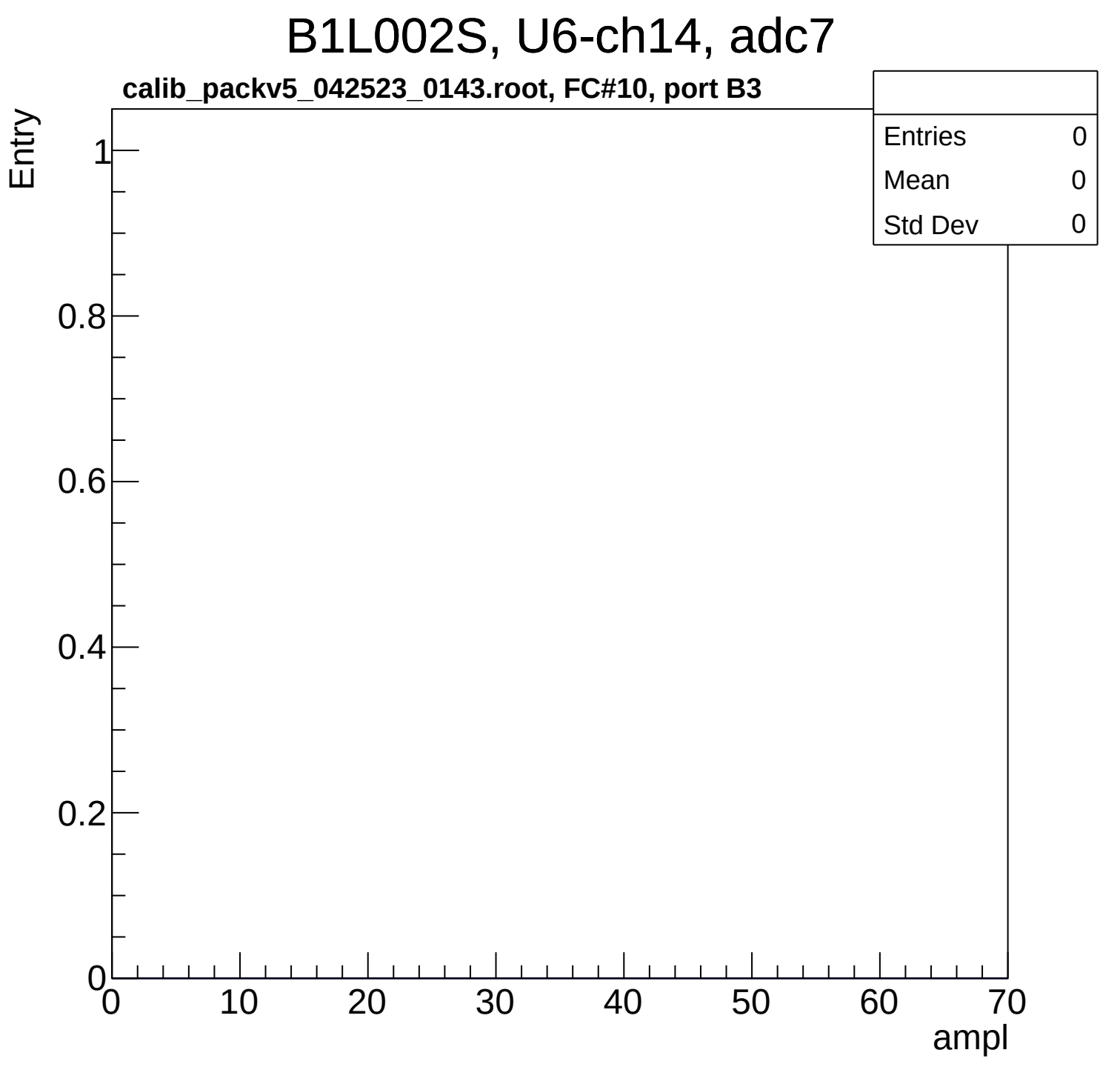

B1L002S, U6-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.82
Std Dev 3.045
Gaus mean : 41.2806
Gaus Width: 2.8690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

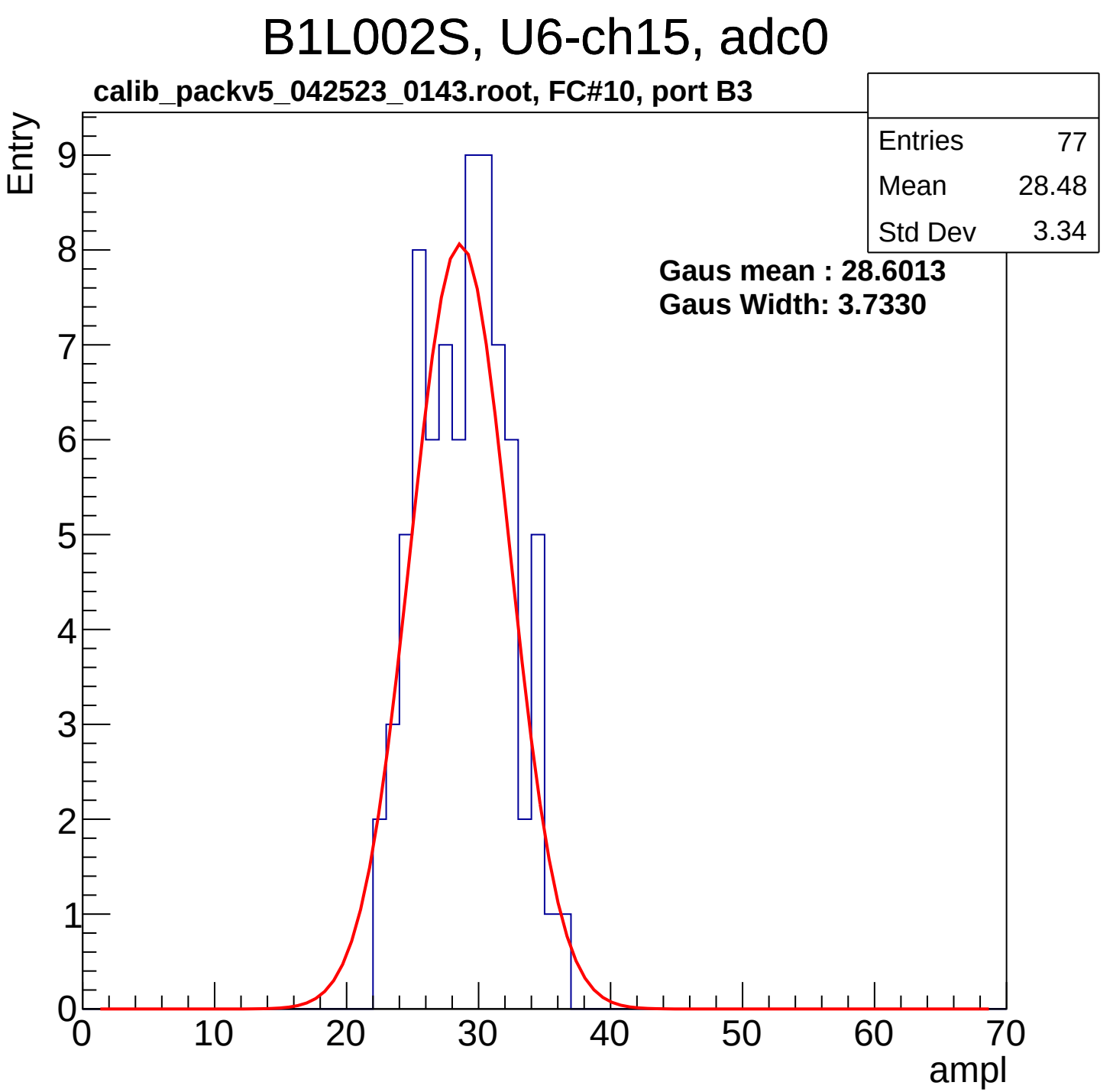

B1L002S, U6-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 47.65
Std Dev 3.149
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

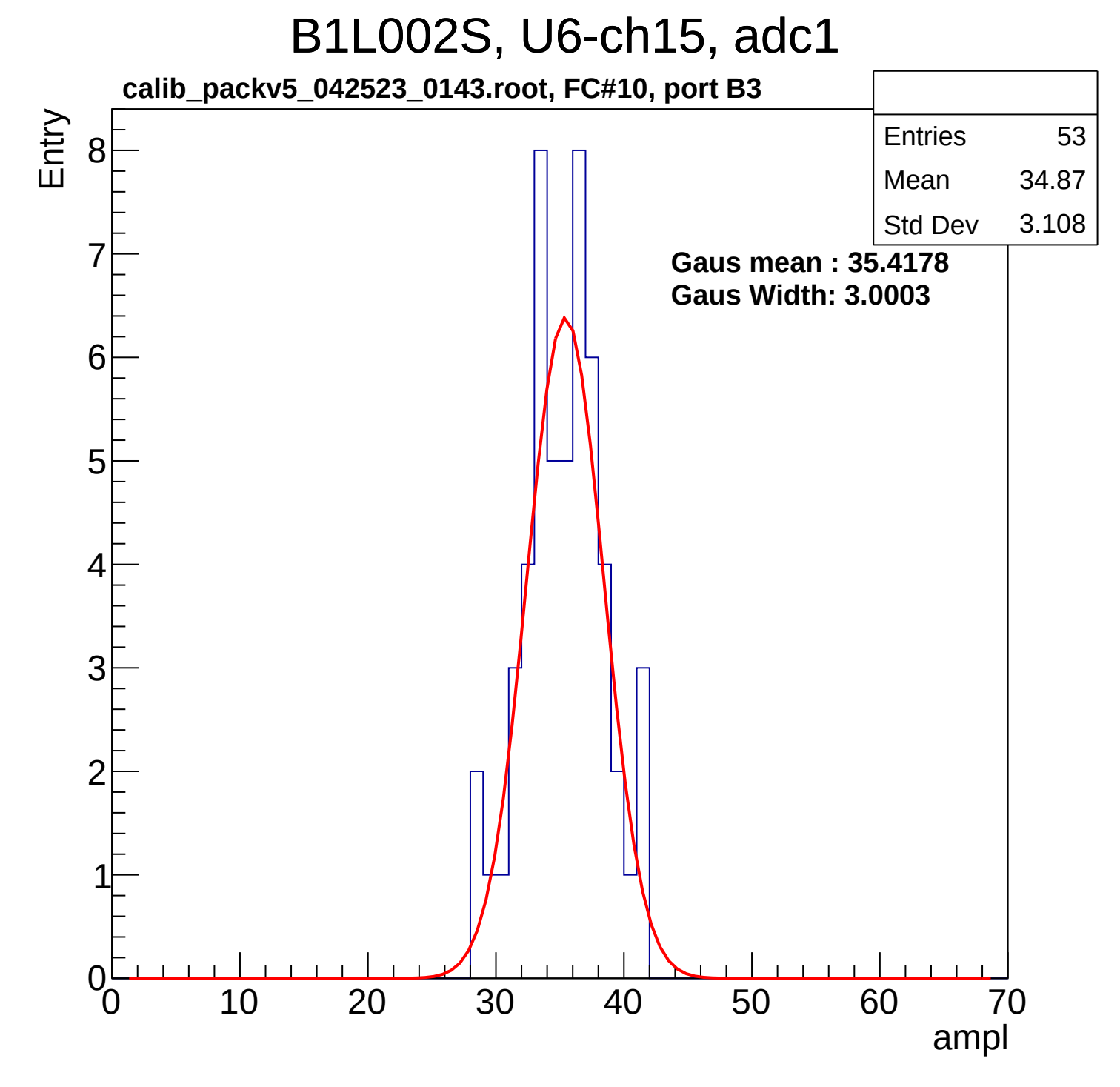

B1L002S, U6-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.28
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

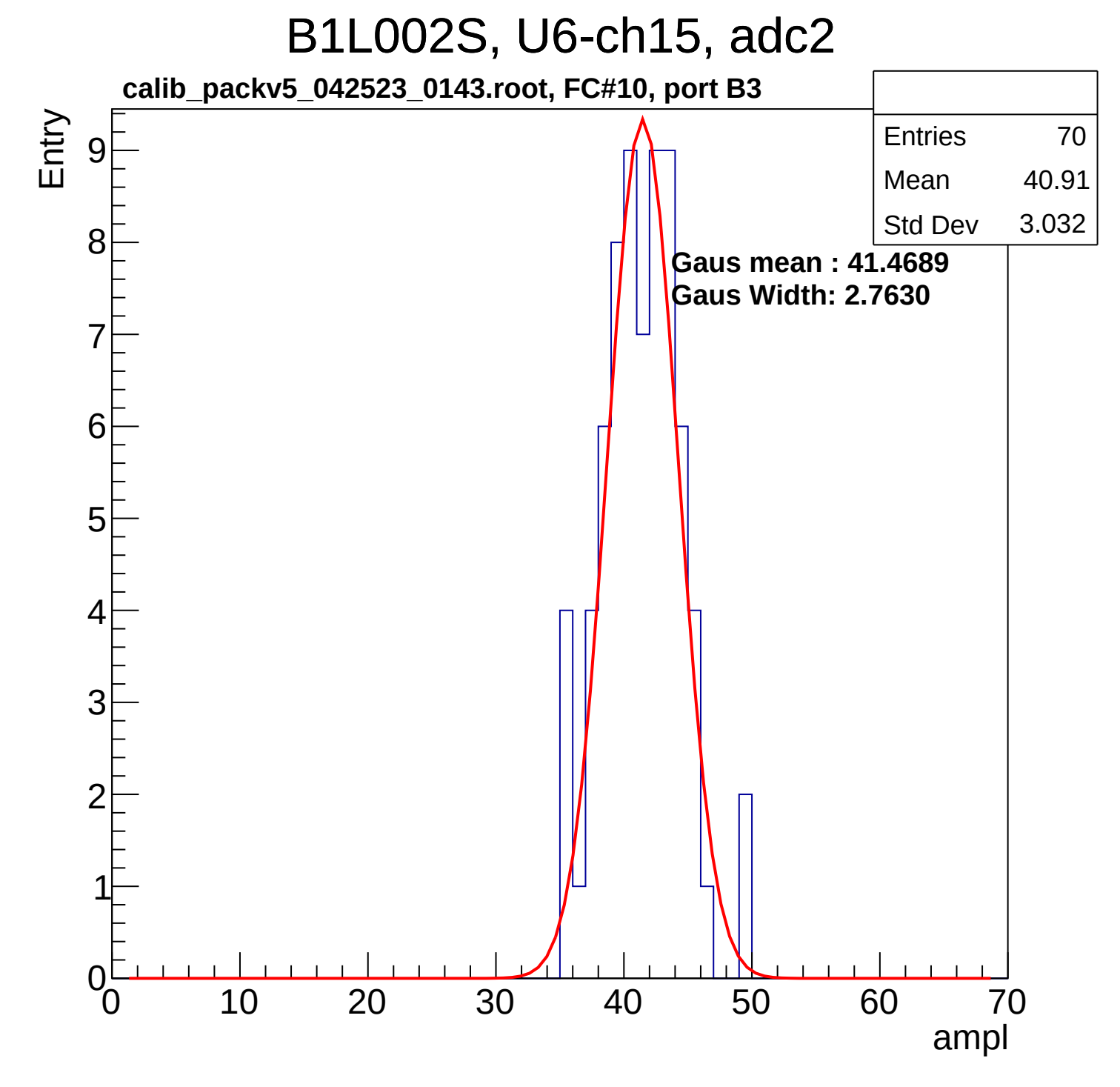

B1L002S, U6-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.17
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

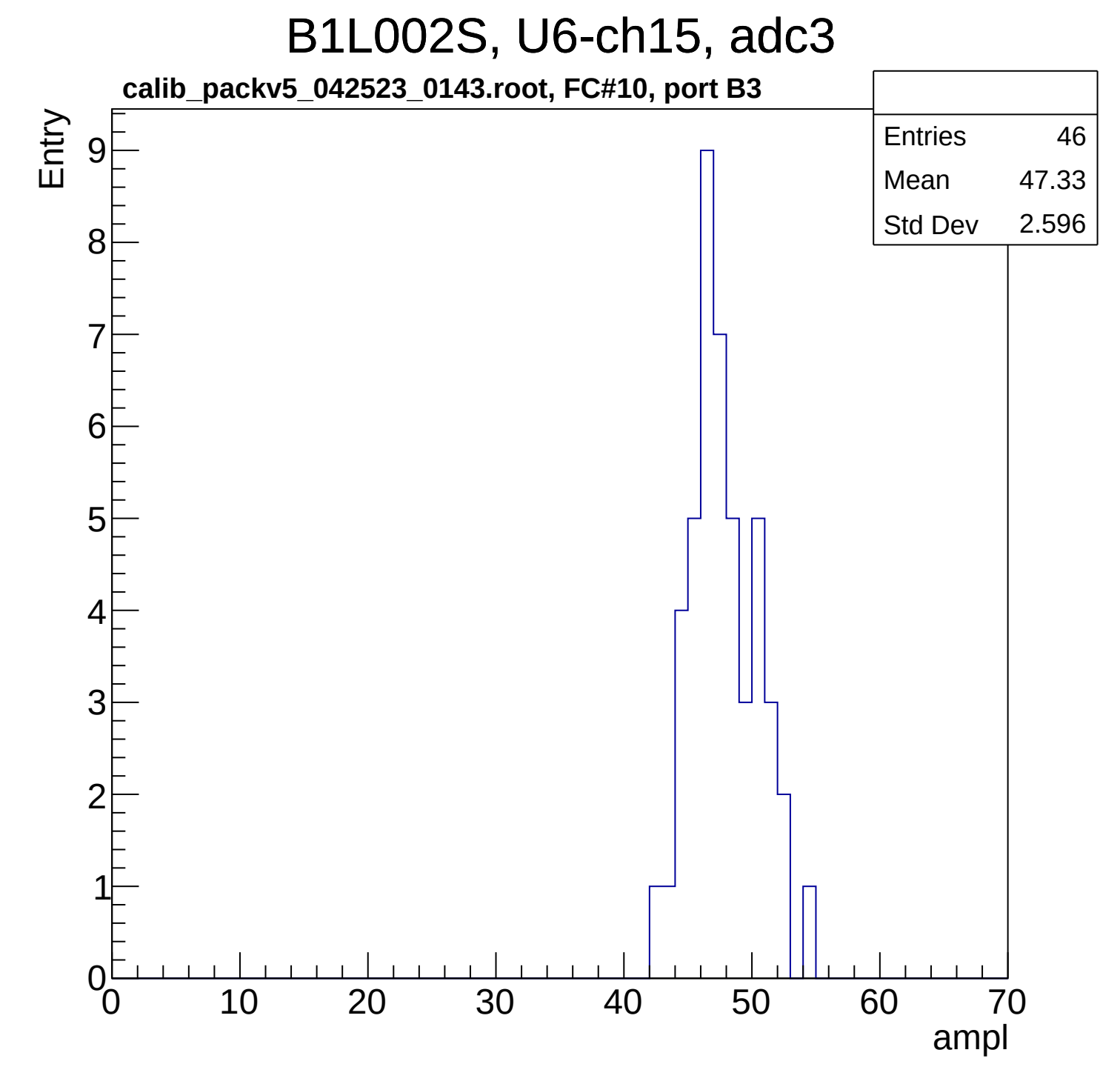

B1L002S, U6-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.52
Std Dev 12.54
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

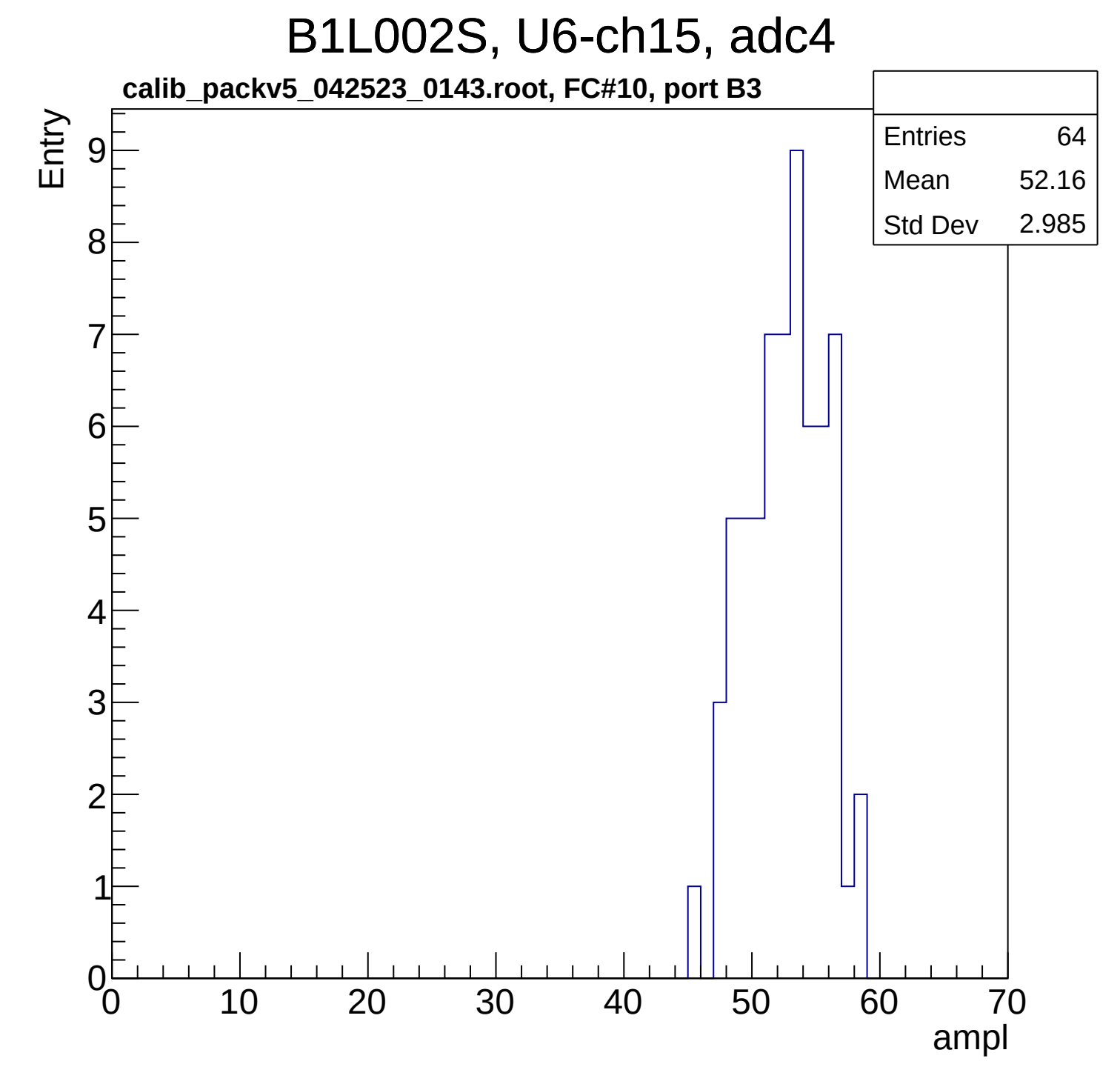

B1L002S, U6-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

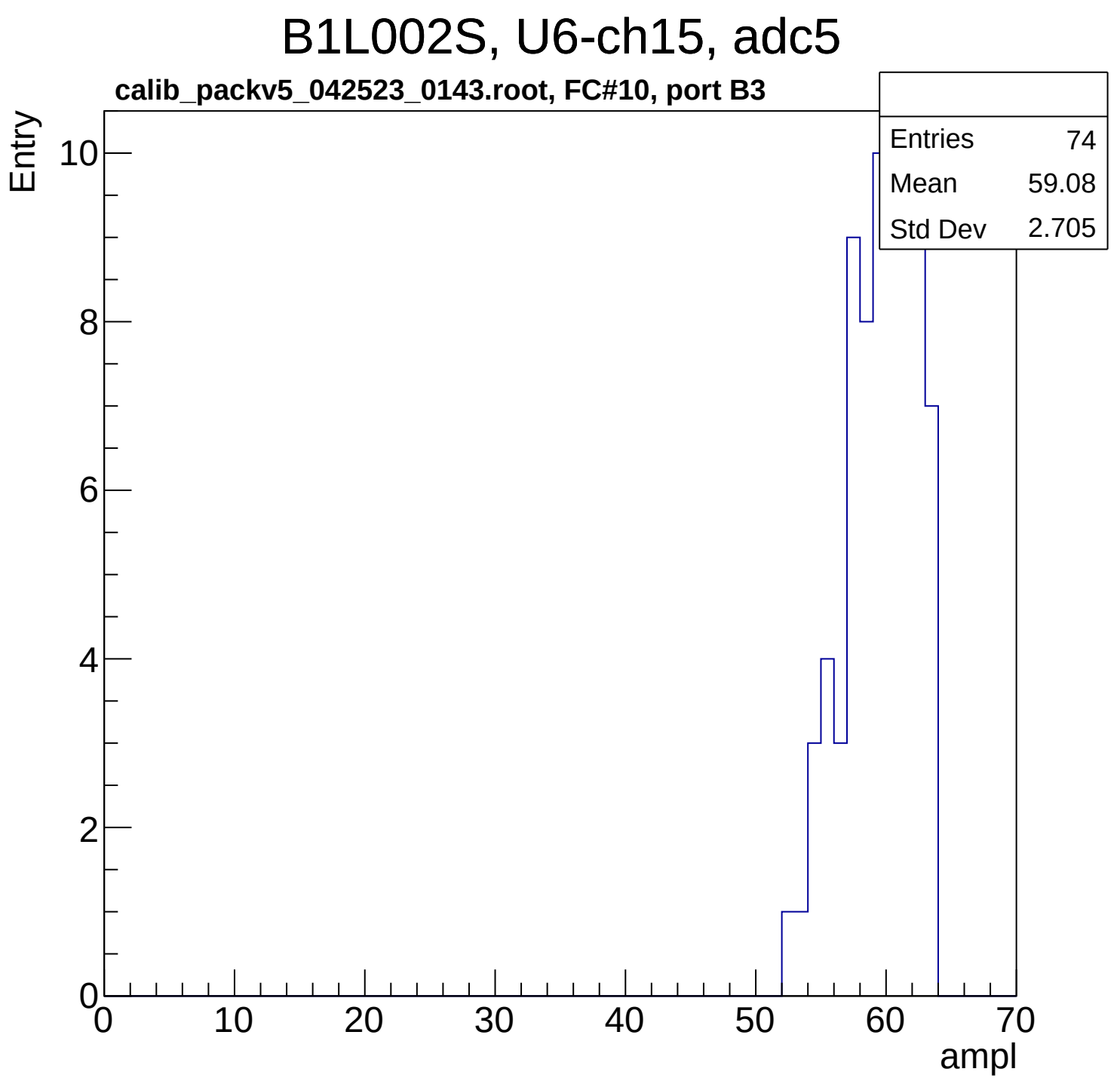

B1L002S, U6-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.48
Std Dev 3.34
Gaus mean : 28.6013
Gaus Width: 3.7330
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

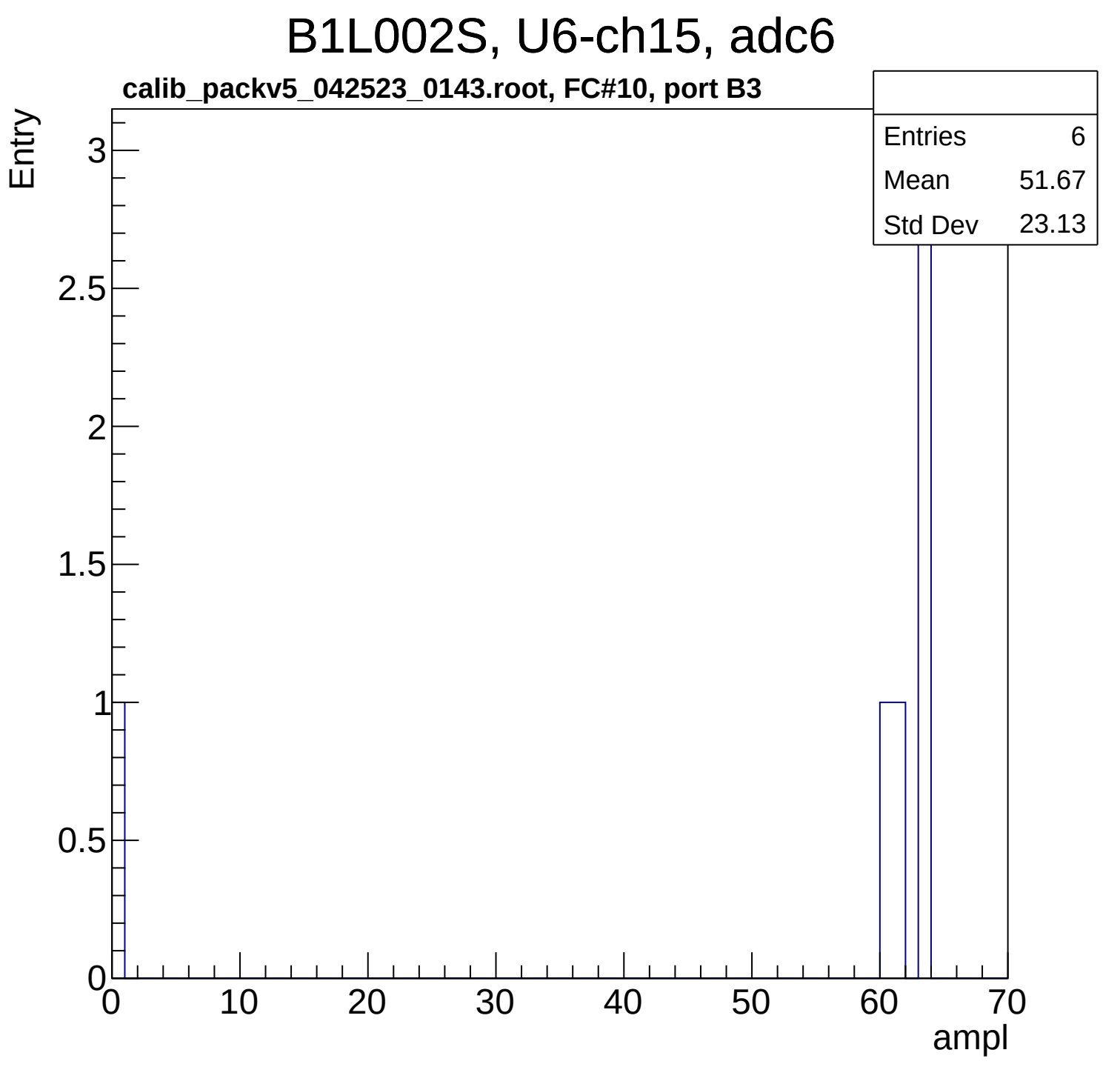

B1L002S, U6-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 34.87
Std Dev 3.108
Gaus mean : 35.4178
Gaus Width: 3.0003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

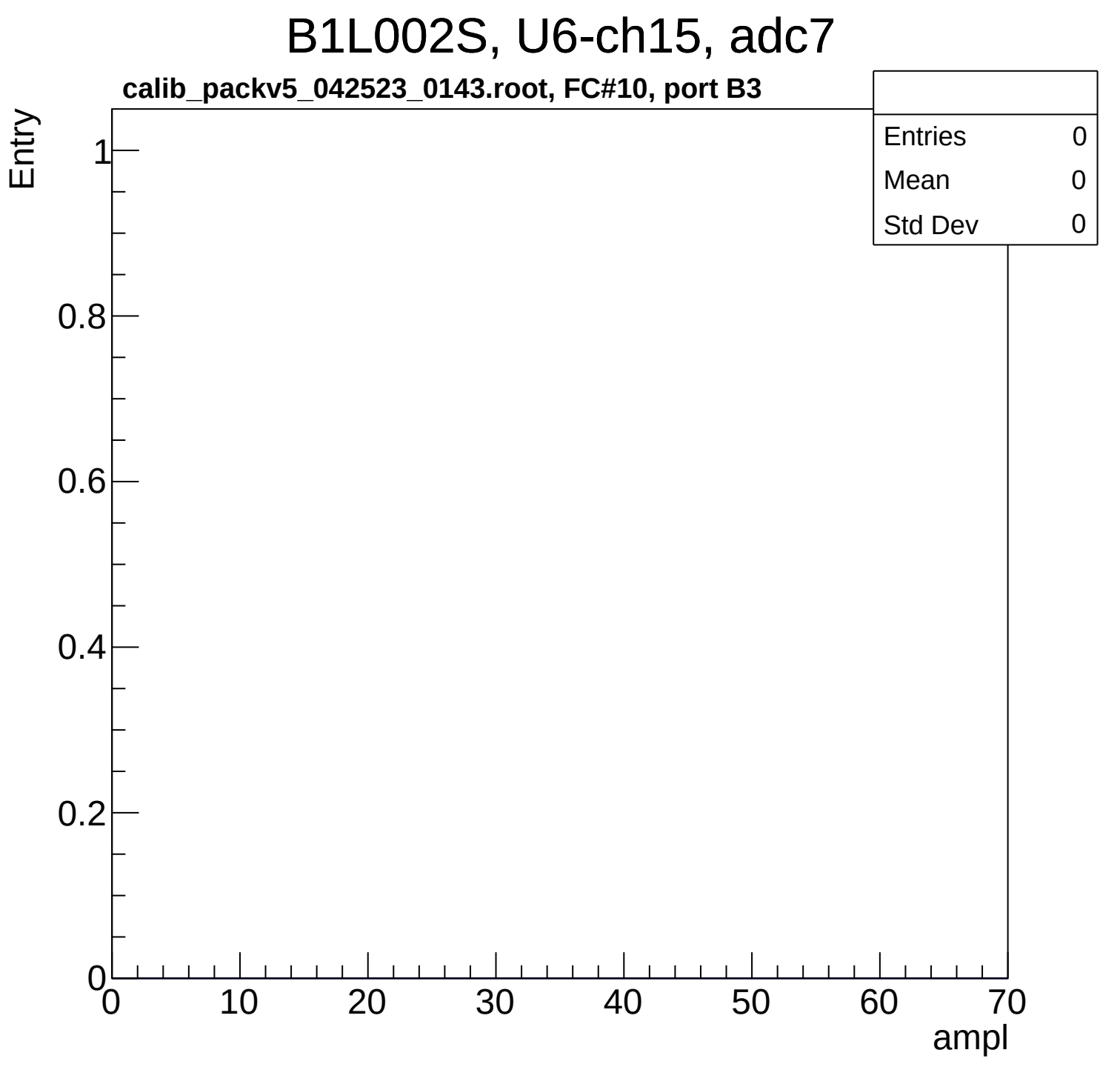

B1L002S, U6-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 40.91
Std Dev 3.032
Gaus mean : 41.4689
Gaus Width: 2.7630
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

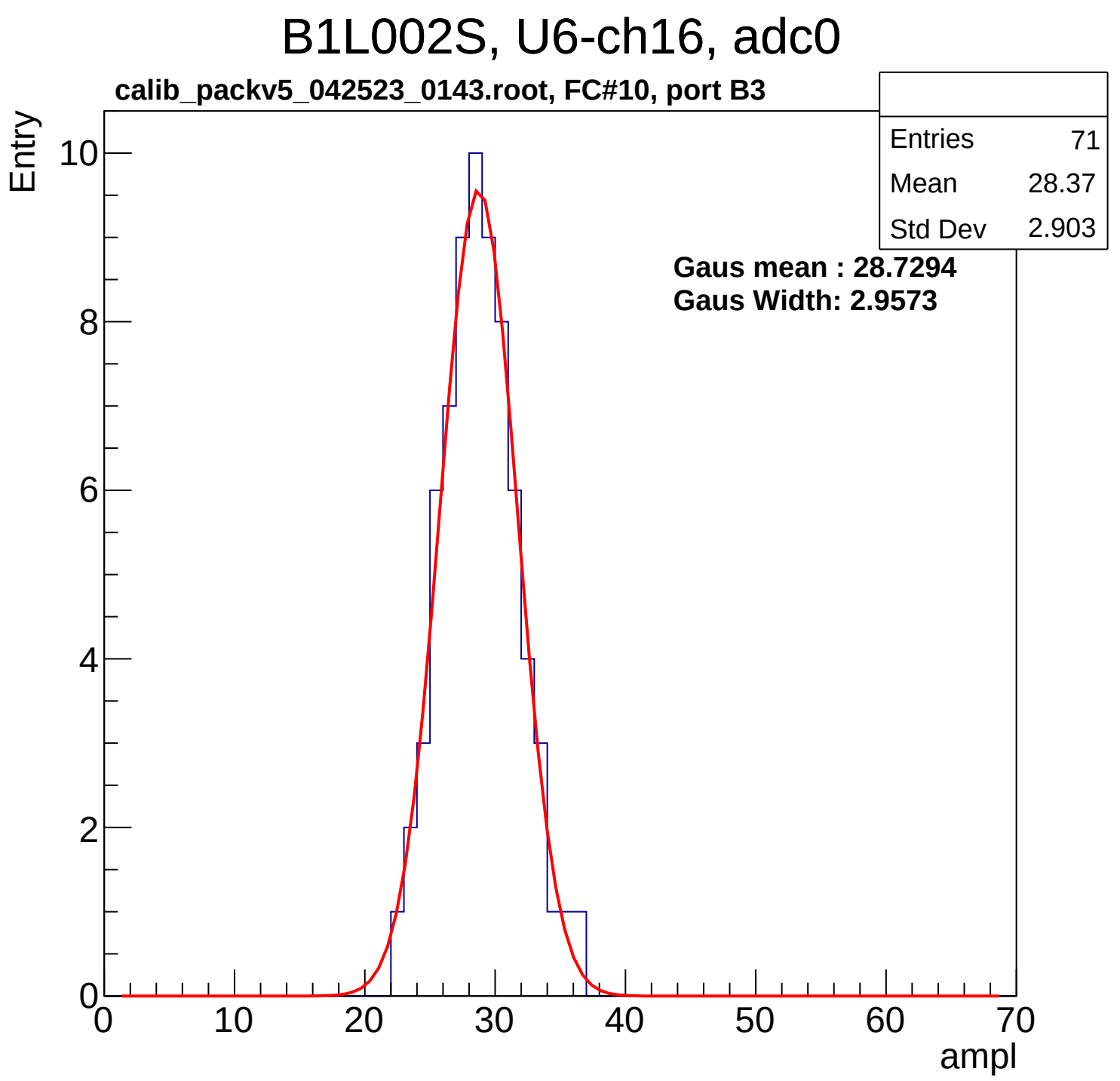

B1L002S, U6-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 47.33
Std Dev 2.596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

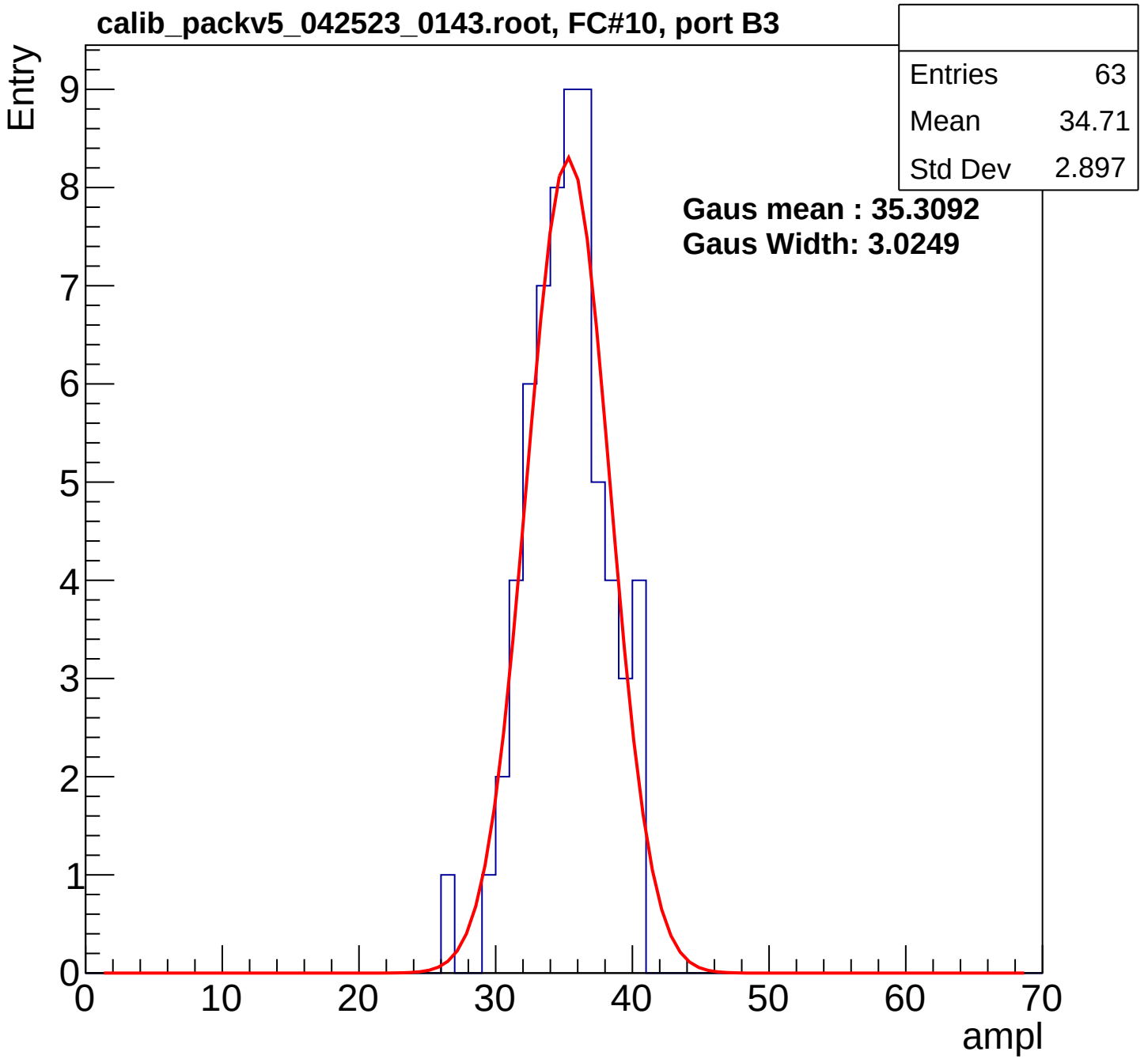

B1L002S, U6-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.16
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

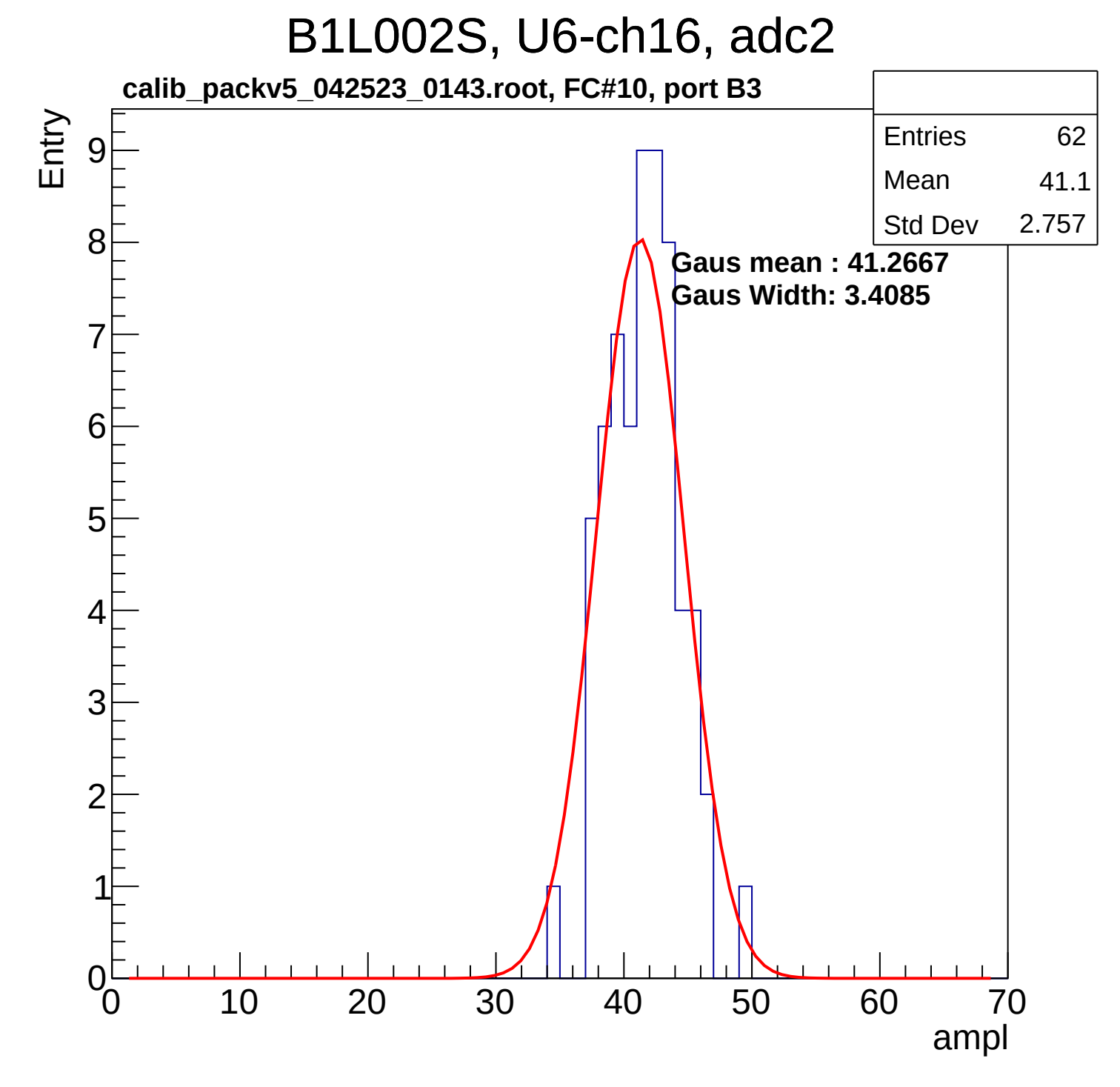

B1L002S, U6-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 59.08
Std Dev 2.705
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

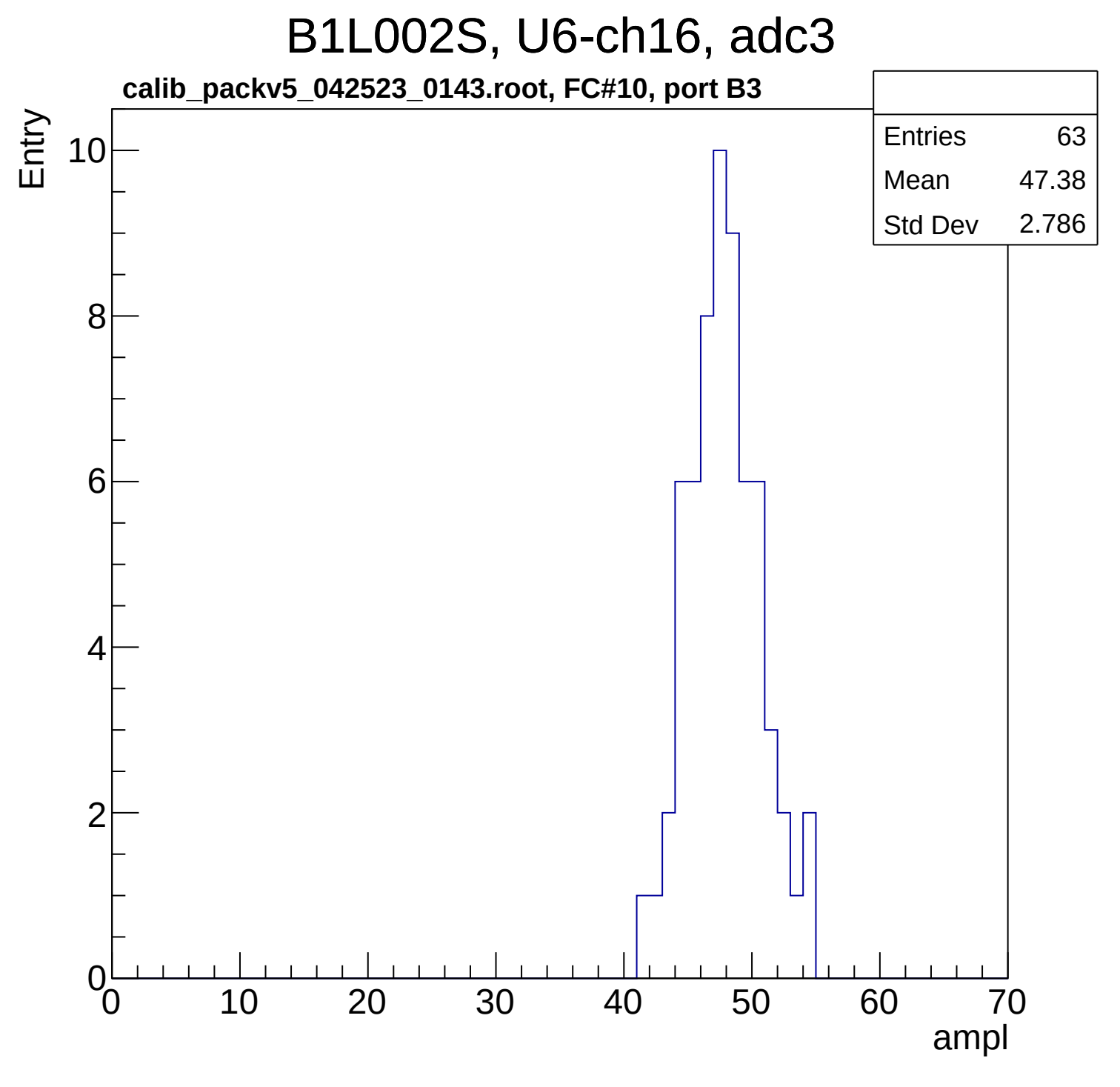

B1L002S, U6-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

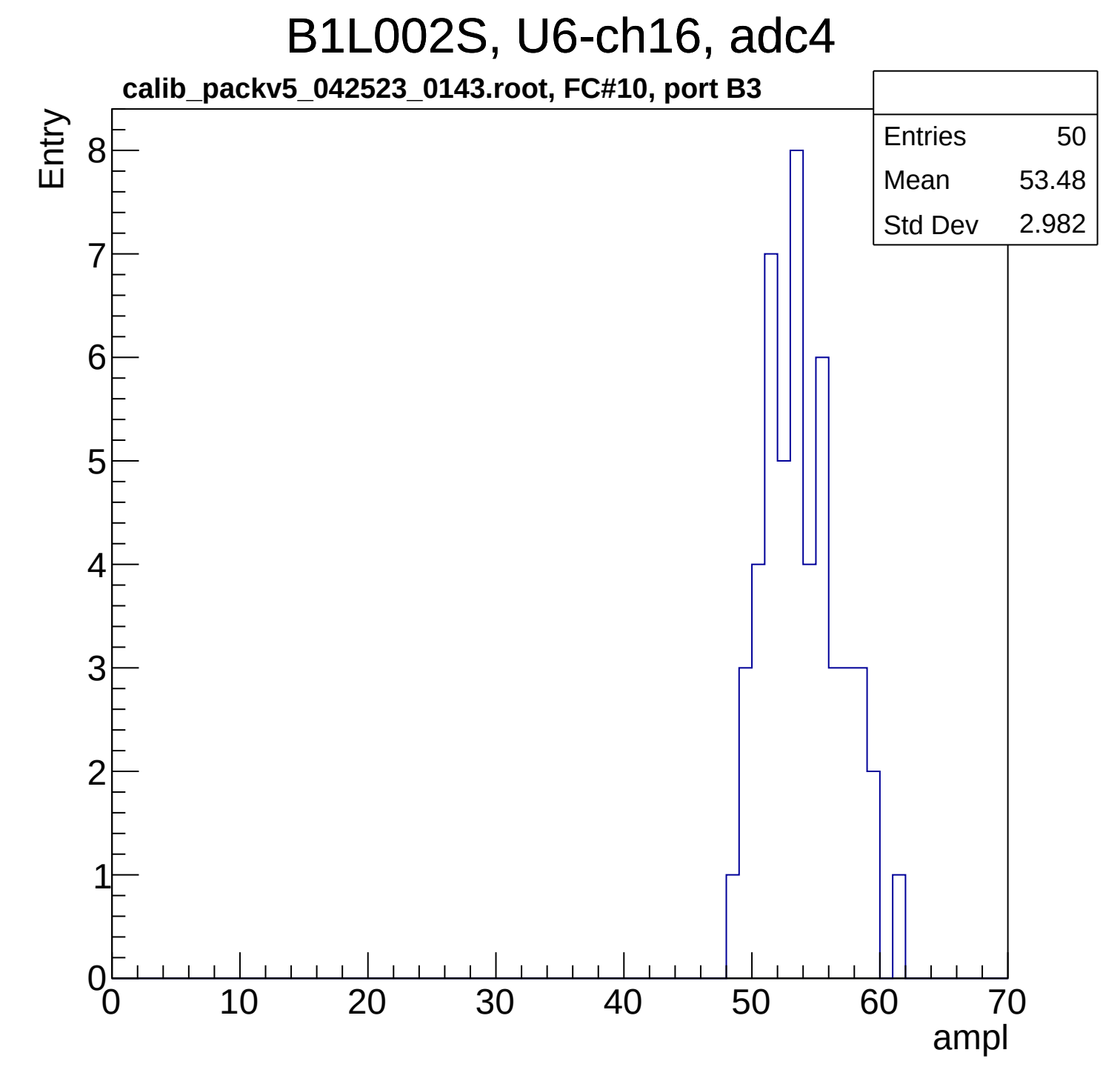

B1L002S, U6-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

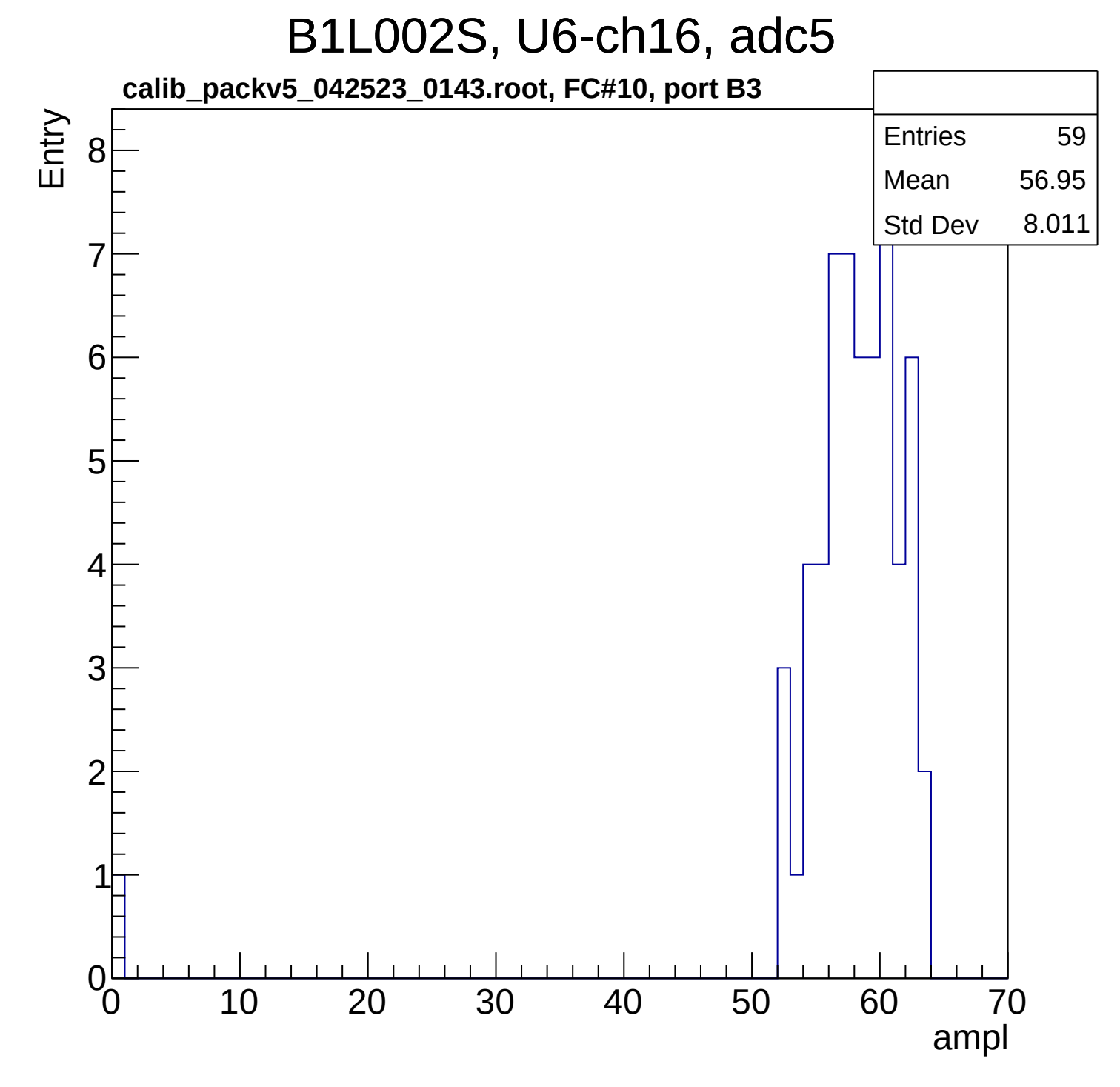

B1L002S, U6-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.37
Std Dev 2.903
Gaus mean : 28.7294
Gaus Width: 2.9573
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

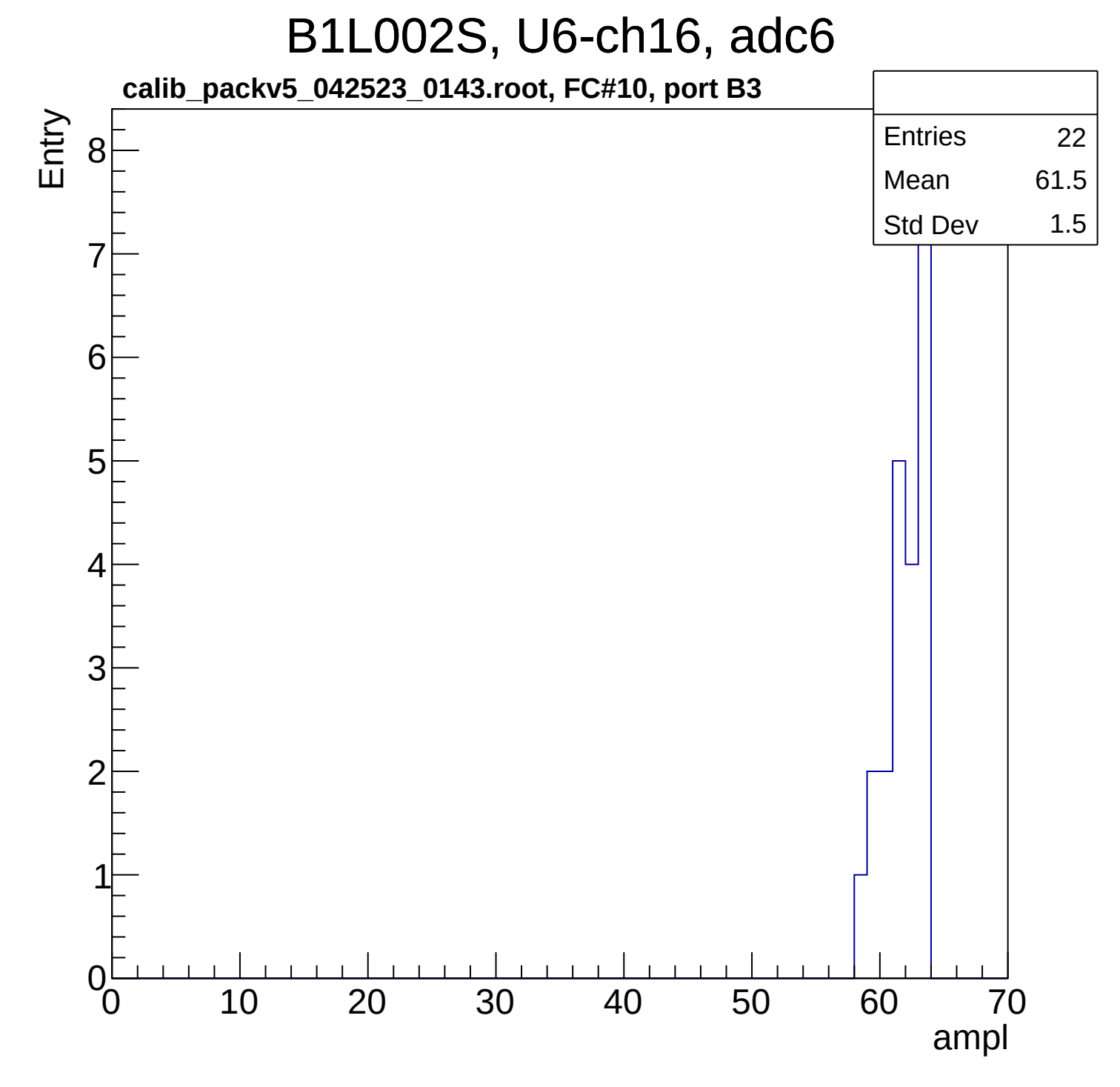

B1L002S, U6-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.71
Std Dev 2.897
Gaus mean : 35.3092
Gaus Width: 3.0249
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

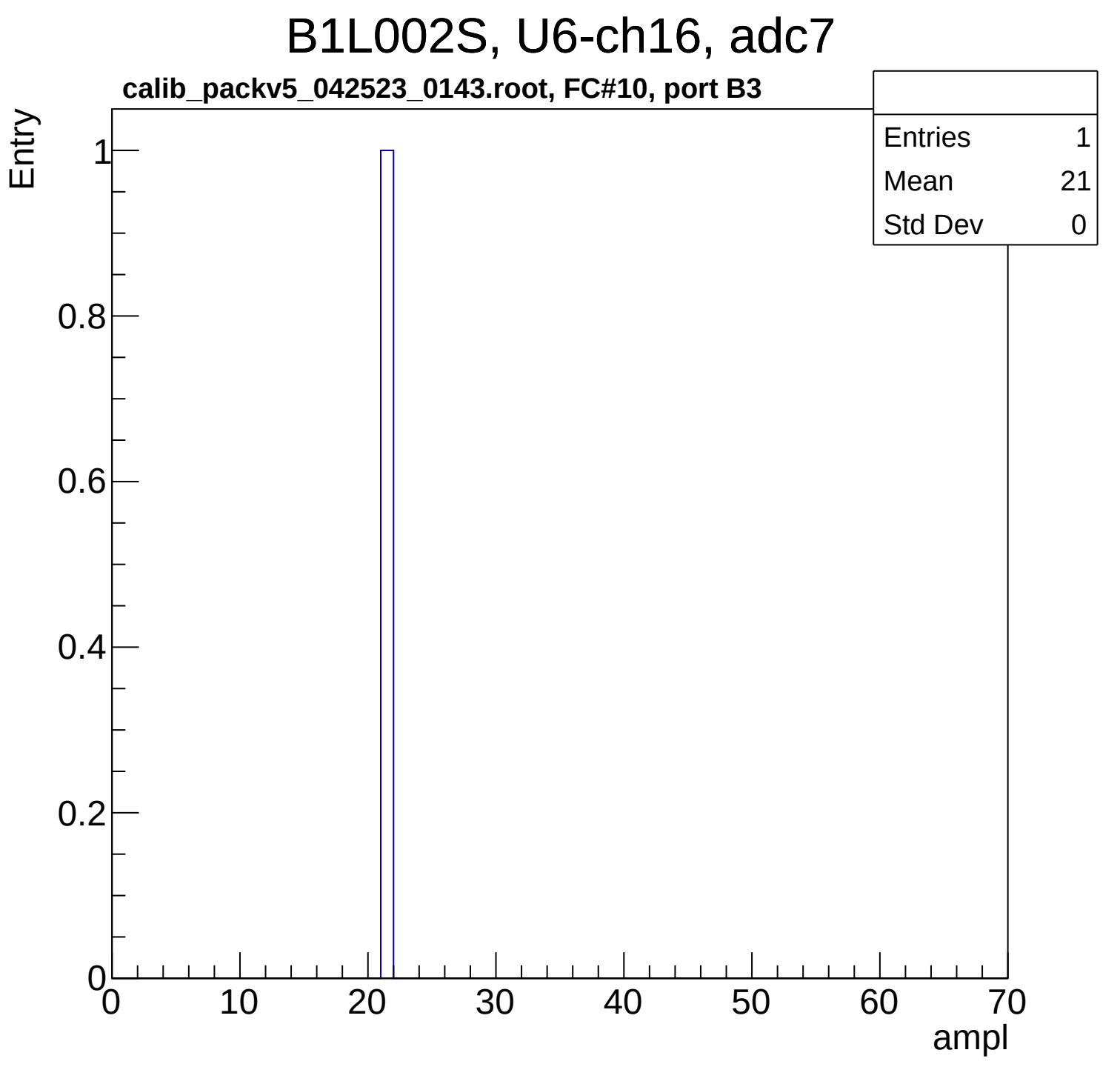

B1L002S, U6-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.1
Std Dev 2.757
Gaus mean : 41.2667
Gaus Width: 3.4085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

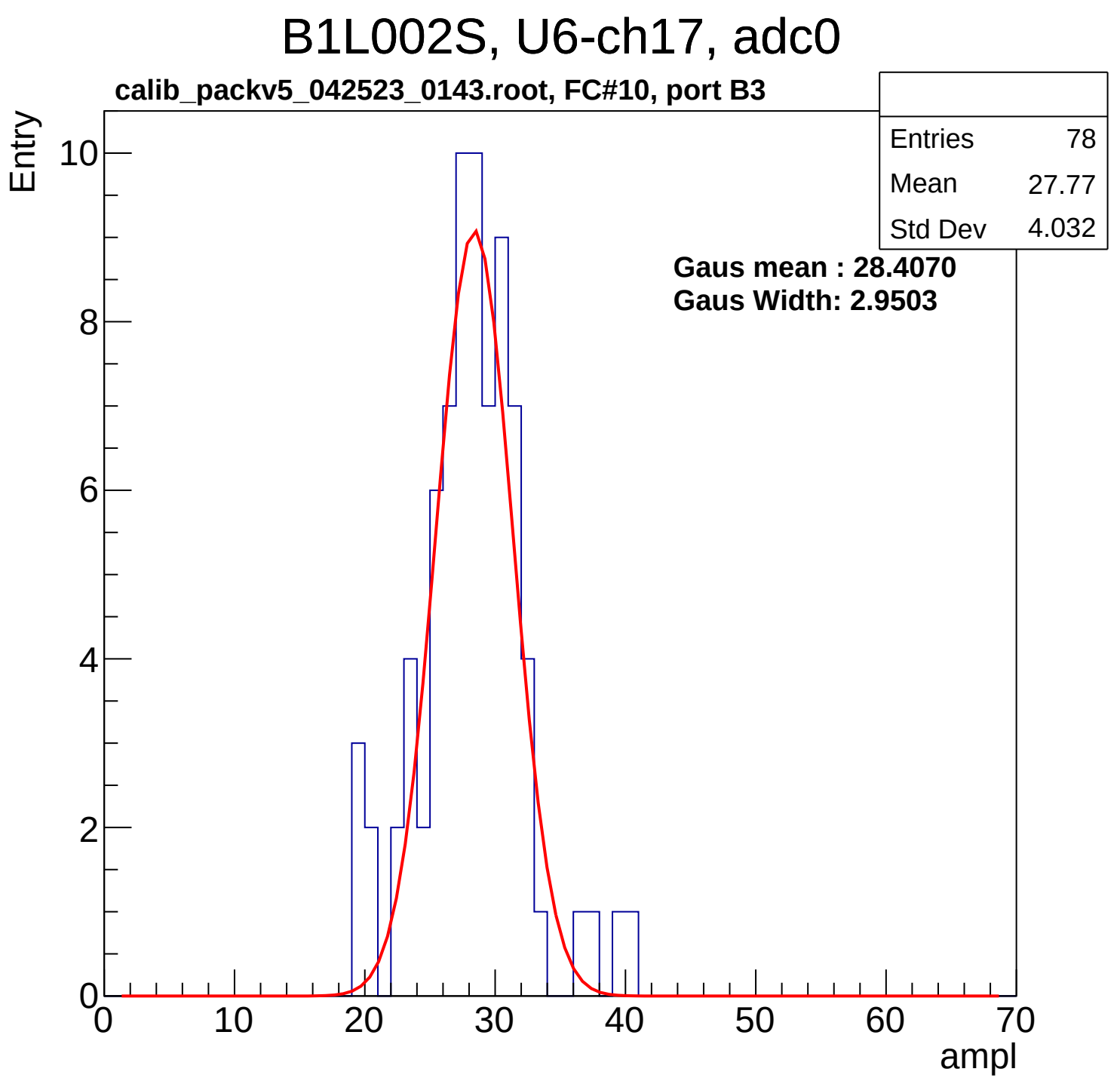

B1L002S, U6-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.38
Std Dev 2.786
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

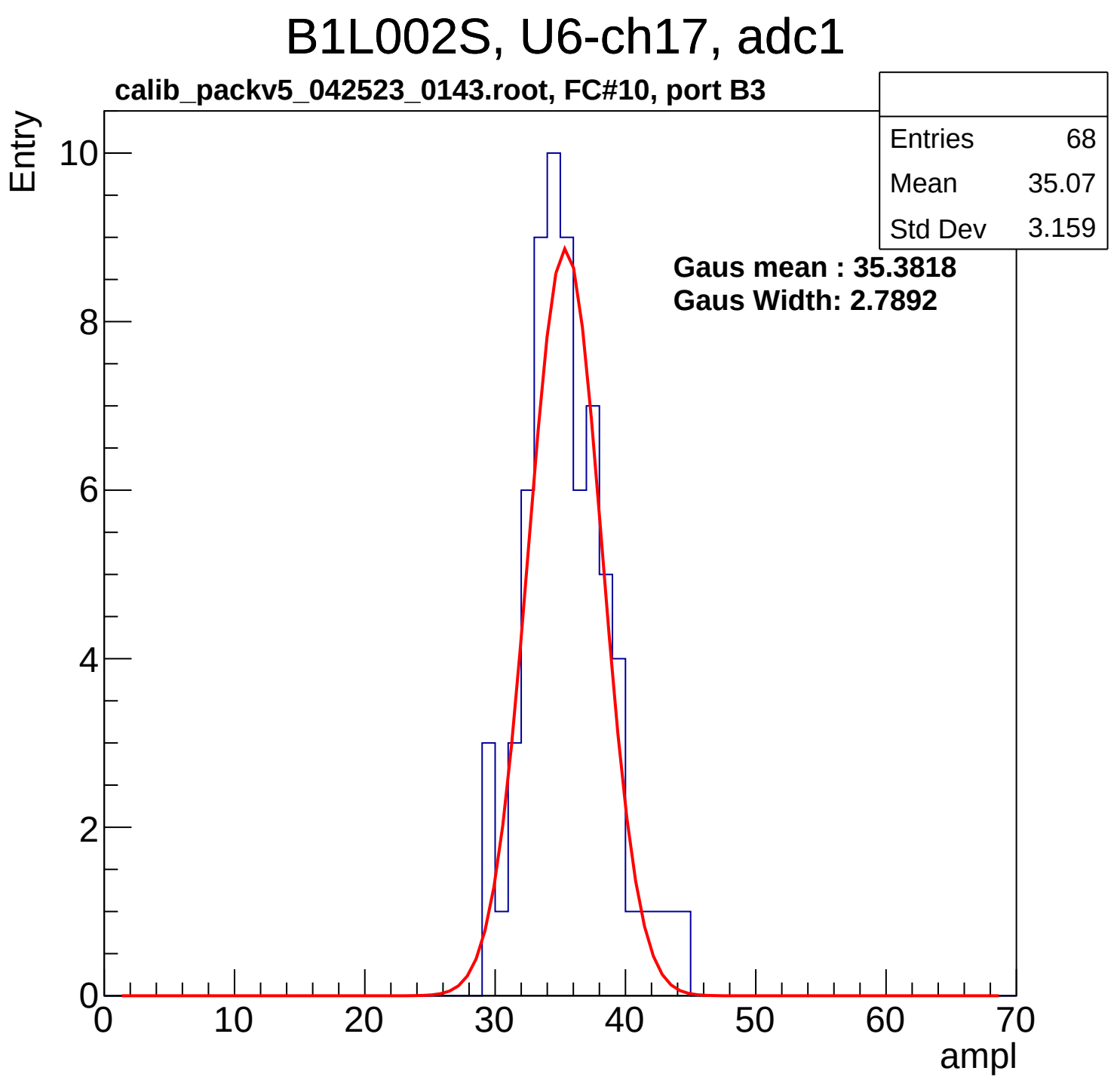

B1L002S, U6-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.48
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

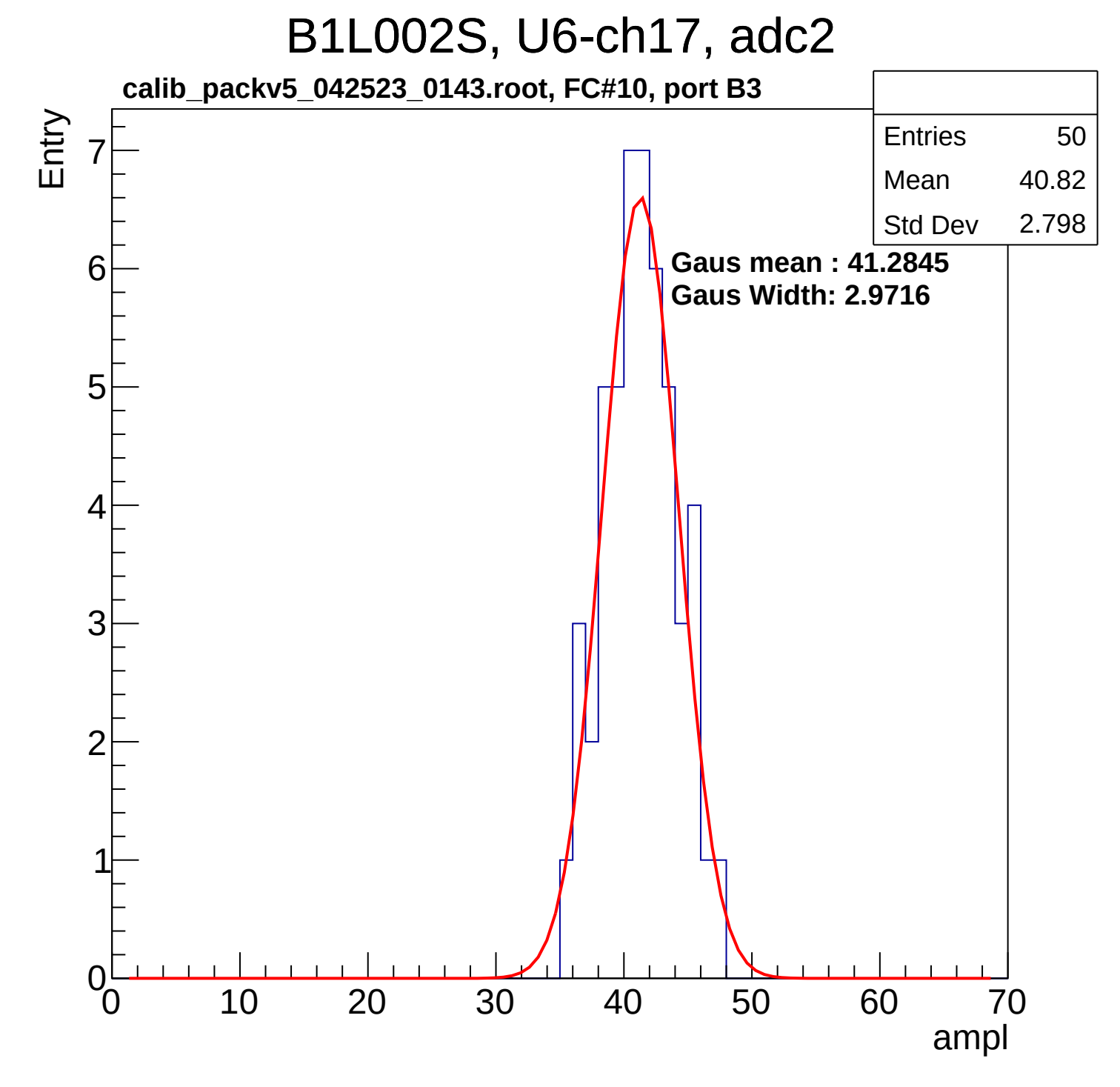

B1L002S, U6-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 56.95
Std Dev 8.011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

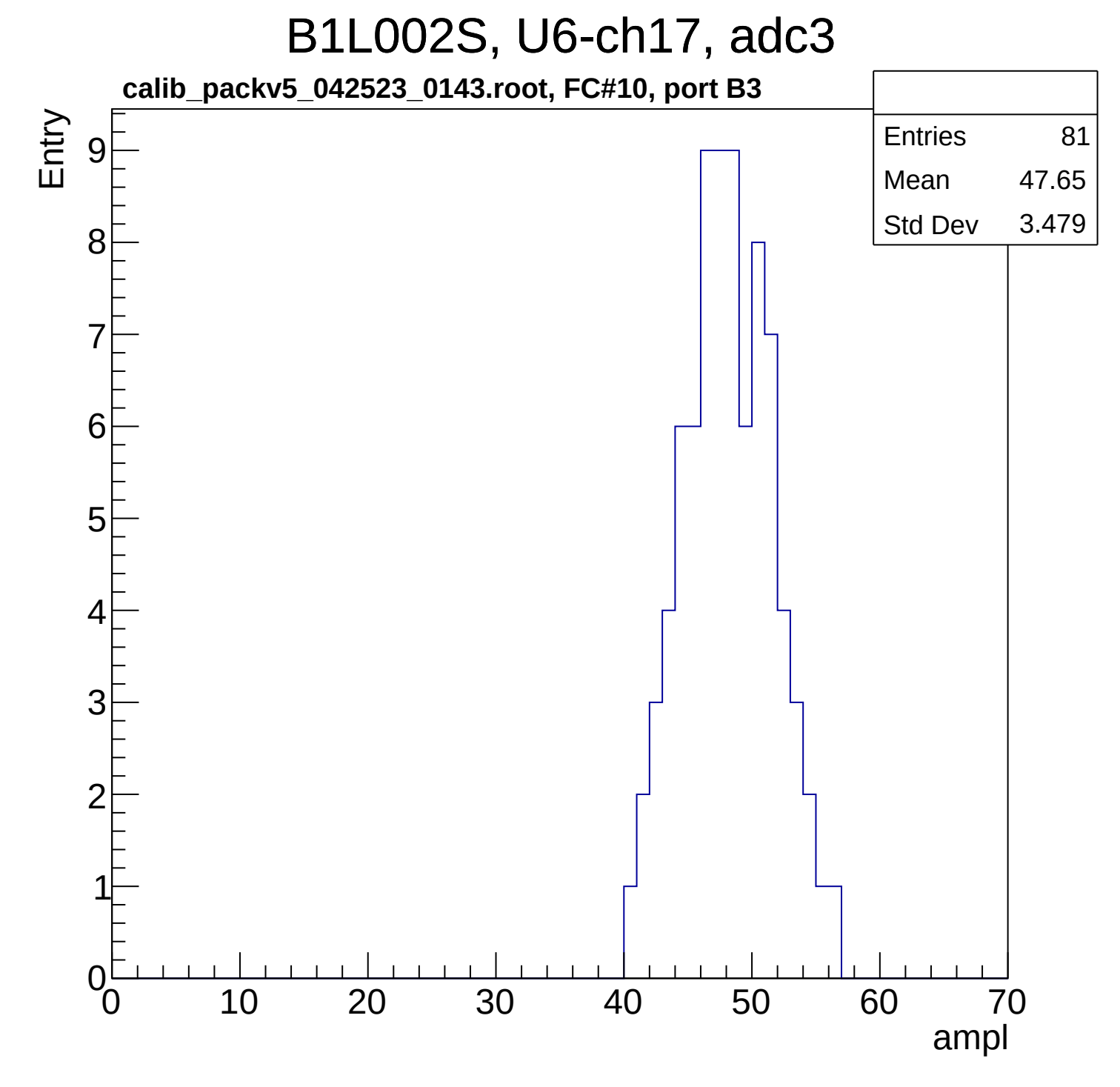

B1L002S, U6-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.5
Std Dev 1.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

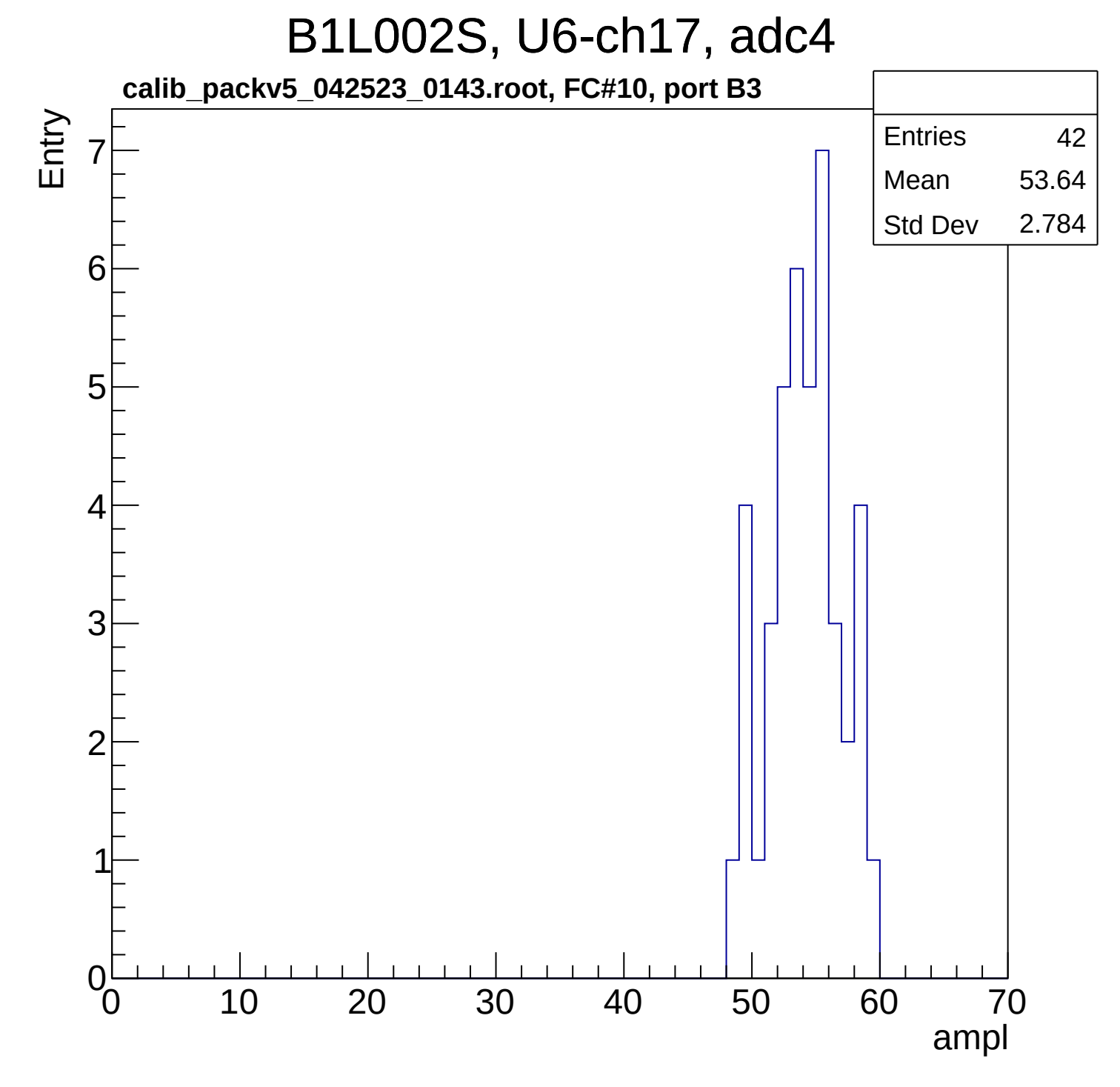

B1L002S, U6-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

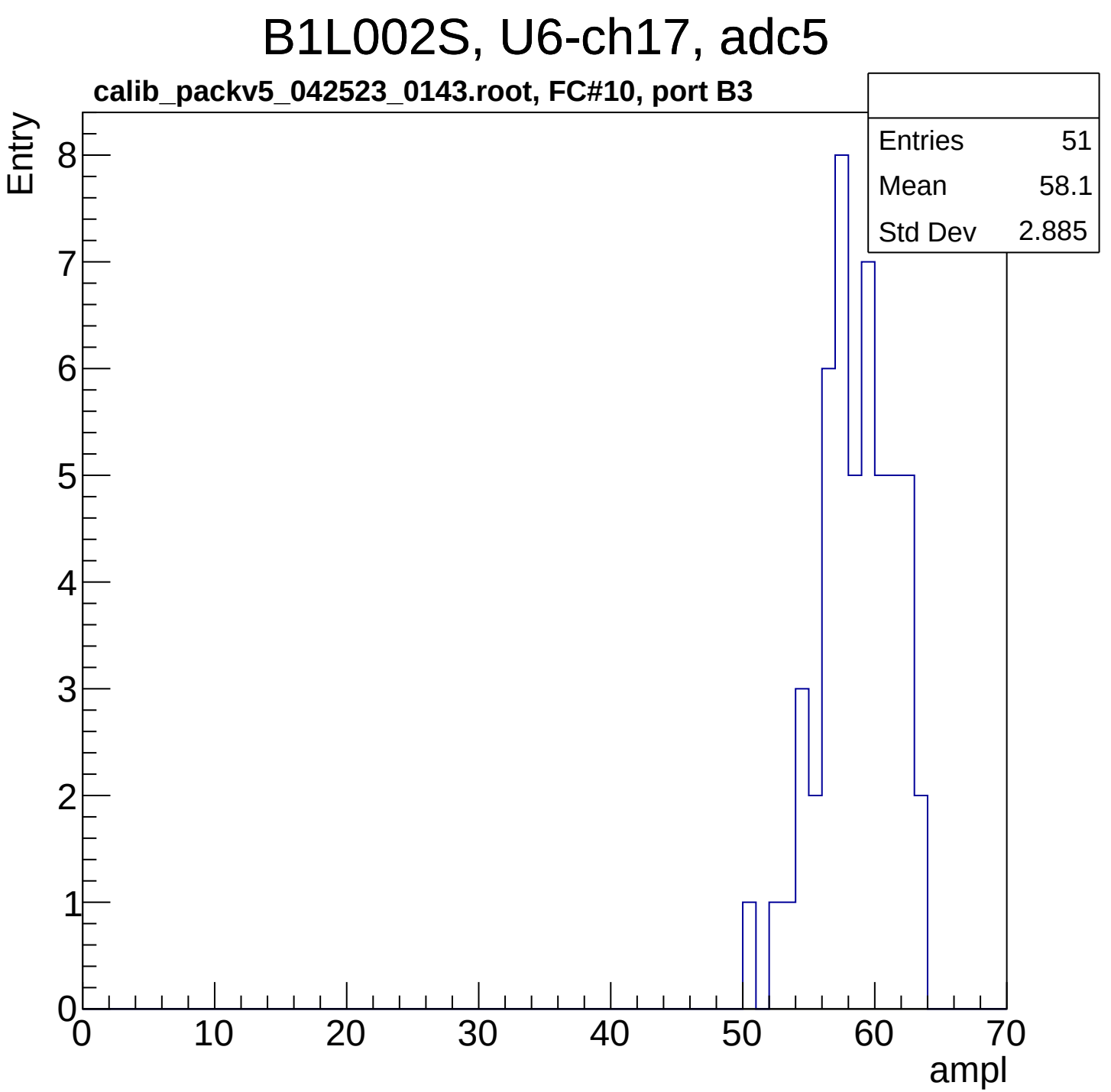

B1L002S, U6-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.77
Std Dev 4.032
Gaus mean : 28.4070
Gaus Width: 2.9503
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

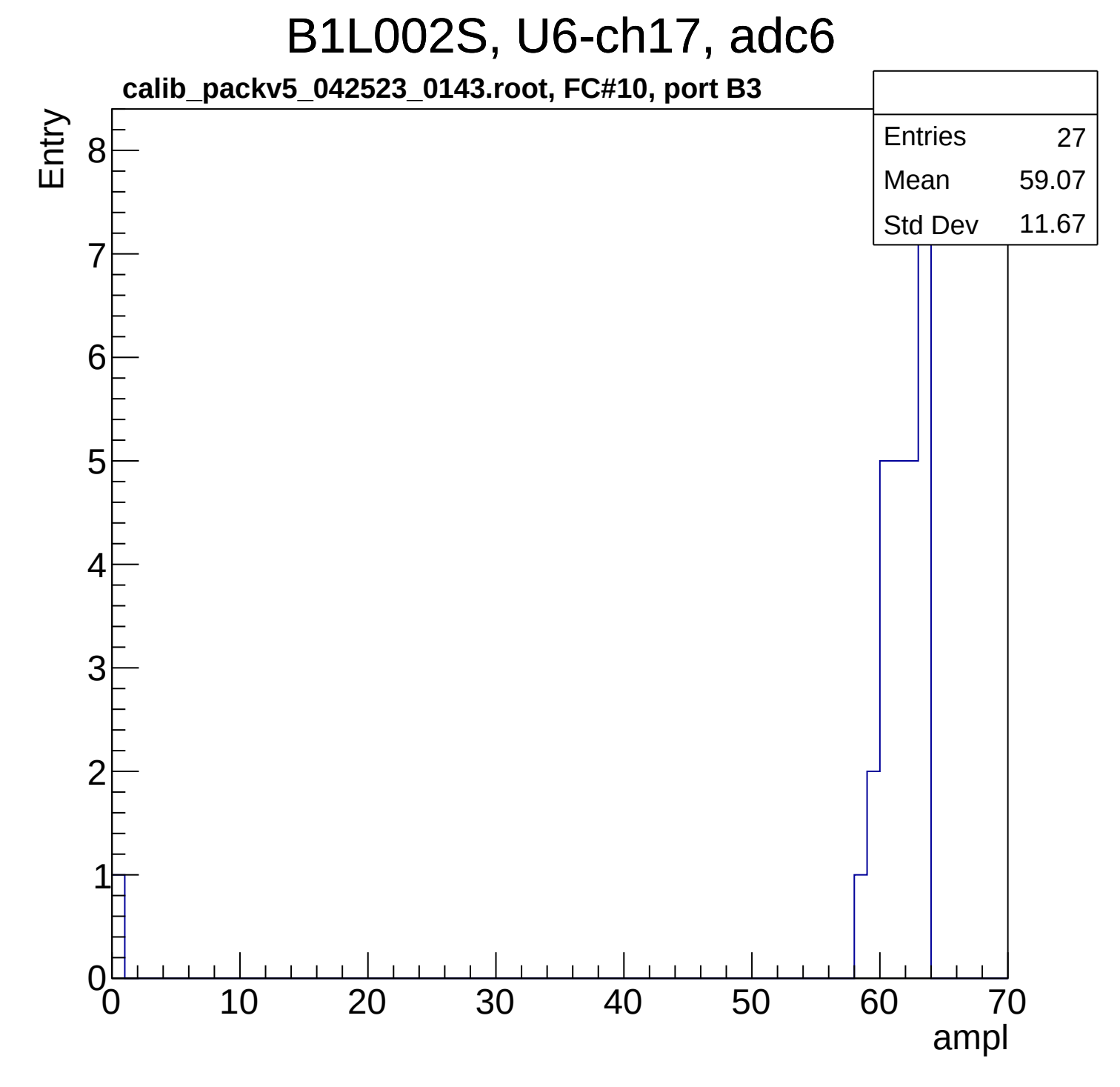

B1L002S, U6-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.07
Std Dev 3.159
Gaus mean : 35.3818
Gaus Width: 2.7892
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

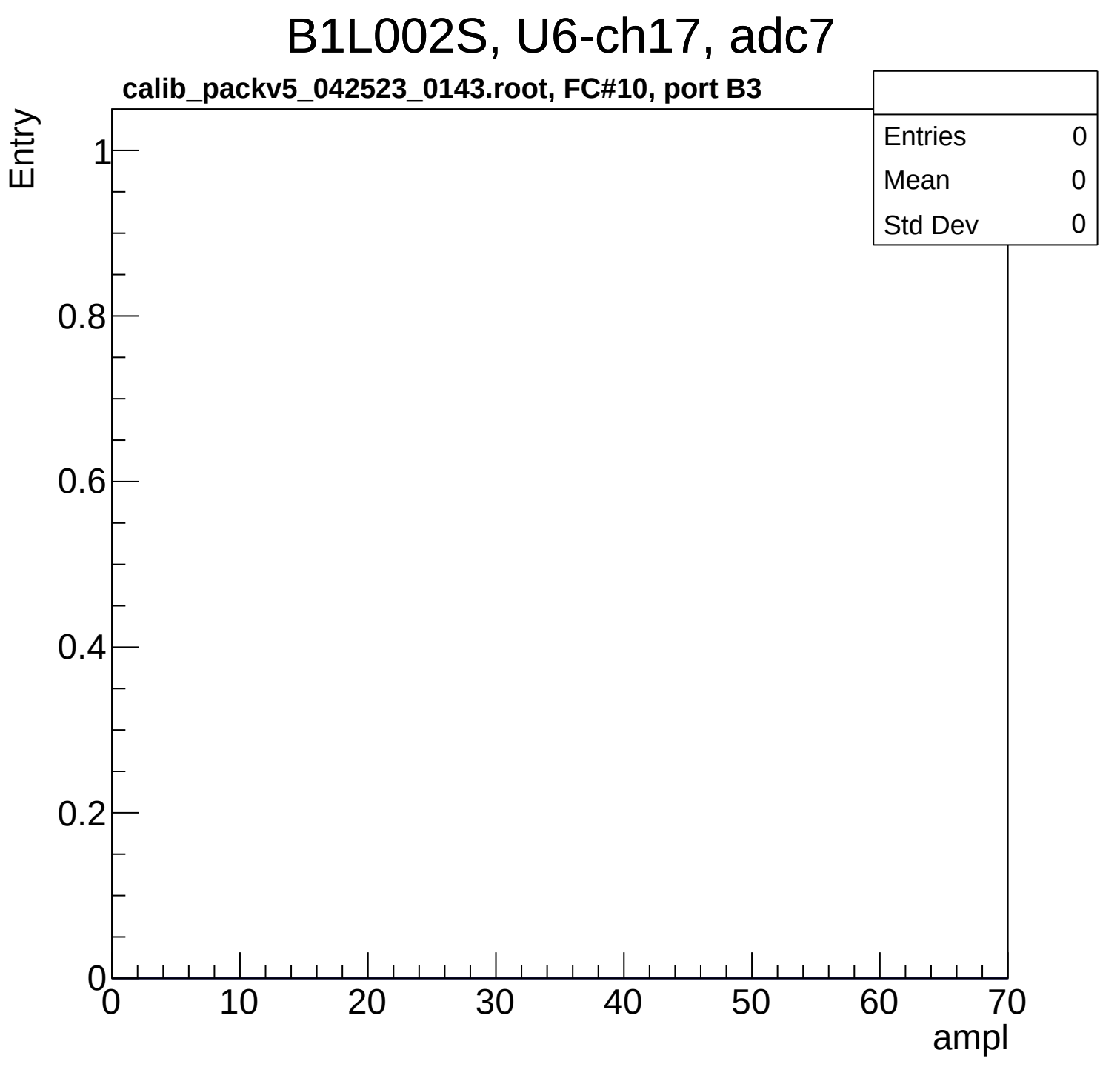

B1L002S, U6-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.82
Std Dev 2.798
Gaus mean : 41.2845
Gaus Width: 2.9716
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

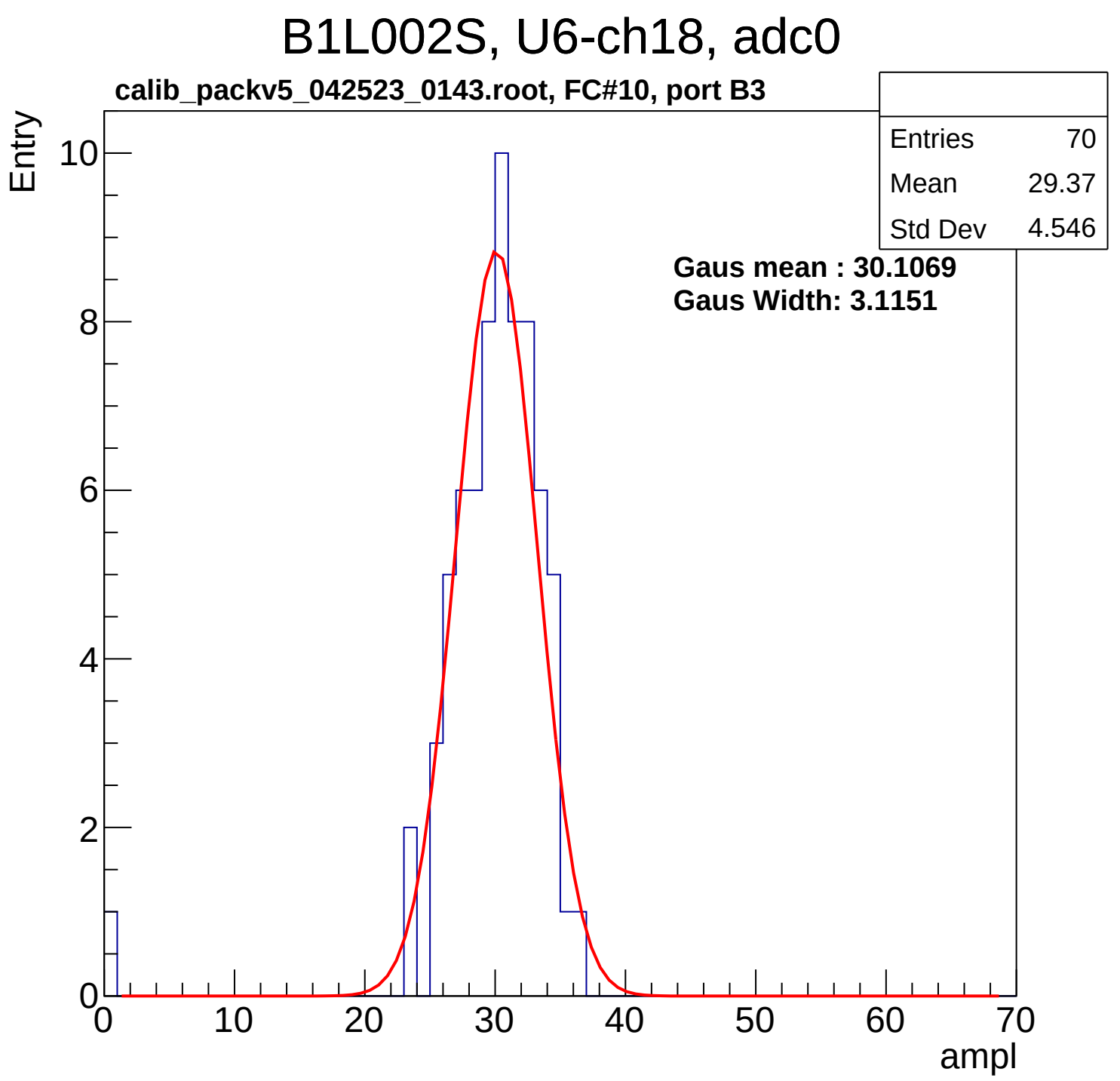

B1L002S, U6-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 47.65
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

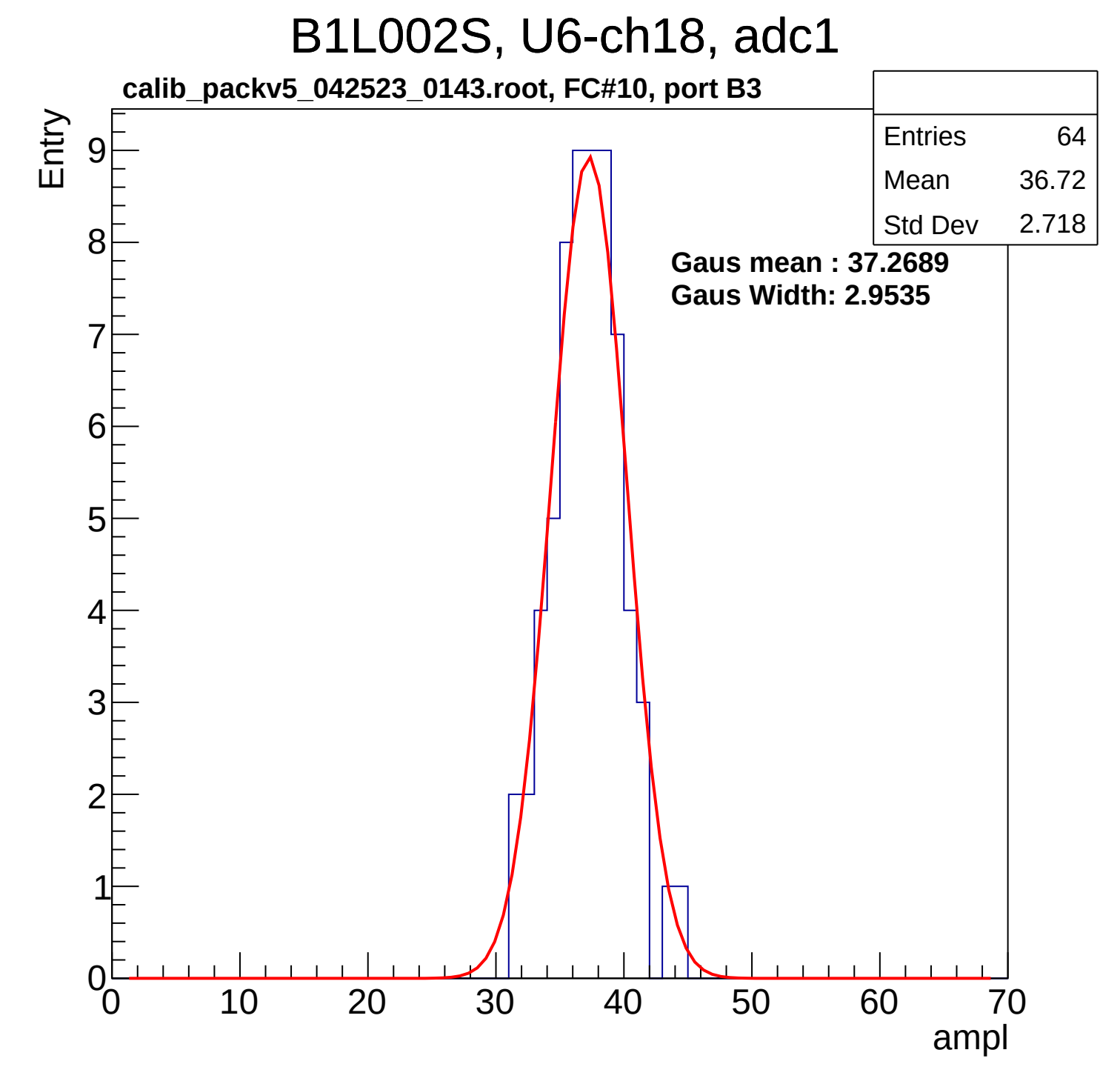

B1L002S, U6-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 53.64
Std Dev 2.784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

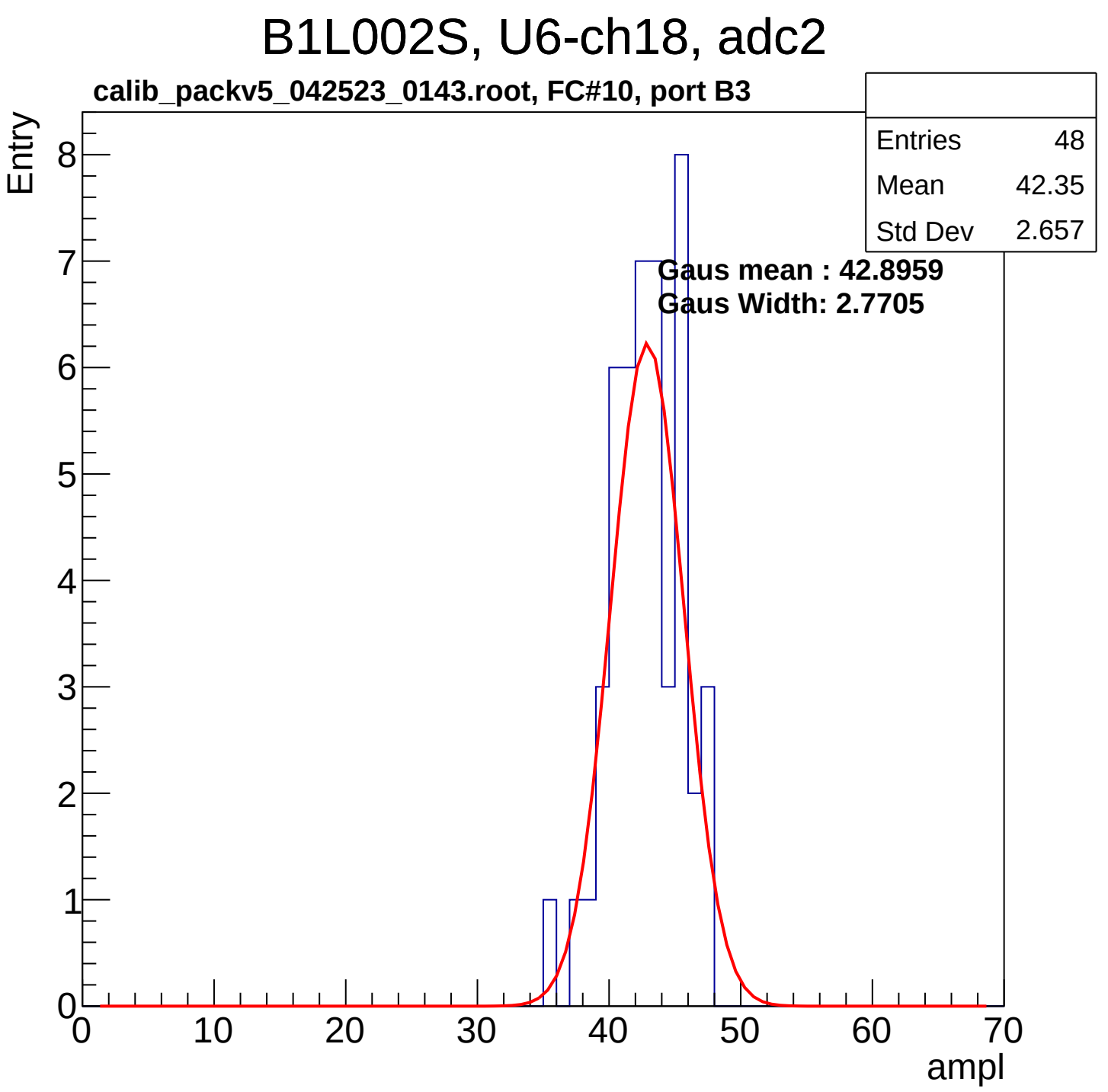

B1L002S, U6-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.1
Std Dev 2.885
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

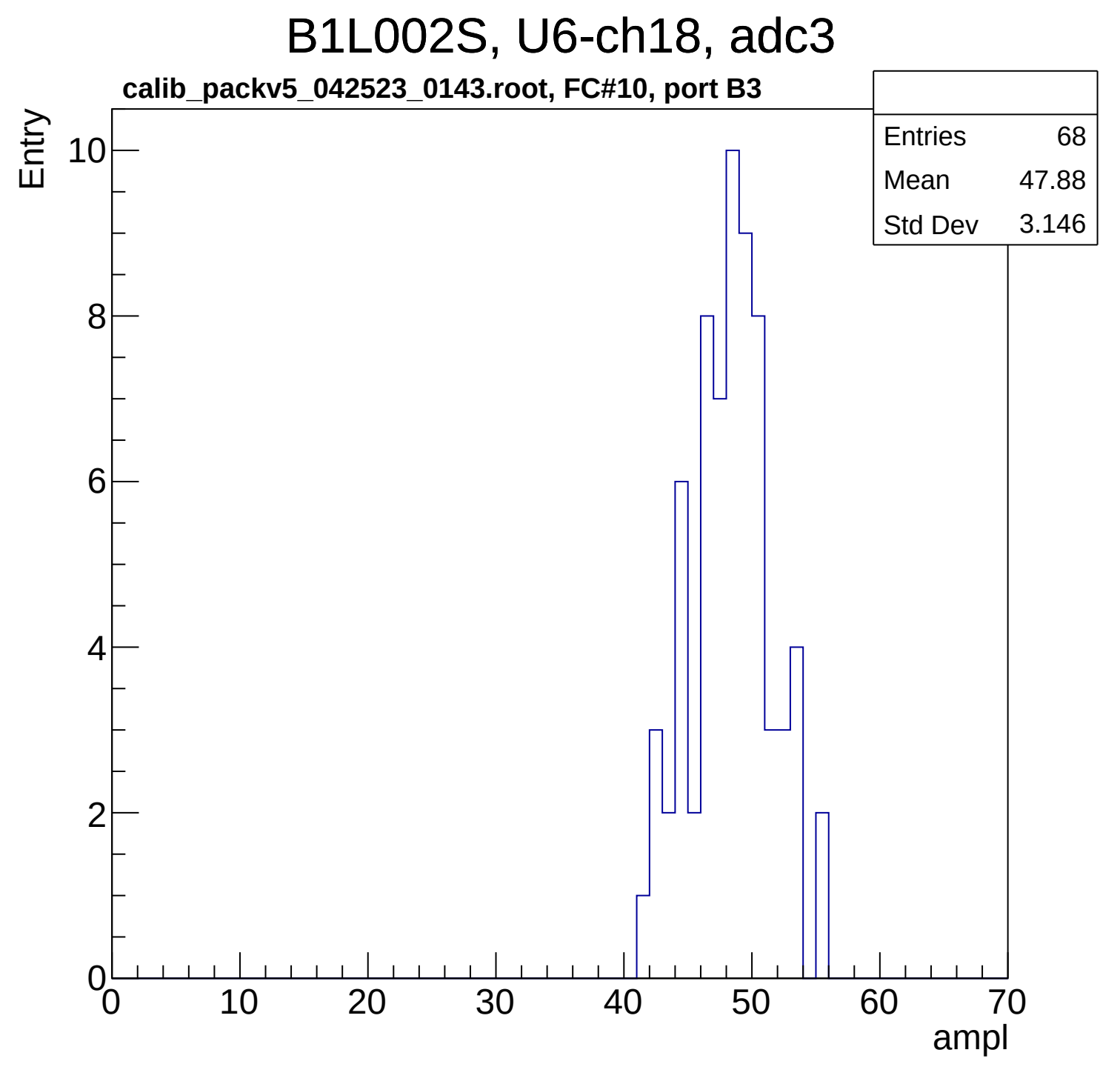

B1L002S, U6-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.07
Std Dev 11.67
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

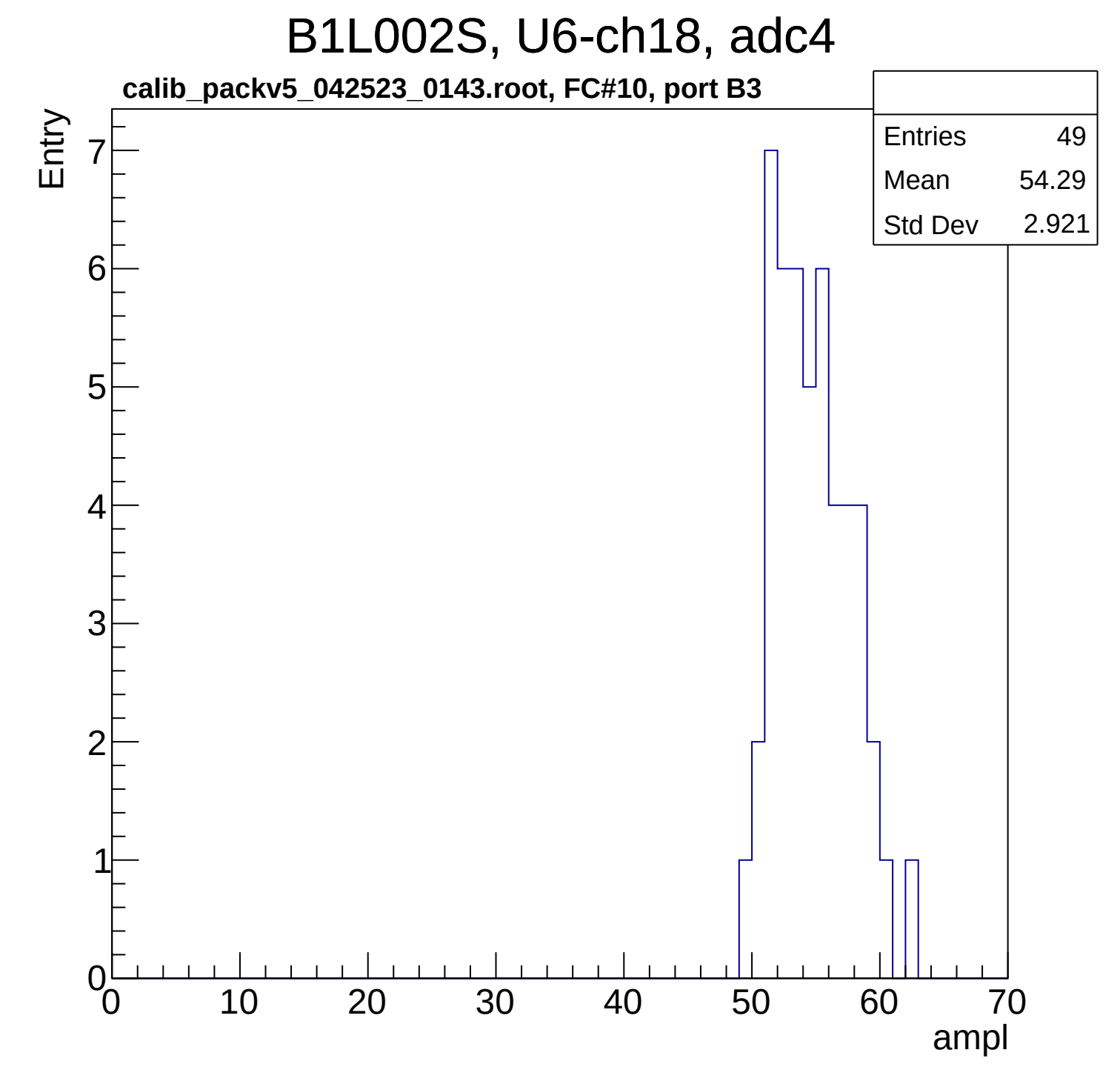

B1L002S, U6-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

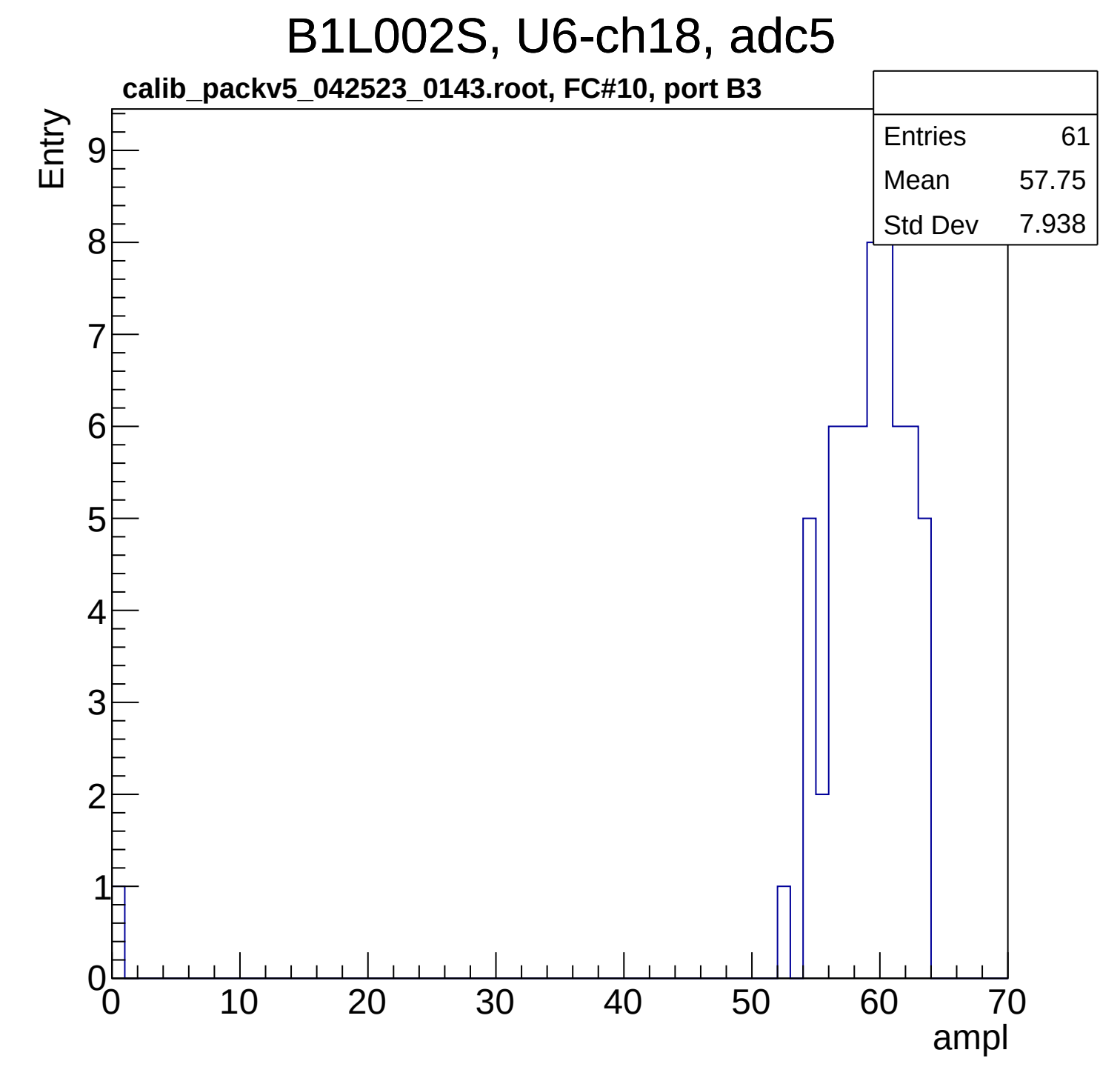

B1L002S, U6-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.37
Std Dev 4.546
Gaus mean : 30.1069
Gaus Width: 3.1151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

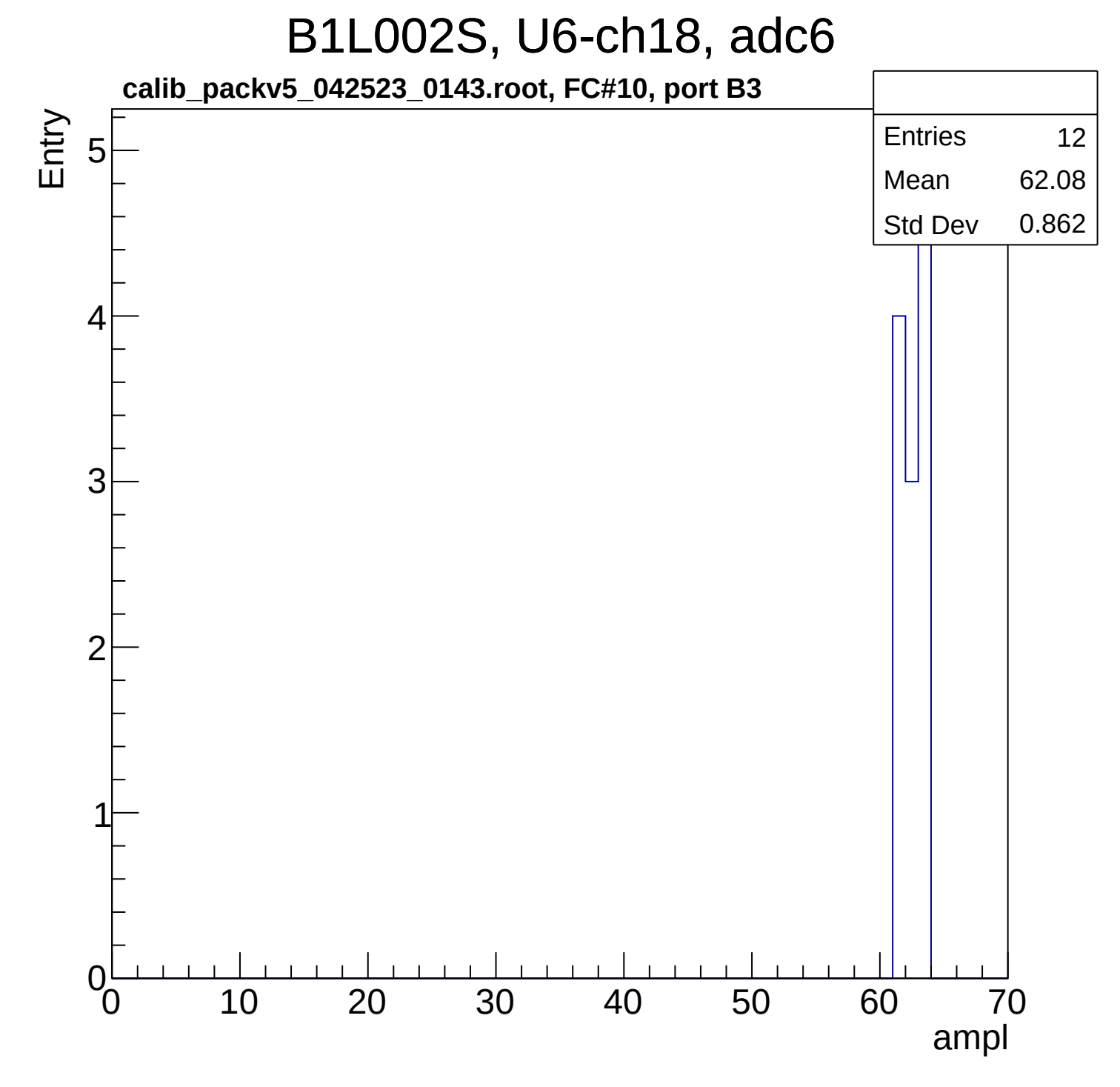

B1L002S, U6-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 36.72
Std Dev 2.718
Gaus mean : 37.2689
Gaus Width: 2.9535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

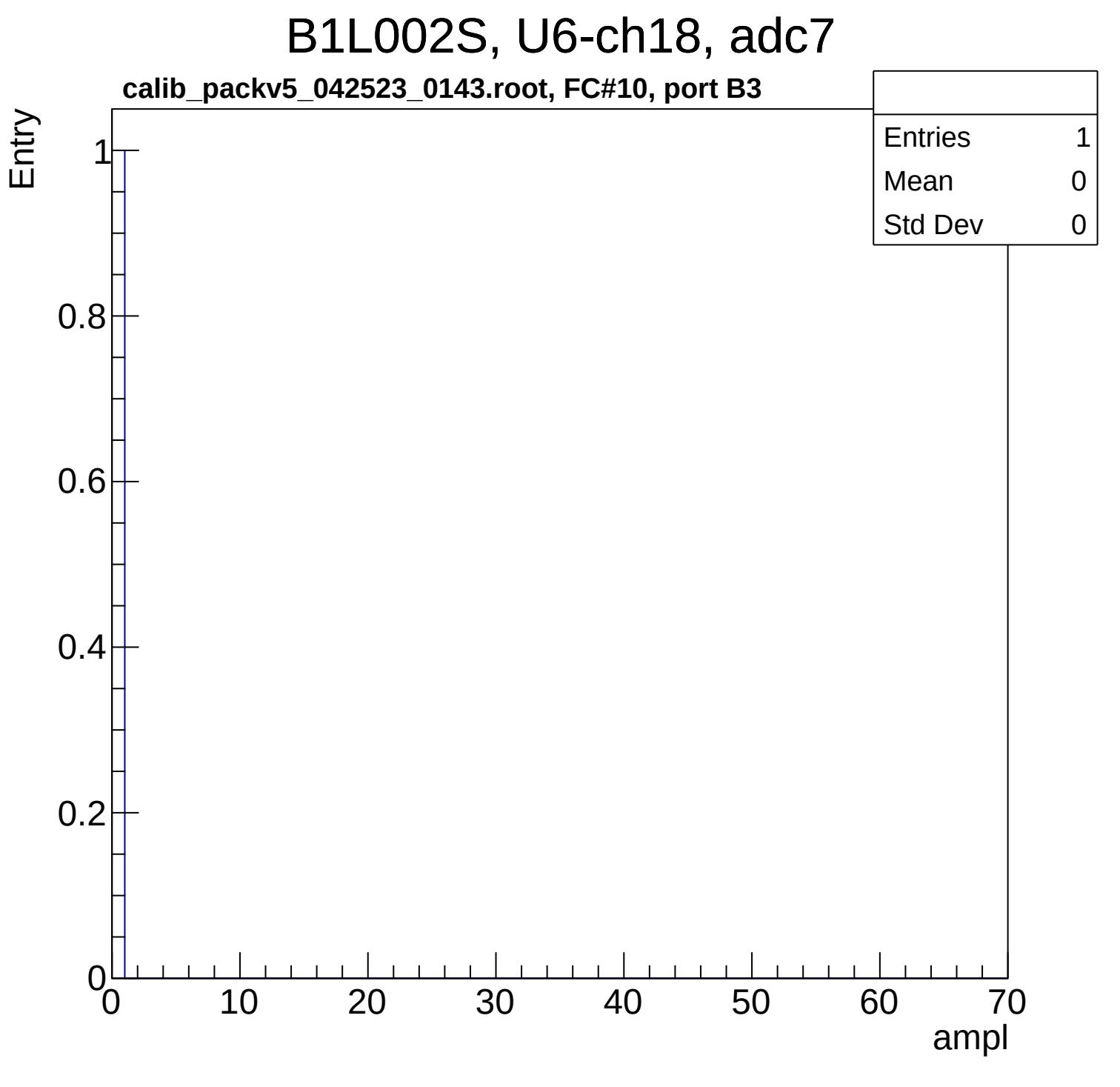

B1L002S, U6-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 42.35
Std Dev 2.657
Gaus mean : 42.8959
Gaus Width: 2.7705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

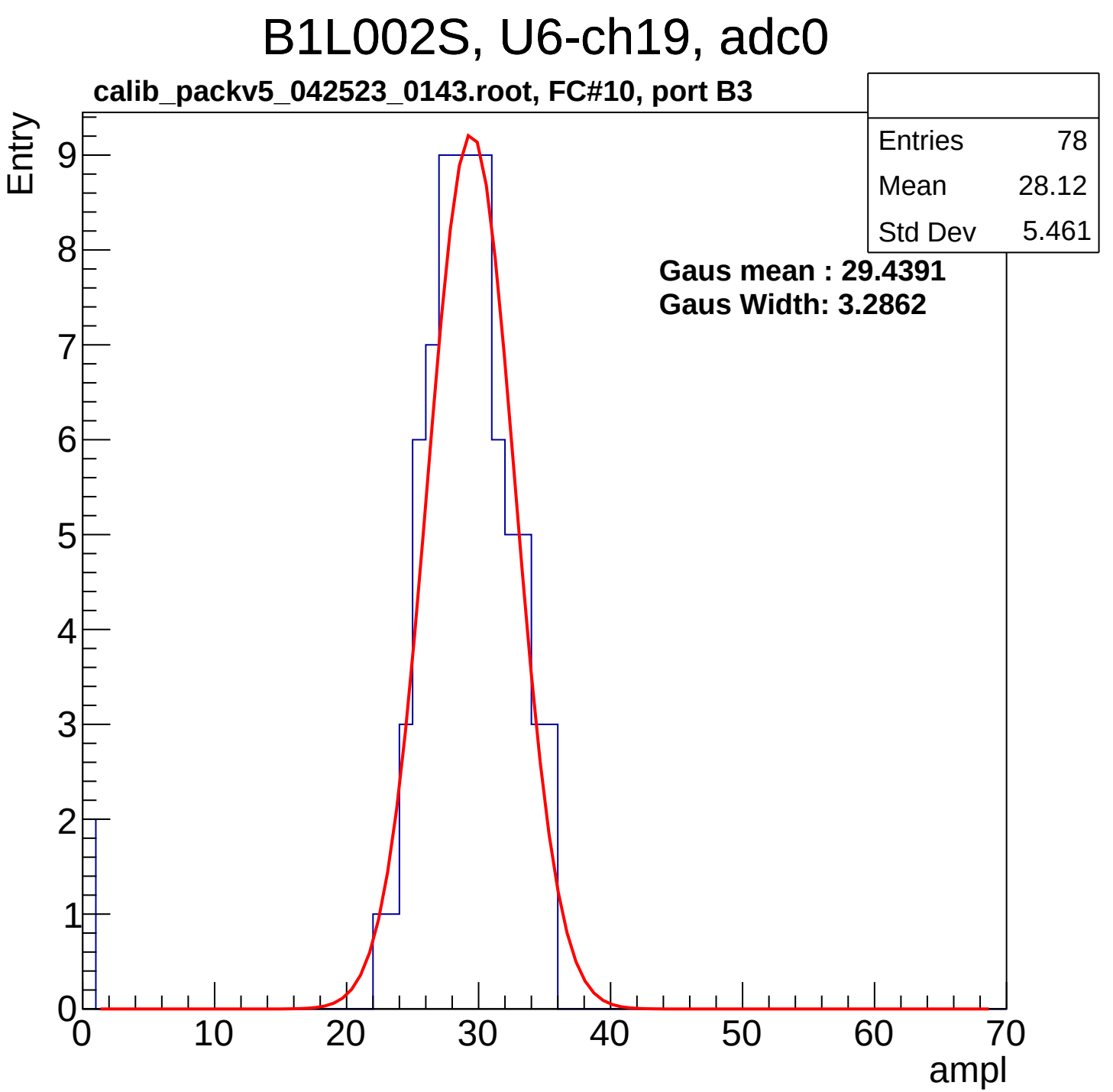

B1L002S, U6-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.88
Std Dev 3.146
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

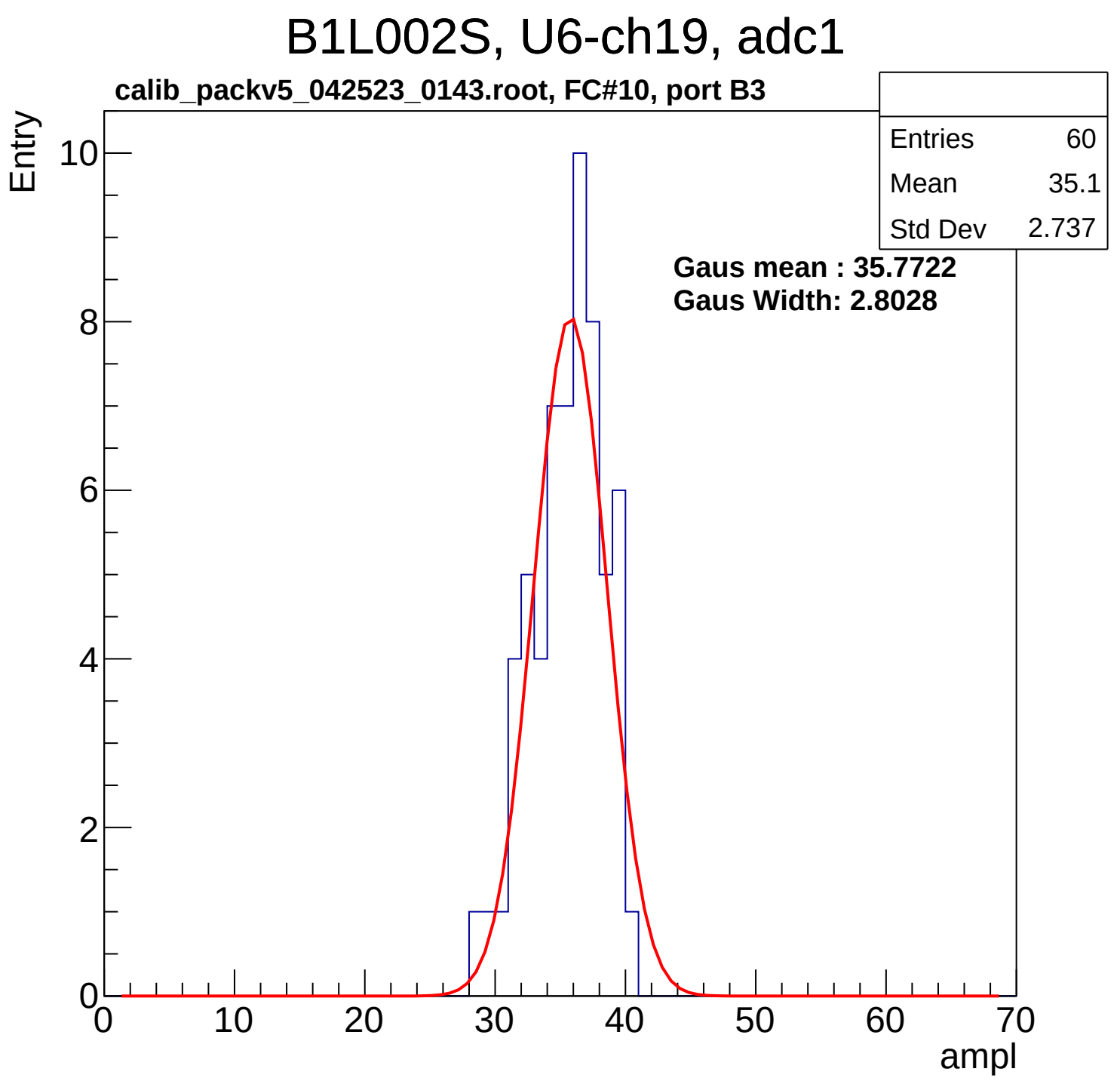

B1L002S, U6-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 54.29
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

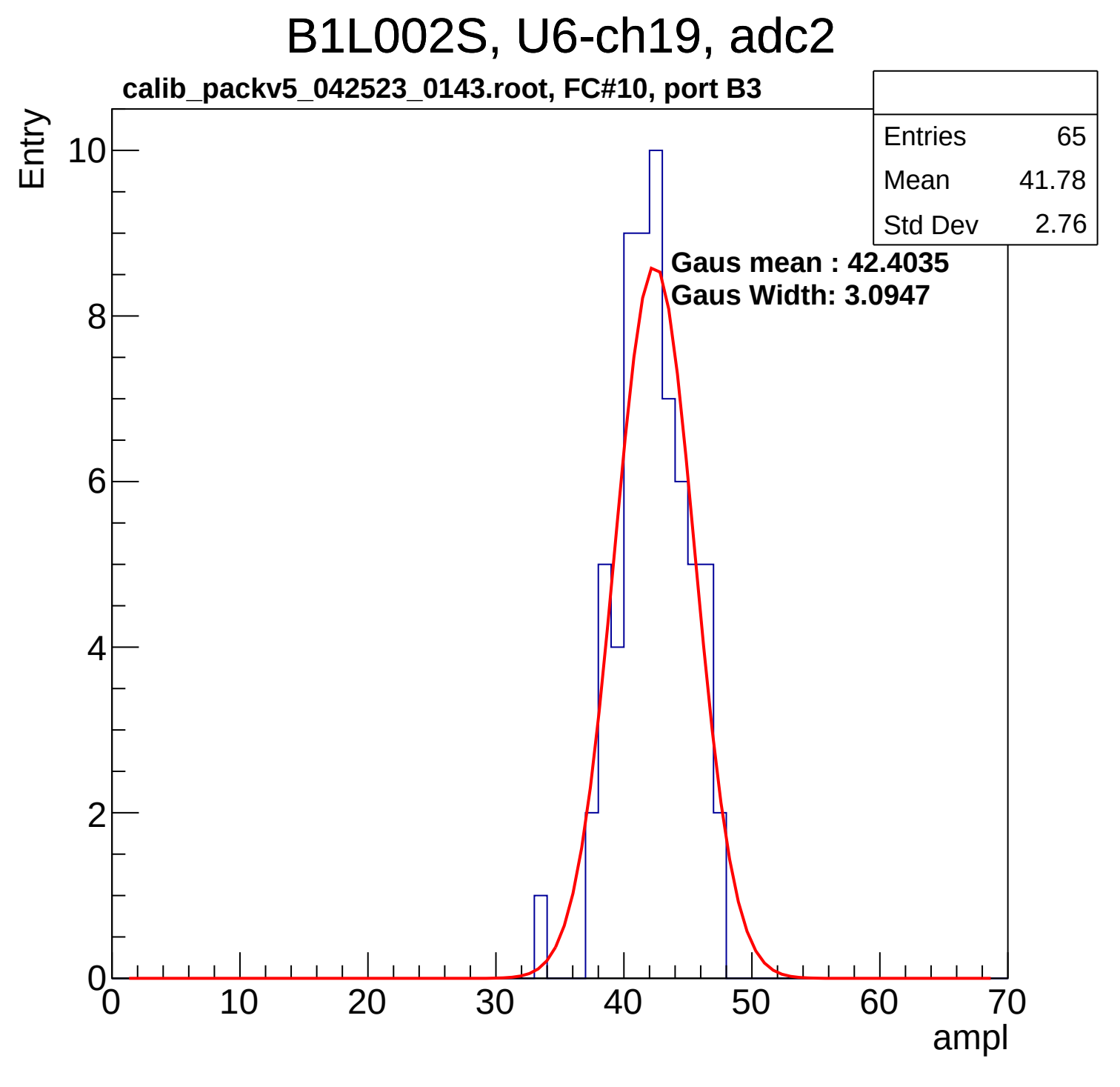

B1L002S, U6-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.75
Std Dev 7.938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

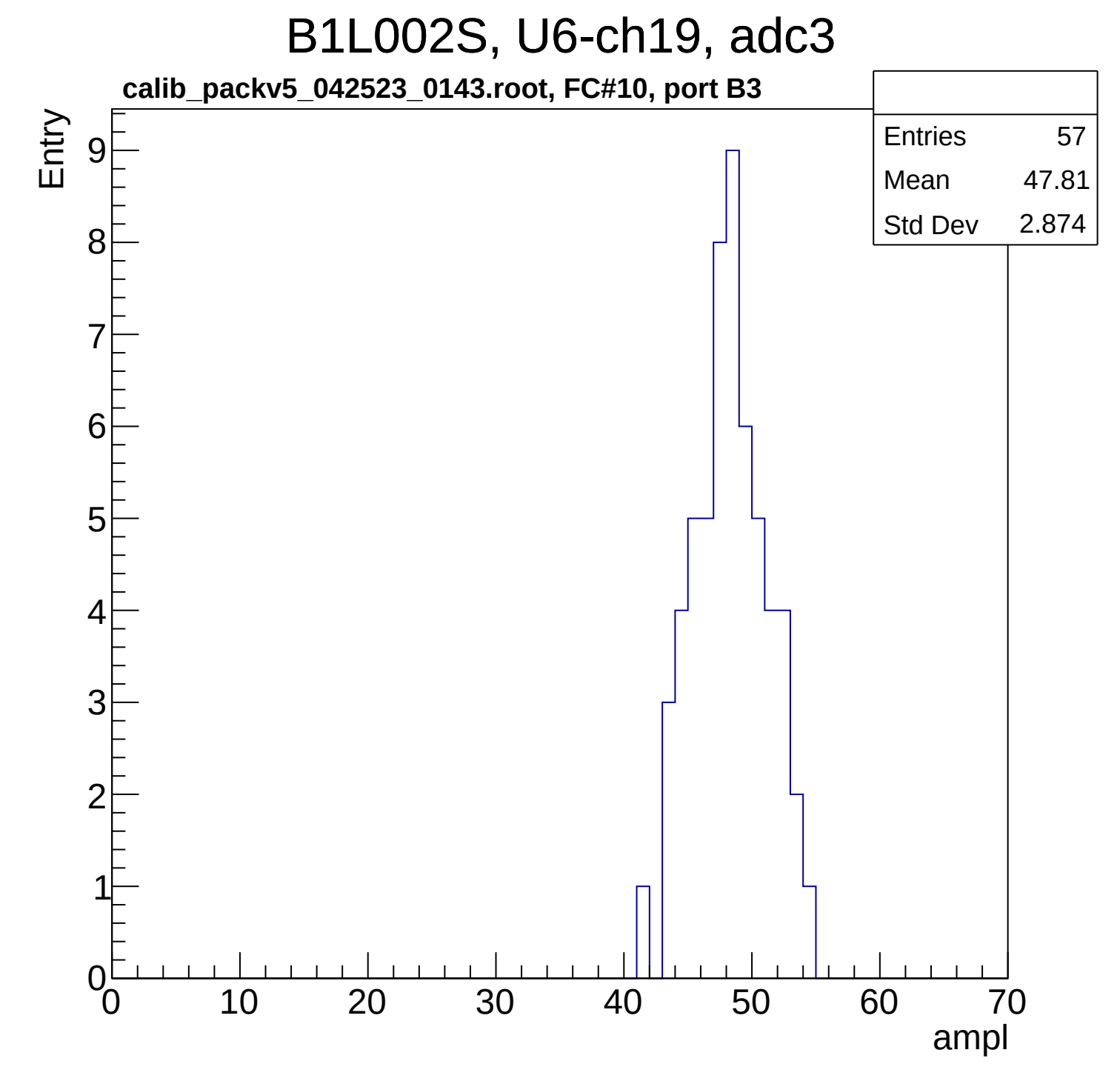

B1L002S, U6-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62.08
Std Dev 0.862
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

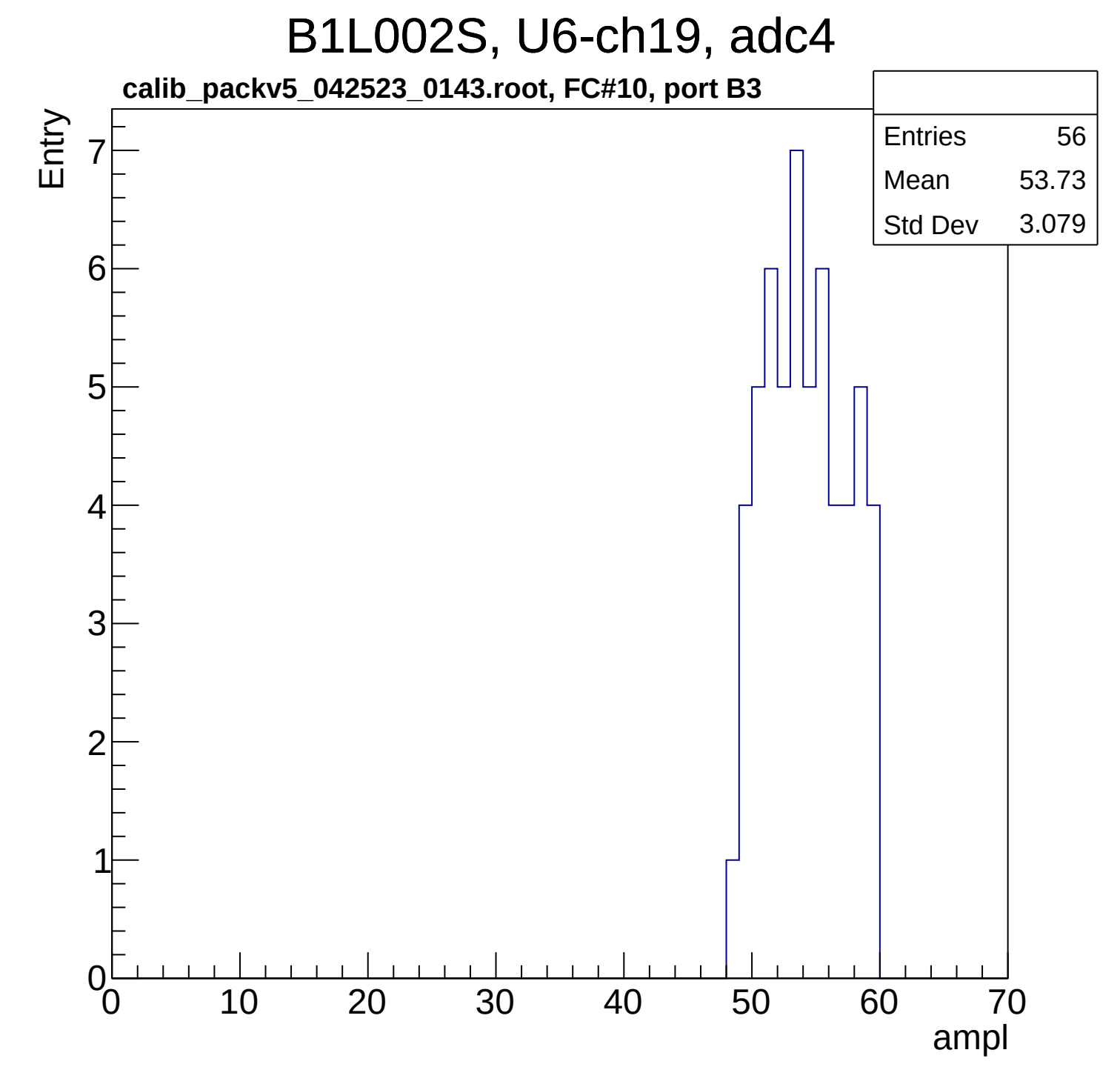

B1L002S, U6-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

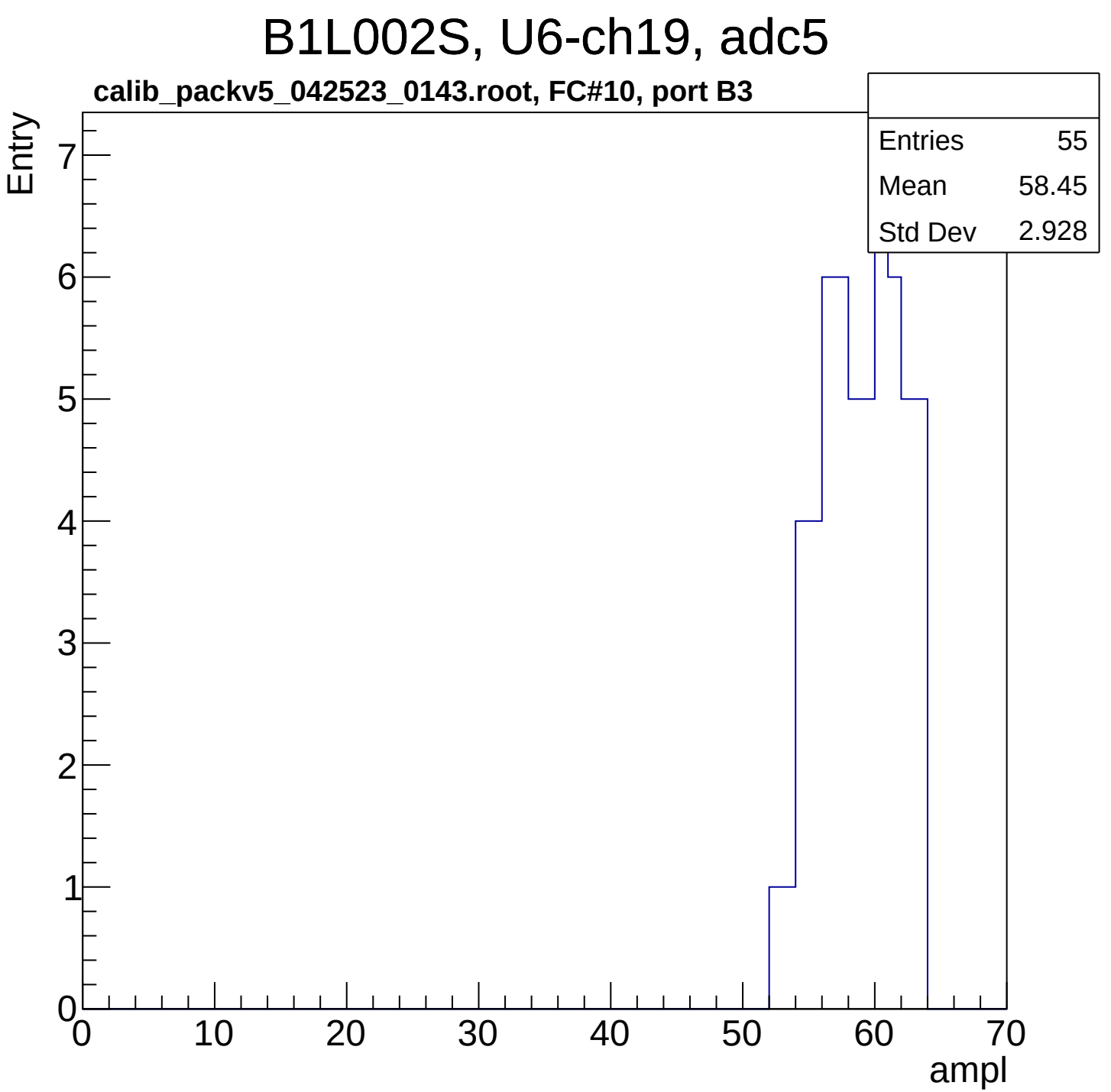

B1L002S, U6-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.12
Std Dev 5.461
Gaus mean : 29.4391
Gaus Width: 3.2862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

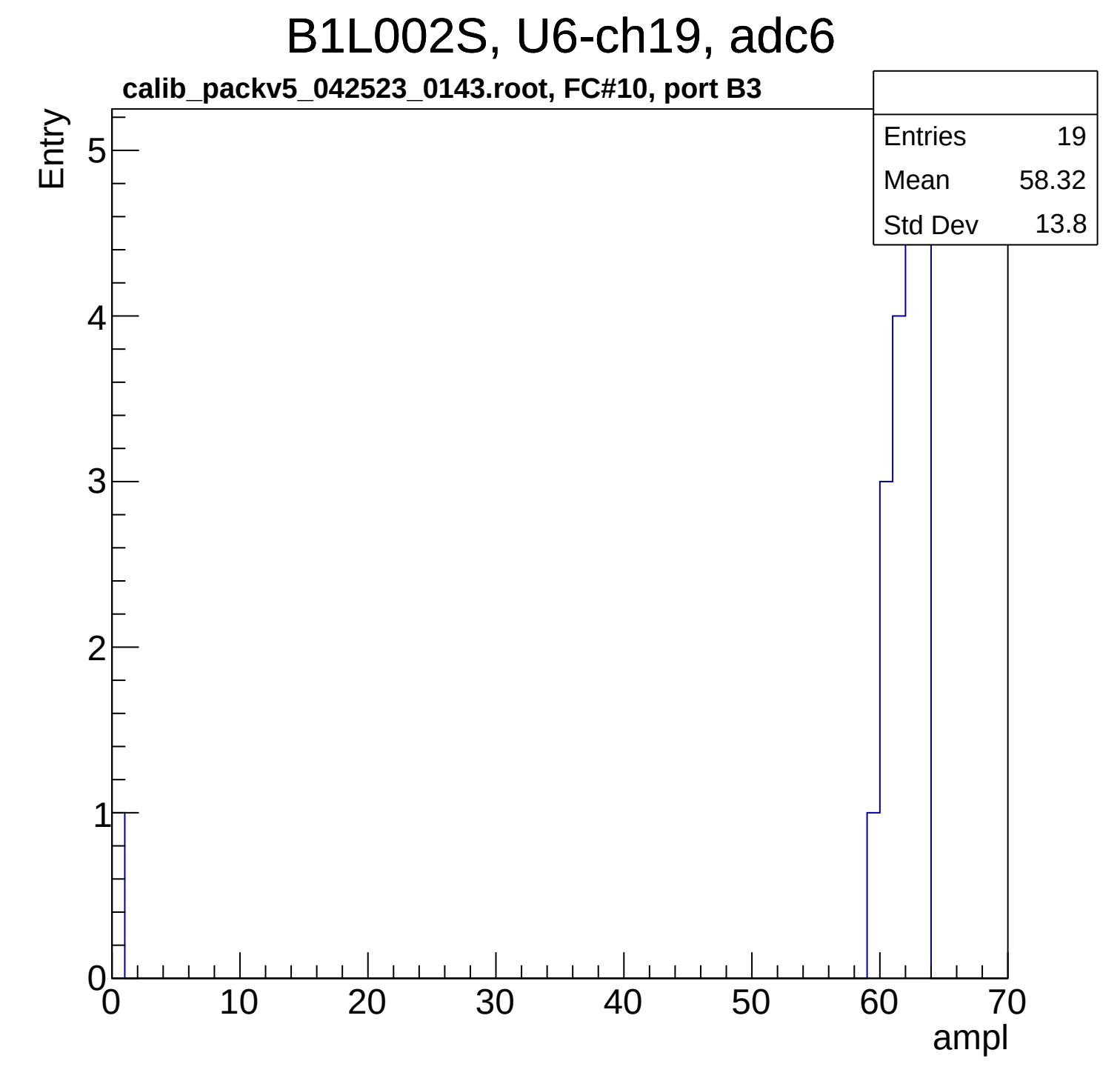

B1L002S, U6-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.1
Std Dev 2.737
Gaus mean : 35.7722
Gaus Width: 2.8028
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

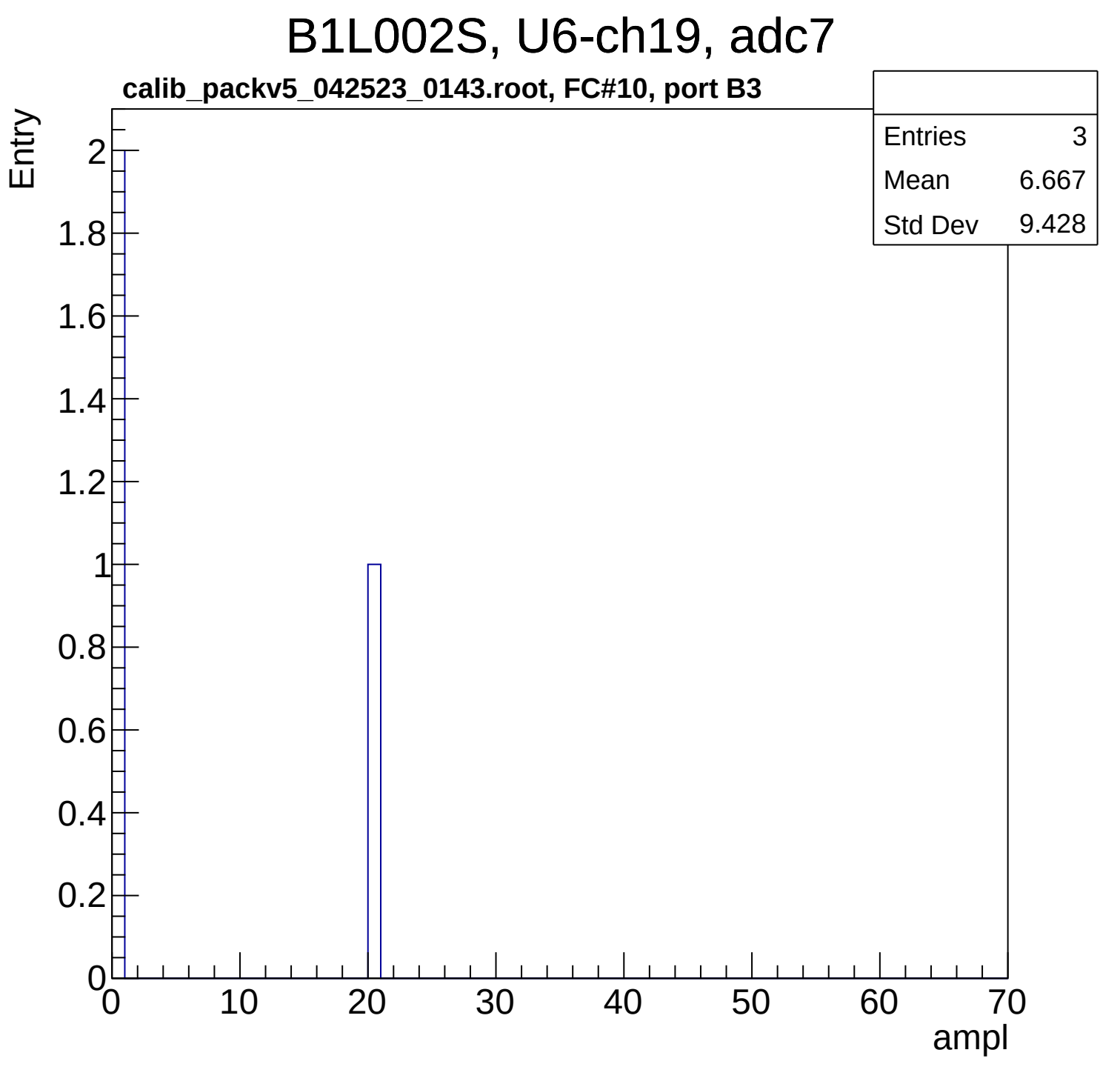

B1L002S, U6-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.78
Std Dev 2.76
Gaus mean : 42.4035
Gaus Width: 3.0947
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

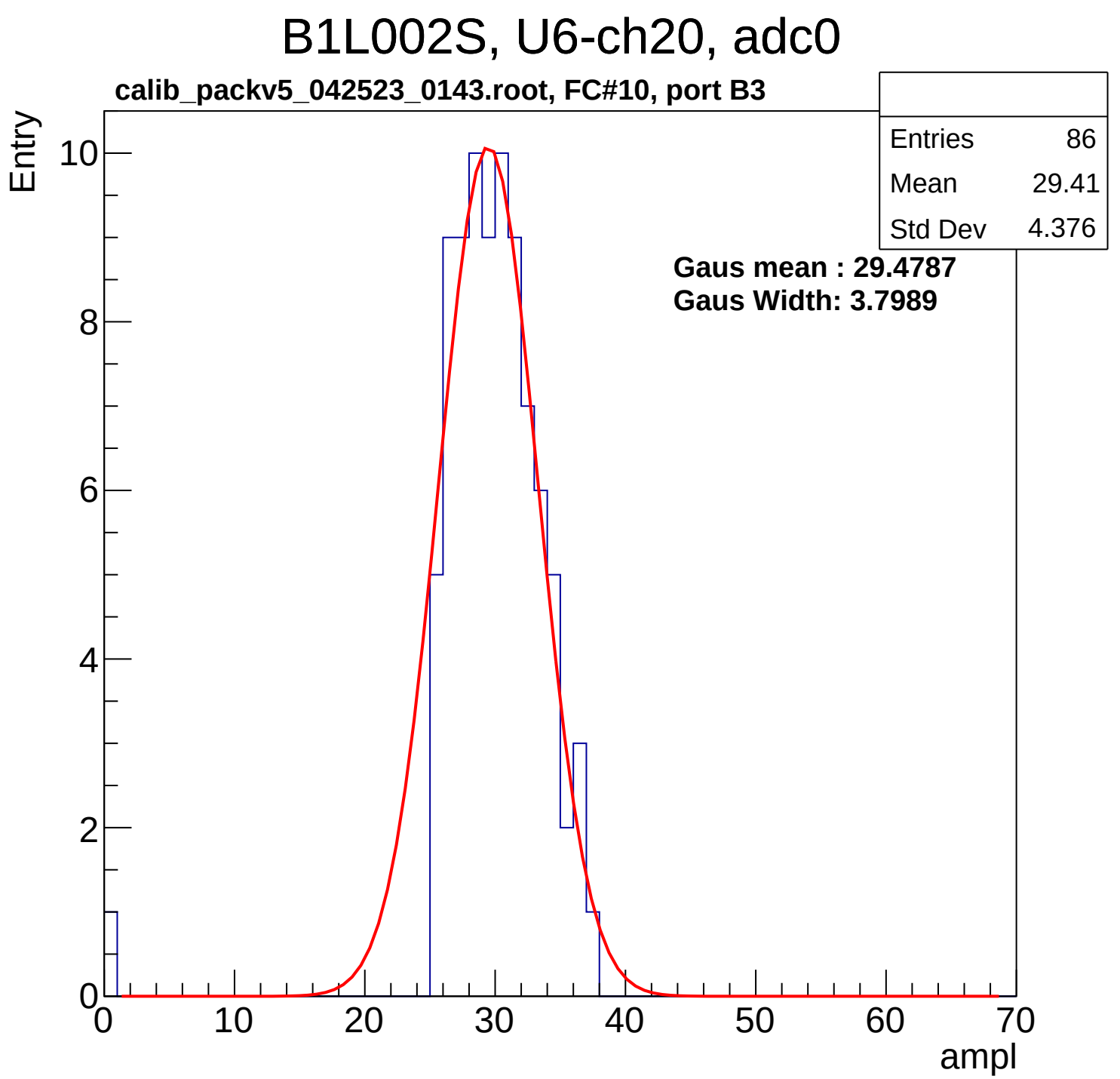

B1L002S, U6-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.81
Std Dev 2.874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

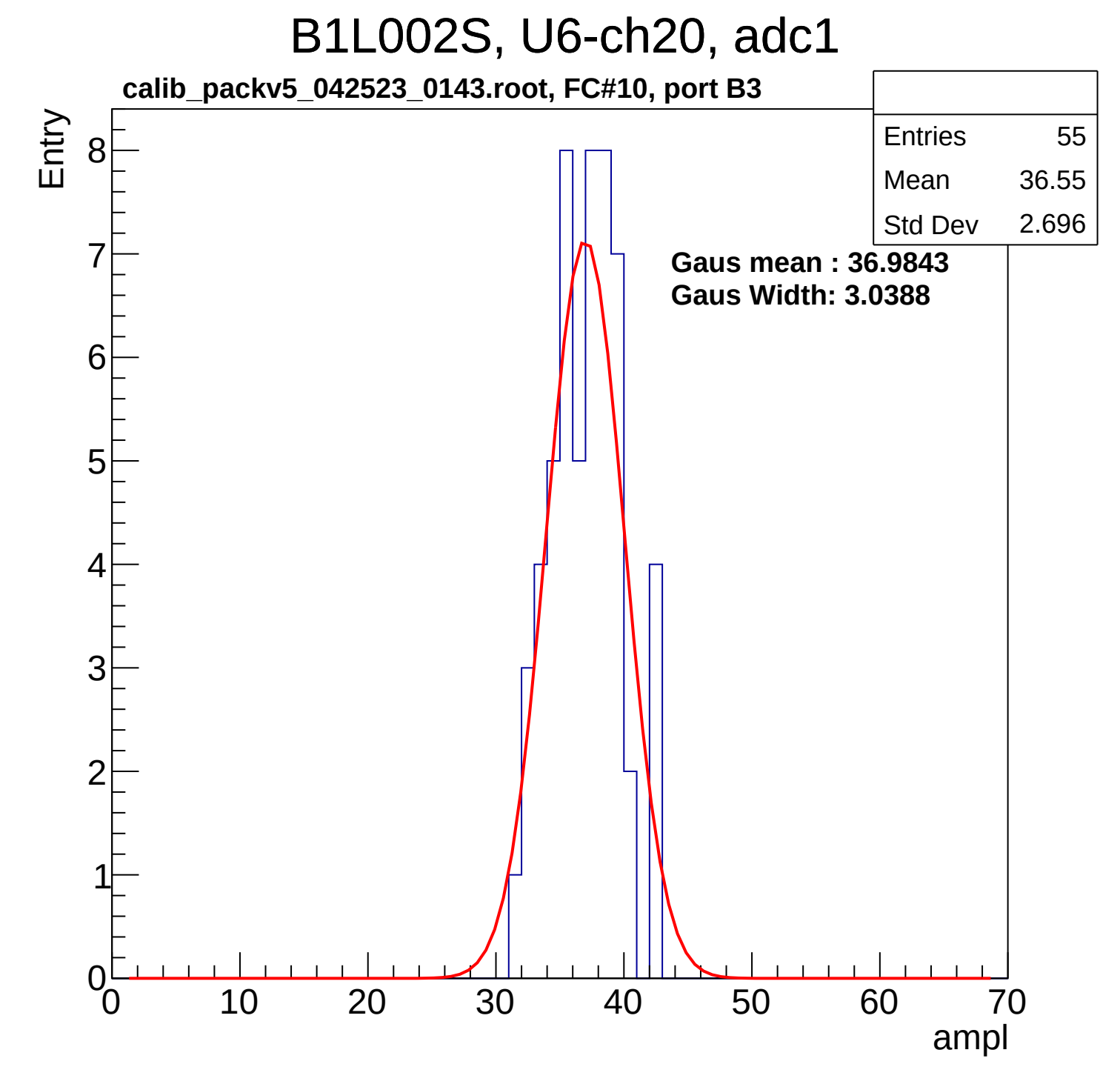

B1L002S, U6-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.73
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

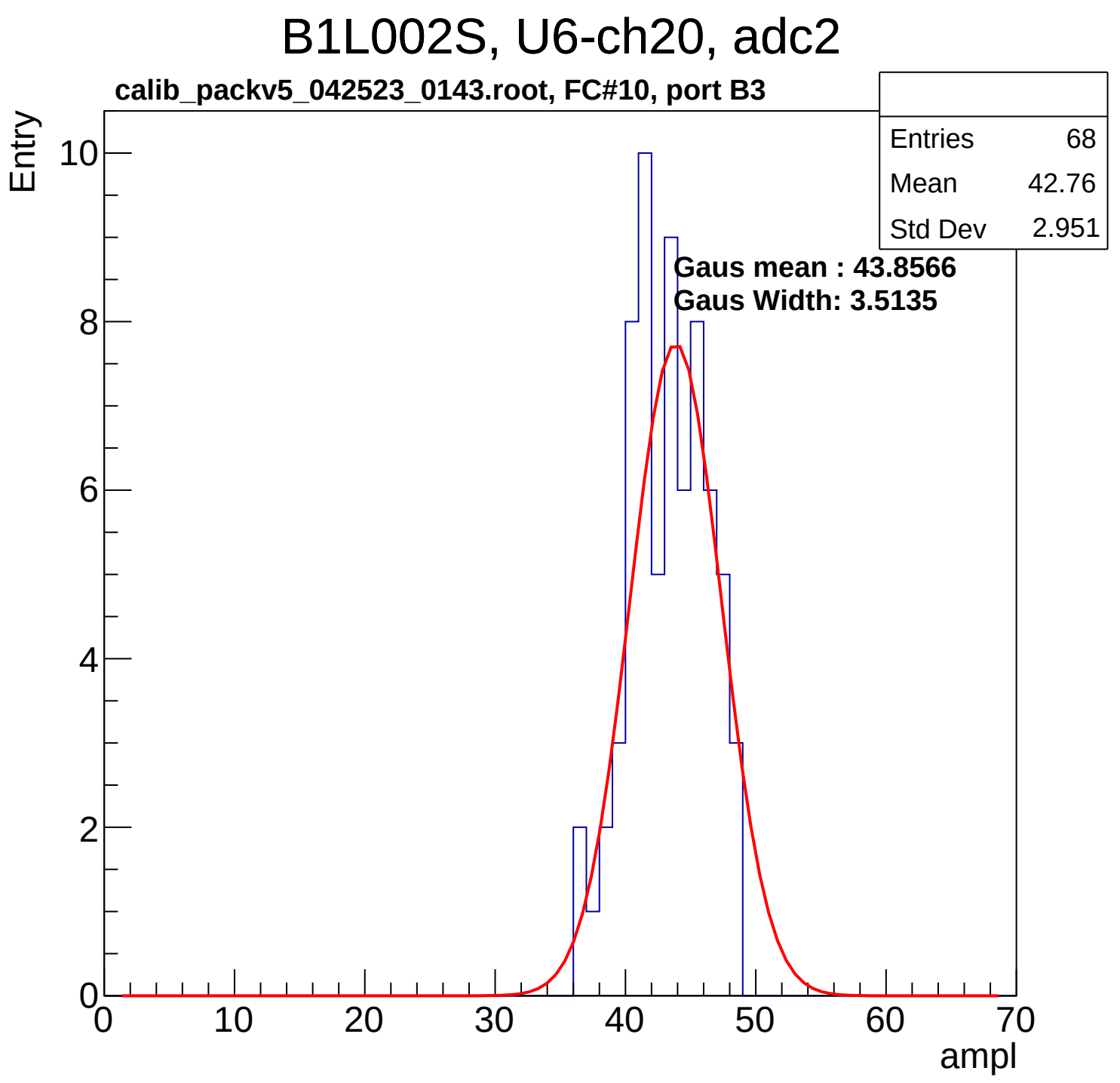

B1L002S, U6-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.45
Std Dev 2.928
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

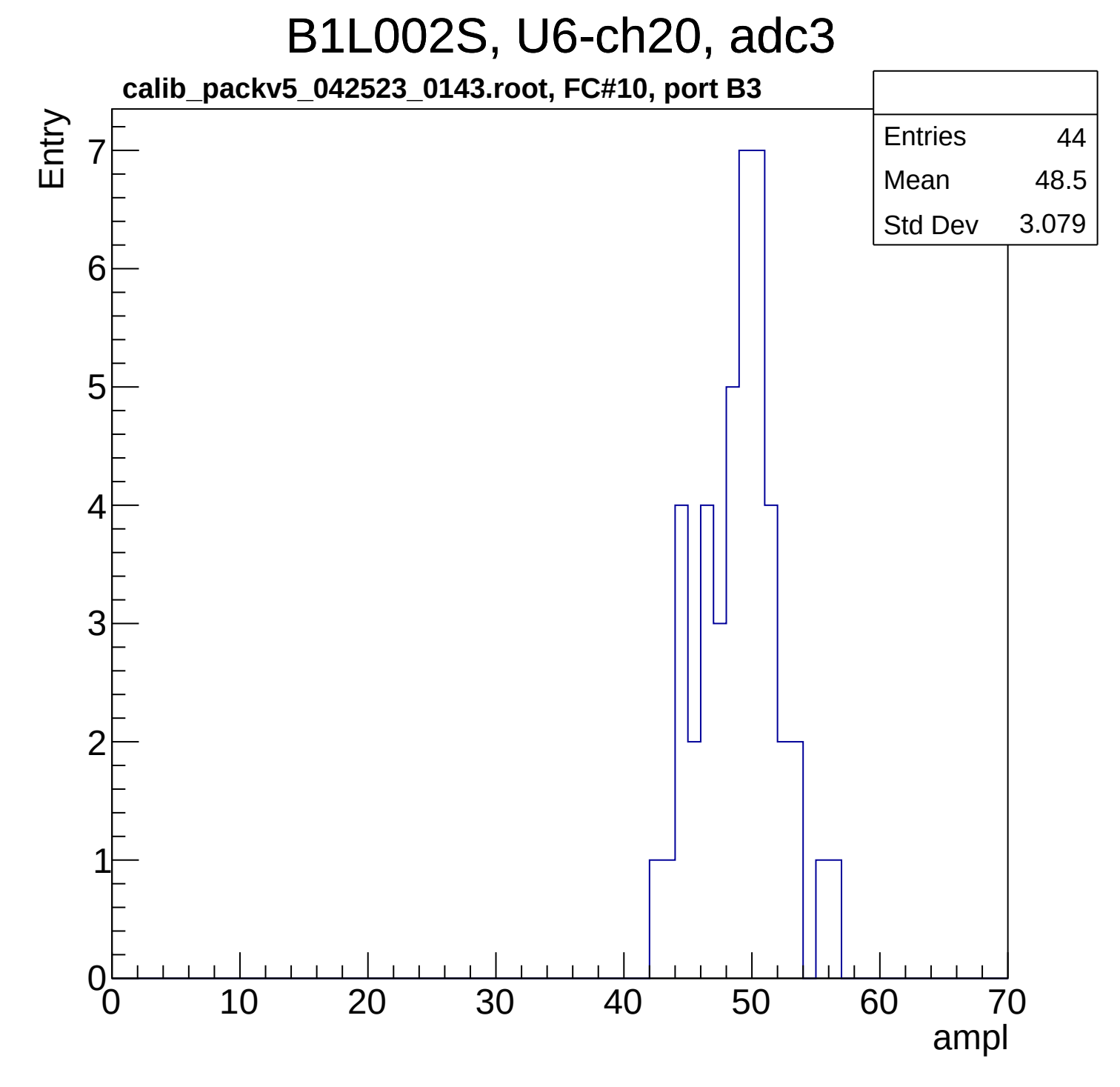

B1L002S, U6-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.32
Std Dev 13.8
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

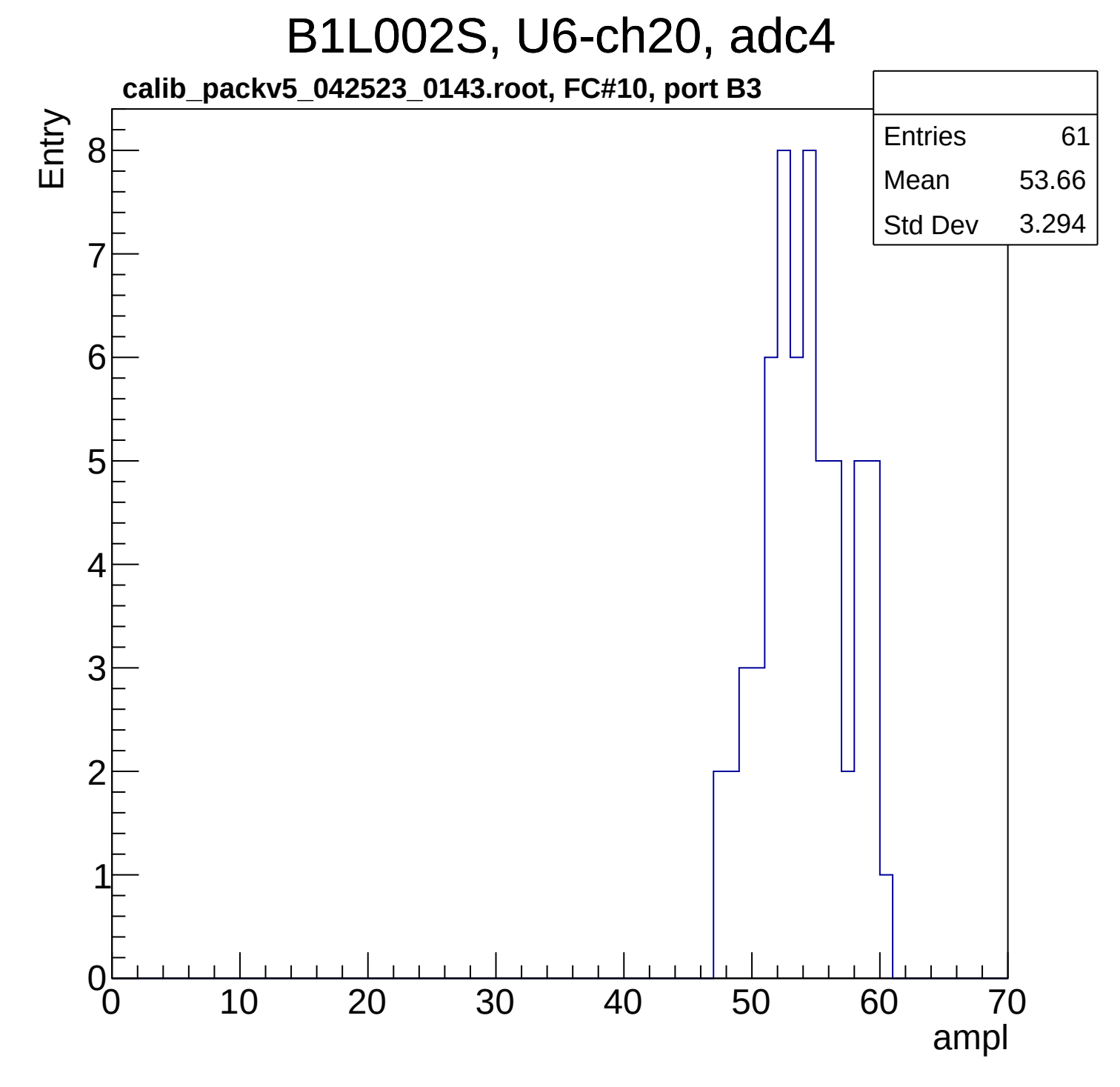

B1L002S, U6-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

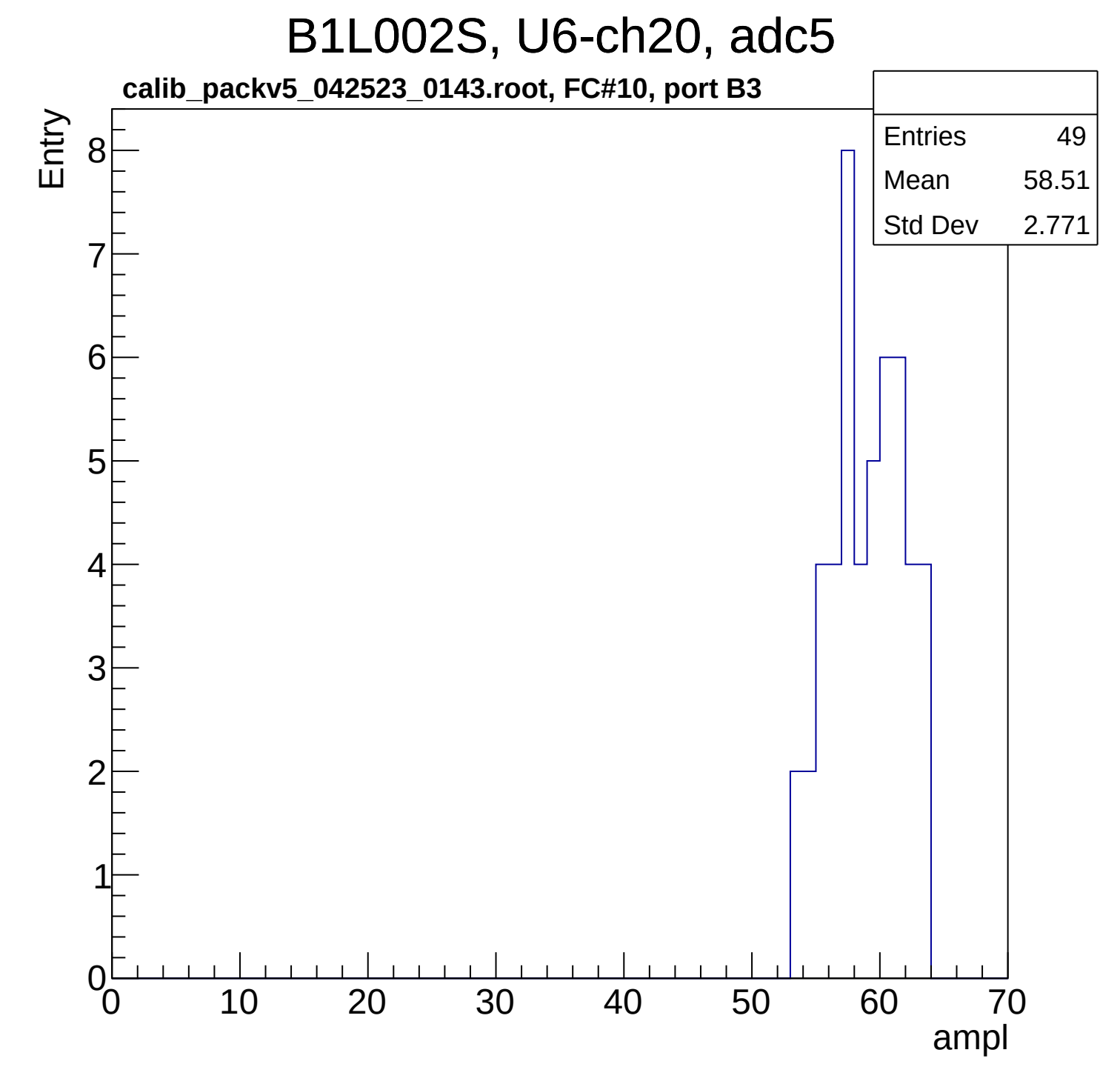

B1L002S, U6-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 29.41
Std Dev 4.376
Gaus mean : 29.4787
Gaus Width: 3.7989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

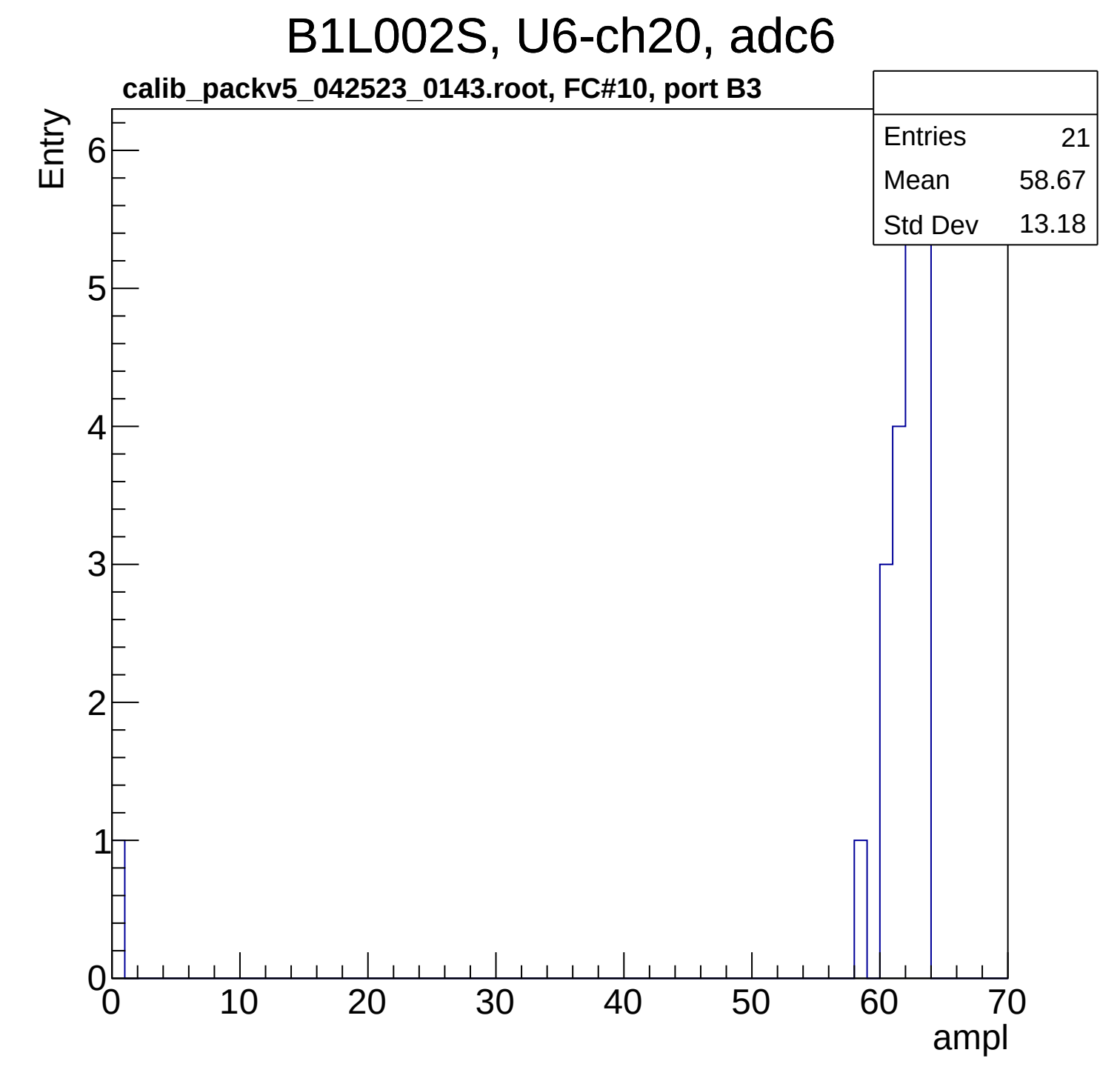

B1L002S, U6-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 36.55
Std Dev 2.696
Gaus mean : 36.9843
Gaus Width: 3.0388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

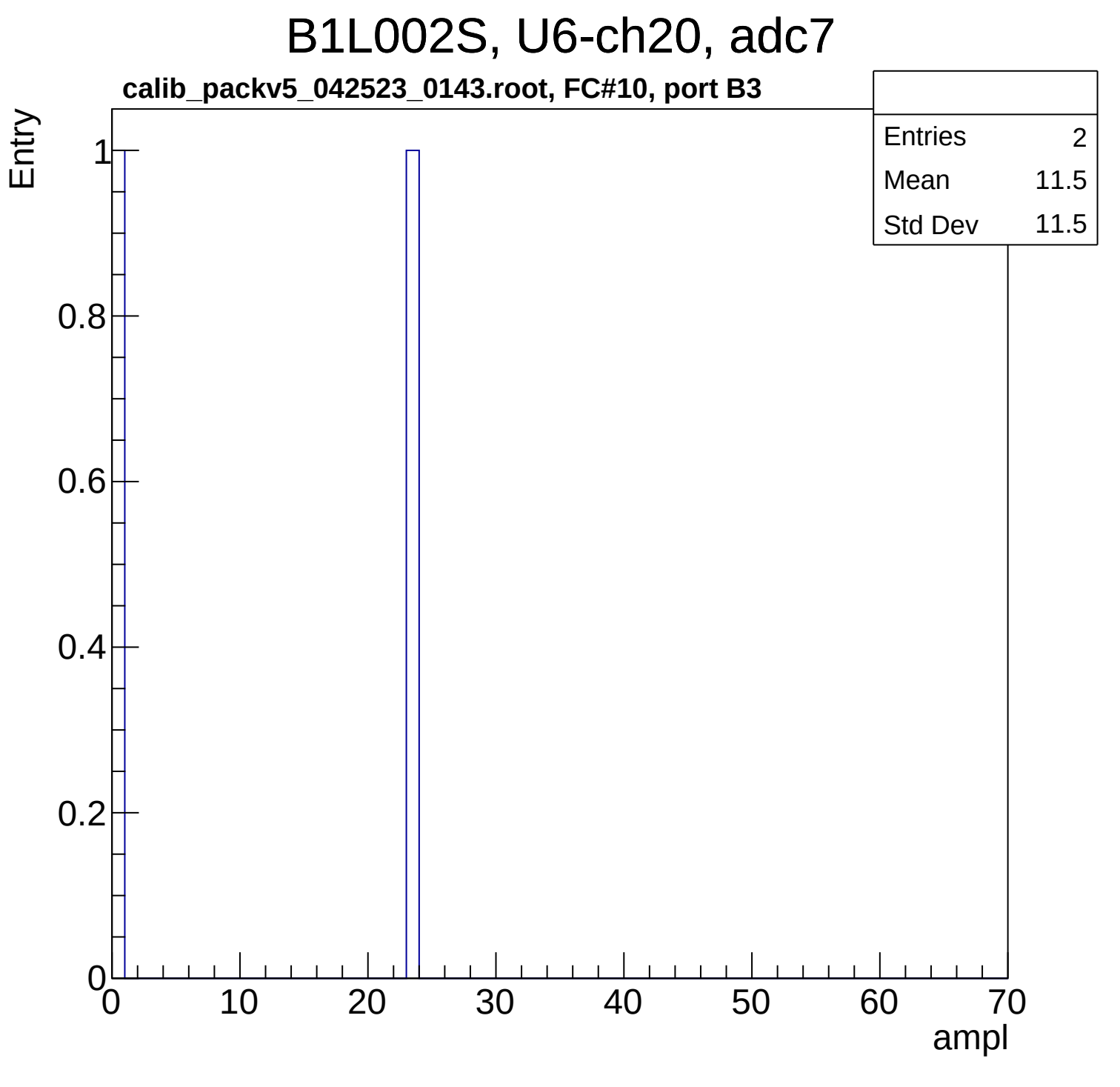

B1L002S, U6-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.76
Std Dev 2.951
Gaus mean : 43.8566
Gaus Width: 3.5135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

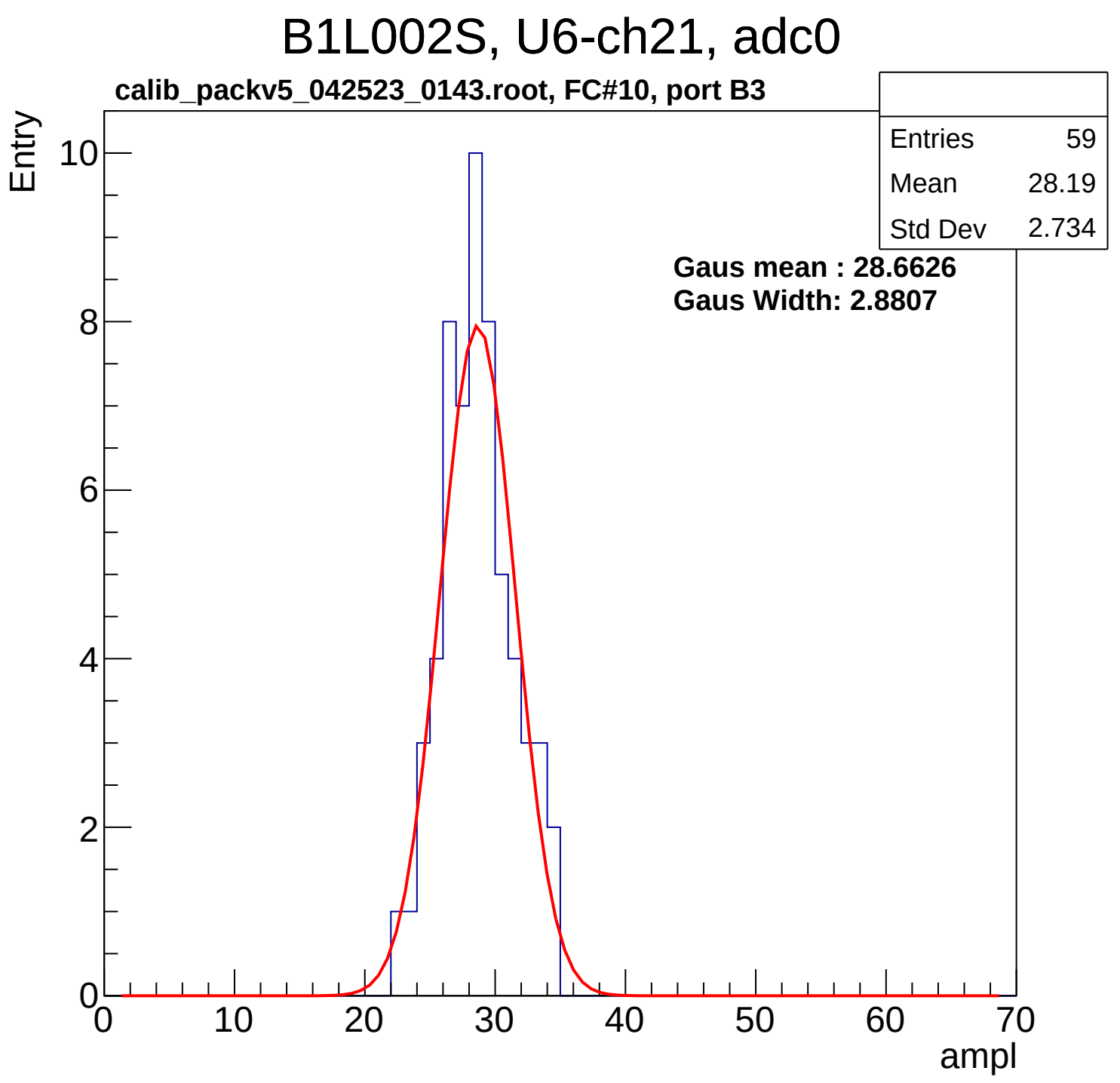

B1L002S, U6-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 48.5
Std Dev 3.079
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

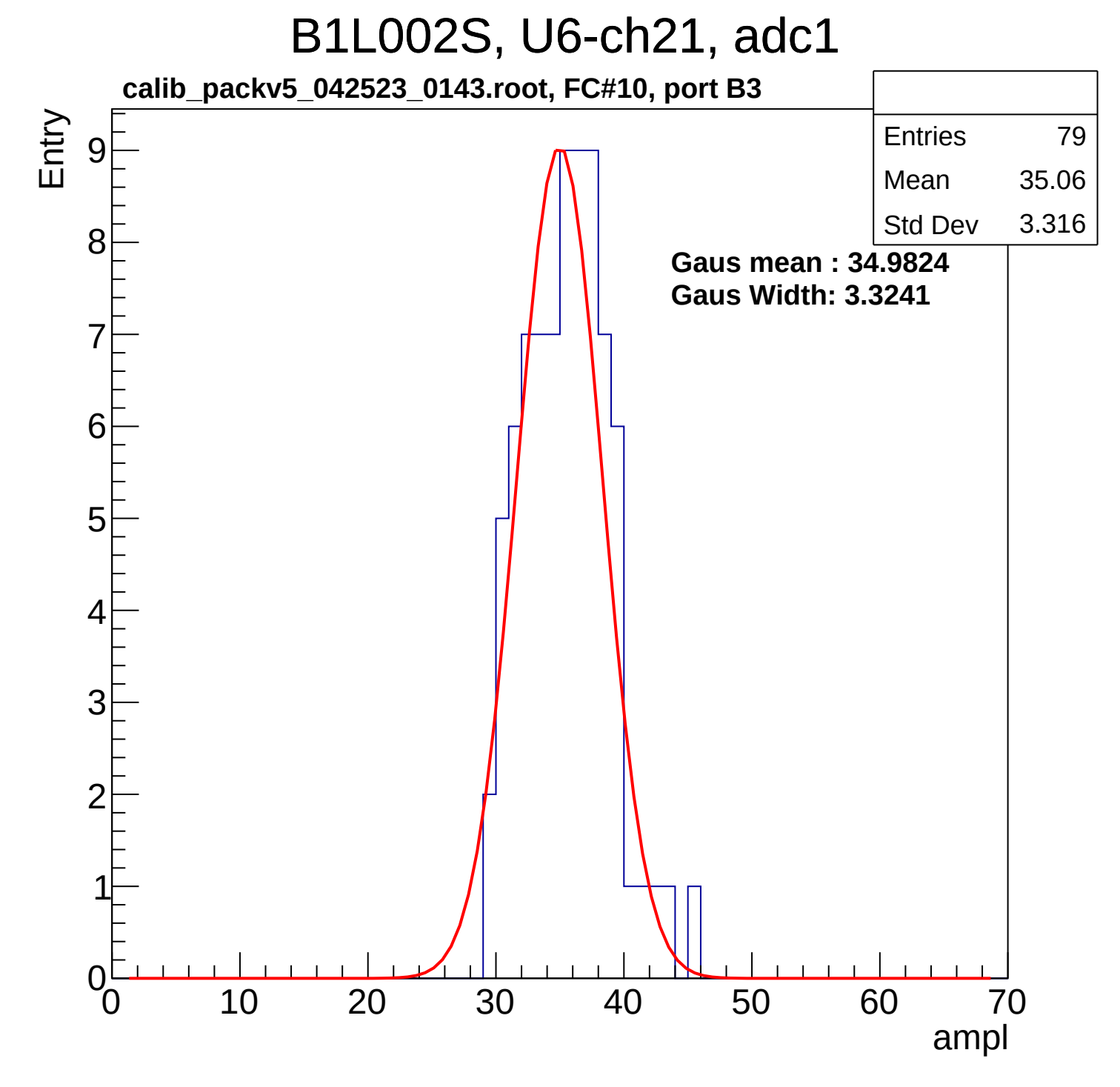

B1L002S, U6-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.66
Std Dev 3.294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

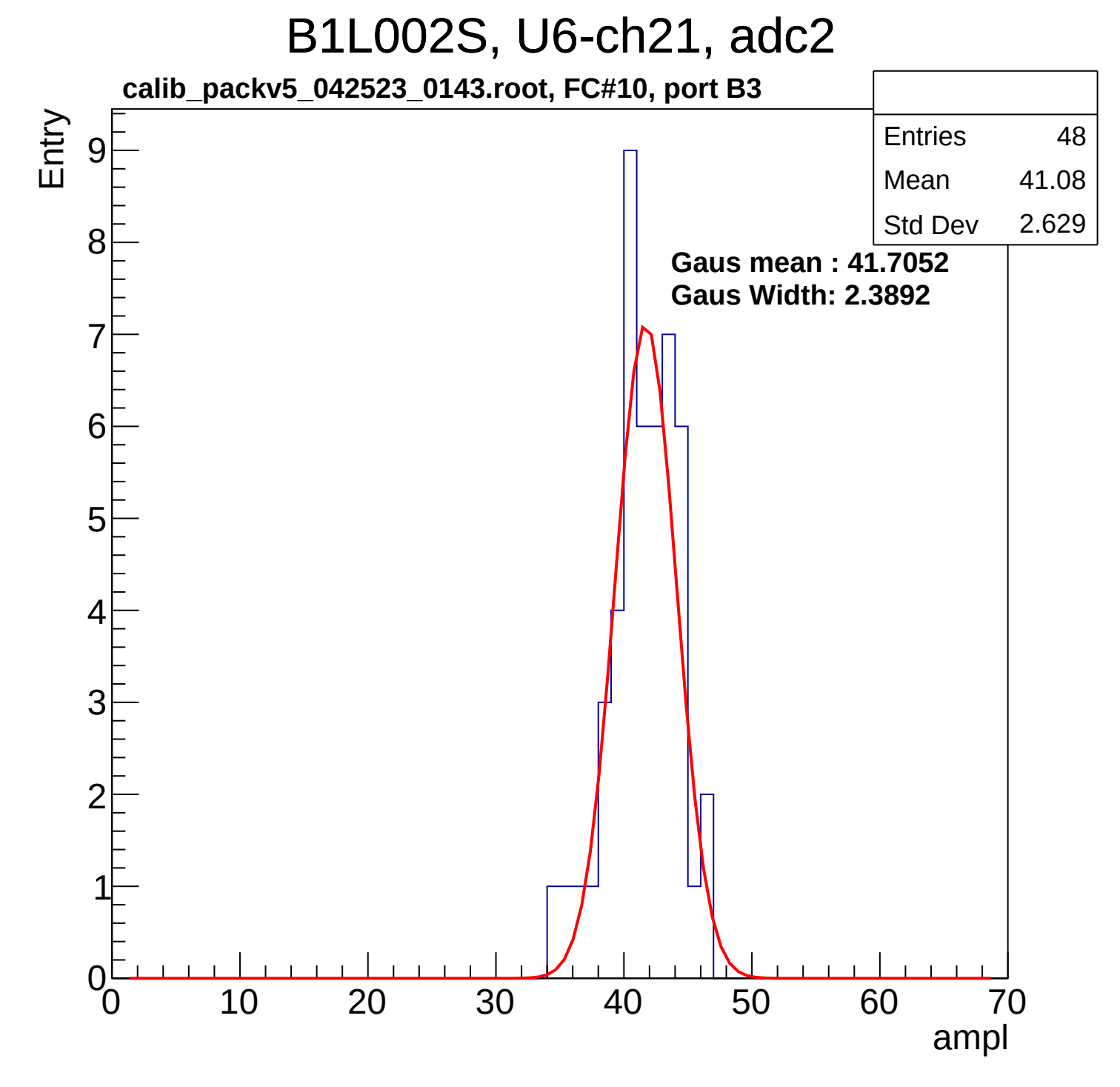

B1L002S, U6-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.51
Std Dev 2.771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

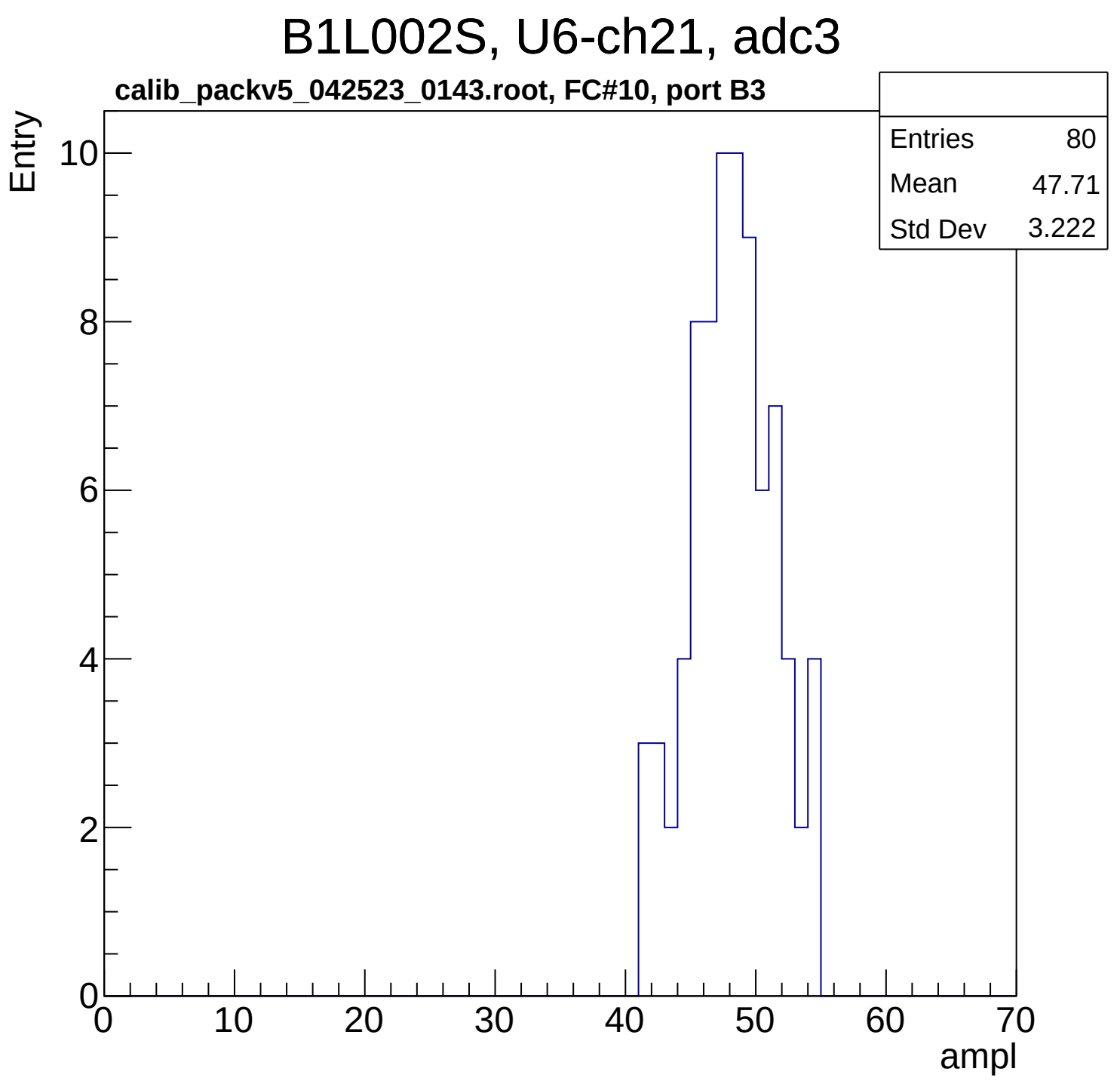

B1L002S, U6-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.67
Std Dev 13.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

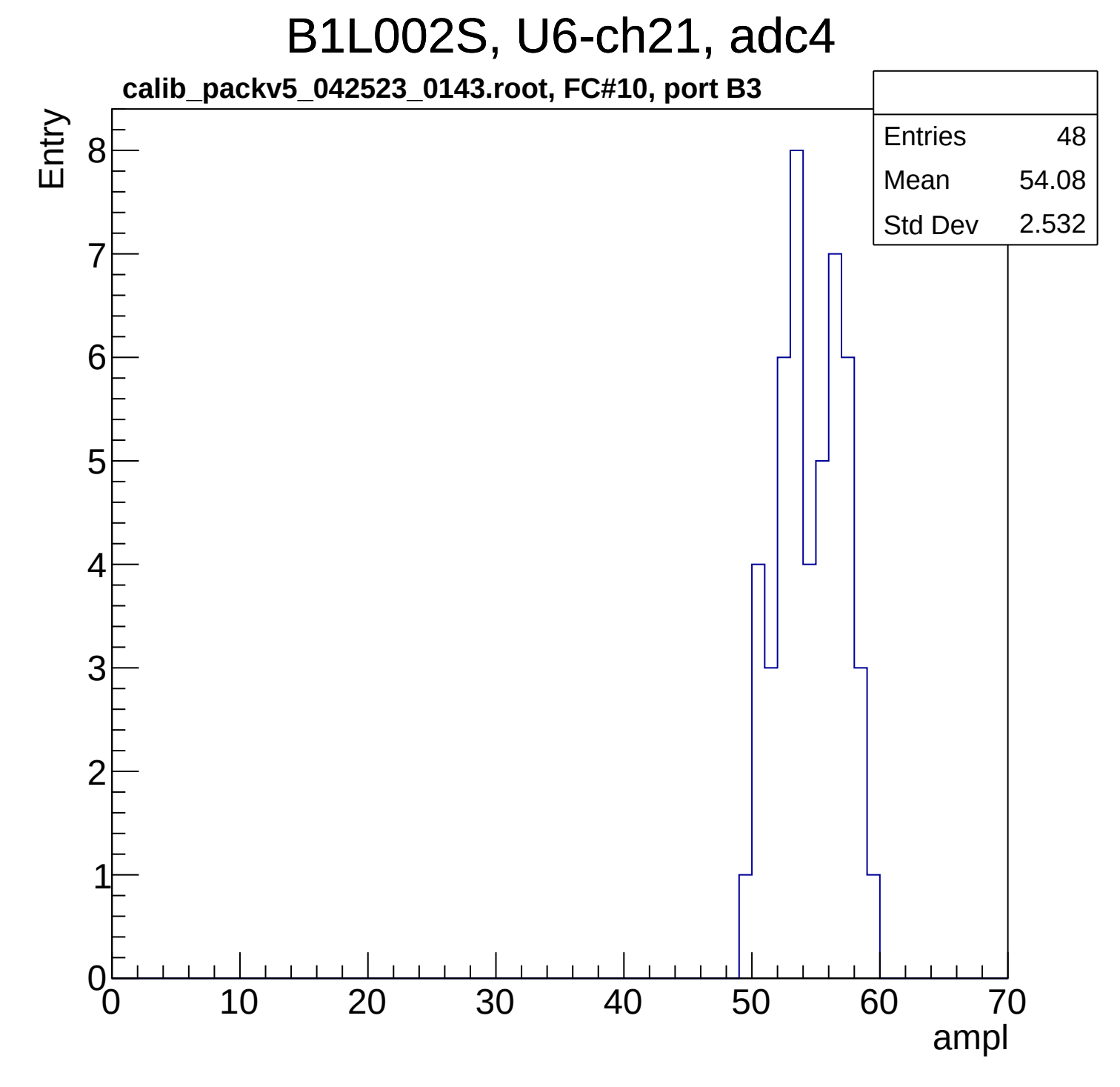

B1L002S, U6-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

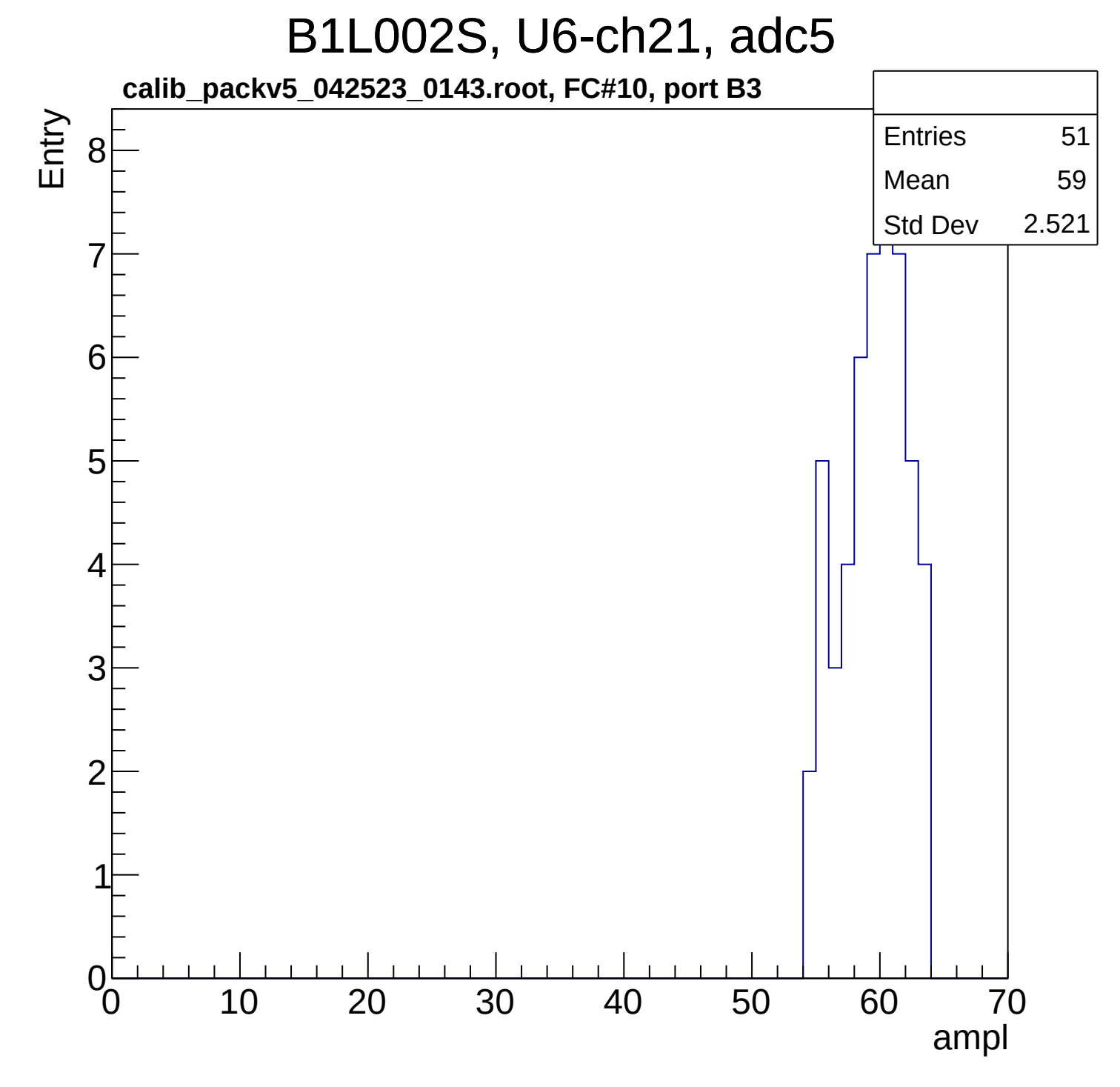

B1L002S, U6-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.19
Std Dev 2.734
Gaus mean : 28.6626
Gaus Width: 2.8807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

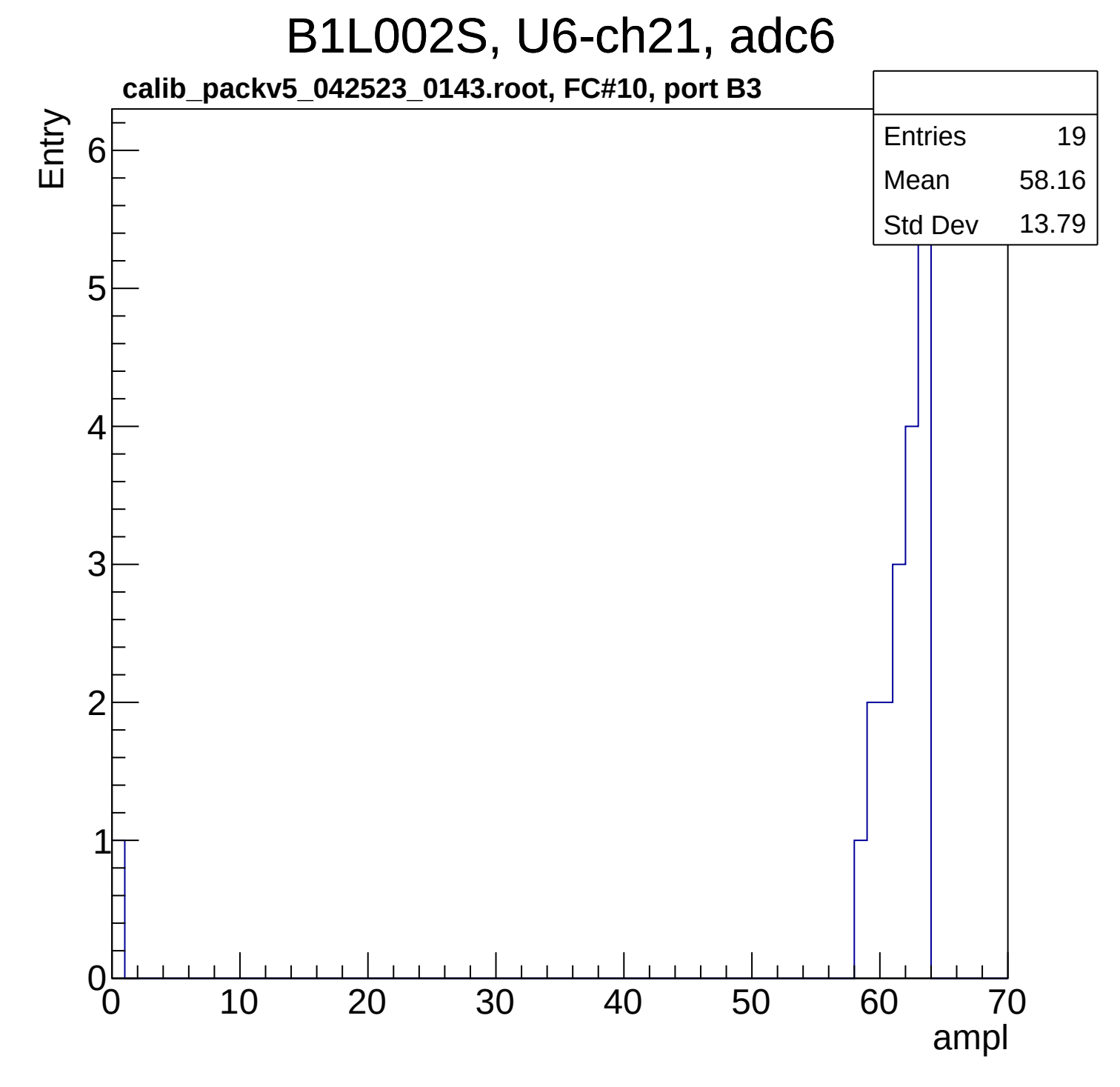

B1L002S, U6-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.06
Std Dev 3.316
Gaus mean : 34.9824
Gaus Width: 3.3241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

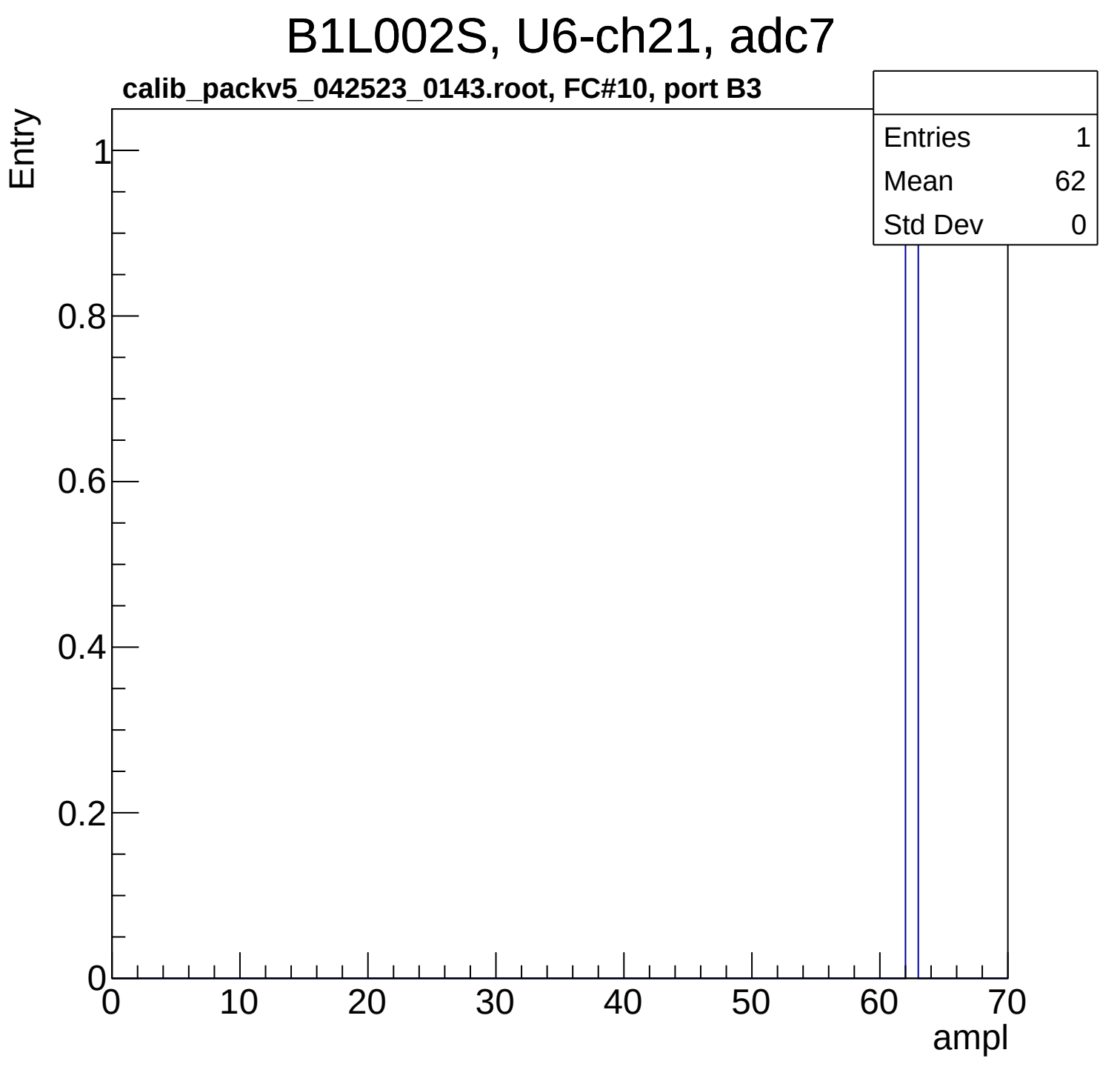

B1L002S, U6-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.08
Std Dev 2.629
Gaus mean : 41.7052
Gaus Width: 2.3892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

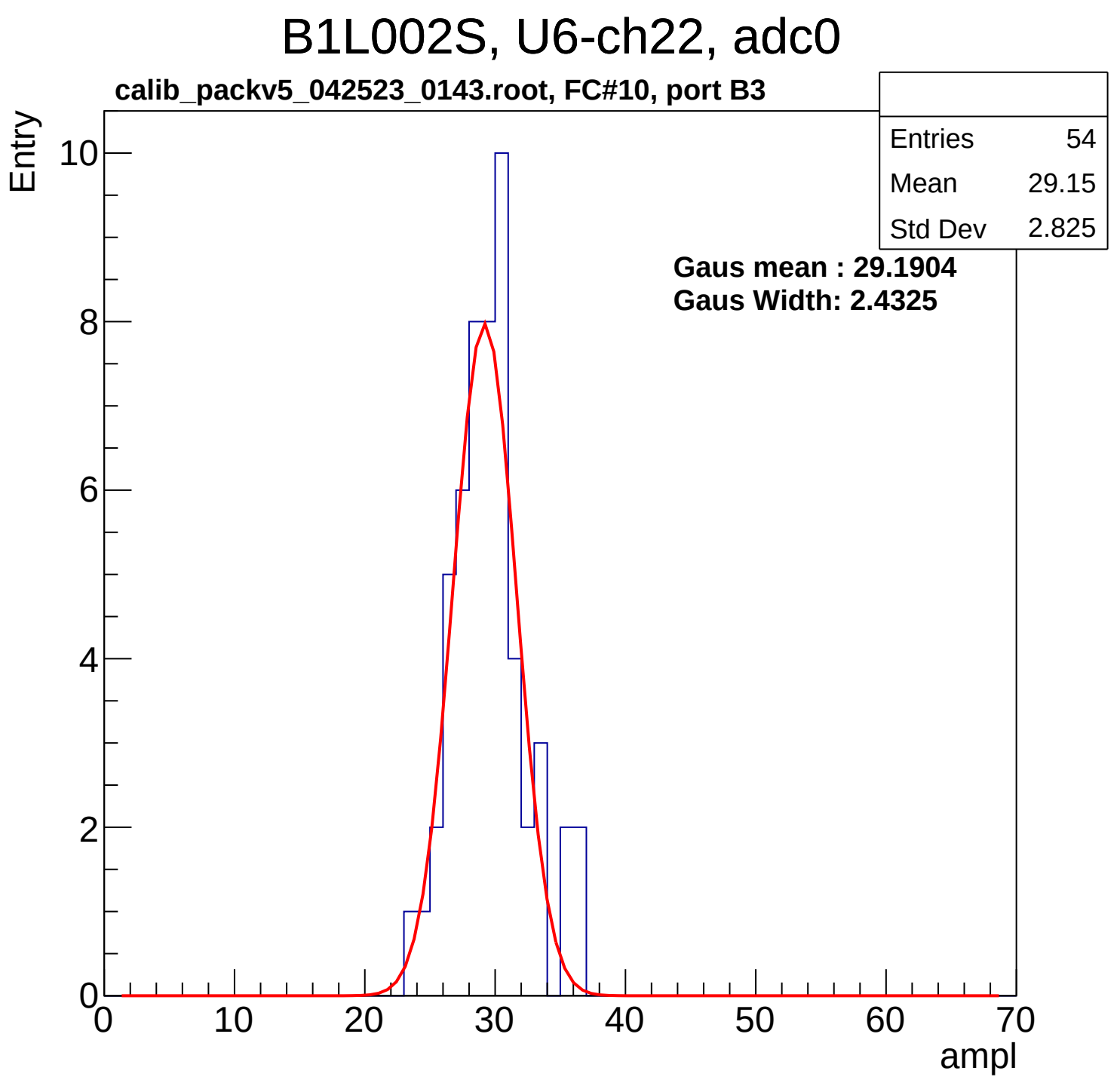

B1L002S, U6-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 47.71
Std Dev 3.222
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

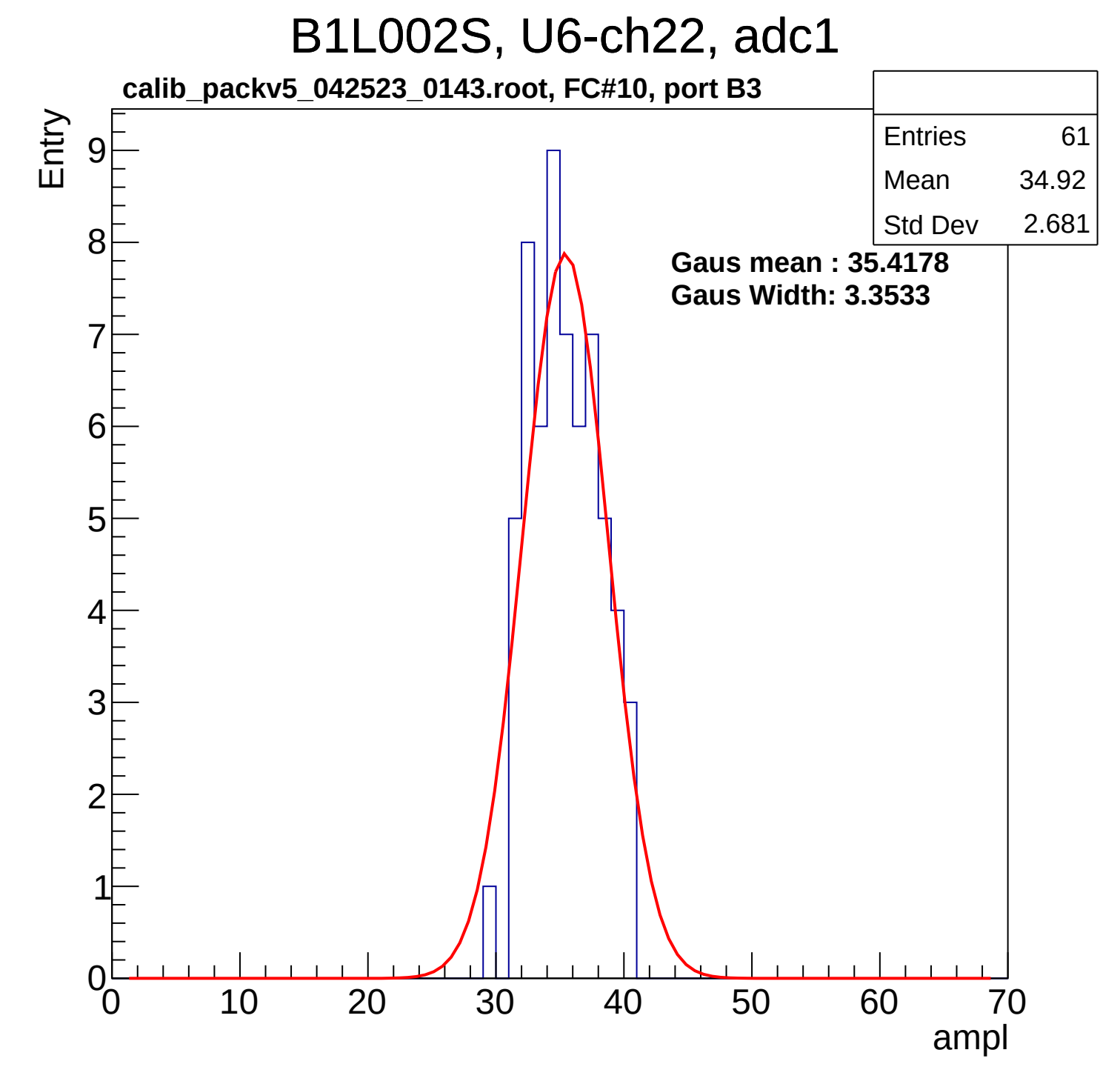

B1L002S, U6-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.08
Std Dev 2.532
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

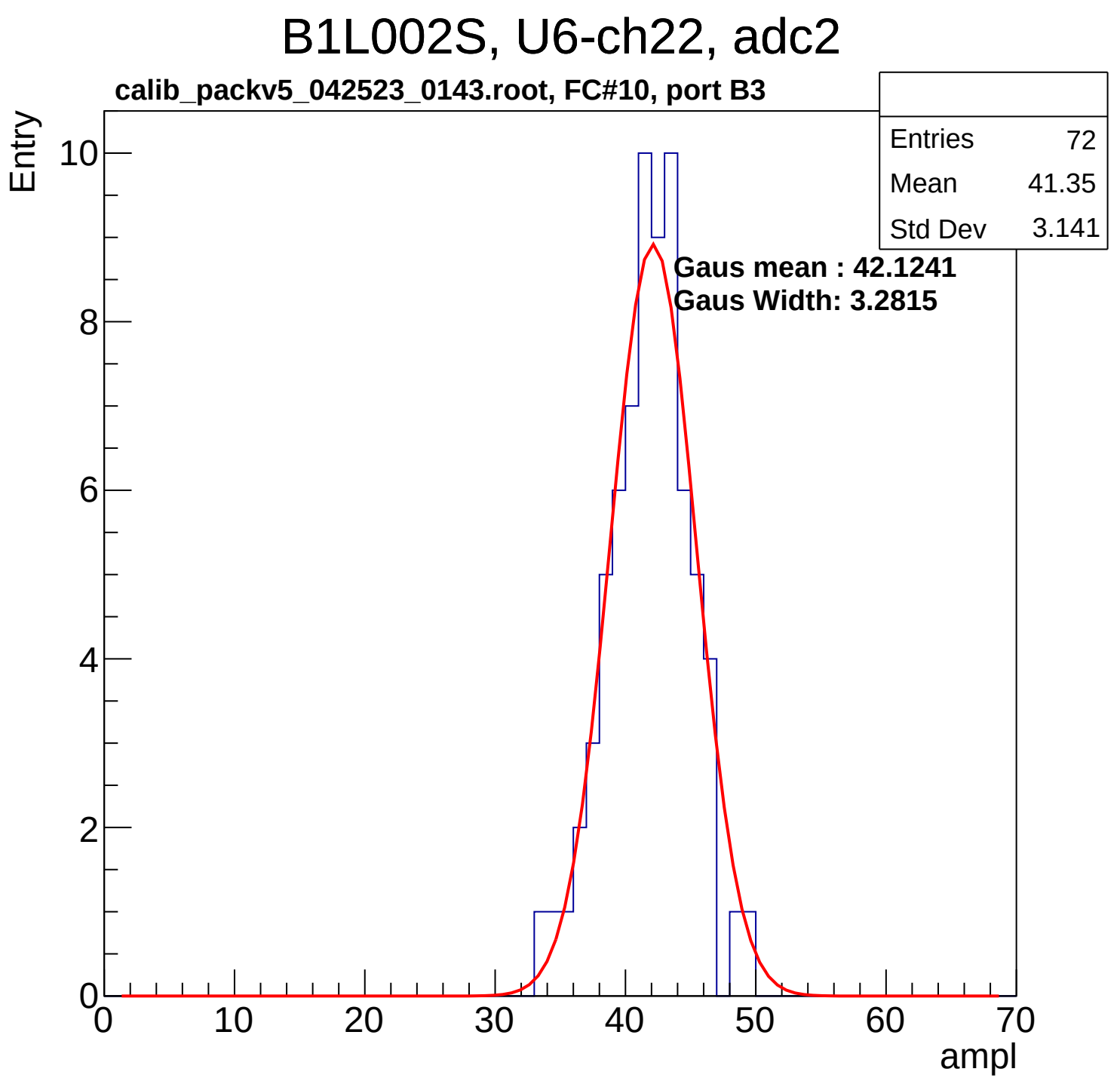

B1L002S, U6-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59
Std Dev 2.521
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

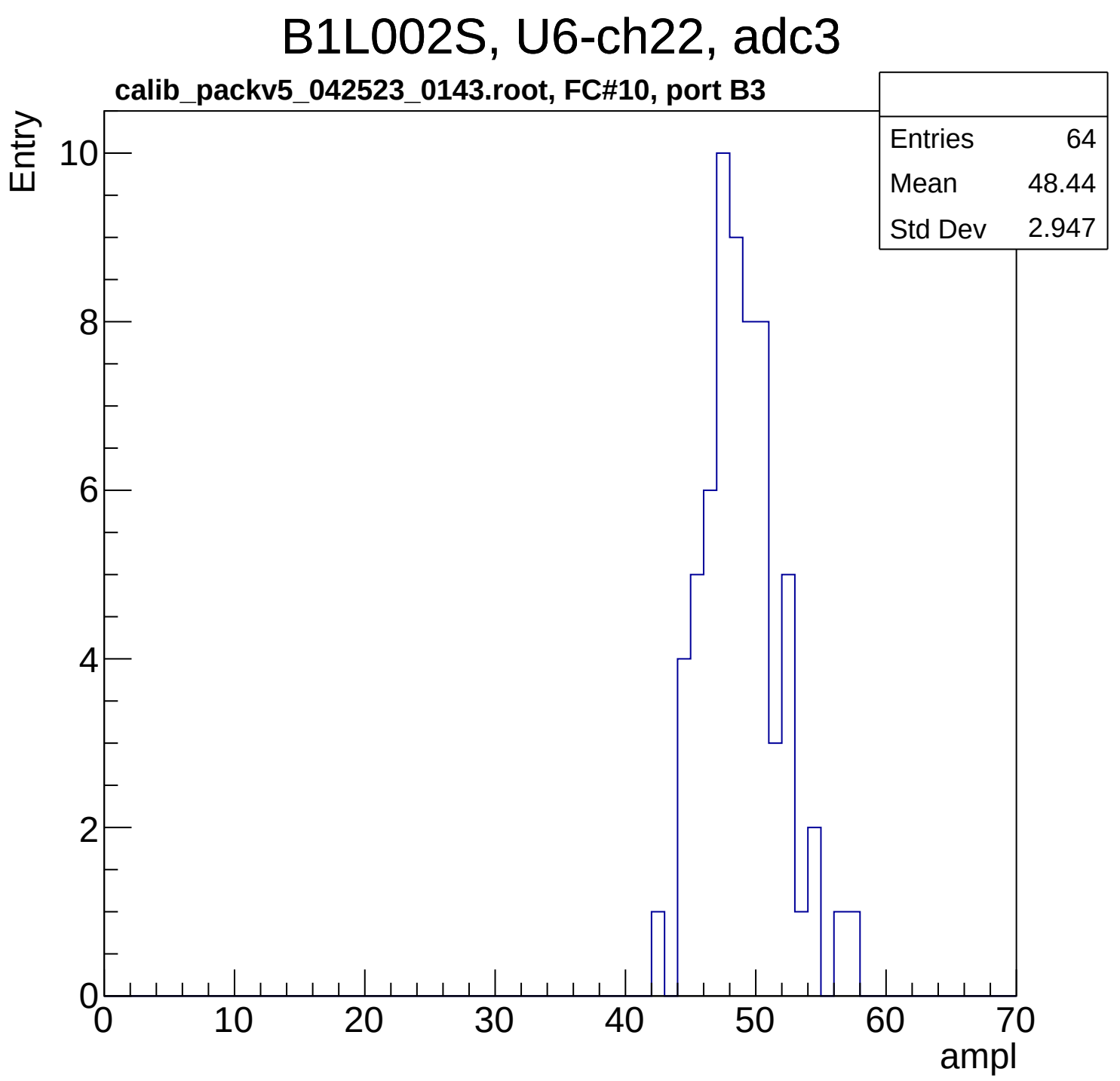

B1L002S, U6-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.16
Std Dev 13.79
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

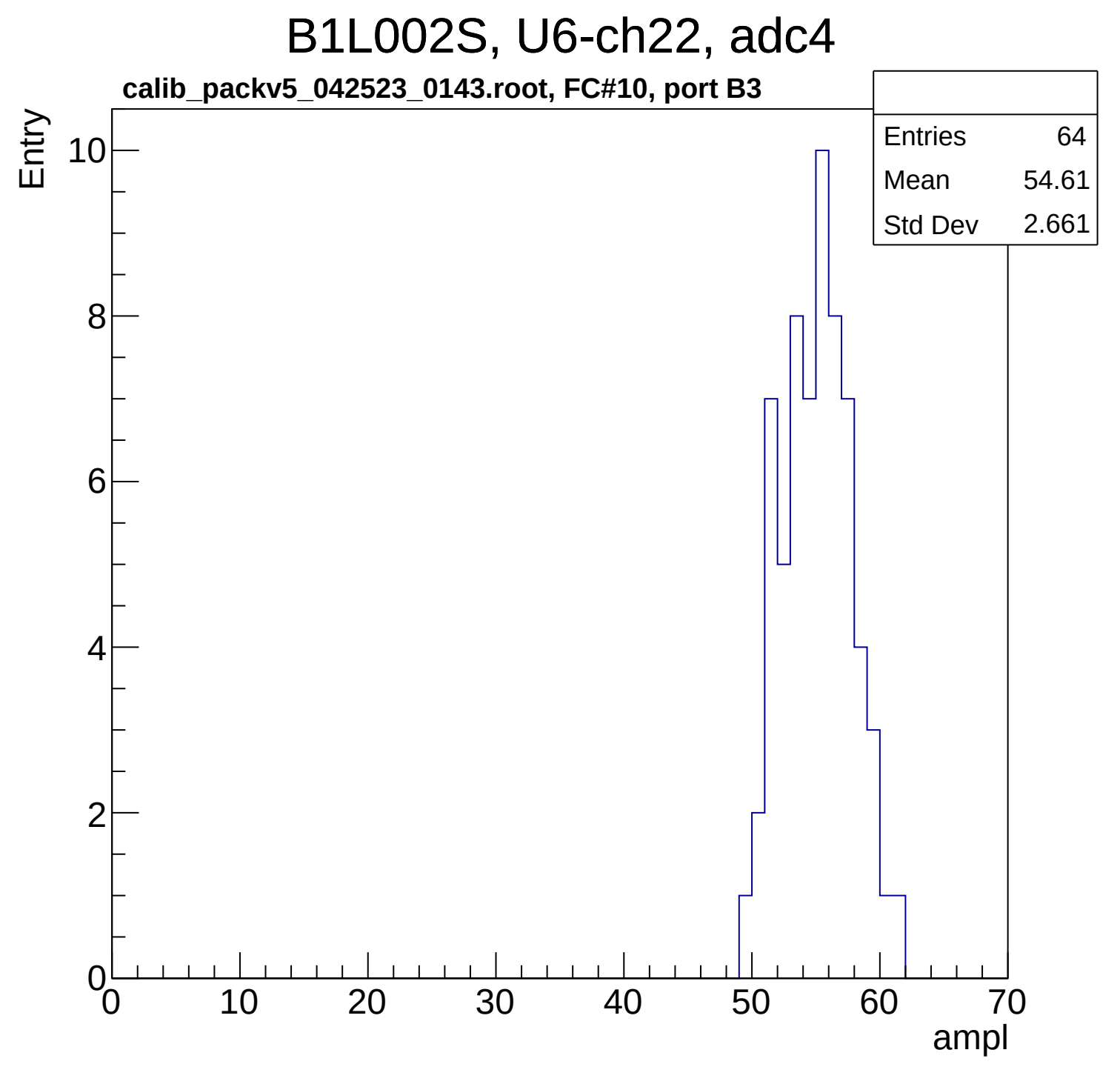

B1L002S, U6-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

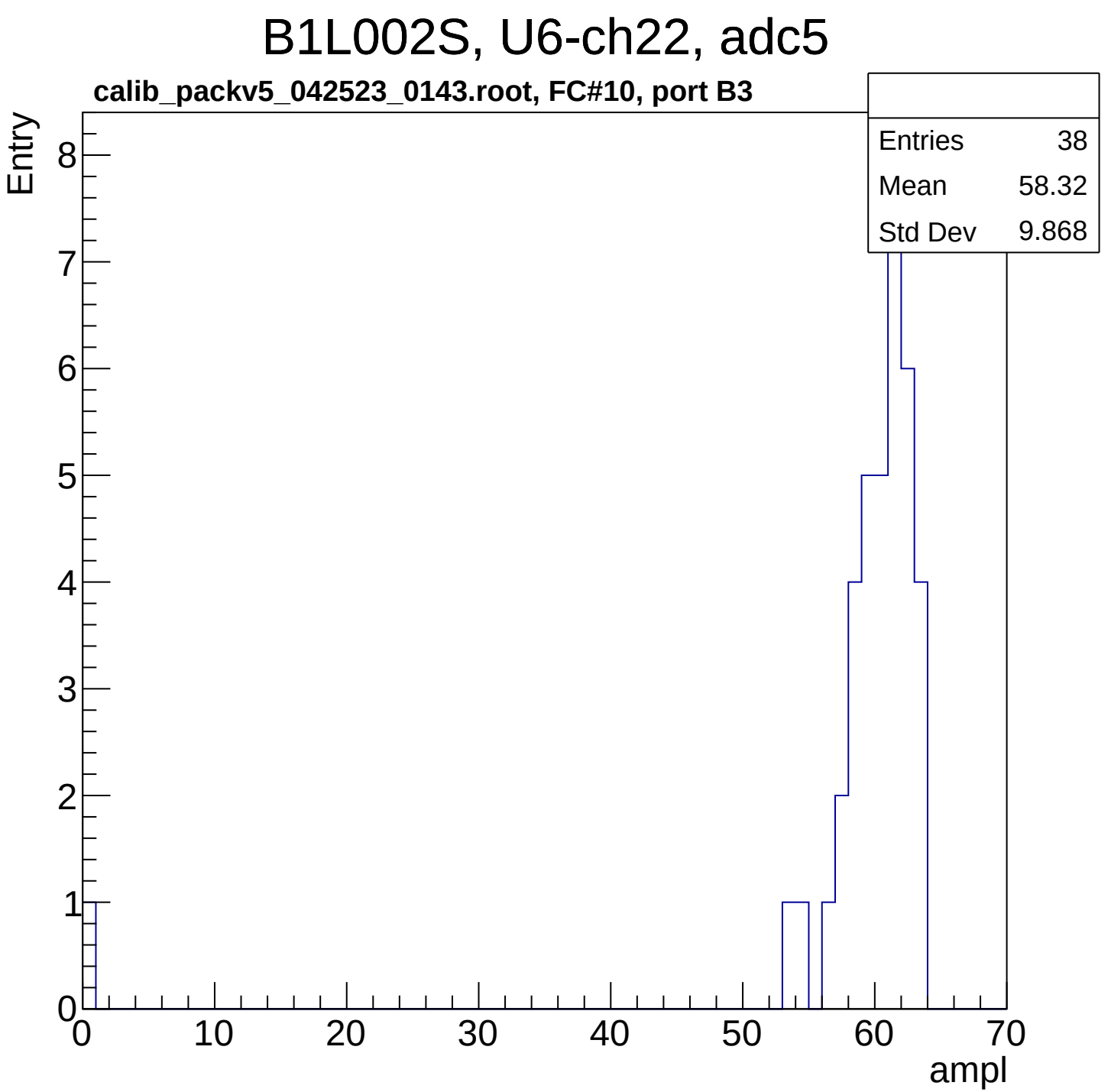

B1L002S, U6-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 29.15
Std Dev 2.825
Gaus mean : 29.1904
Gaus Width: 2.4325
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

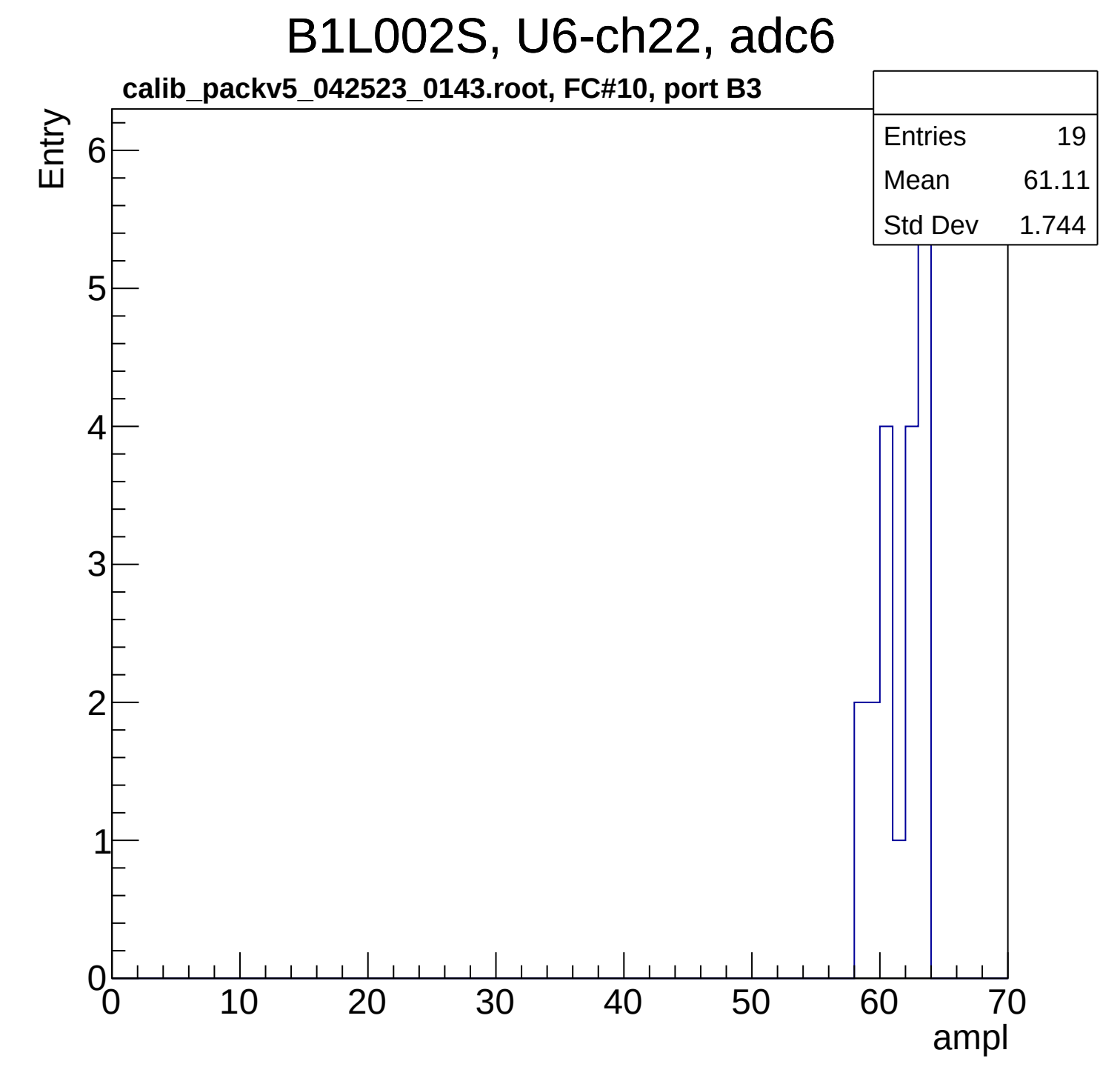

B1L002S, U6-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.92
Std Dev 2.681
Gaus mean : 35.4178
Gaus Width: 3.3533
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

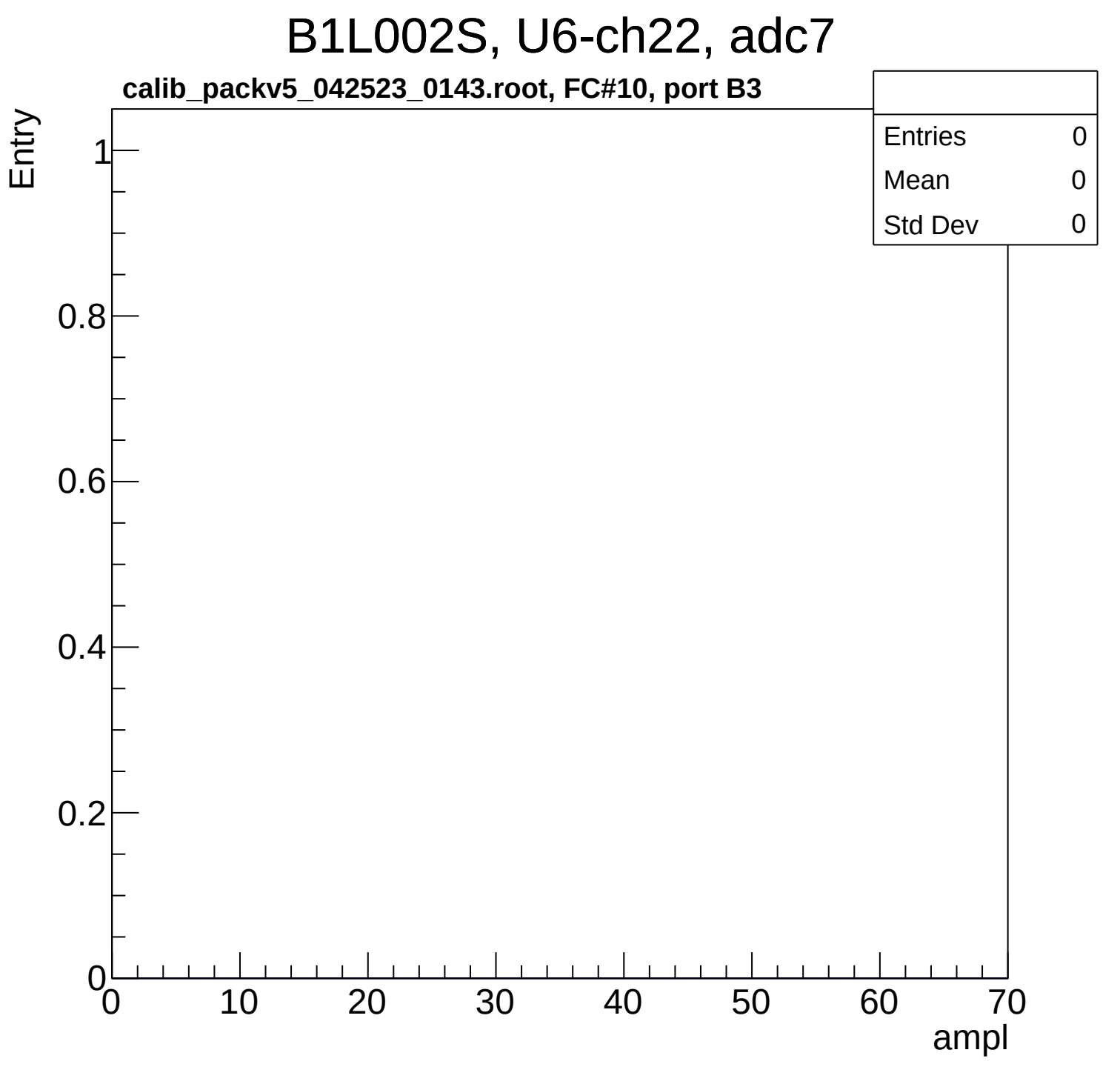

B1L002S, U6-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.35
Std Dev 3.141
Gaus mean : 42.1241
Gaus Width: 3.2815
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

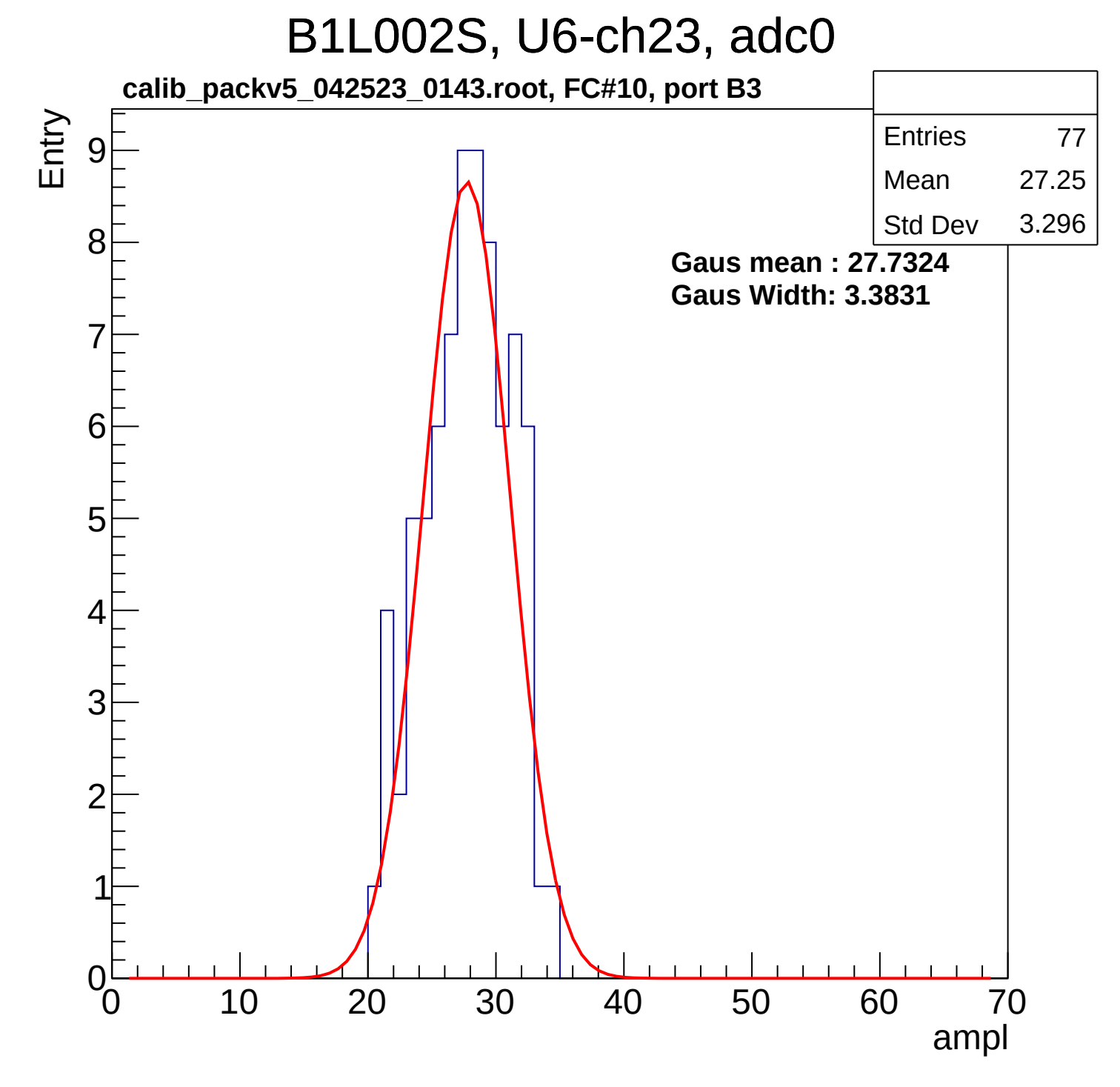

B1L002S, U6-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.44
Std Dev 2.947
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

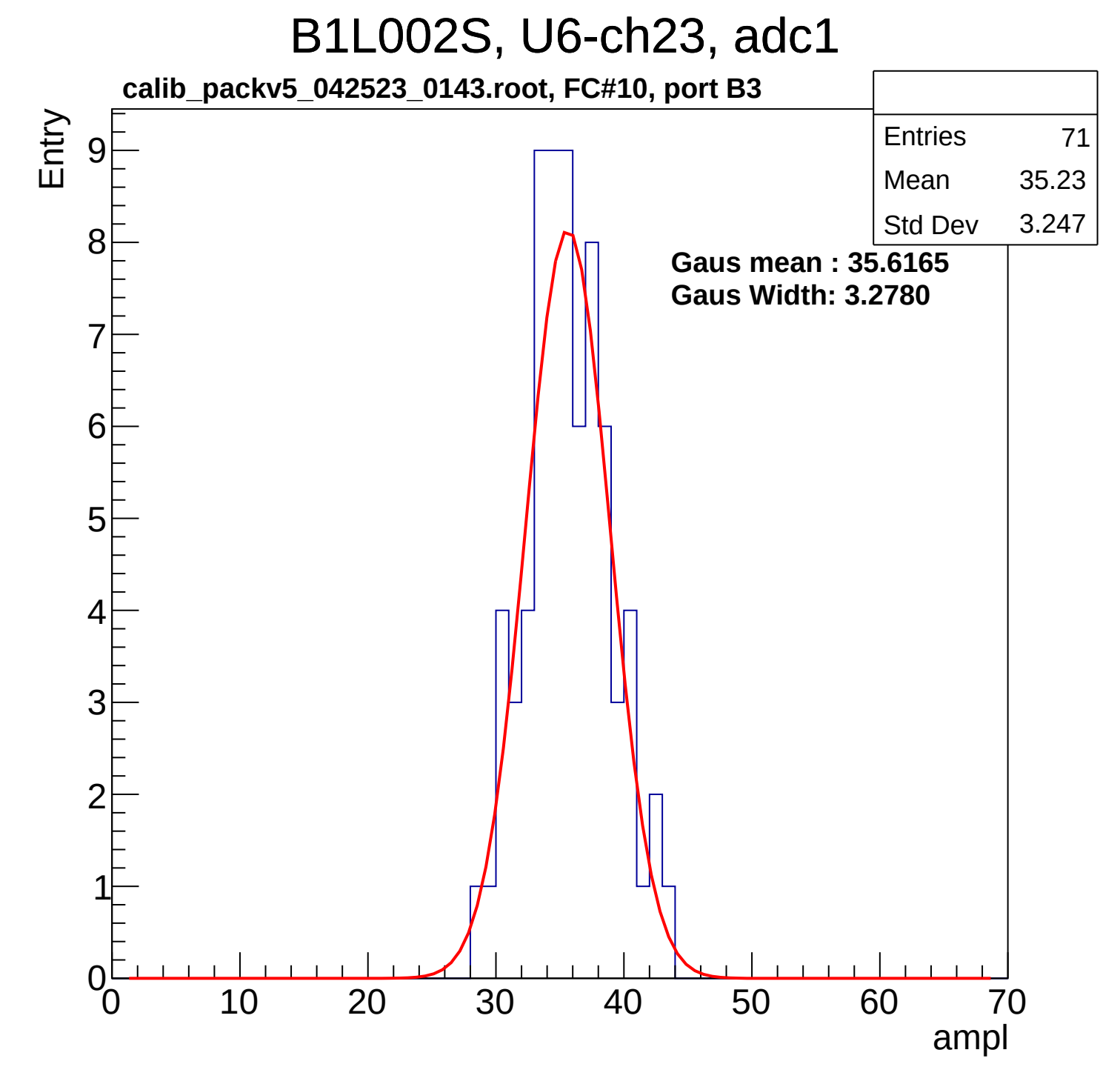

B1L002S, U6-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.61
Std Dev 2.661
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

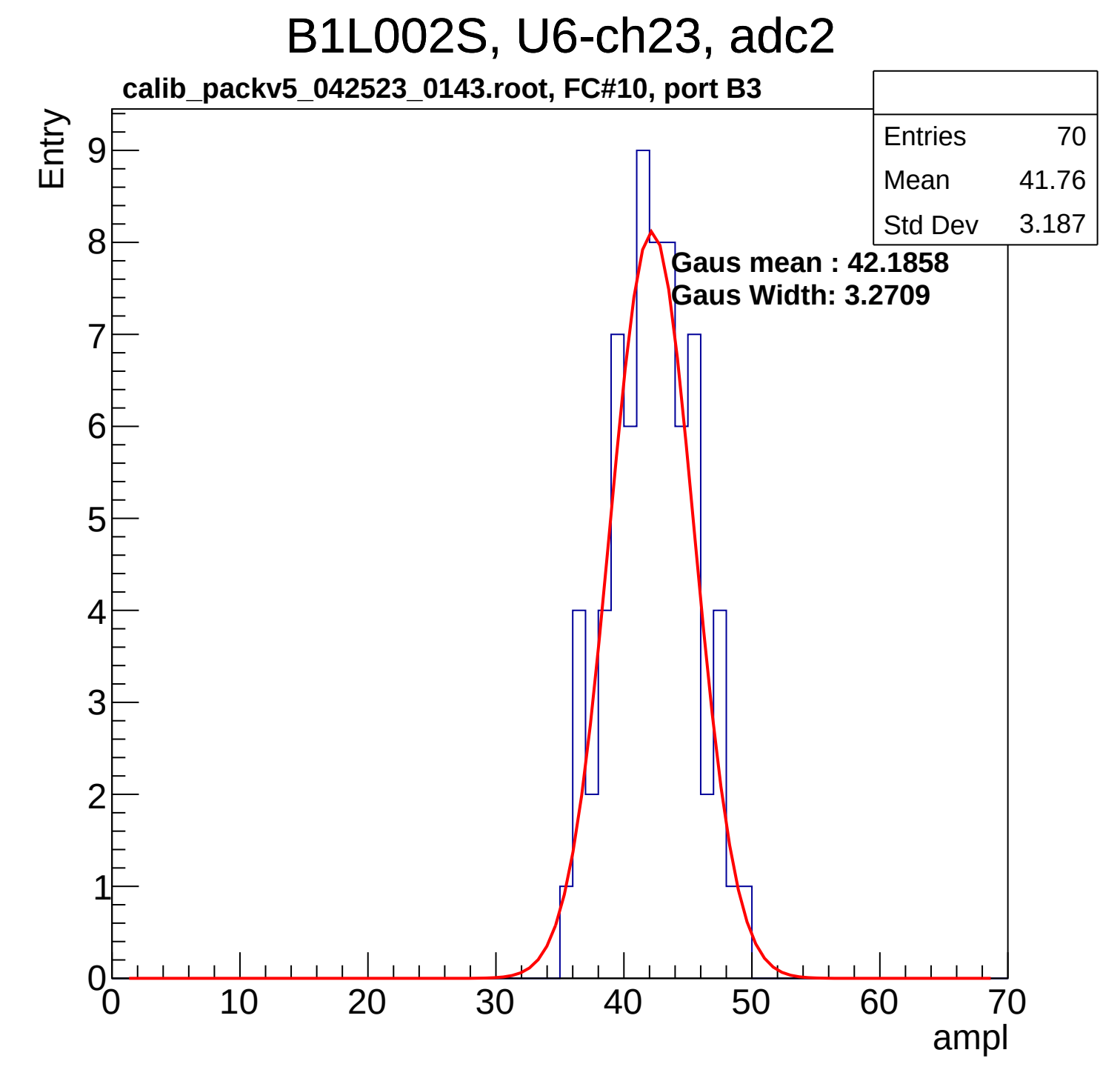

B1L002S, U6-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 58.32
Std Dev 9.868
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

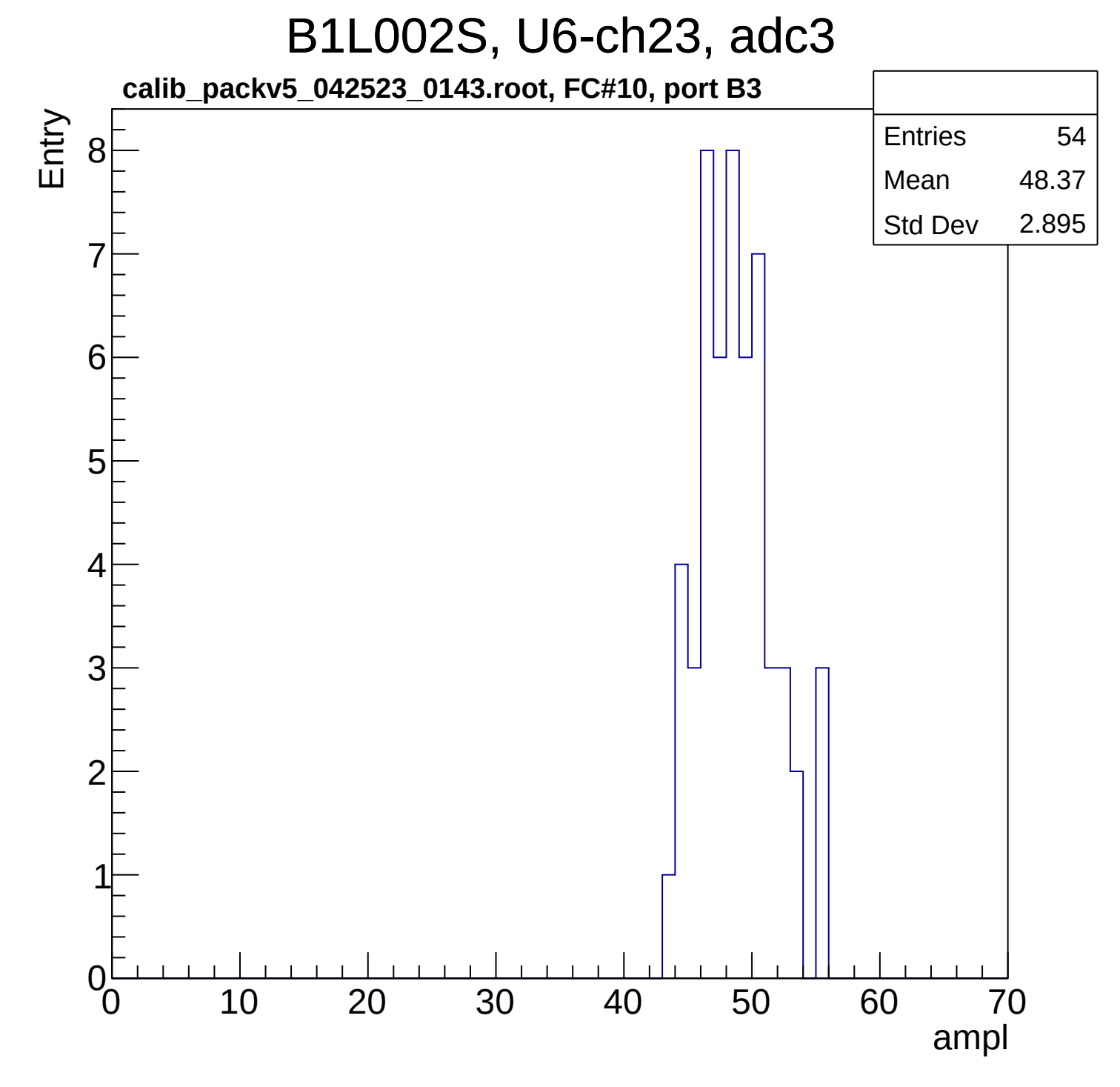

B1L002S, U6-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.11
Std Dev 1.744
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

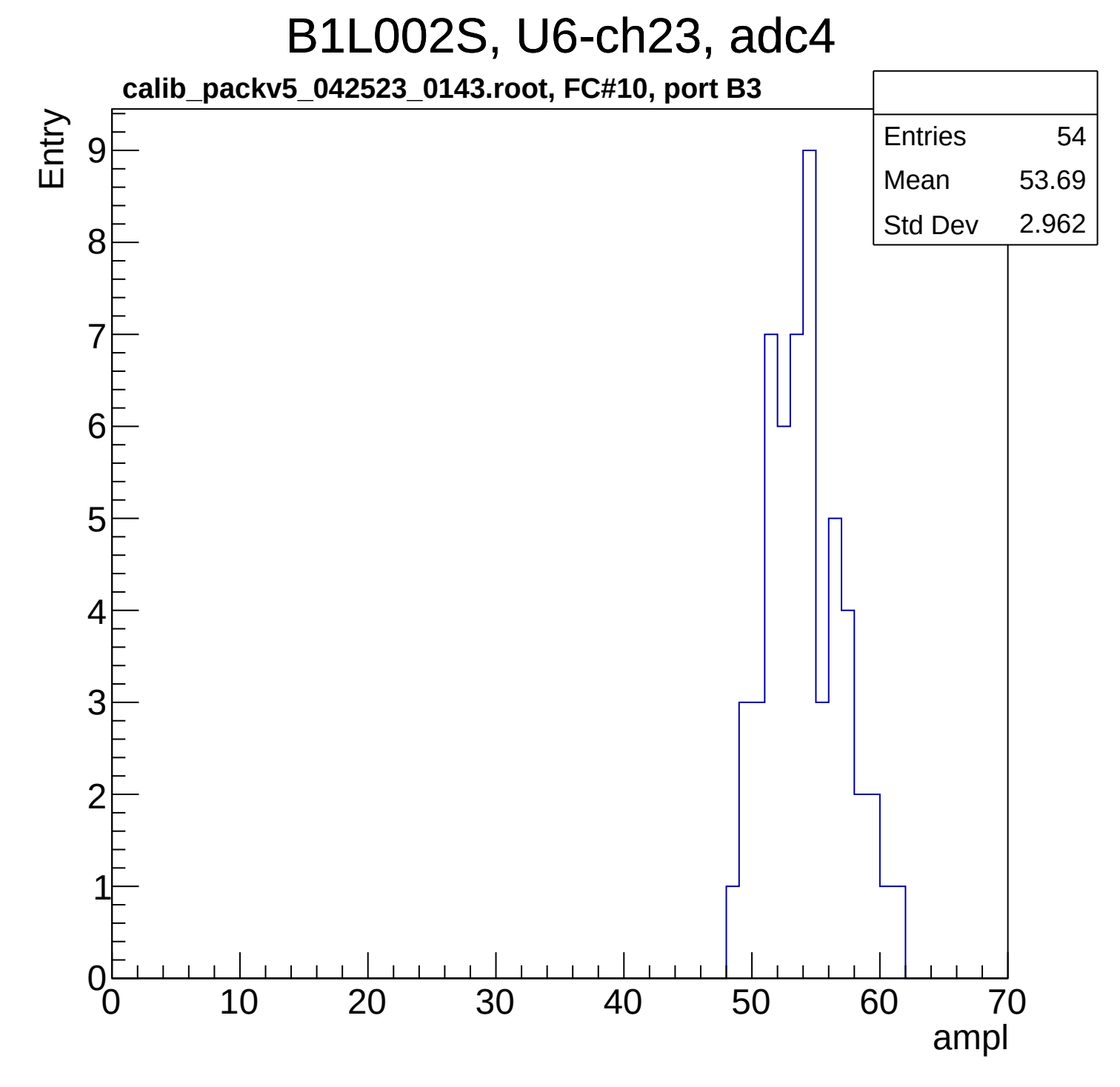

B1L002S, U6-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

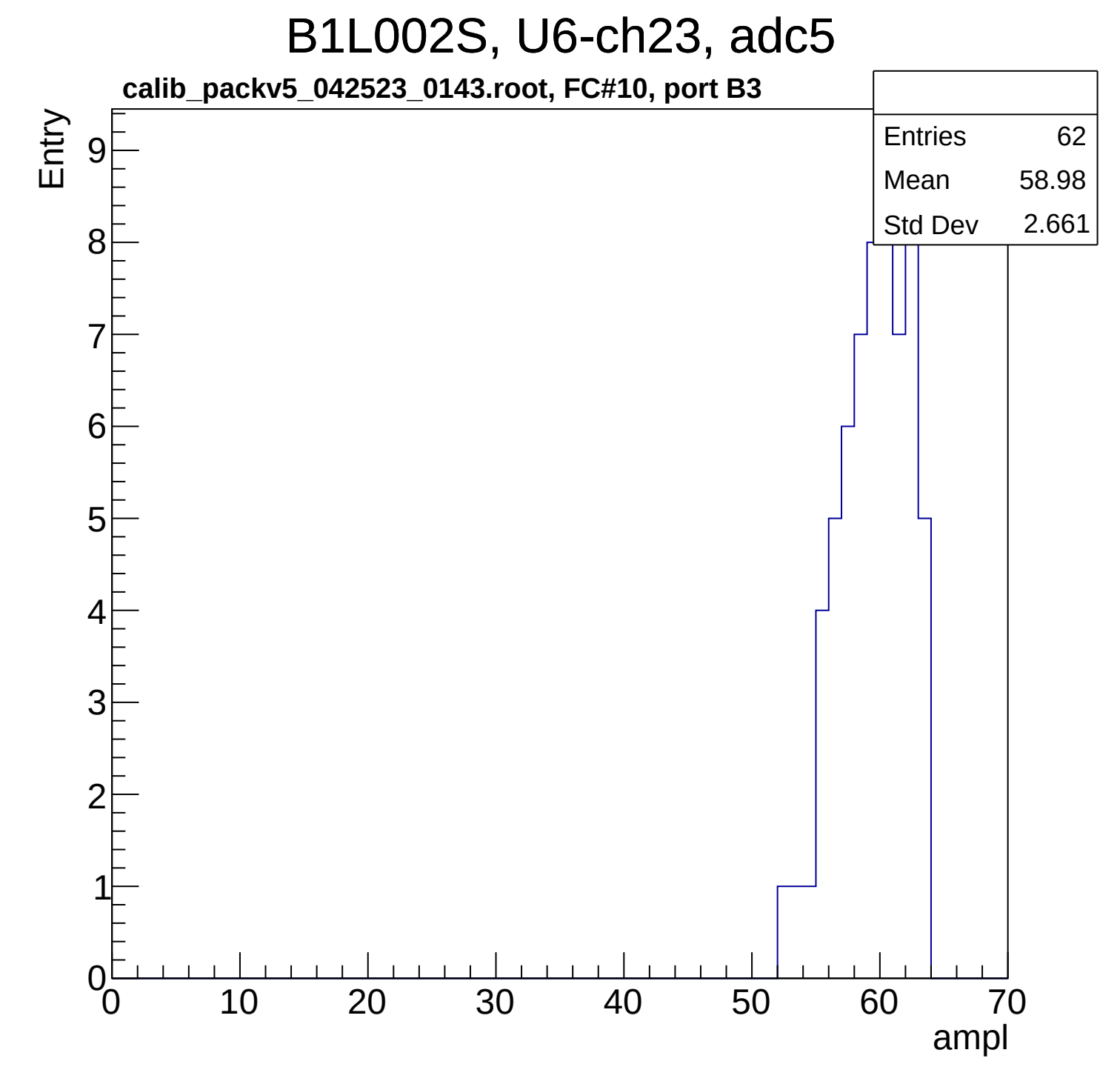

B1L002S, U6-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.25
Std Dev 3.296
Gaus mean : 27.7324
Gaus Width: 3.3831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

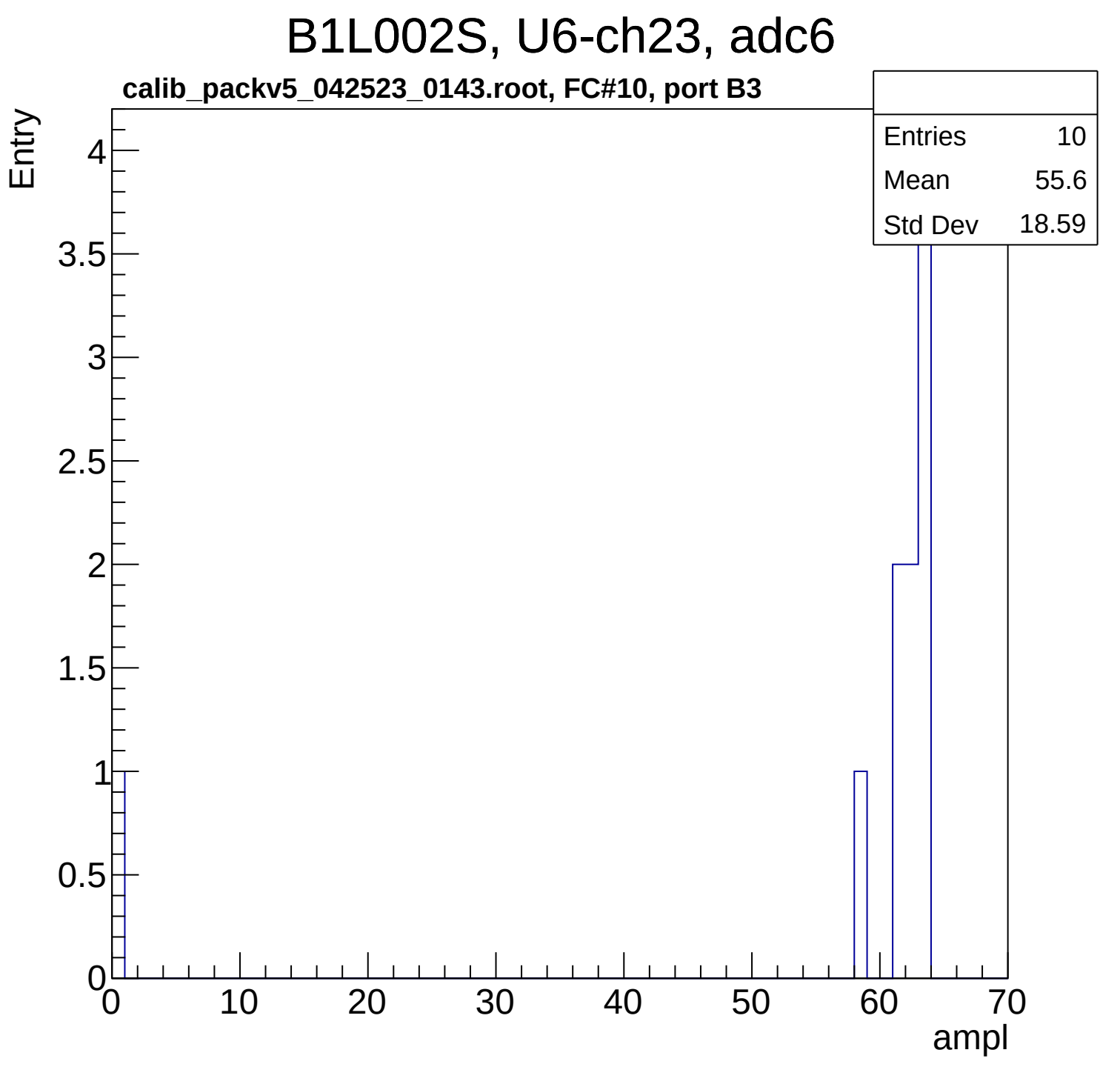

B1L002S, U6-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.23
Std Dev 3.247
Gaus mean : 35.6165
Gaus Width: 3.2780
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

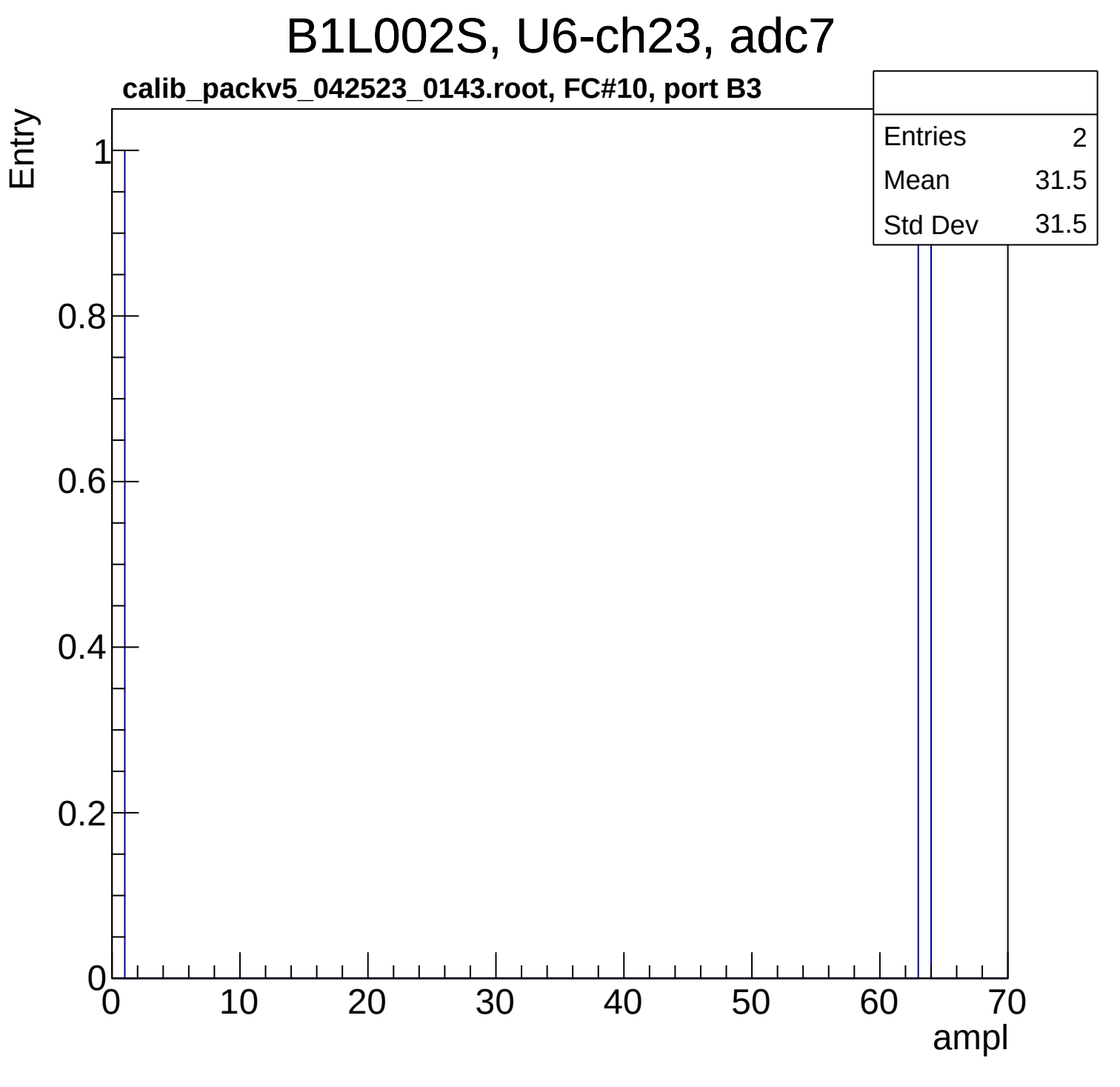

B1L002S, U6-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.76
Std Dev 3.187
Gaus mean : 42.1858
Gaus Width: 3.2709
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

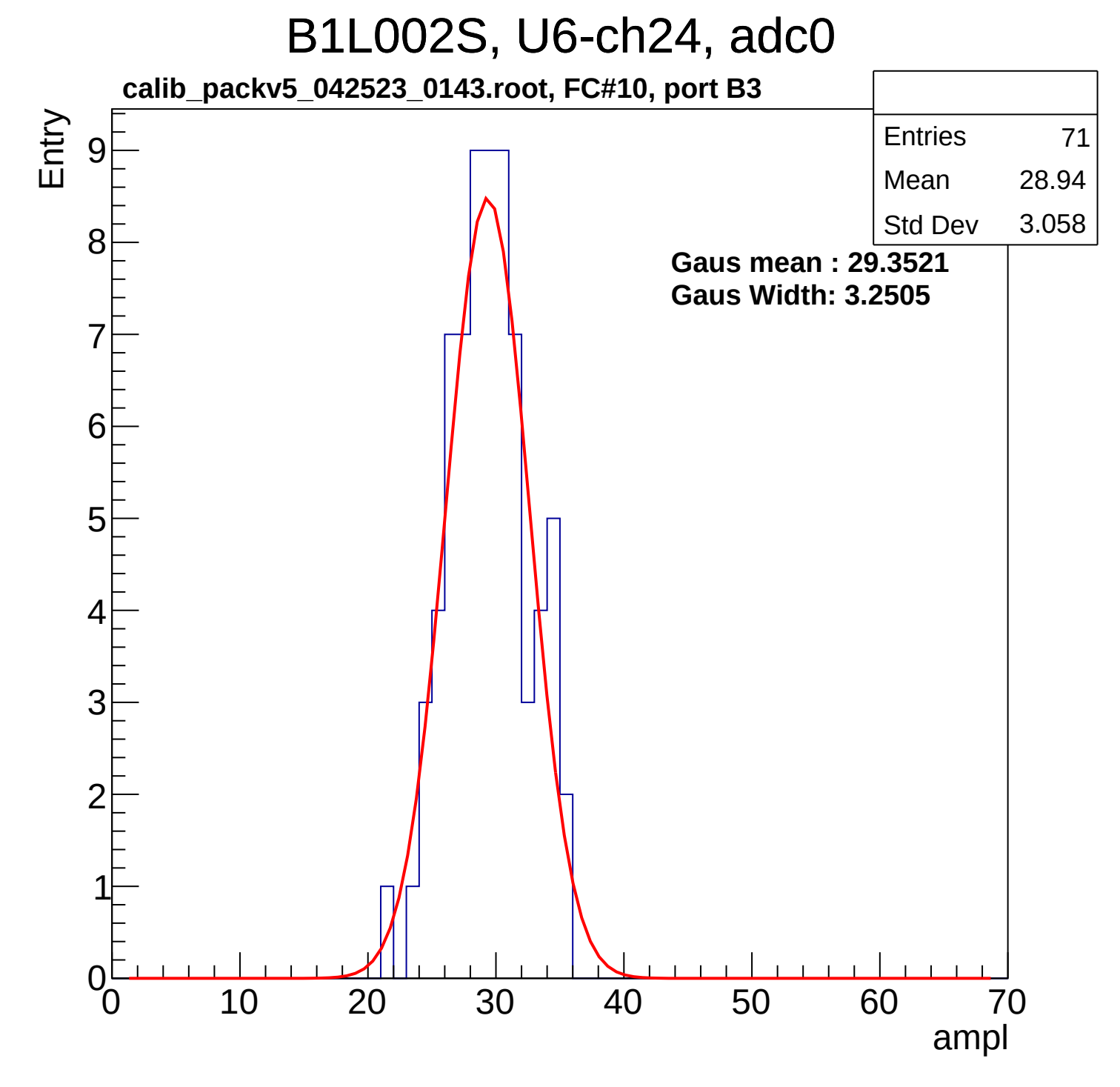

B1L002S, U6-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.37
Std Dev 2.895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

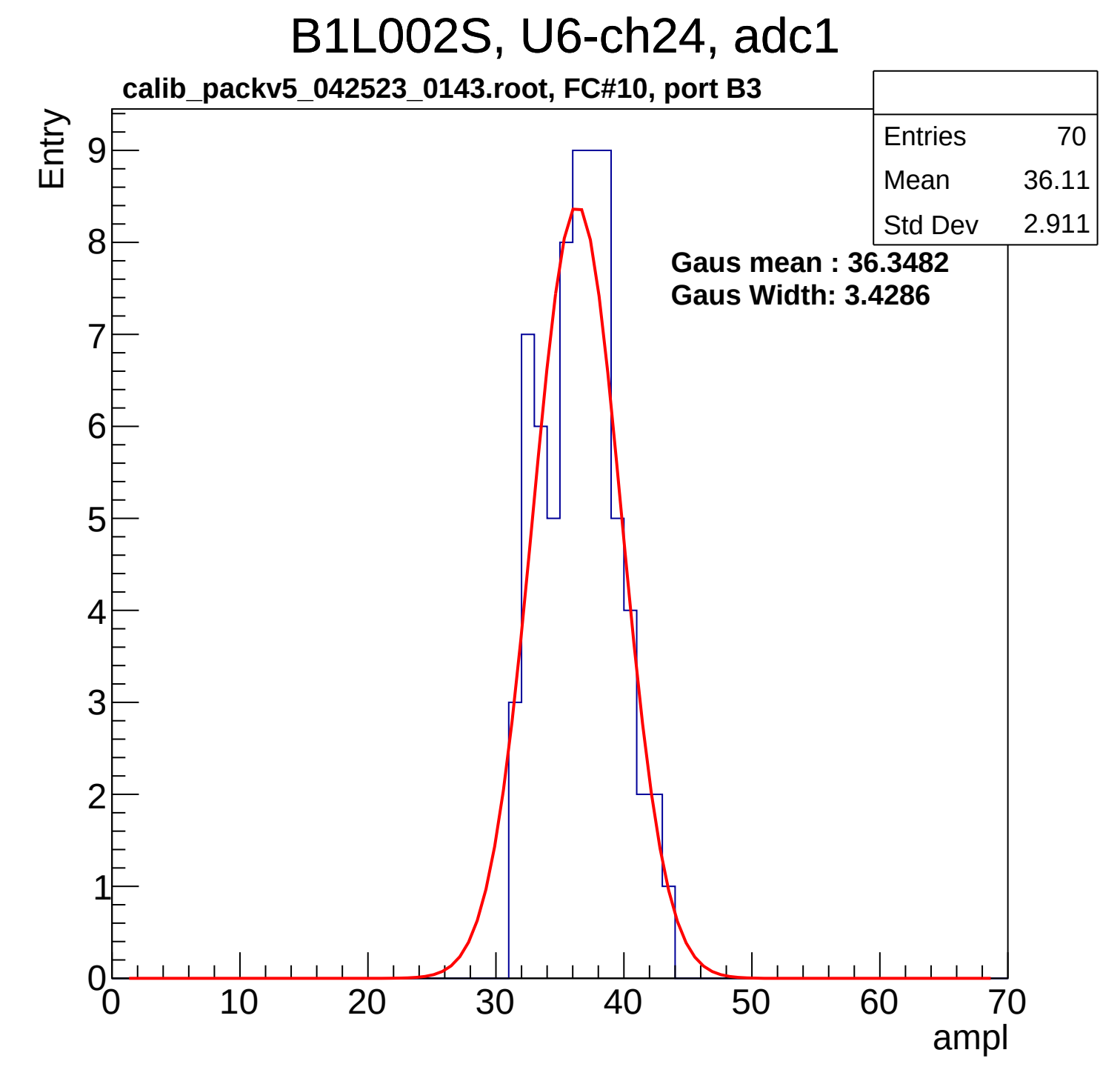

B1L002S, U6-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.69
Std Dev 2.962
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

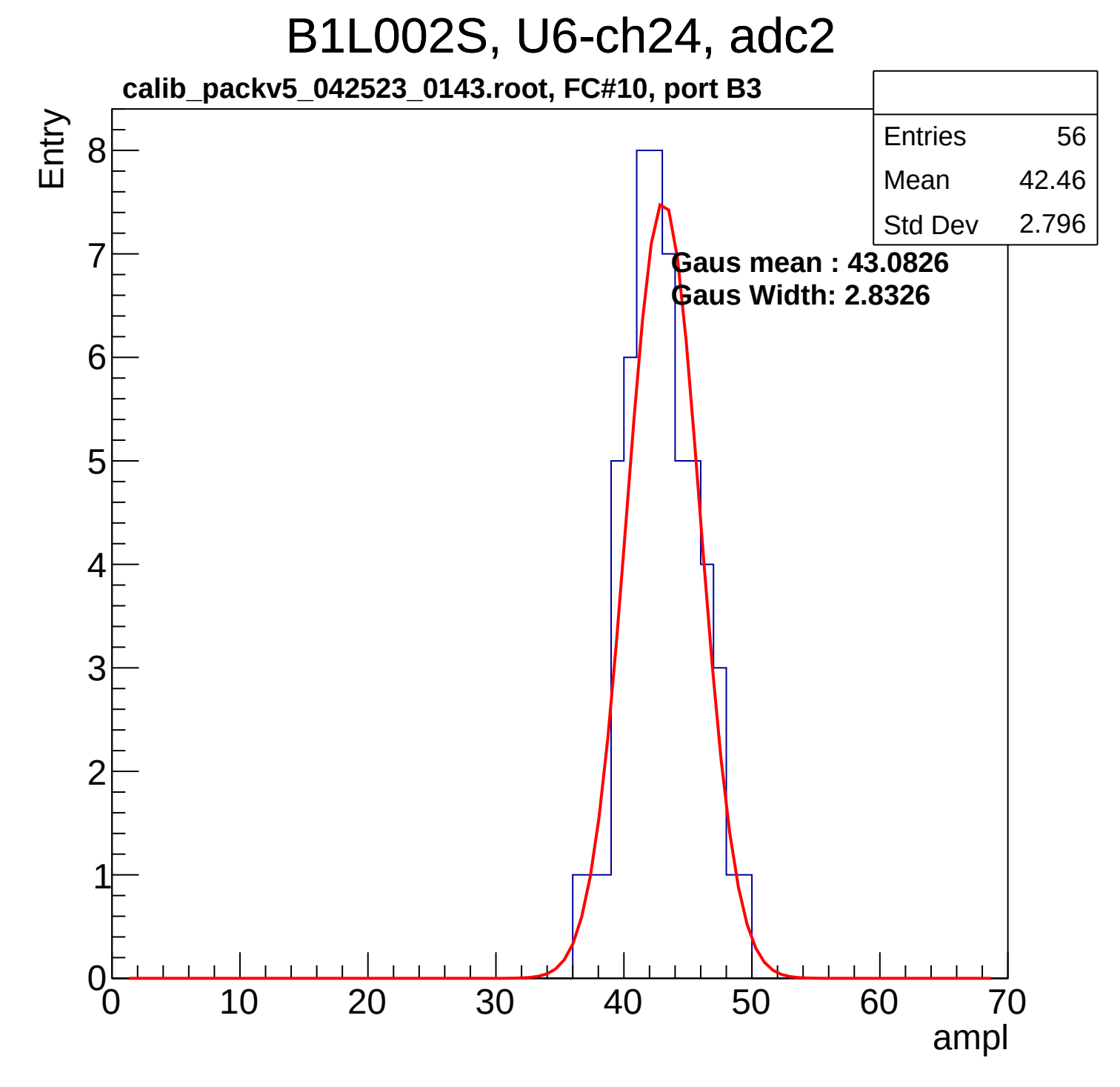

B1L002S, U6-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.98
Std Dev 2.661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

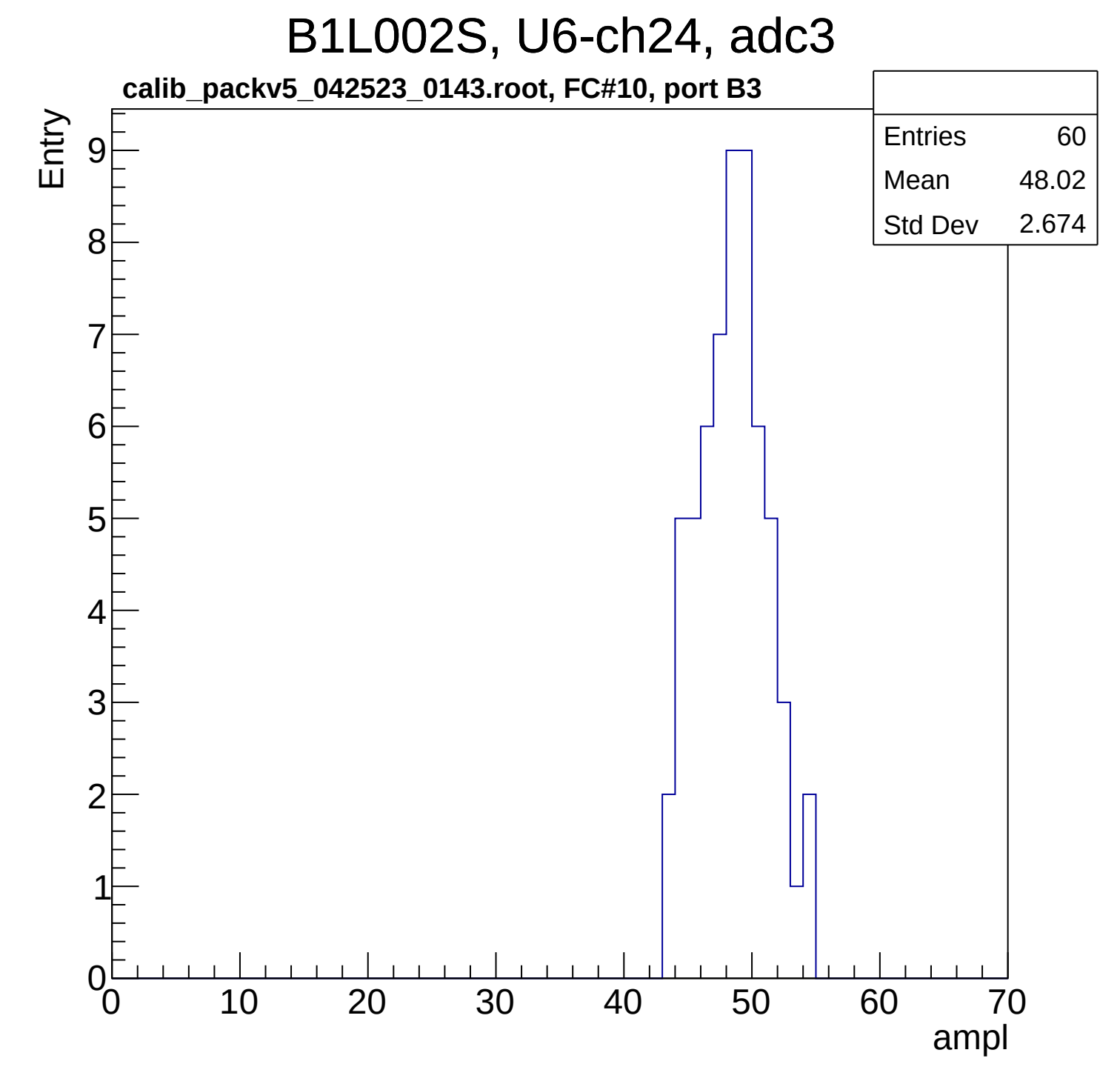

B1L002S, U6-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.6
Std Dev 18.59
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

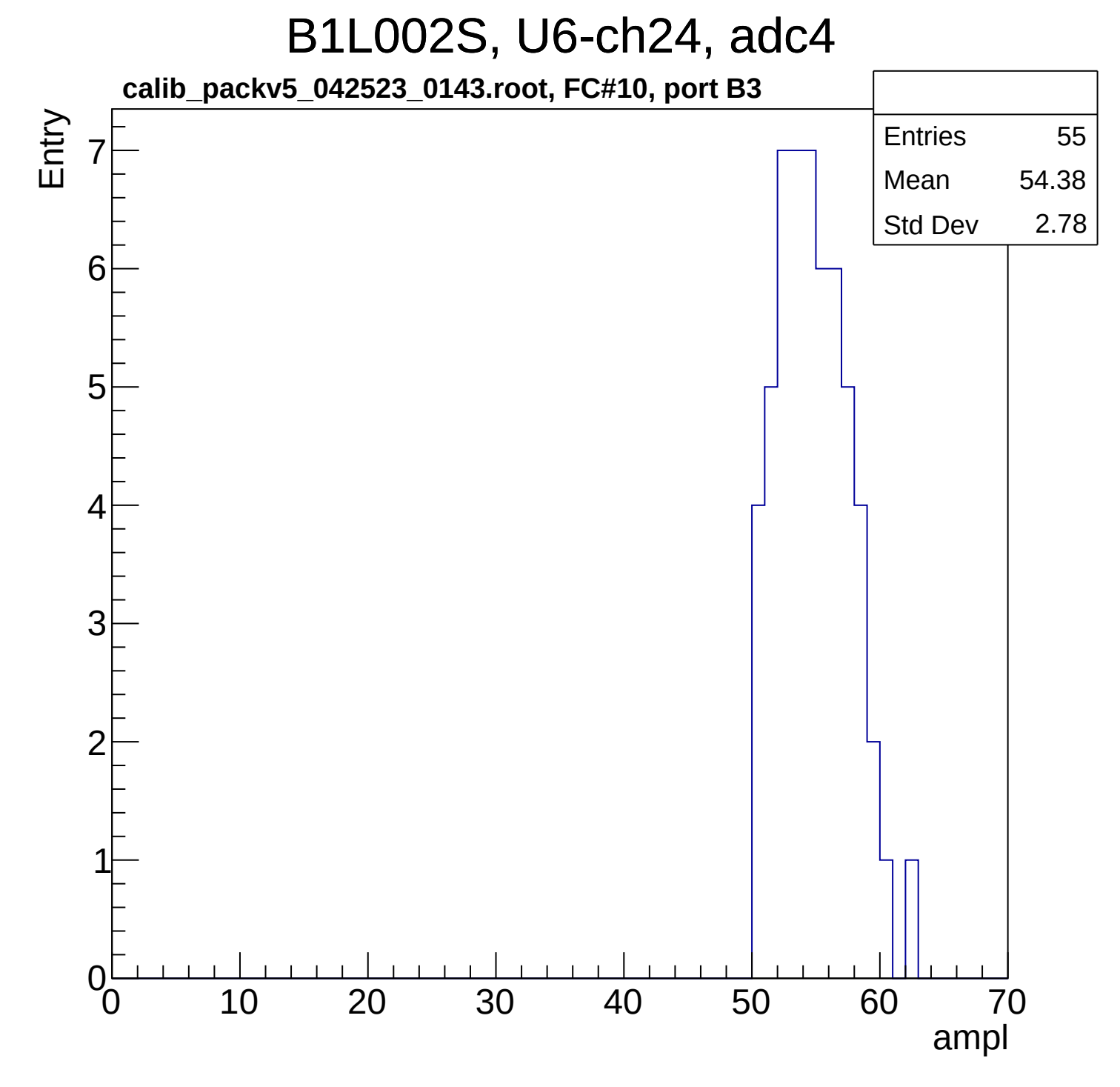

B1L002S, U6-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

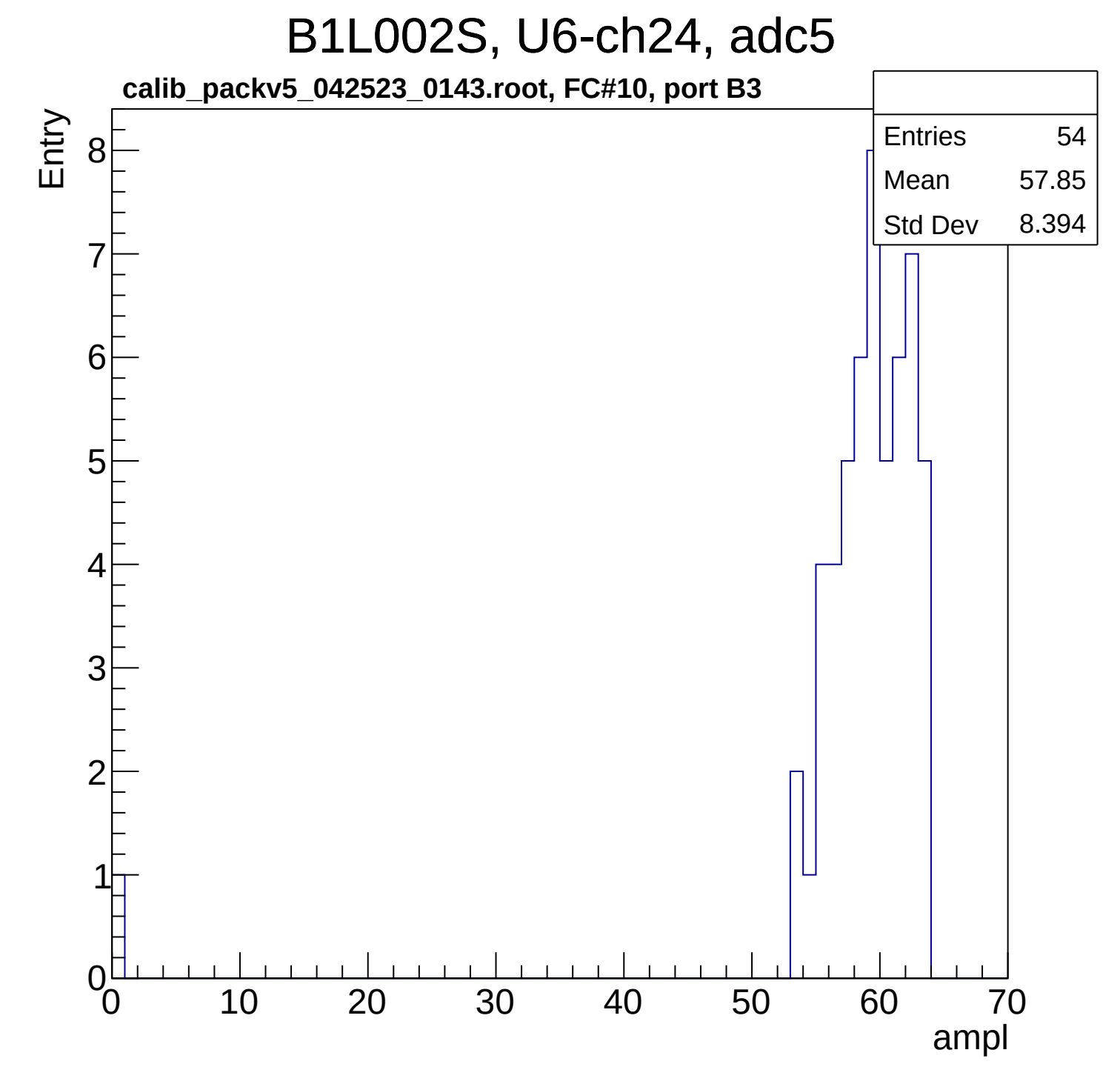

B1L002S, U6-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.94
Std Dev 3.058
Gaus mean : 29.3521
Gaus Width: 3.2505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

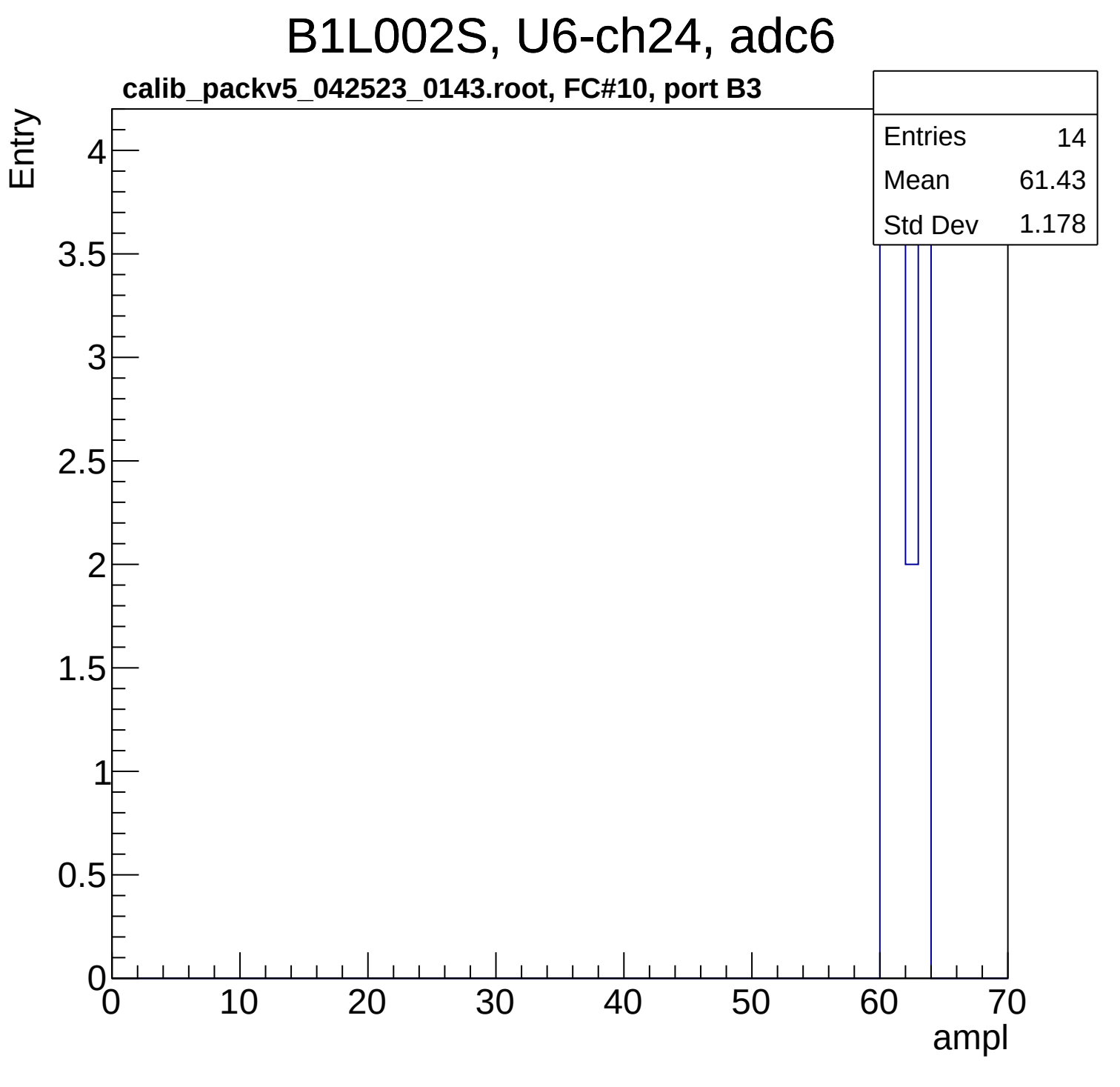

B1L002S, U6-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 36.11
Std Dev 2.911
Gaus mean : 36.3482
Gaus Width: 3.4286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

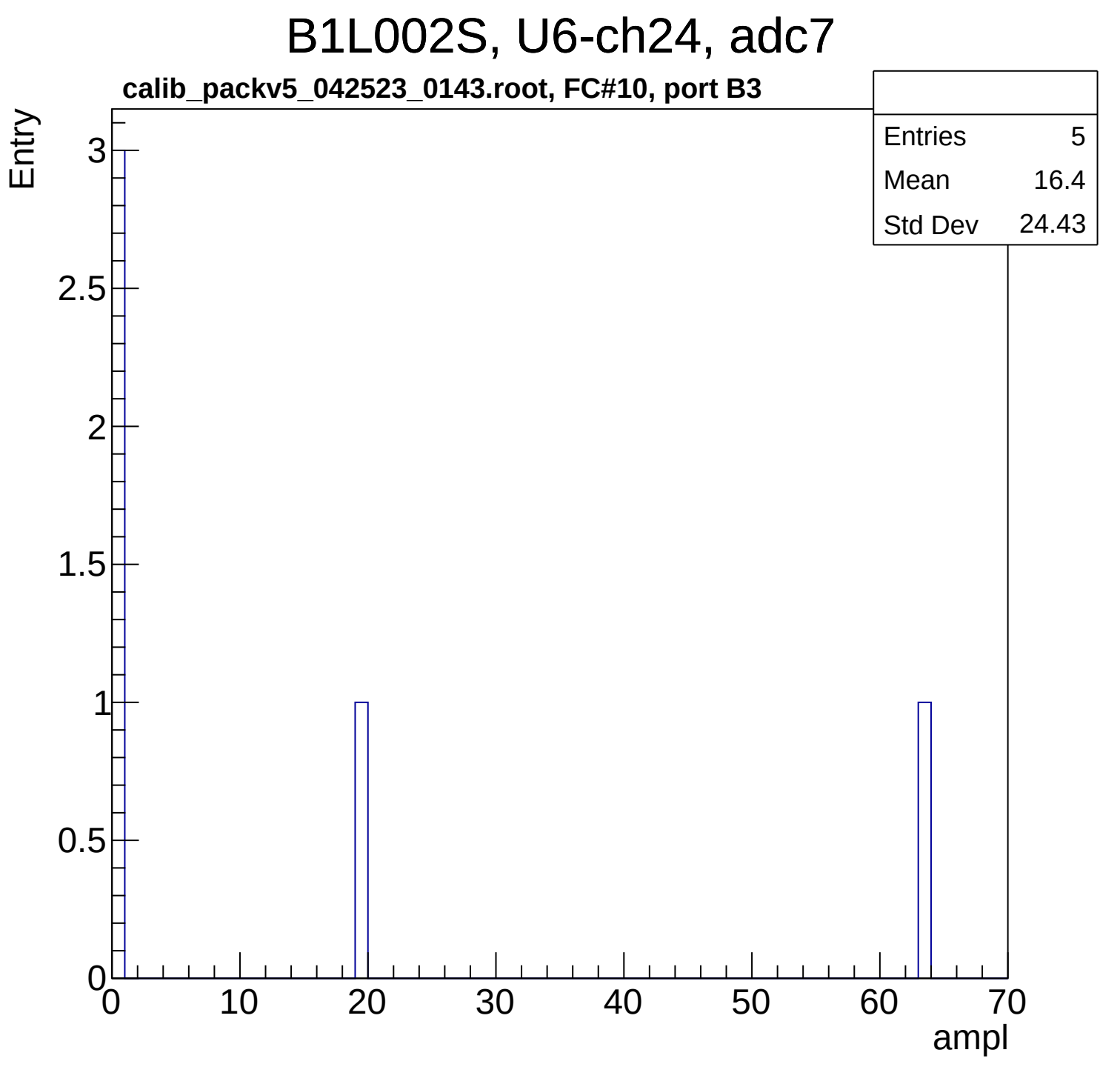

B1L002S, U6-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.46
Std Dev 2.796
Gaus mean : 43.0826
Gaus Width: 2.8326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

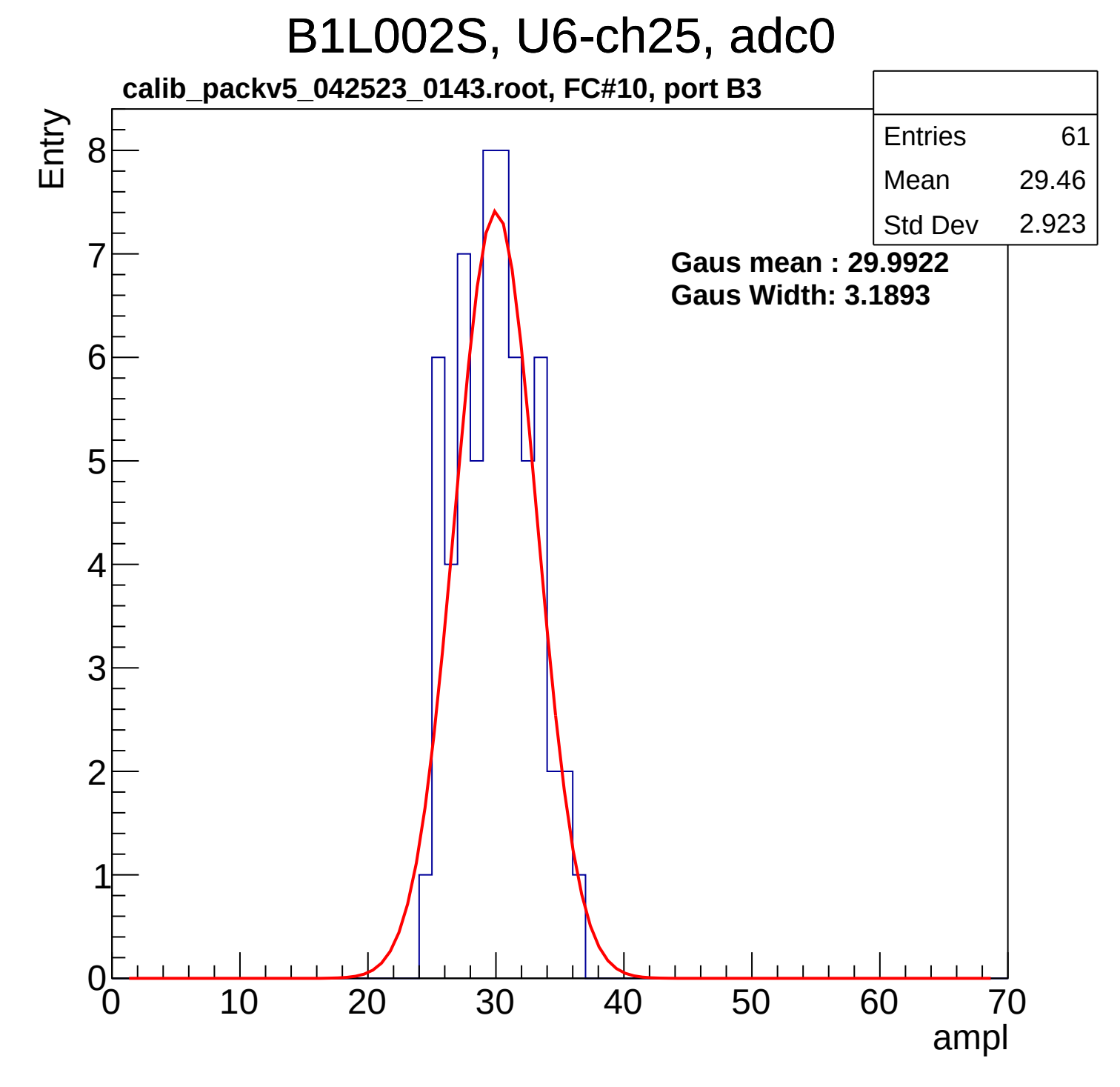

B1L002S, U6-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.02
Std Dev 2.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

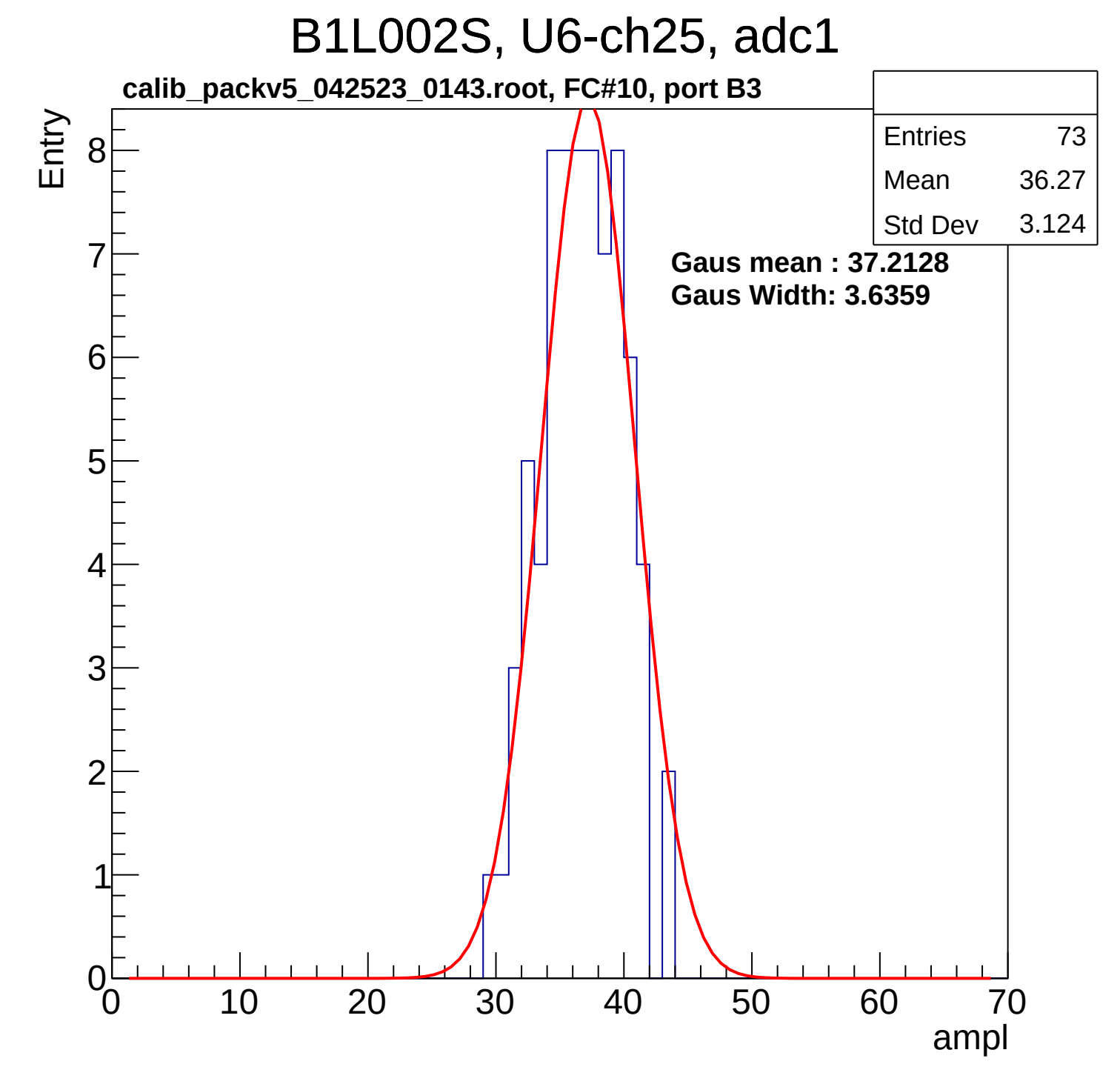

B1L002S, U6-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.38
Std Dev 2.78
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

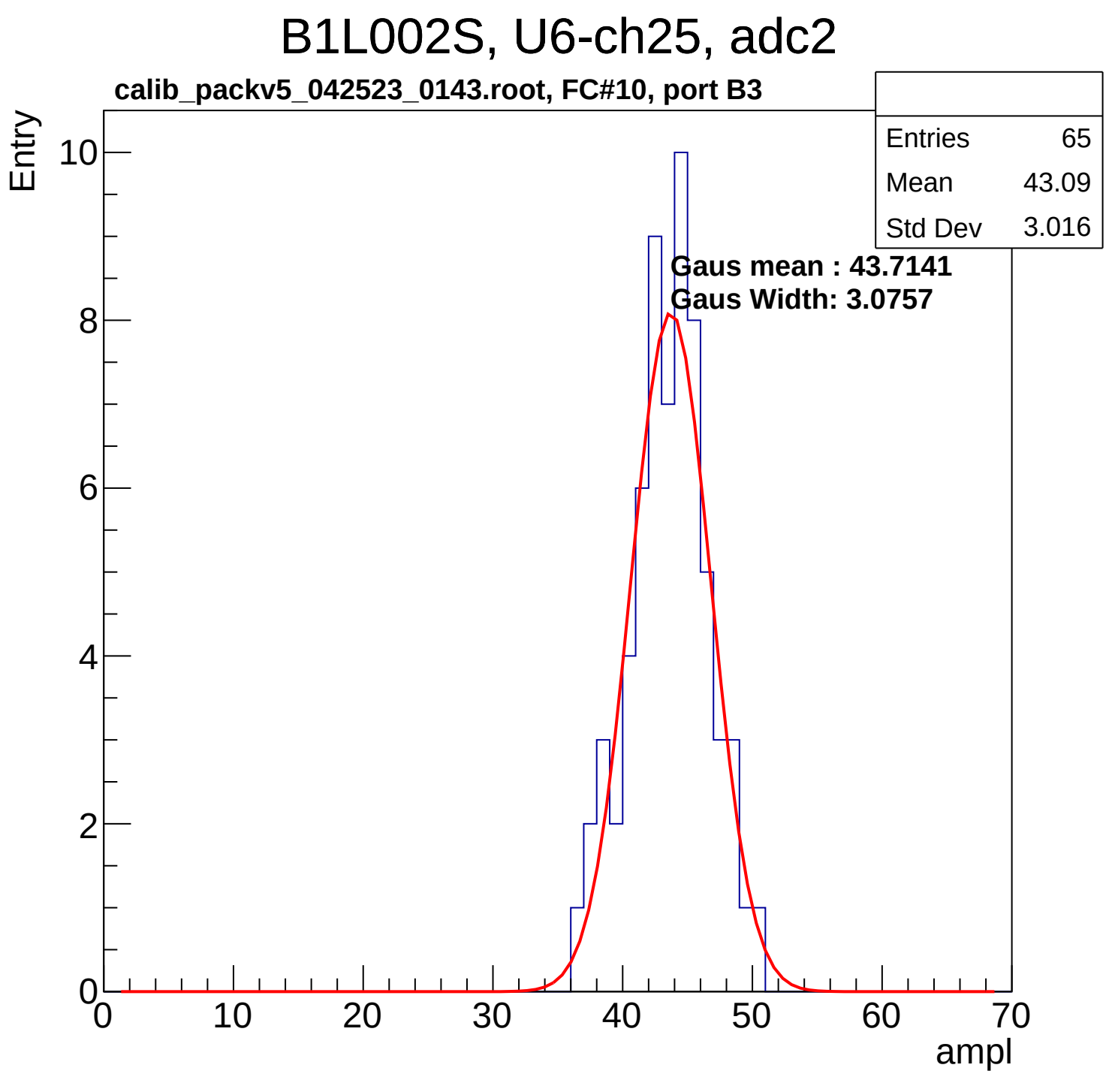

B1L002S, U6-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.85
Std Dev 8.394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

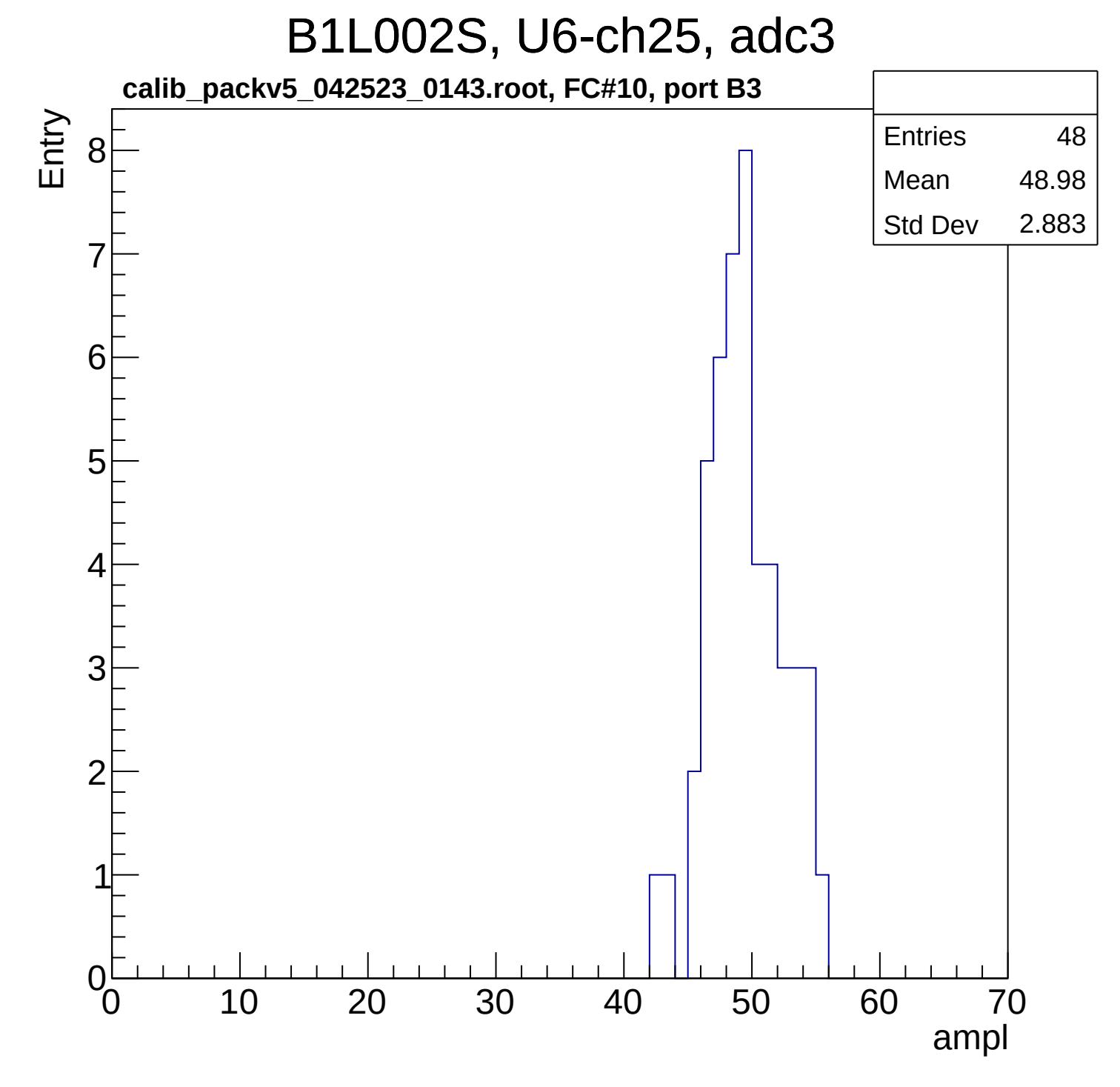

B1L002S, U6-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.43
Std Dev 1.178
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

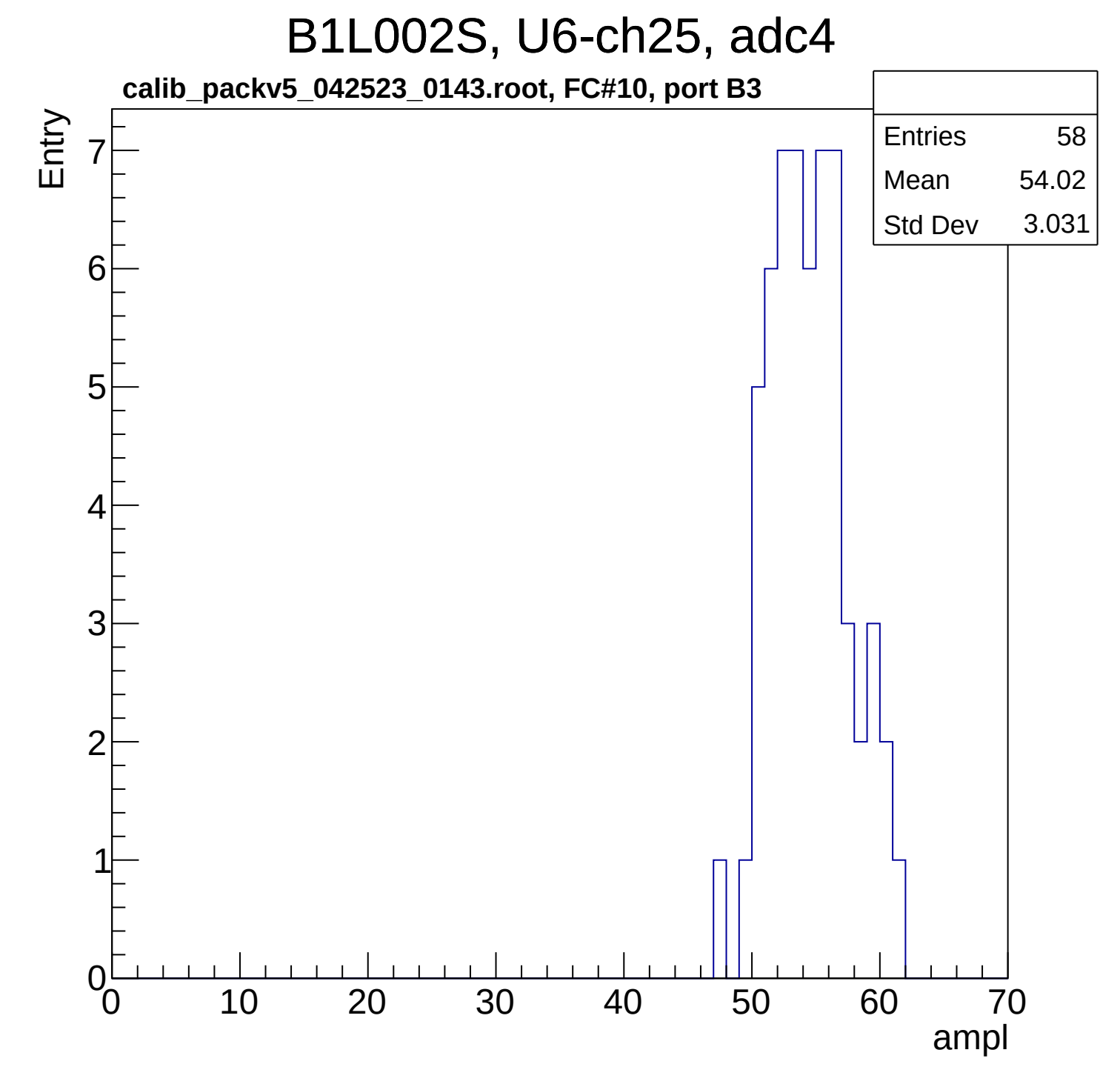

B1L002S, U6-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 16.4
Std Dev 24.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

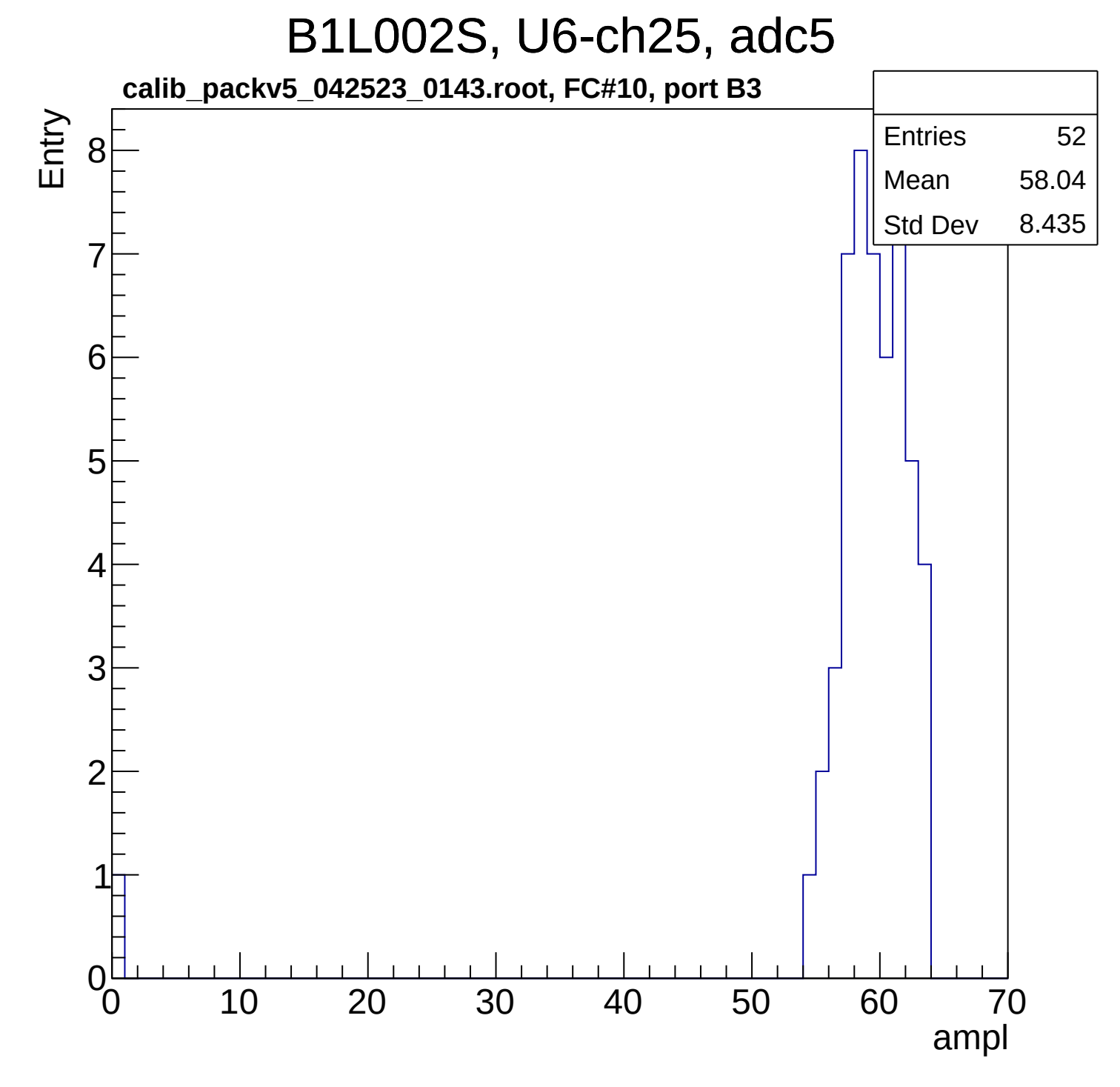

B1L002S, U6-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.46
Std Dev 2.923
Gaus mean : 29.9922
Gaus Width: 3.1893
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

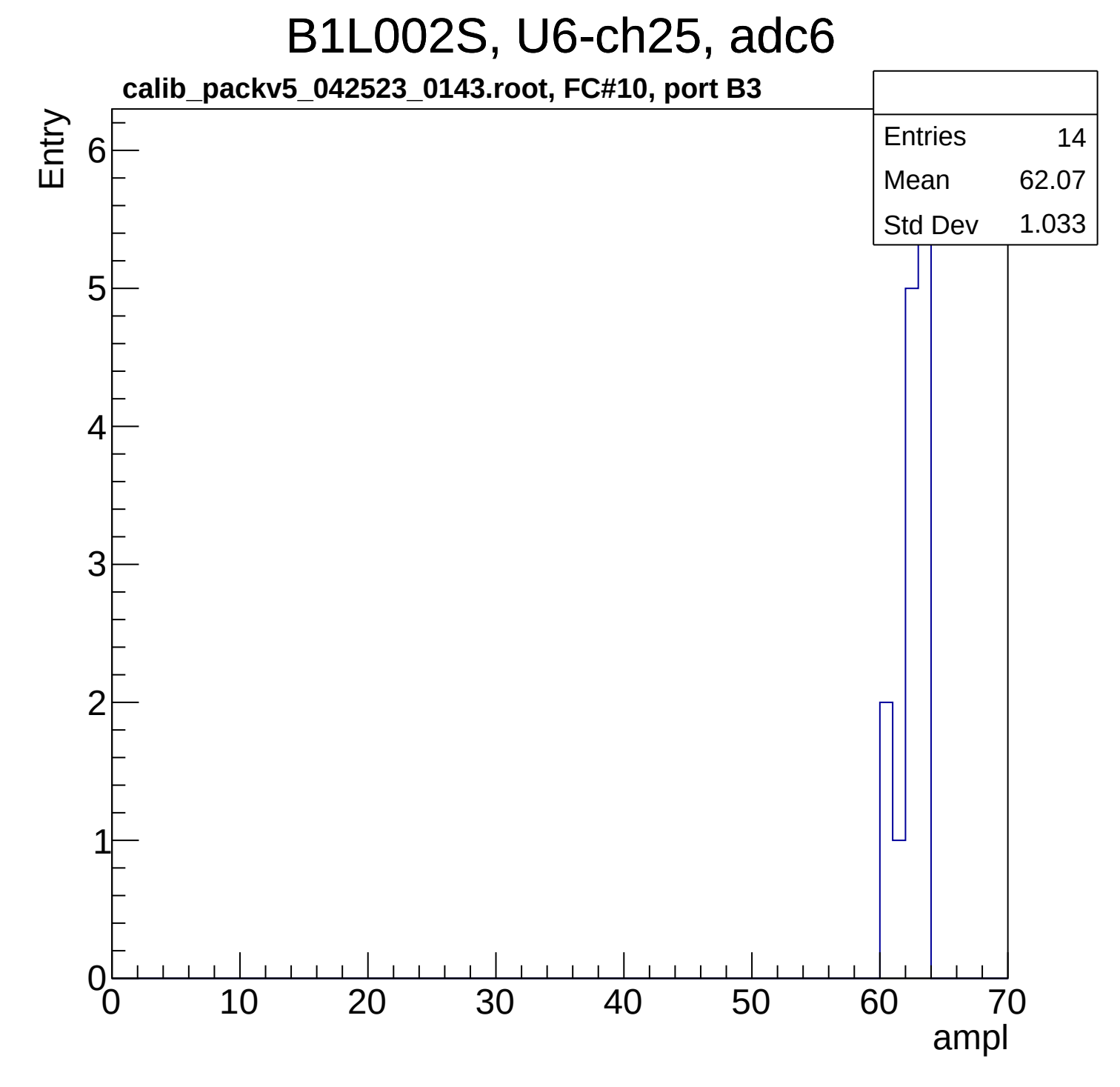

B1L002S, U6-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.27
Std Dev 3.124
Gaus mean : 37.2128
Gaus Width: 3.6359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

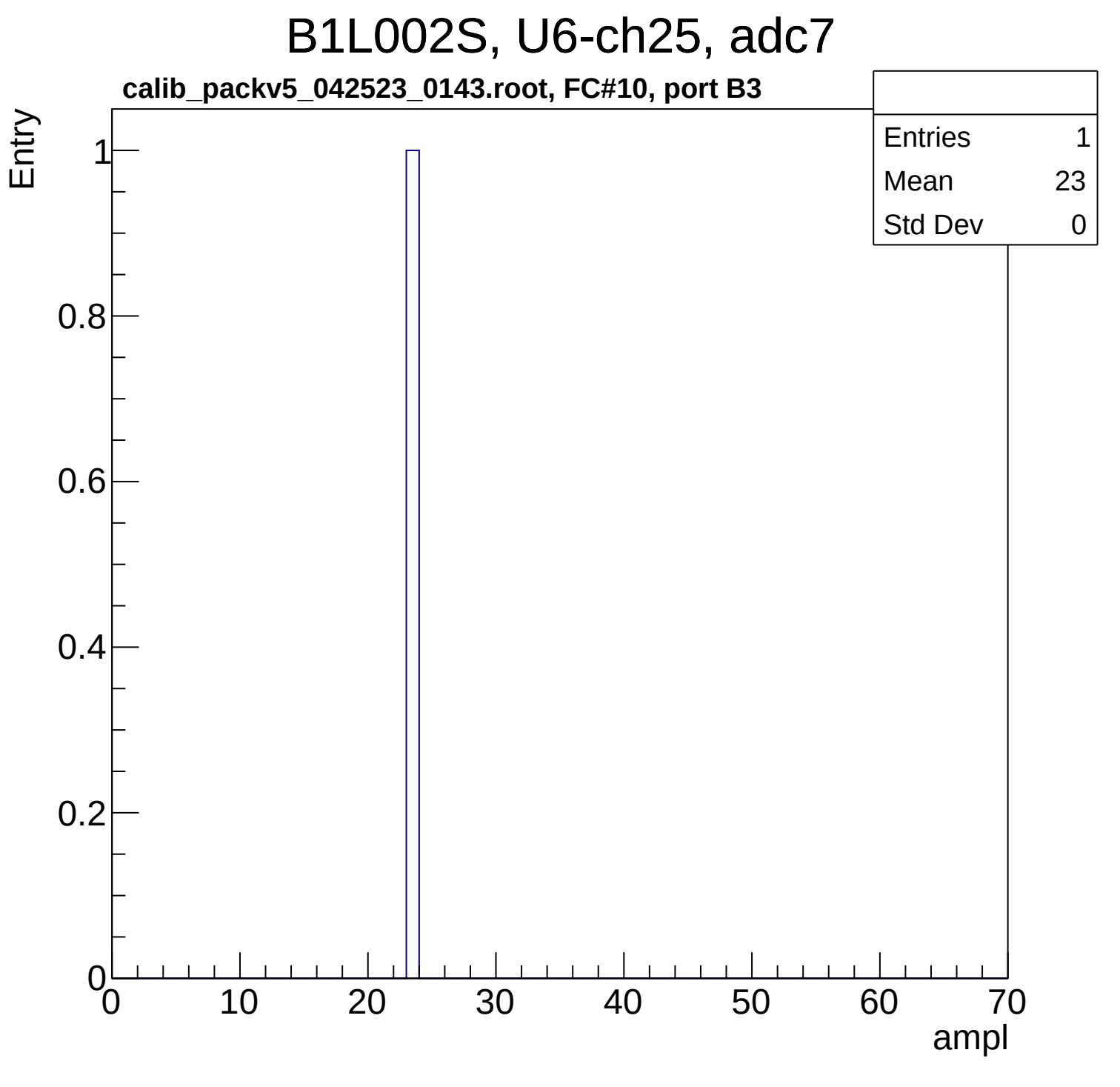

B1L002S, U6-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 43.09
Std Dev 3.016
Gaus mean : 43.7141
Gaus Width: 3.0757
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

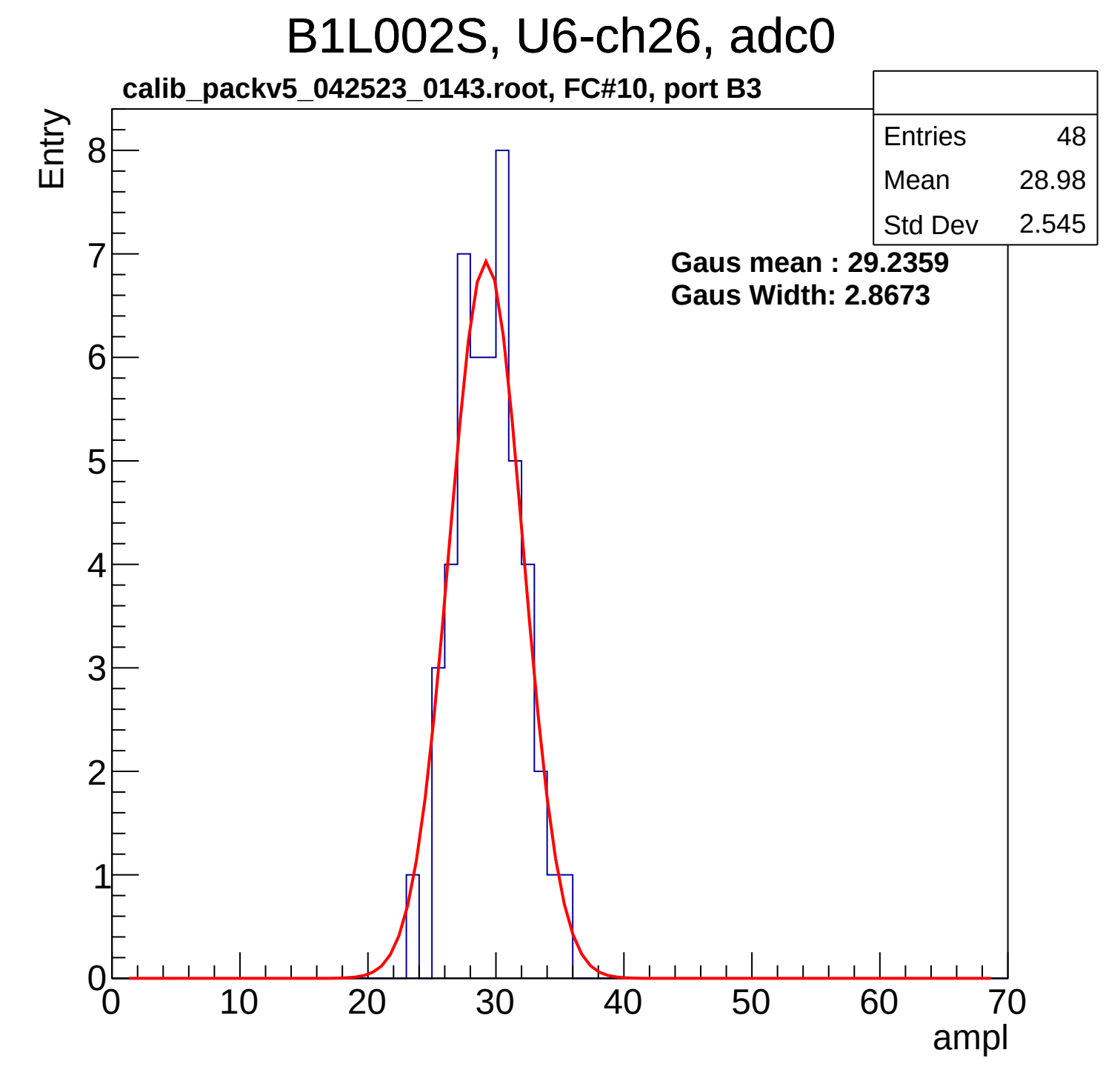

B1L002S, U6-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 48.98
Std Dev 2.883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

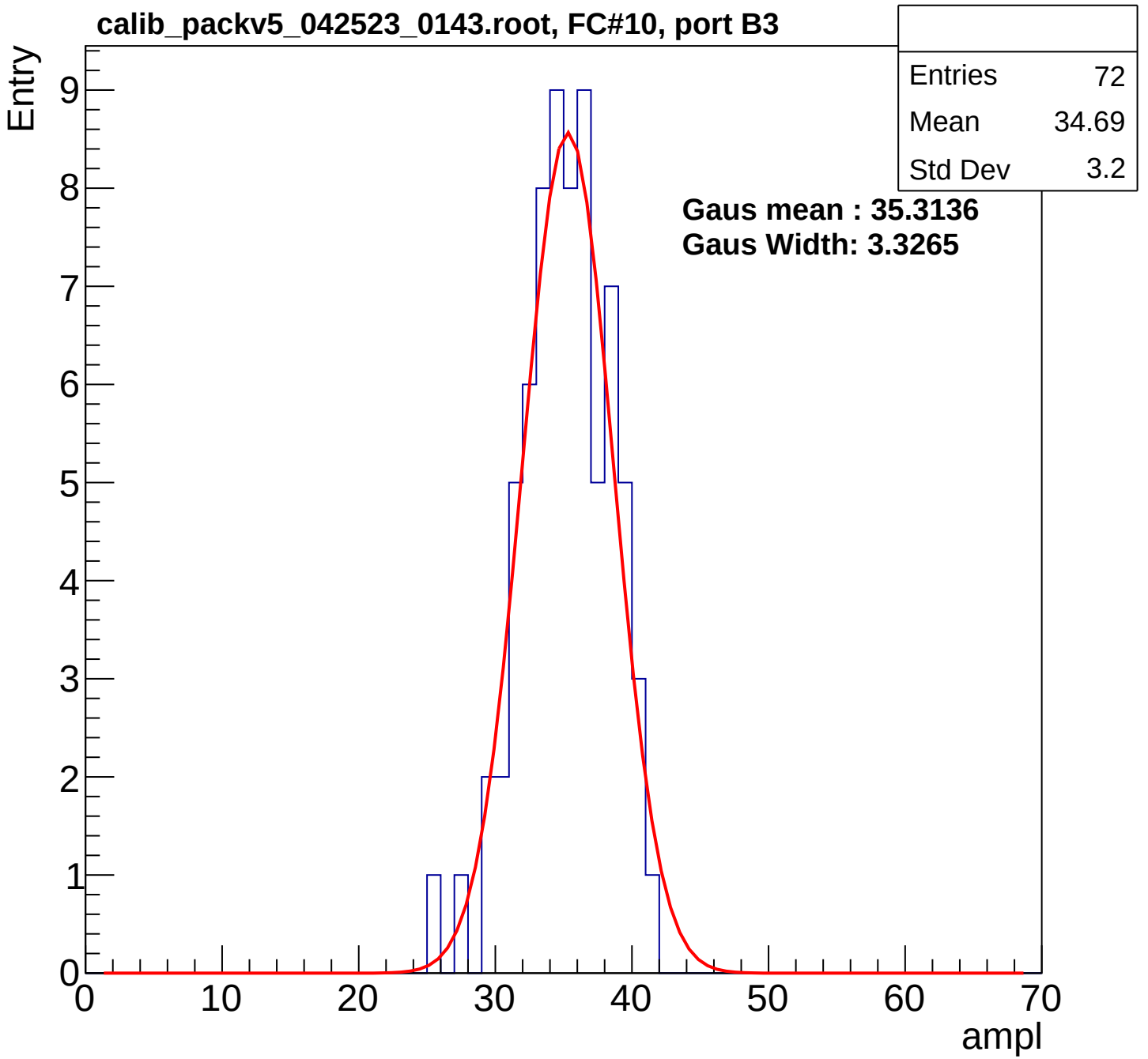

B1L002S, U6-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.02
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

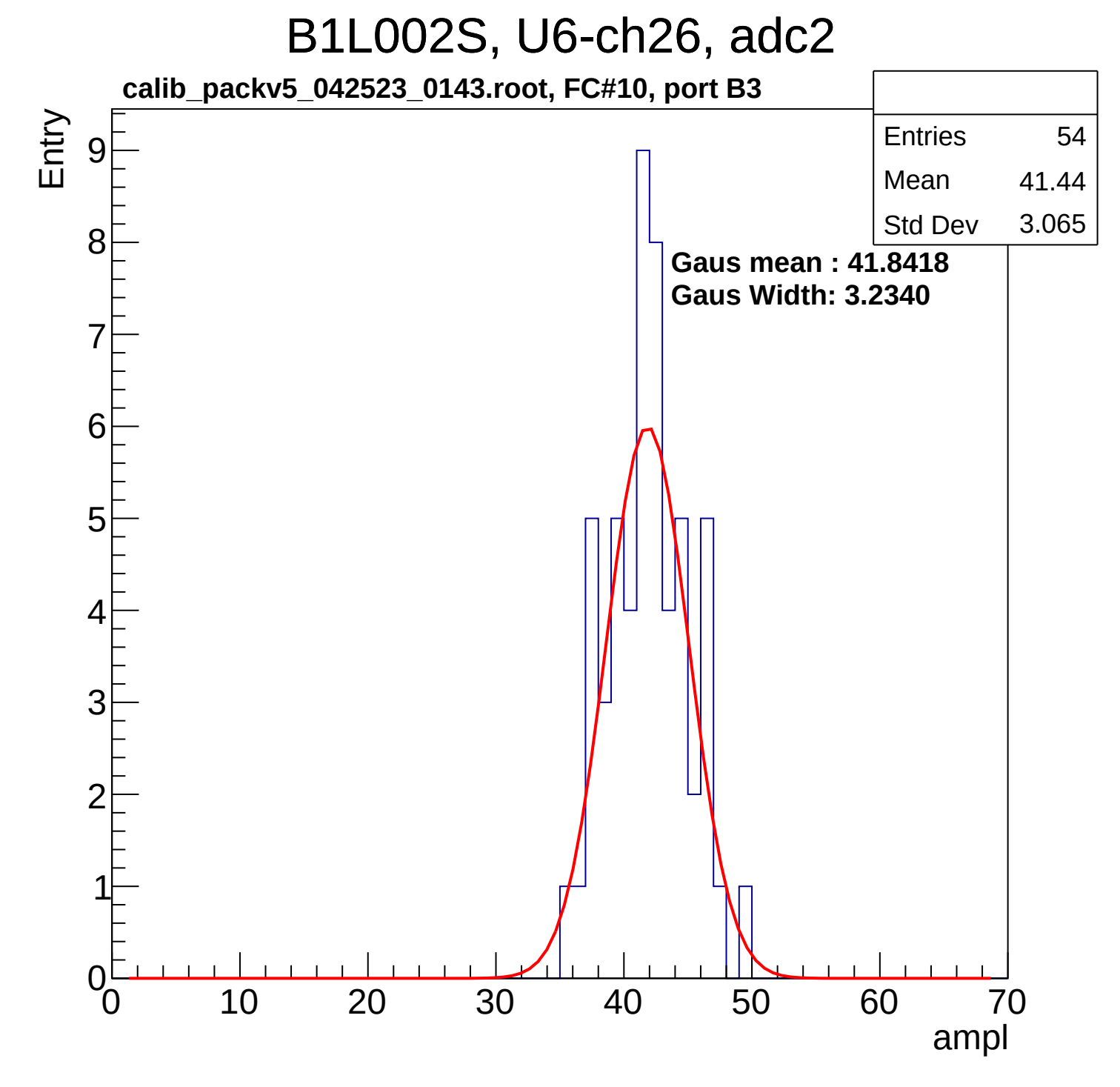

B1L002S, U6-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.04
Std Dev 8.435
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

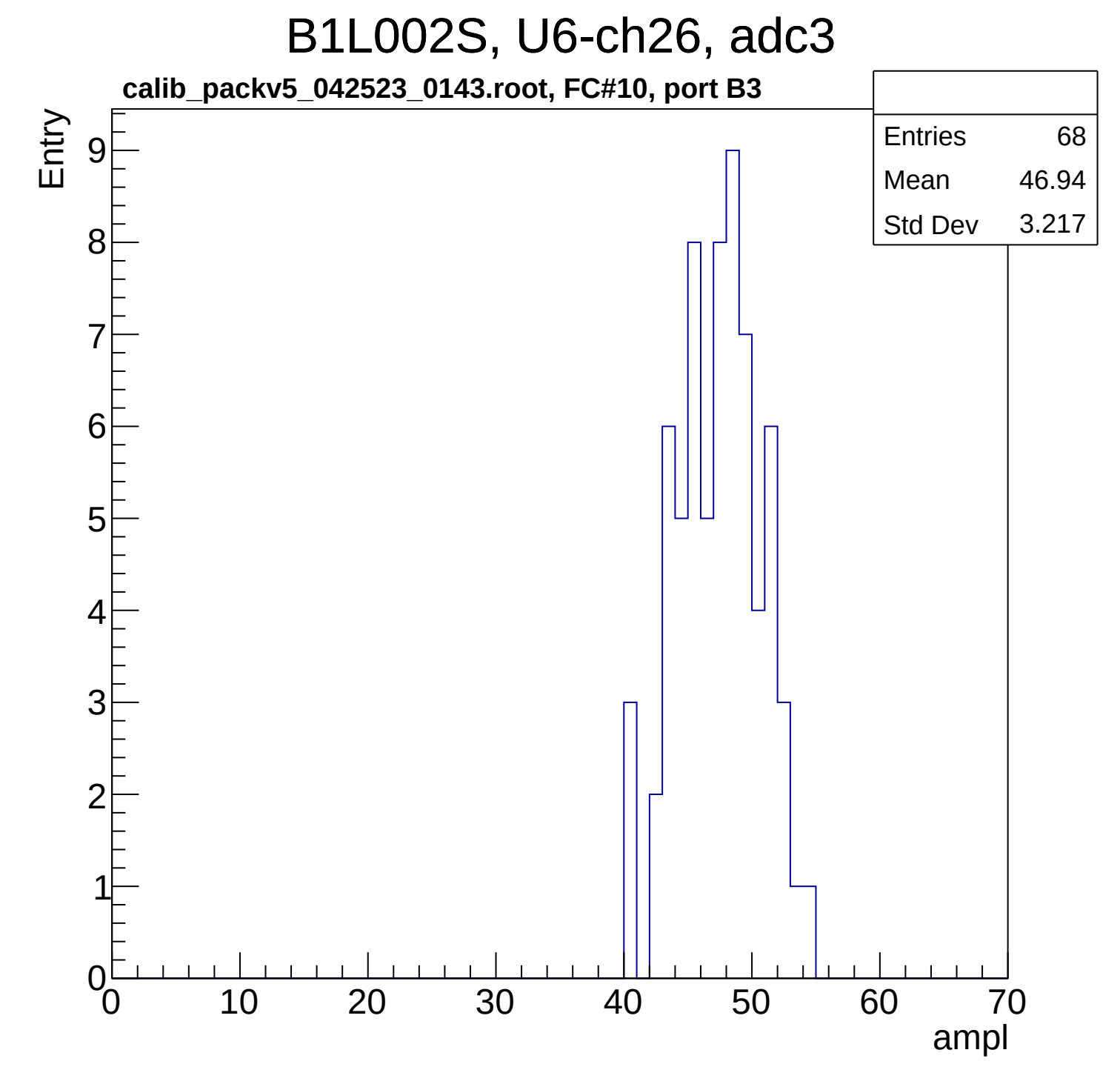

B1L002S, U6-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62.07
Std Dev 1.033
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

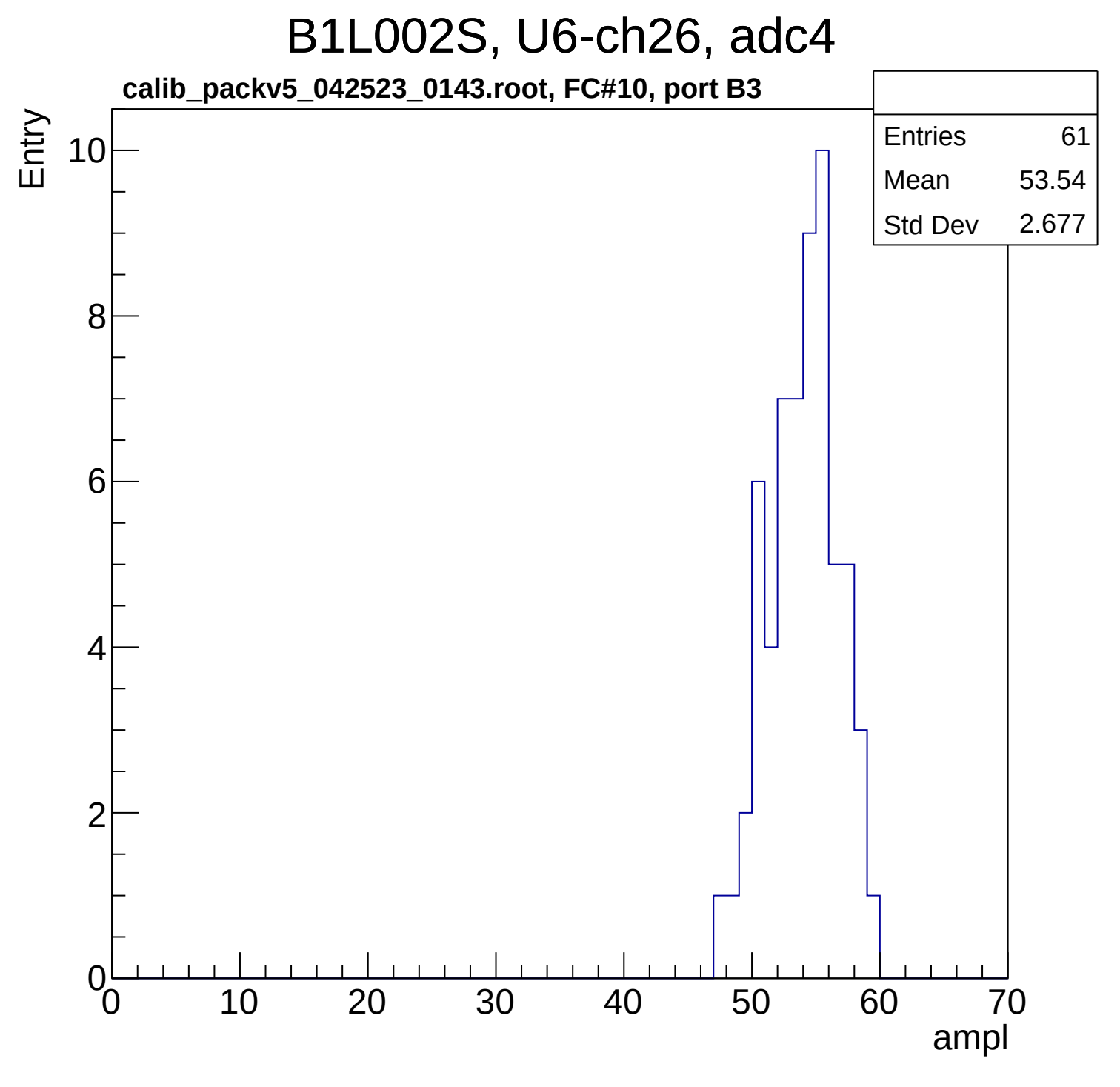

B1L002S, U6-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

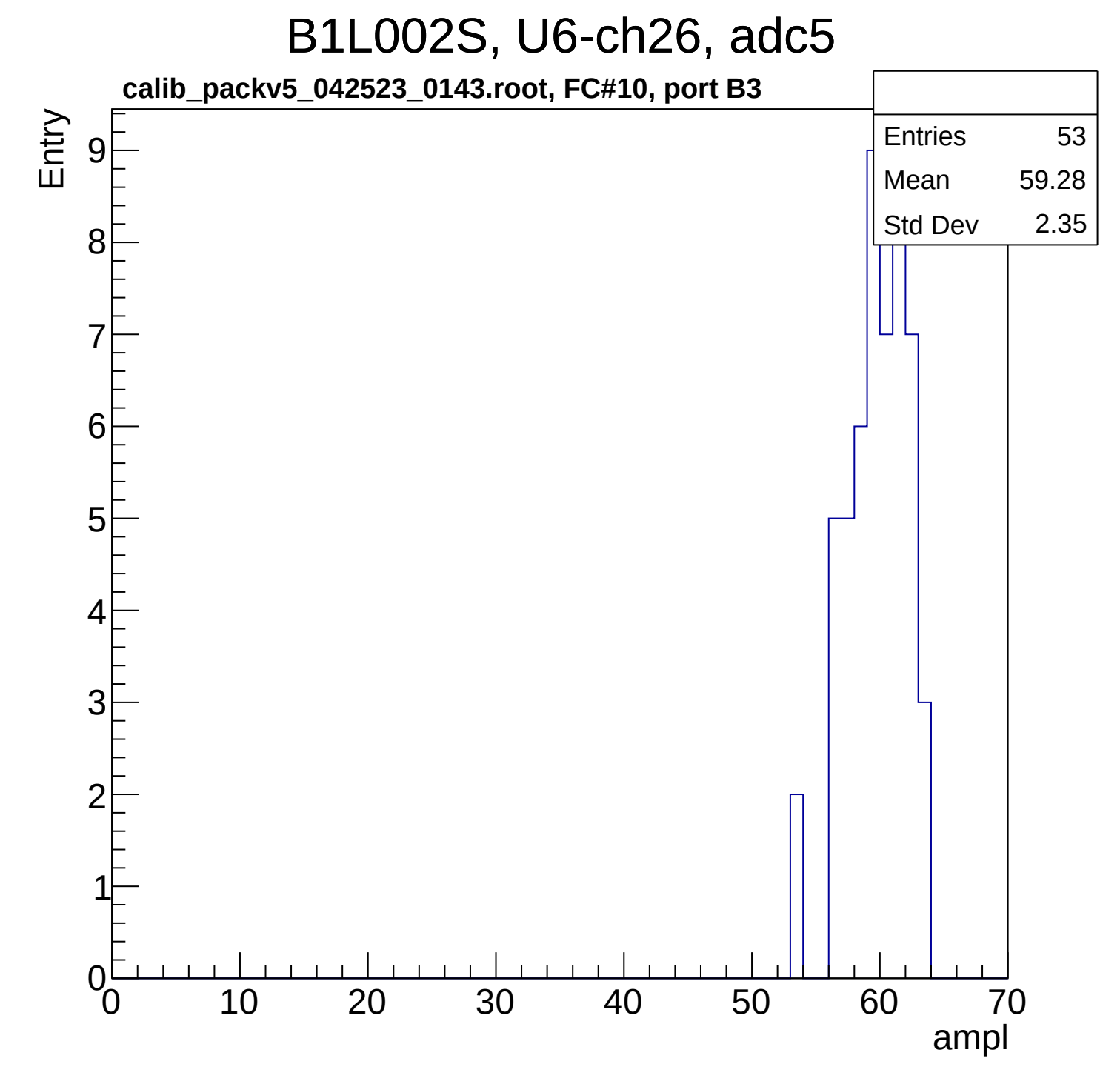

B1L002S, U6-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.98
Std Dev 2.545
Gaus mean : 29.2359
Gaus Width: 2.8673
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

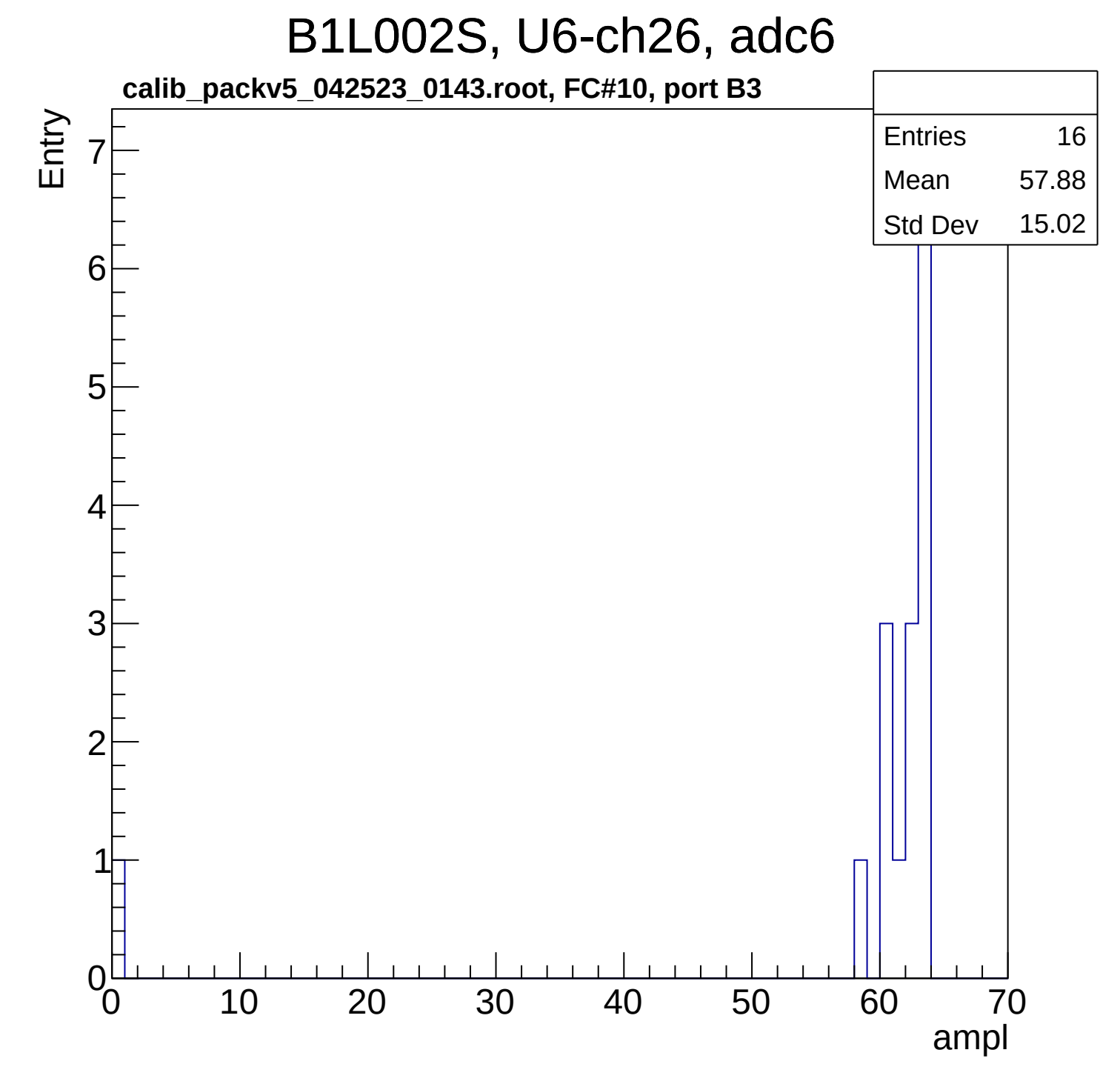

B1L002S, U6-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.69
Std Dev 3.2
Gaus mean : 35.3136
Gaus Width: 3.3265
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

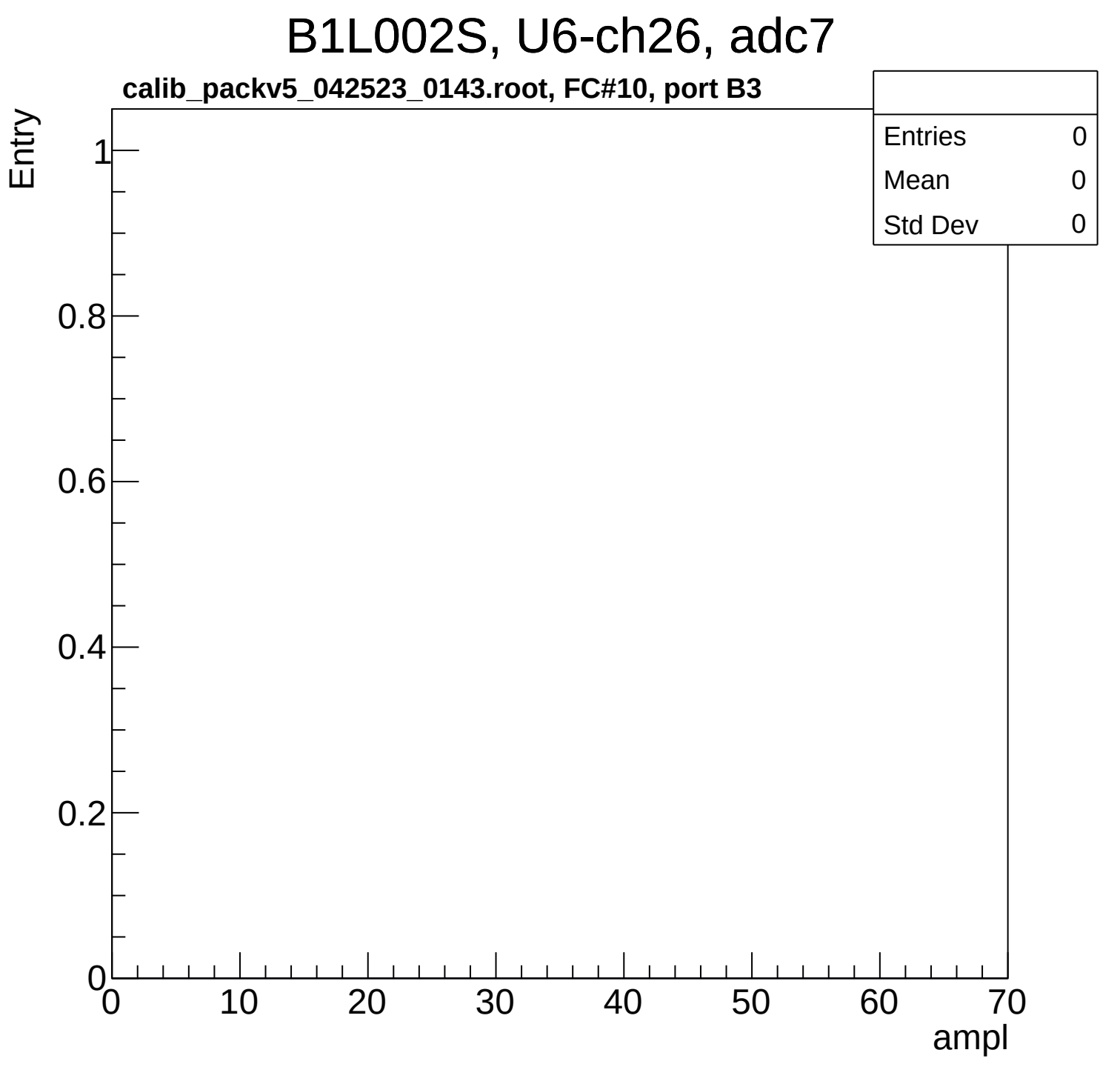

B1L002S, U6-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 41.44
Std Dev 3.065
Gaus mean : 41.8418
Gaus Width: 3.2340
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

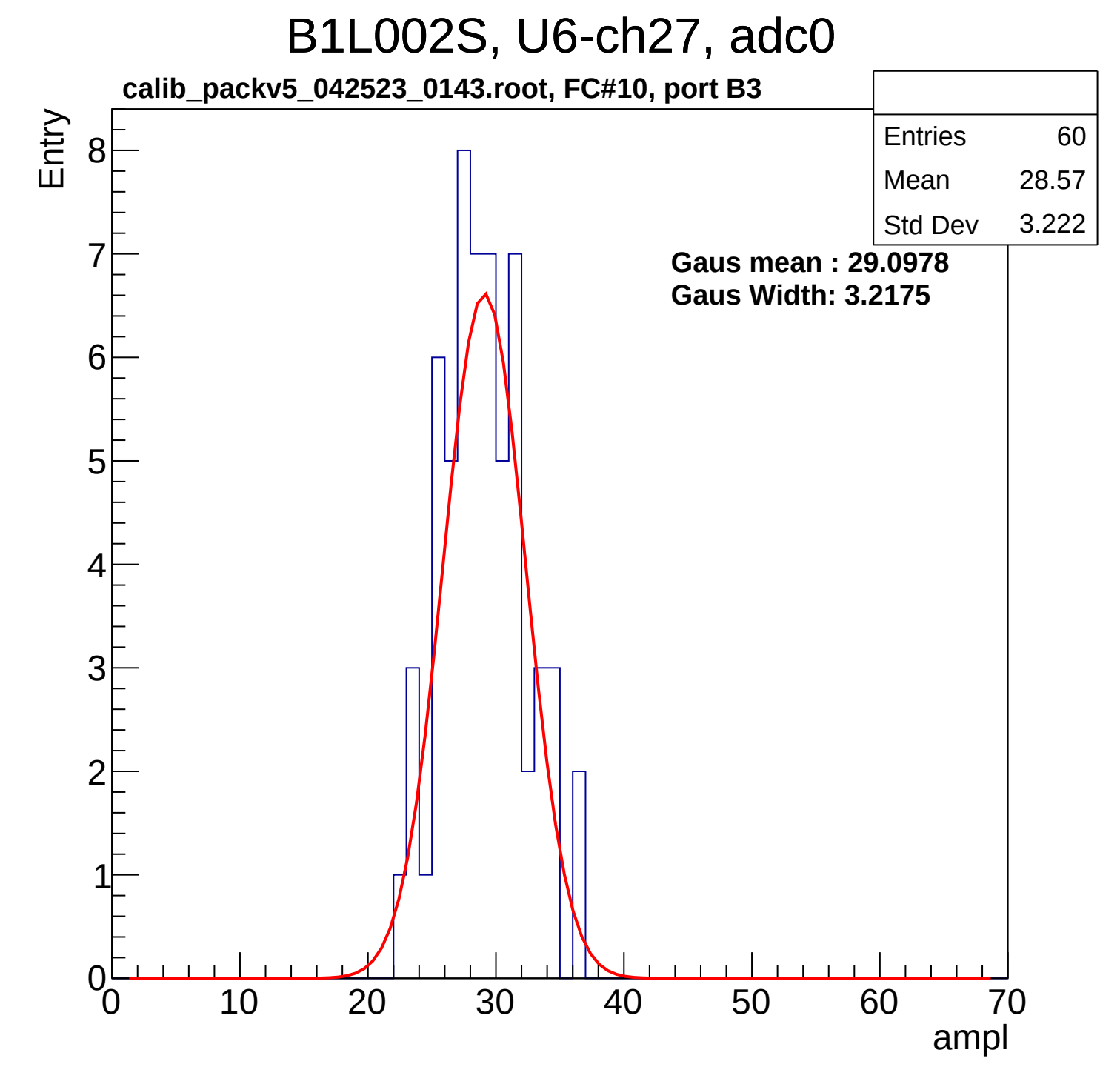

B1L002S, U6-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.94
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

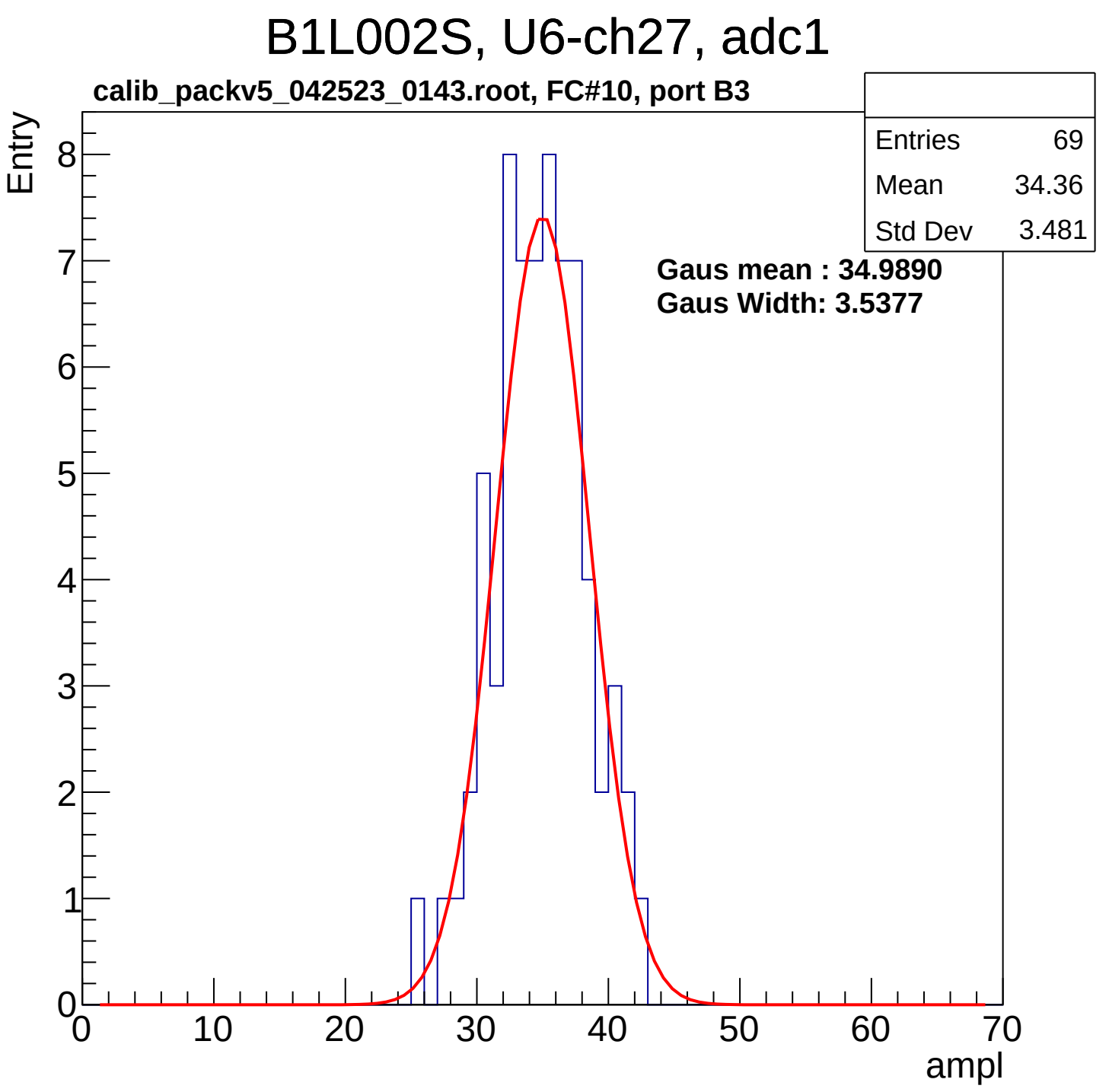

B1L002S, U6-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.54
Std Dev 2.677
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

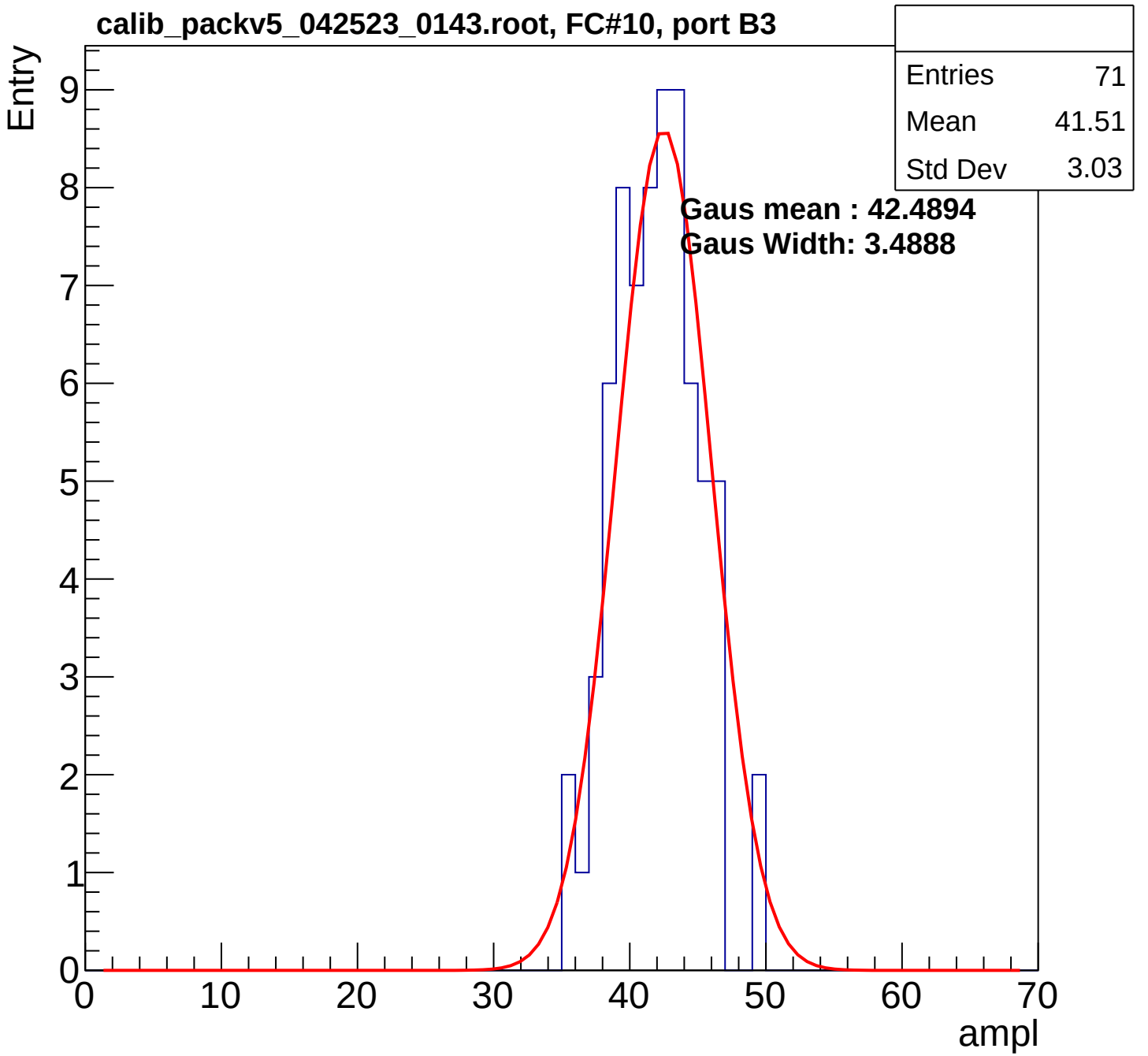

B1L002S, U6-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.28
Std Dev 2.35
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

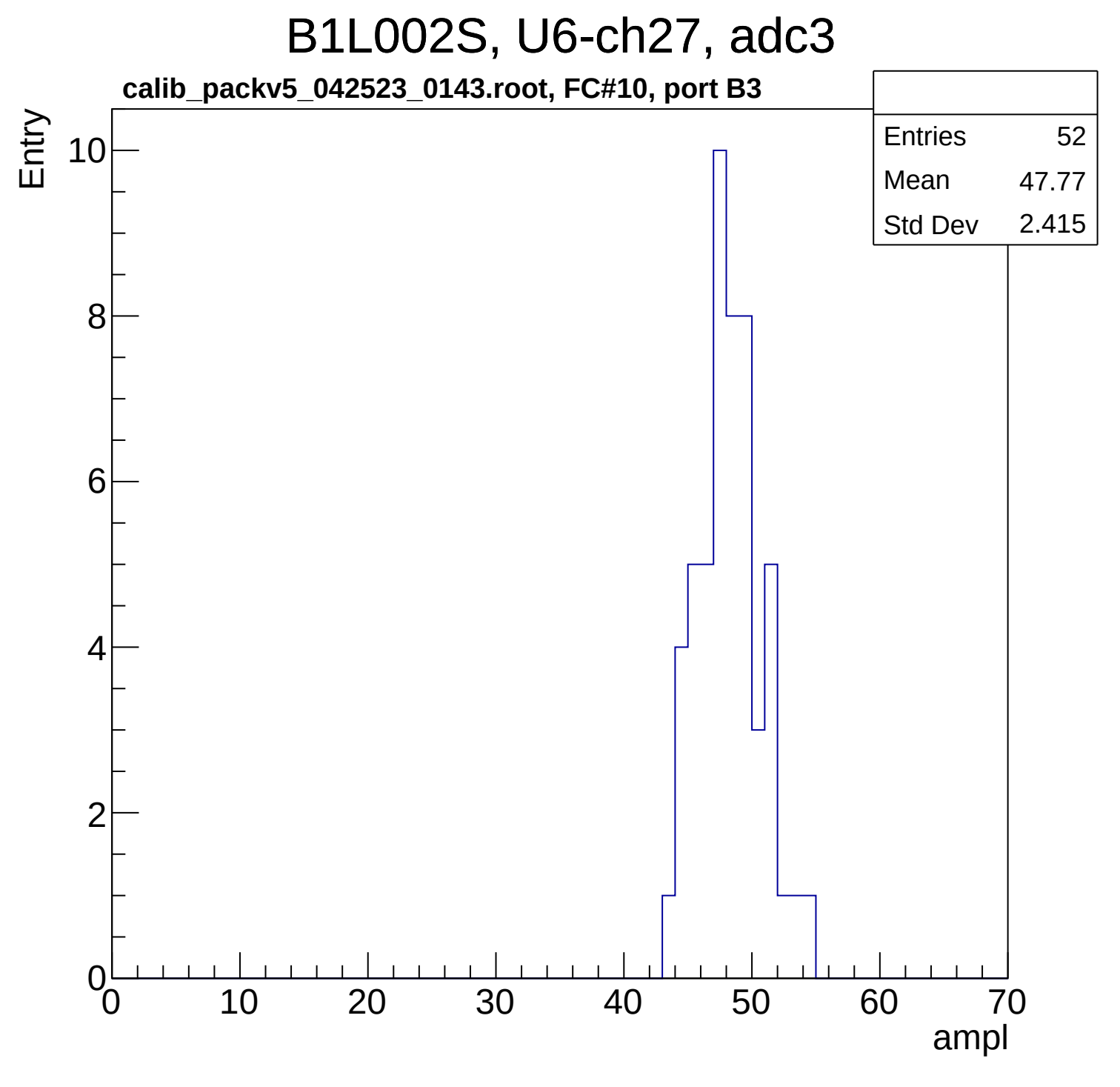

B1L002S, U6-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.88
Std Dev 15.02
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

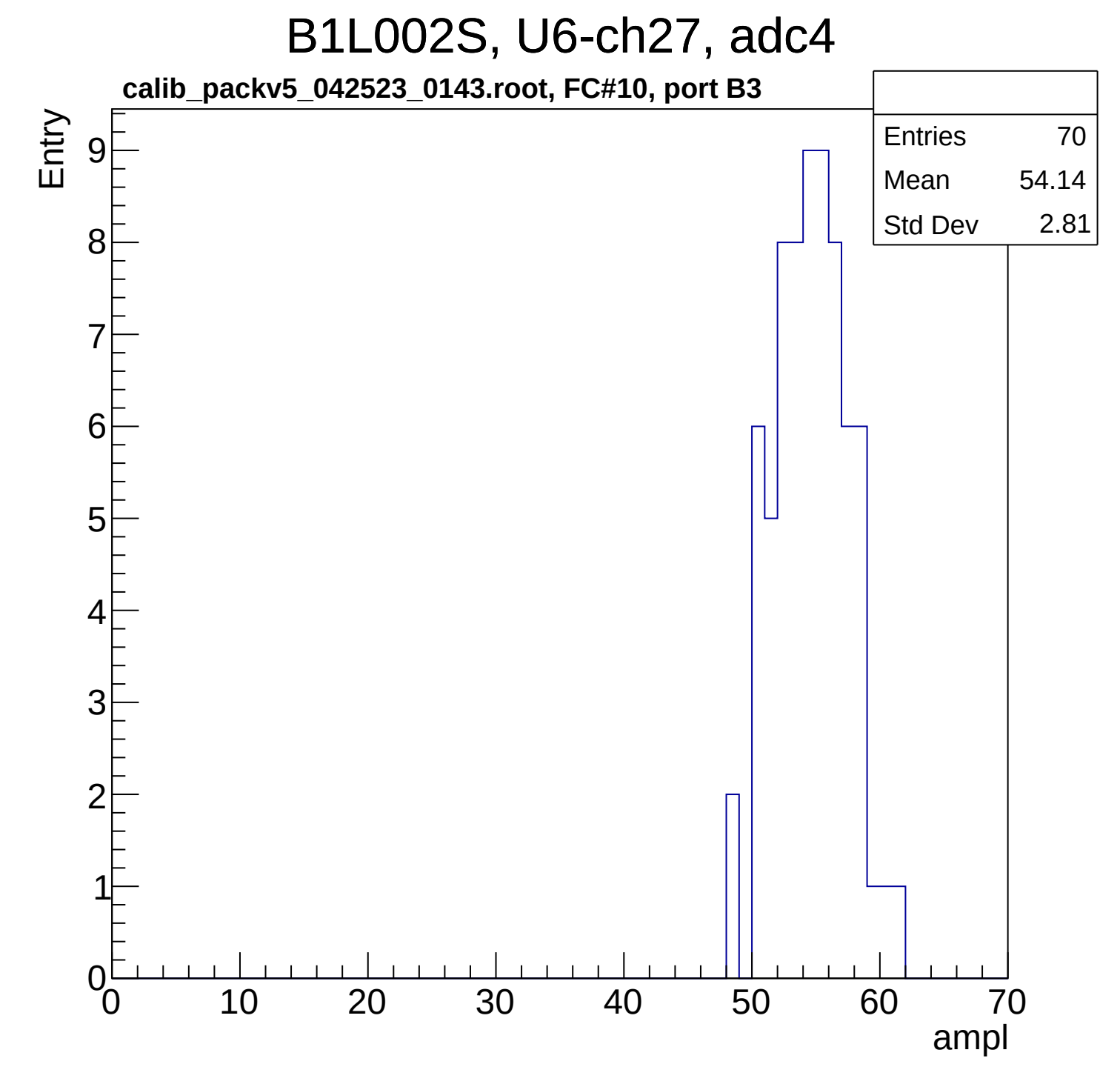

B1L002S, U6-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

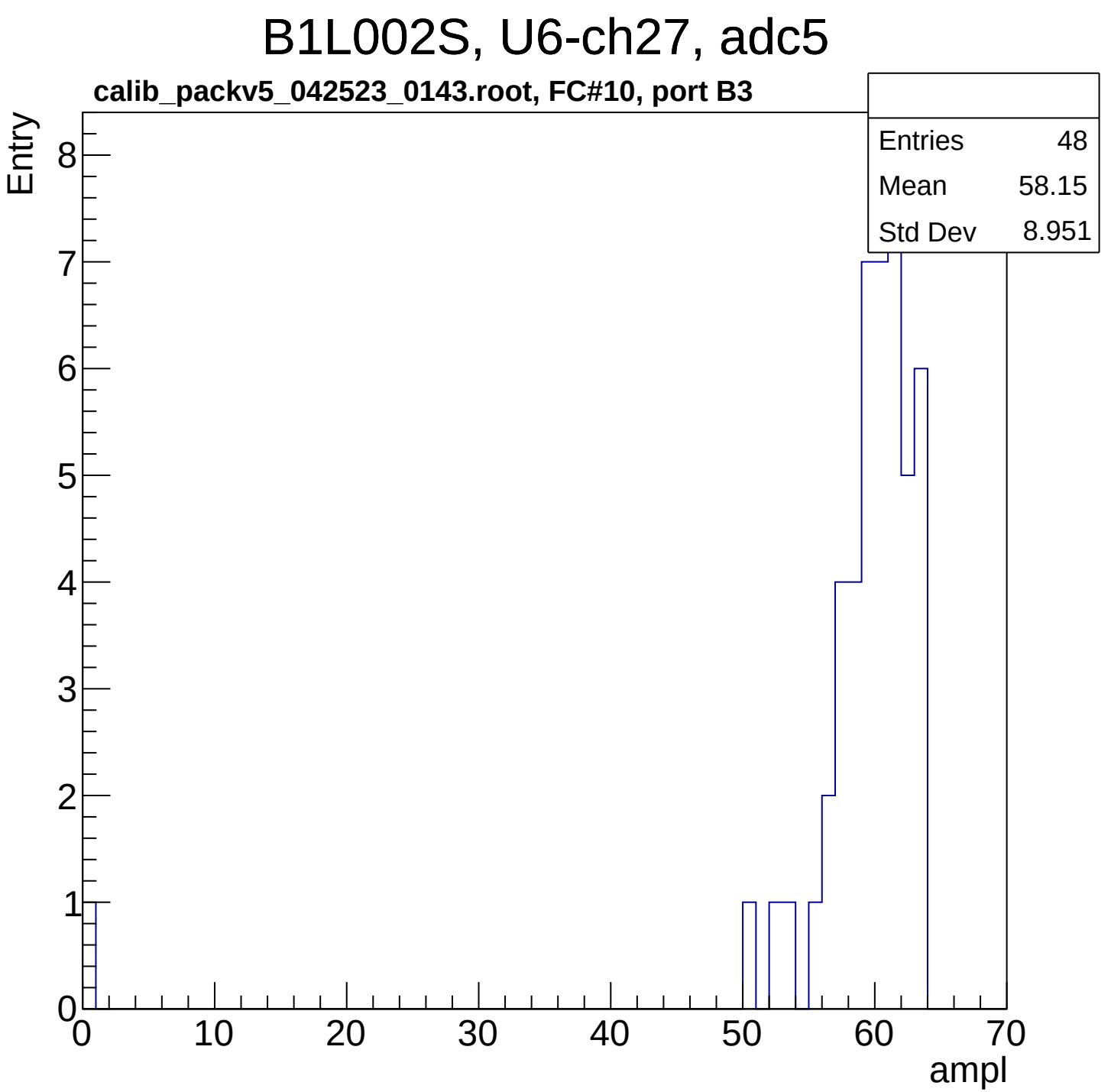

B1L002S, U6-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.57
Std Dev 3.222
Gaus mean : 29.0978
Gaus Width: 3.2175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

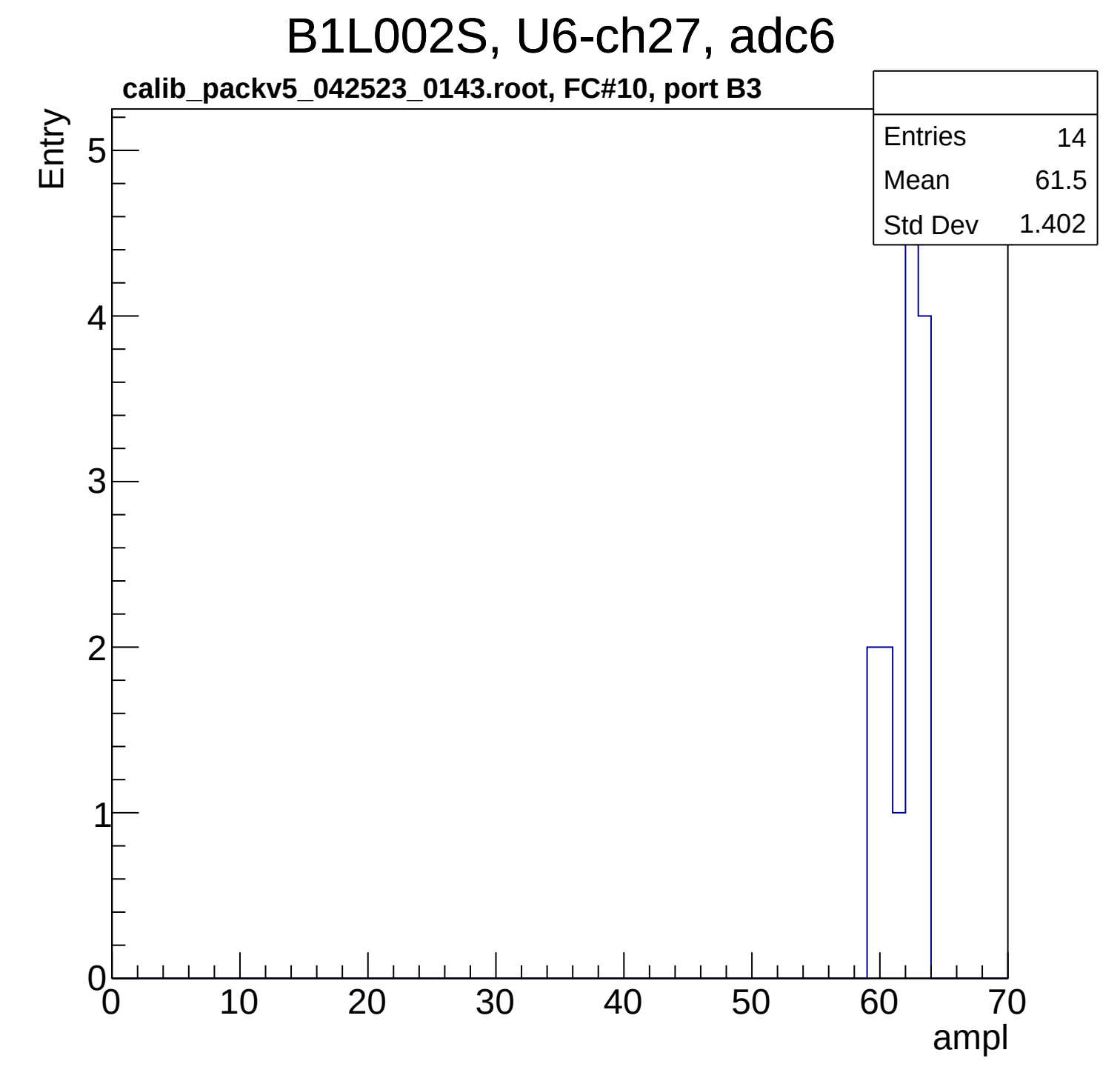

B1L002S, U6-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.36
Std Dev 3.481
Gaus mean : 34.9890
Gaus Width: 3.5377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

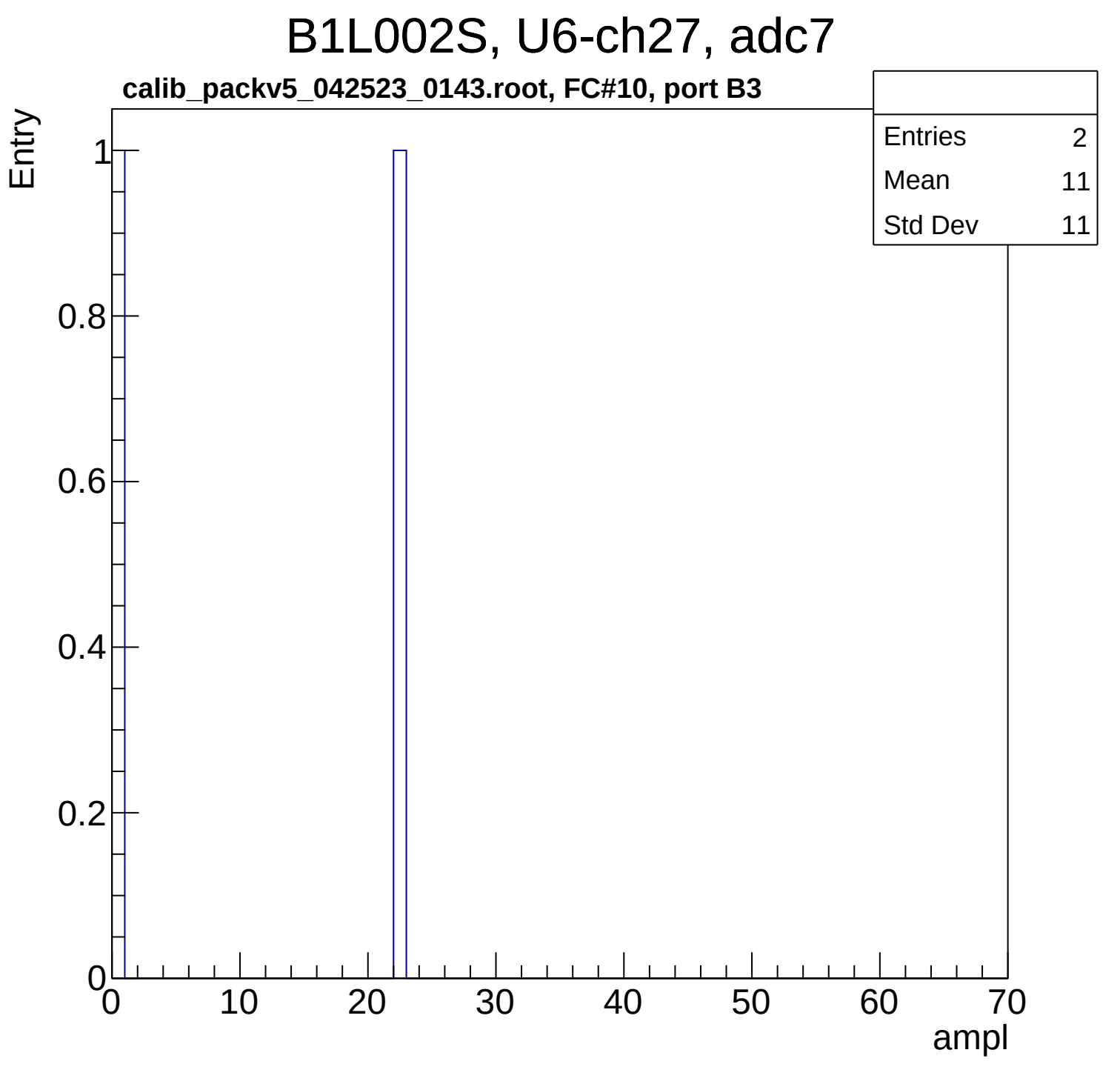

B1L002S, U6-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.51
Std Dev 3.03
Gaus mean : 42.4894
Gaus Width: 3.4888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

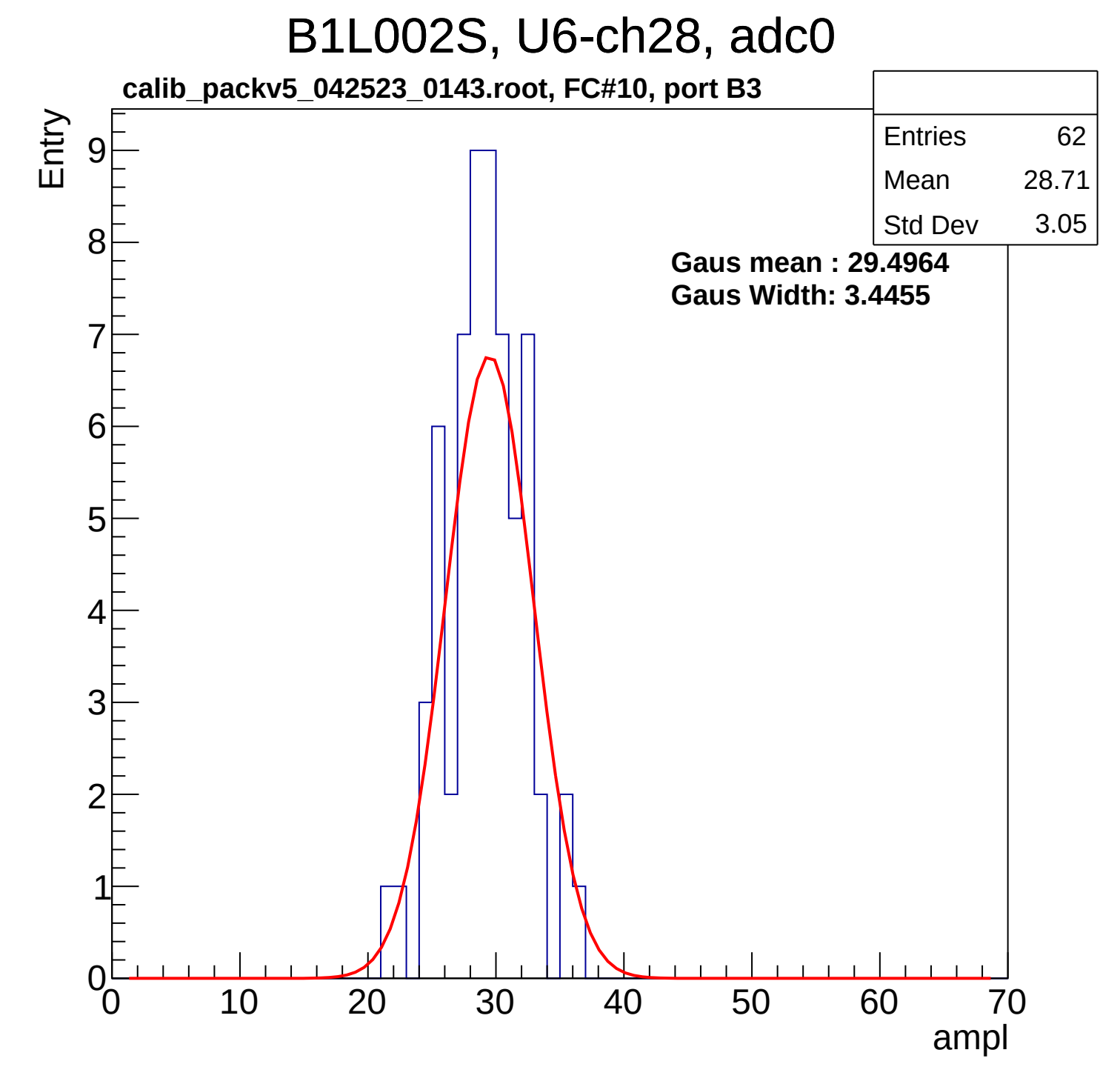

B1L002S, U6-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.77
Std Dev 2.415
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

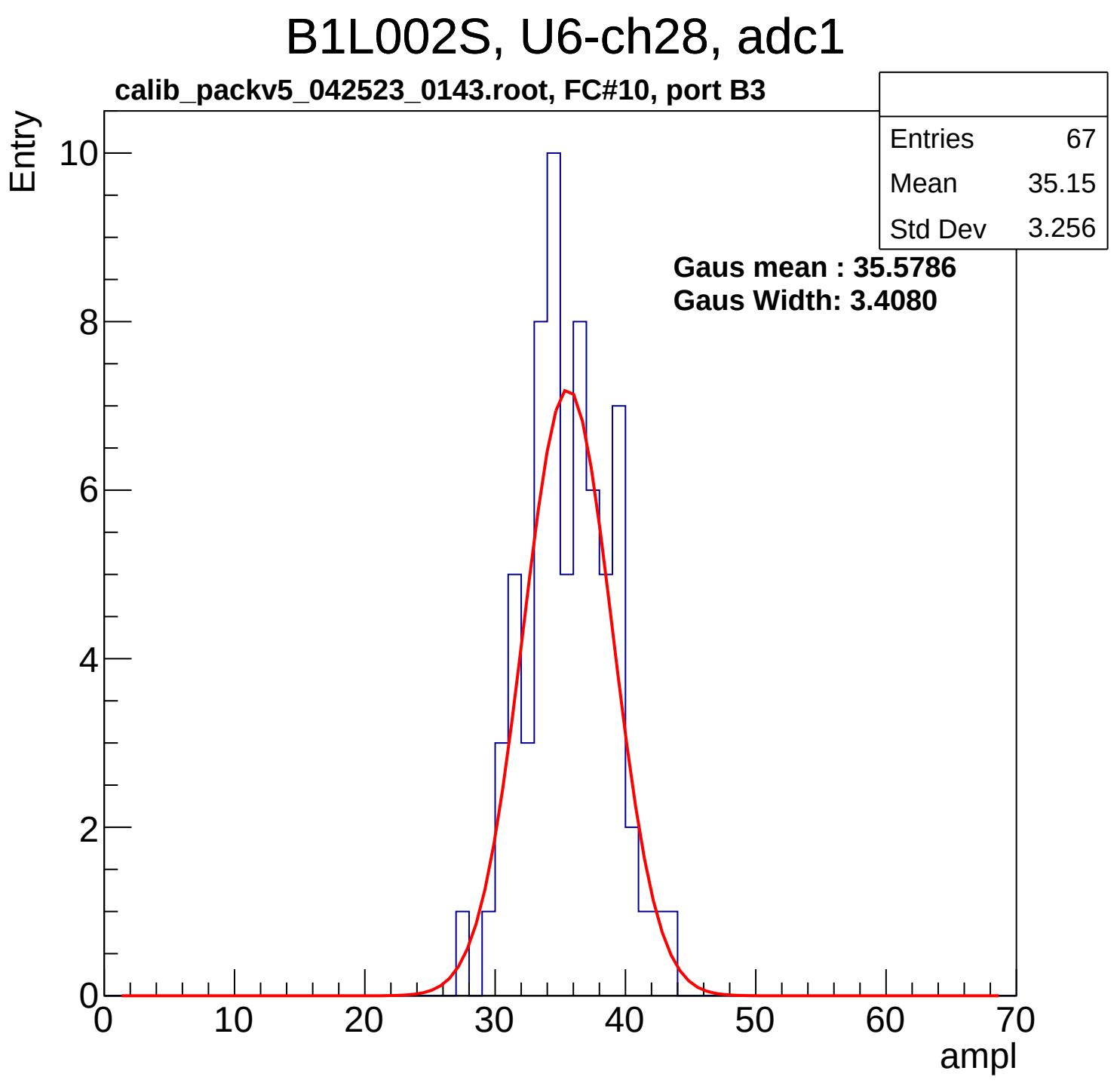

B1L002S, U6-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 54.14
Std Dev 2.81
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

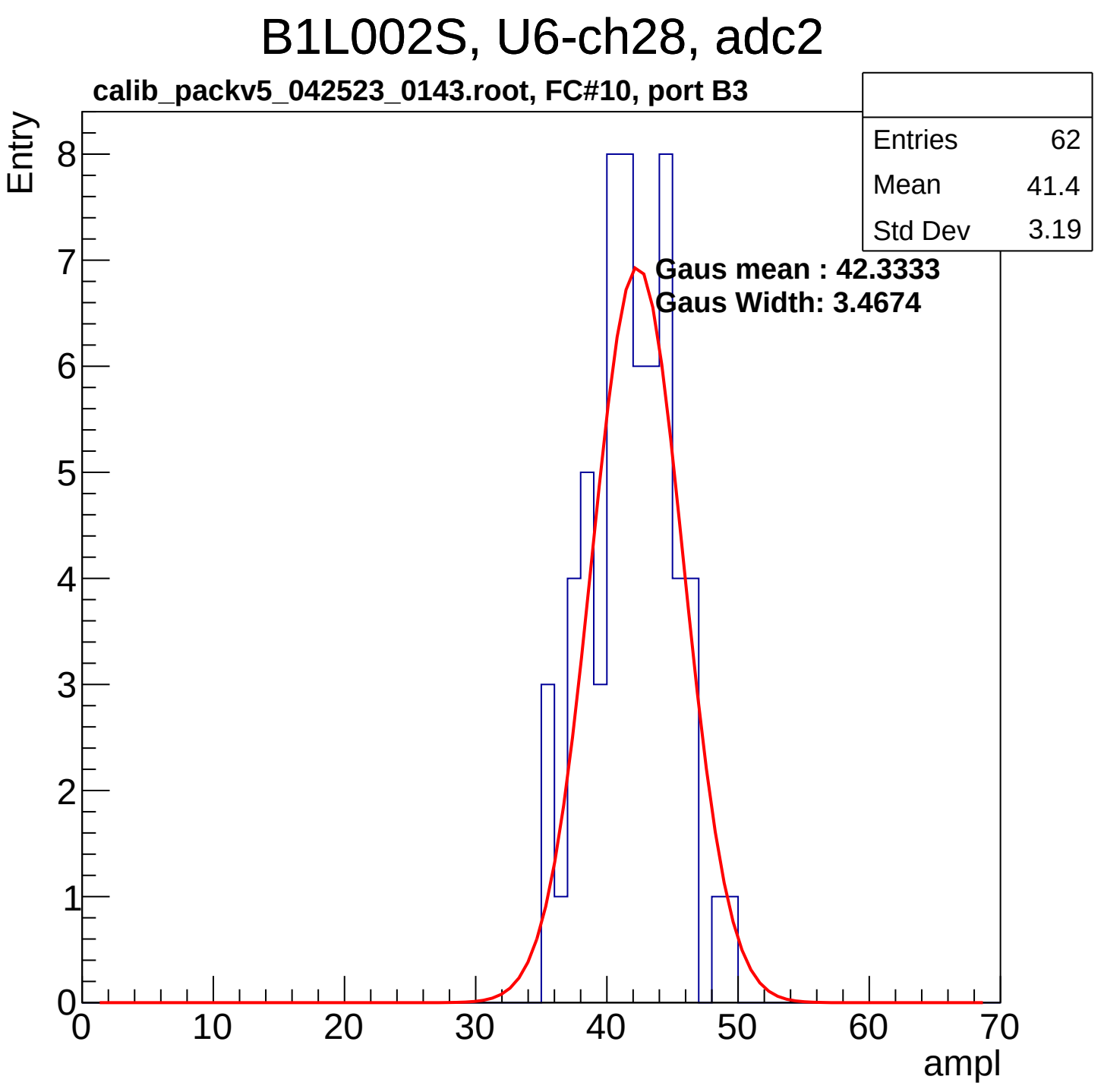

B1L002S, U6-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.15
Std Dev 8.951
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

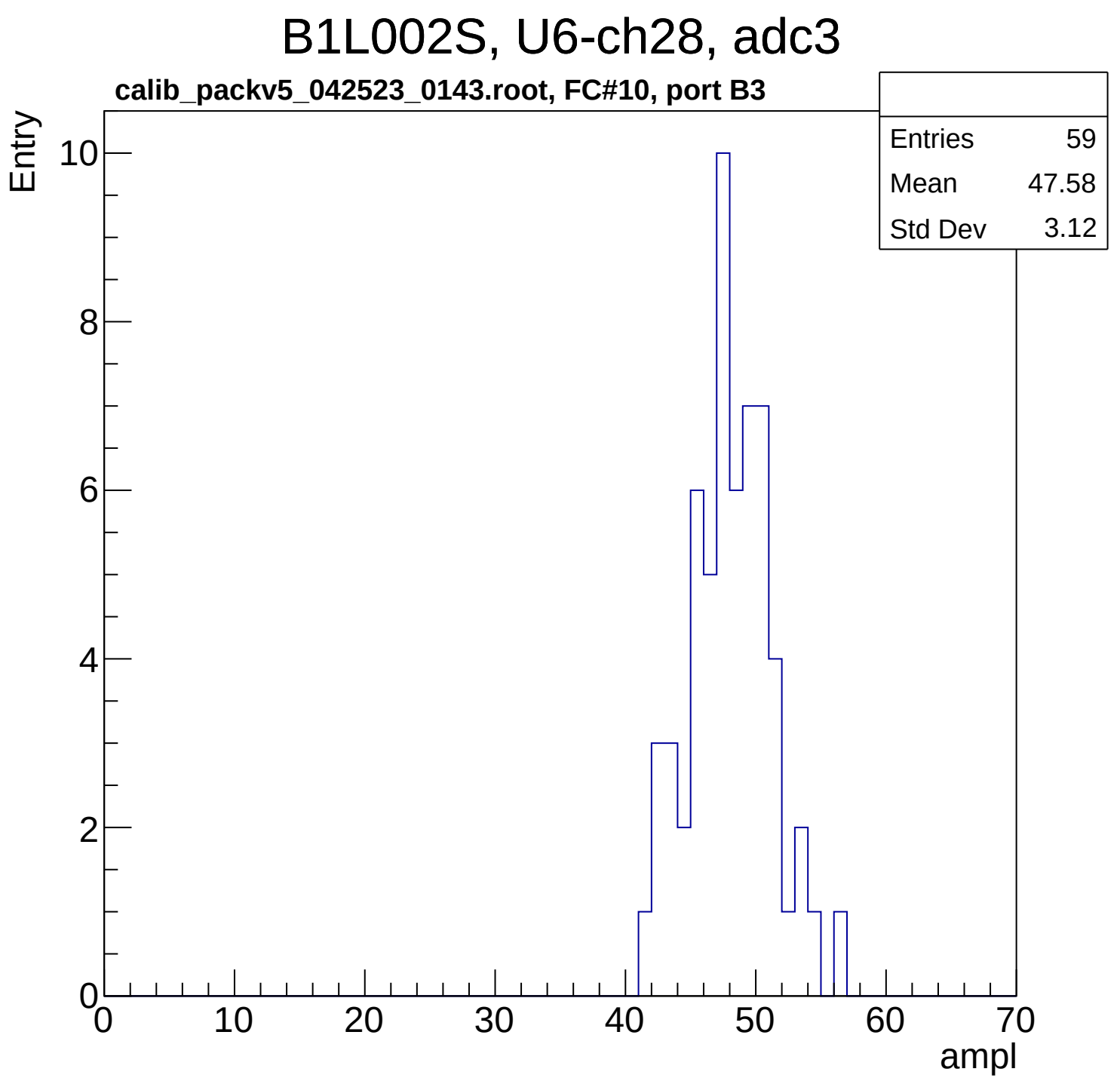

B1L002S, U6-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.5
Std Dev 1.402
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

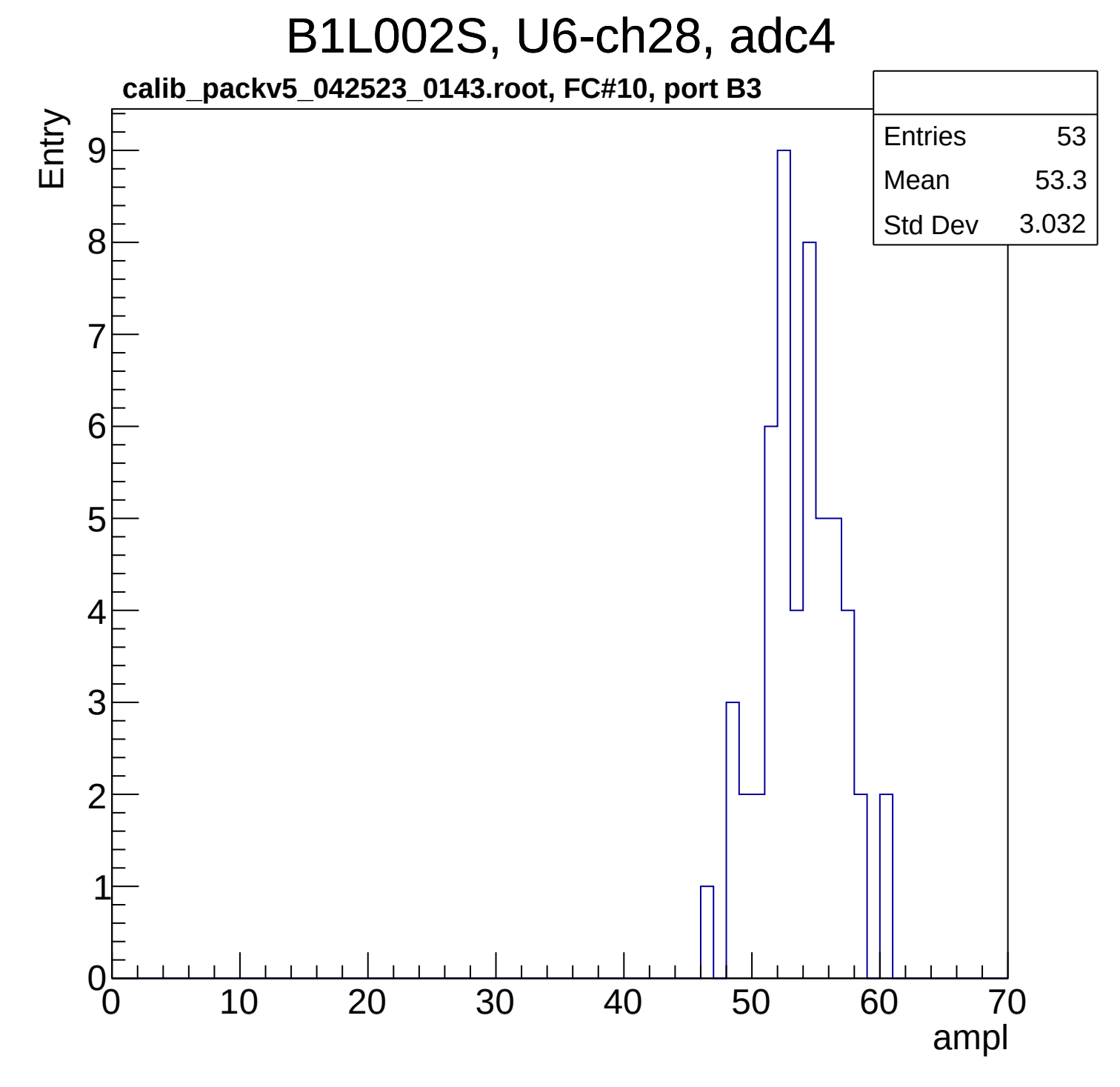

B1L002S, U6-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

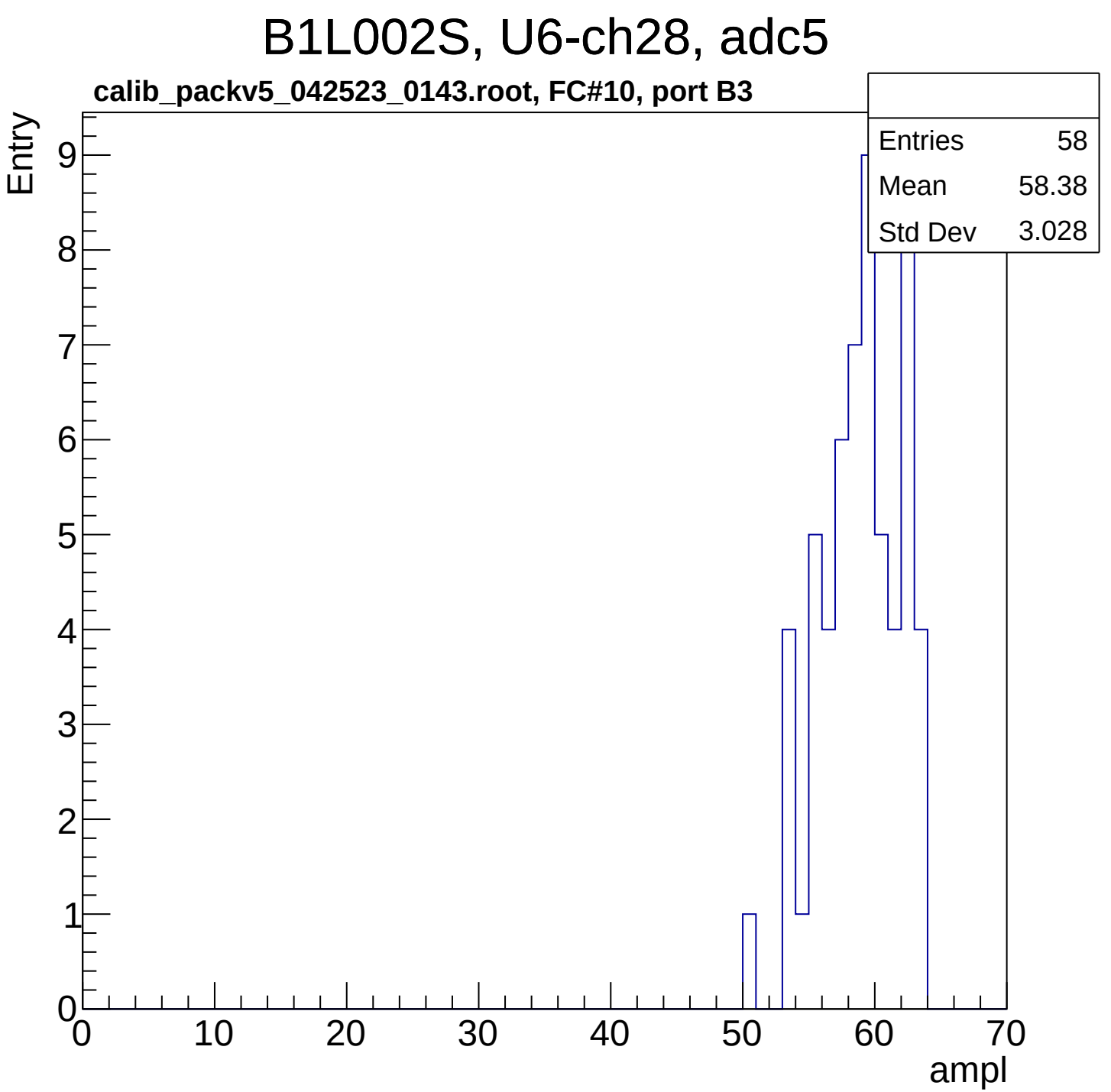

B1L002S, U6-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.71
Std Dev 3.05
Gaus mean : 29.4964
Gaus Width: 3.4455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

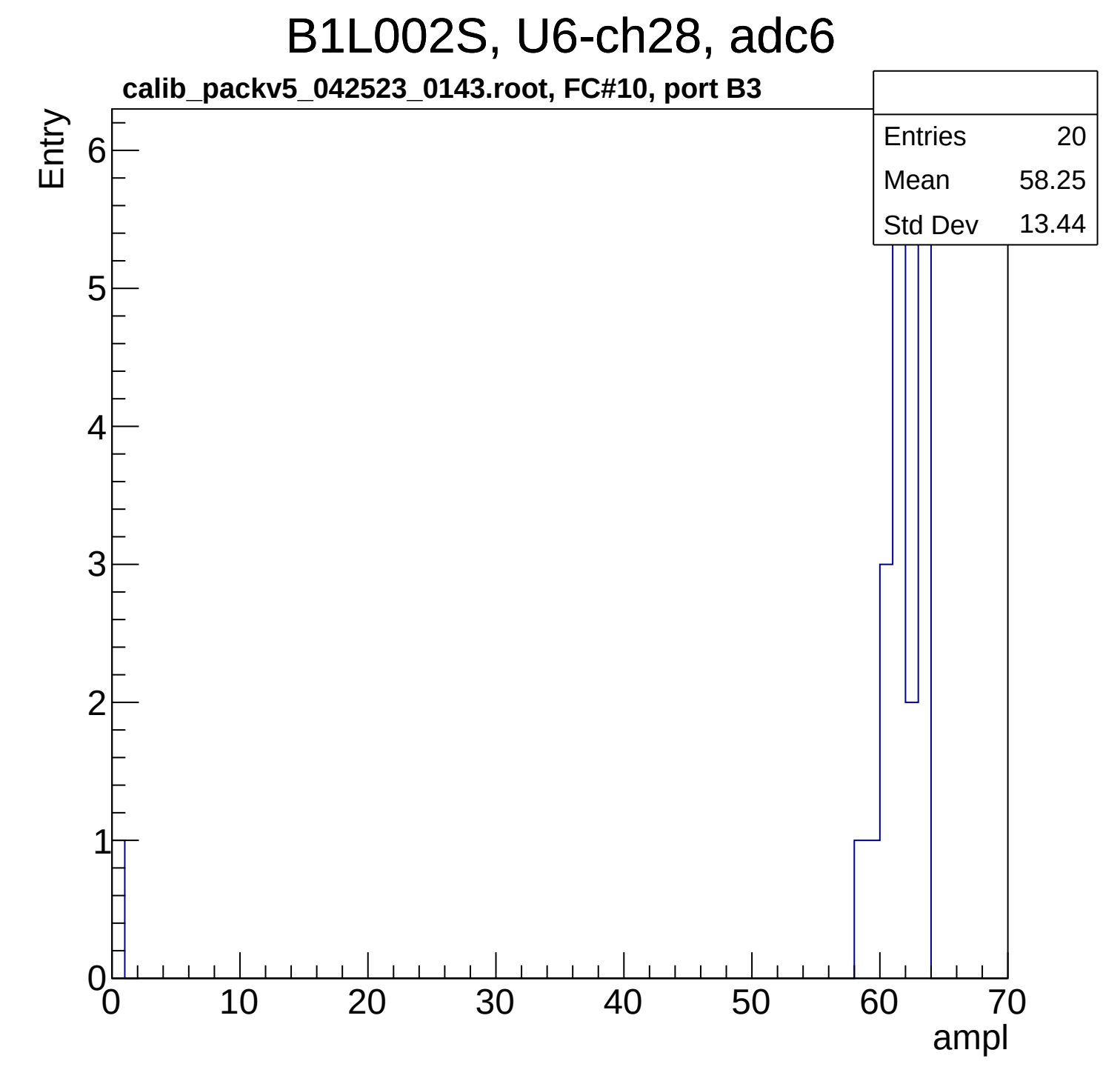

B1L002S, U6-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.15
Std Dev 3.256
Gaus mean : 35.5786
Gaus Width: 3.4080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

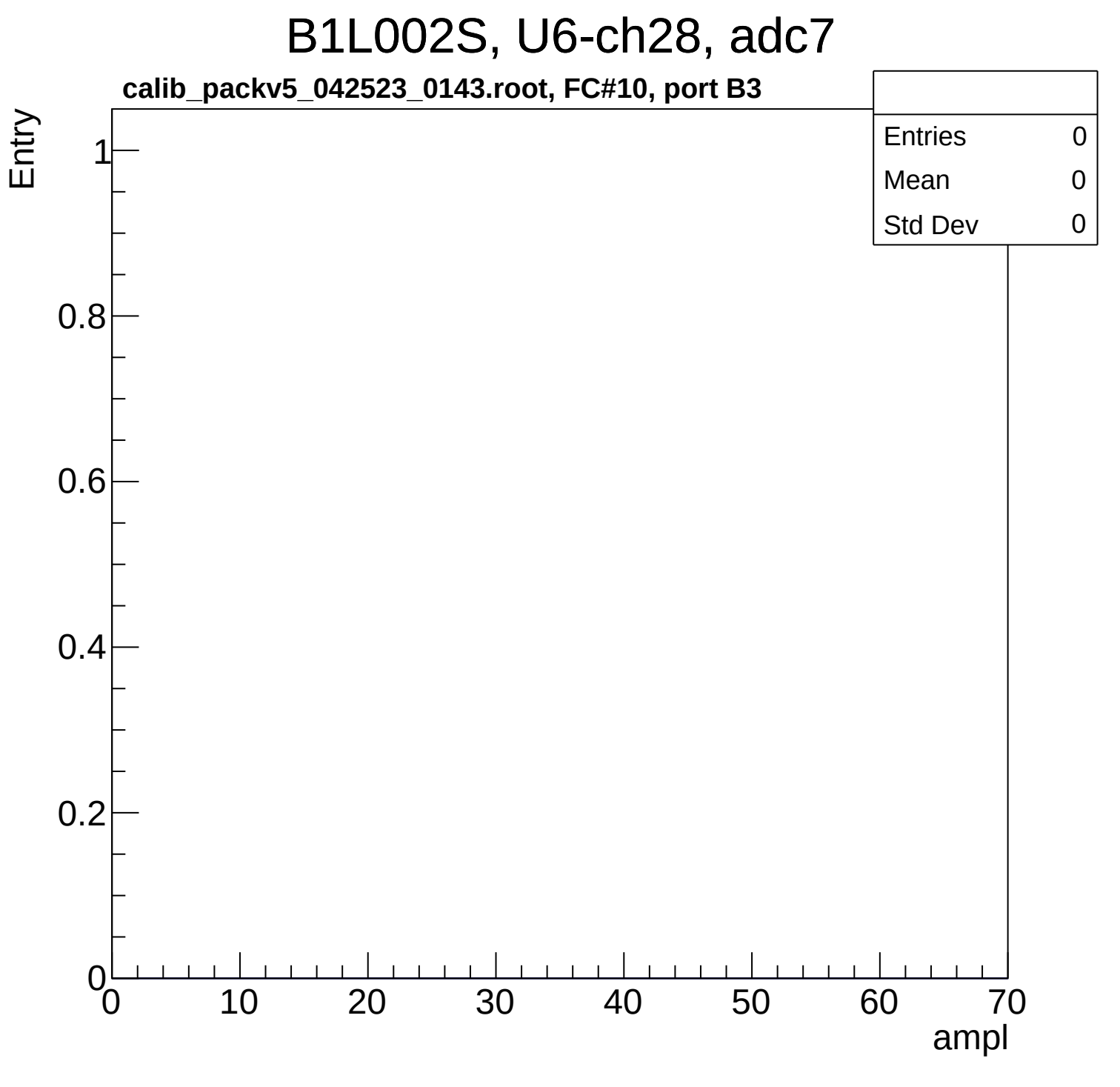

B1L002S, U6-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.4
Std Dev 3.19
Gaus mean : 42.3333
Gaus Width: 3.4674
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

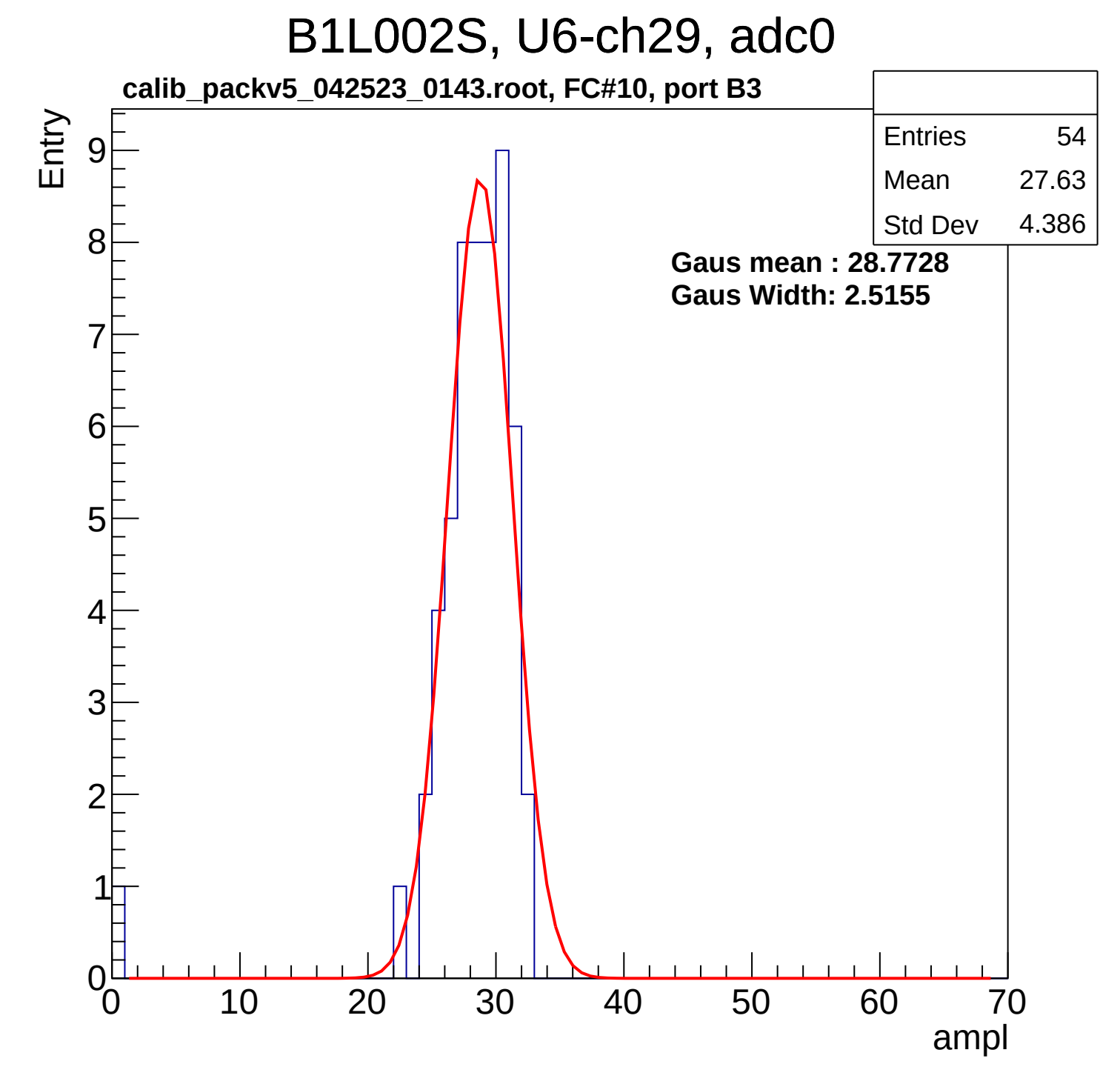

B1L002S, U6-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.58
Std Dev 3.12
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

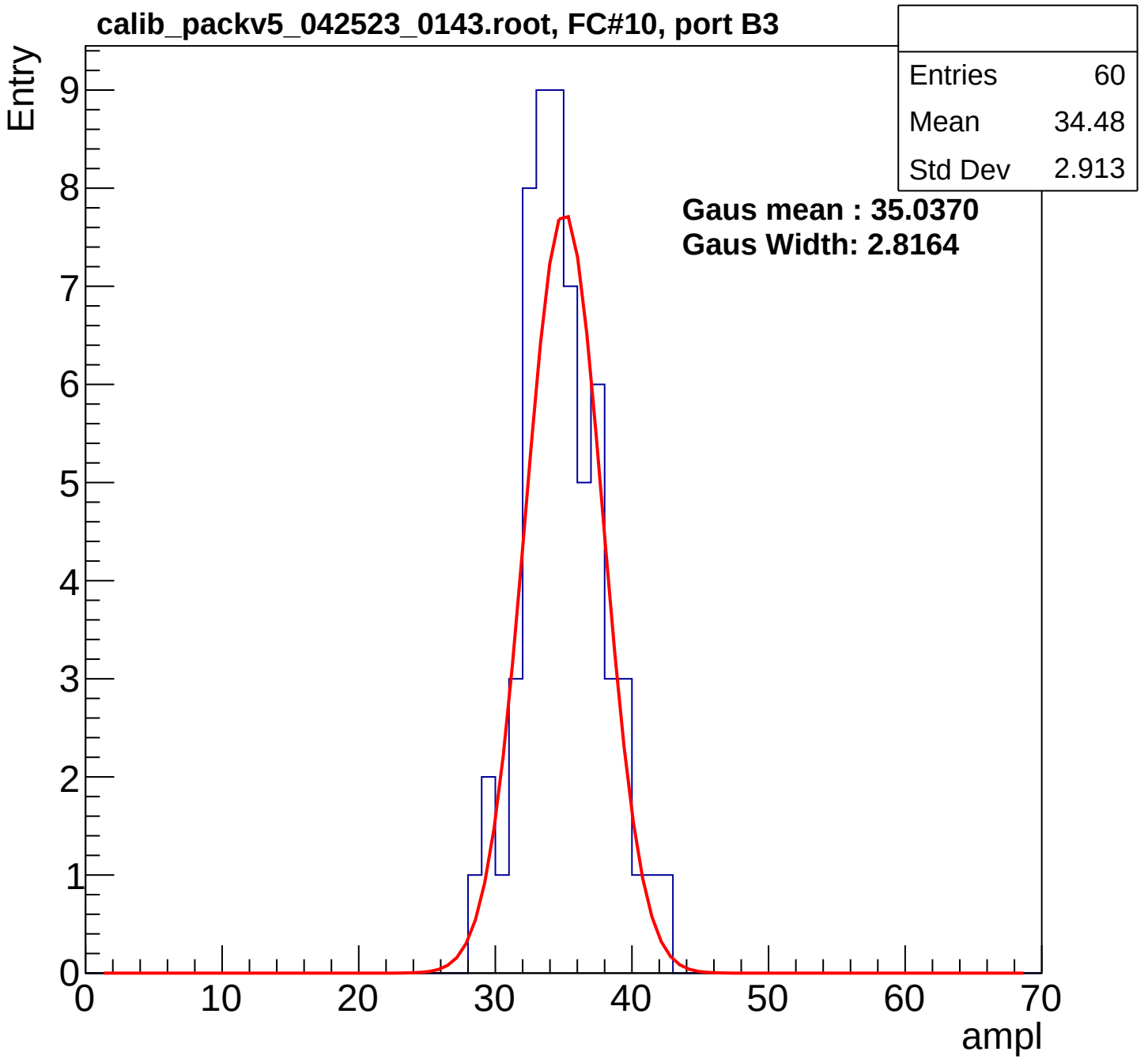

B1L002S, U6-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.3
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

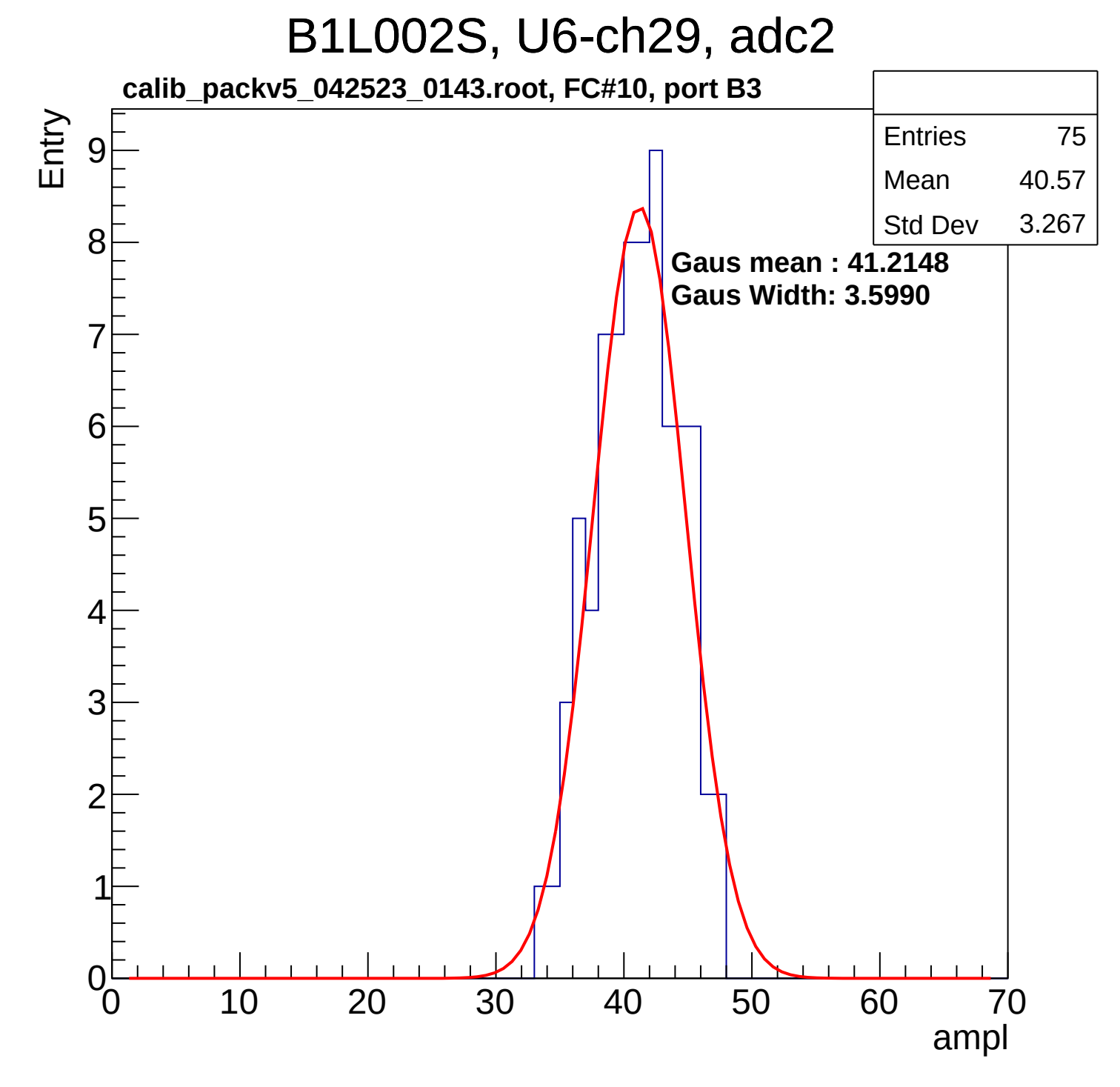

B1L002S, U6-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.38
Std Dev 3.028
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

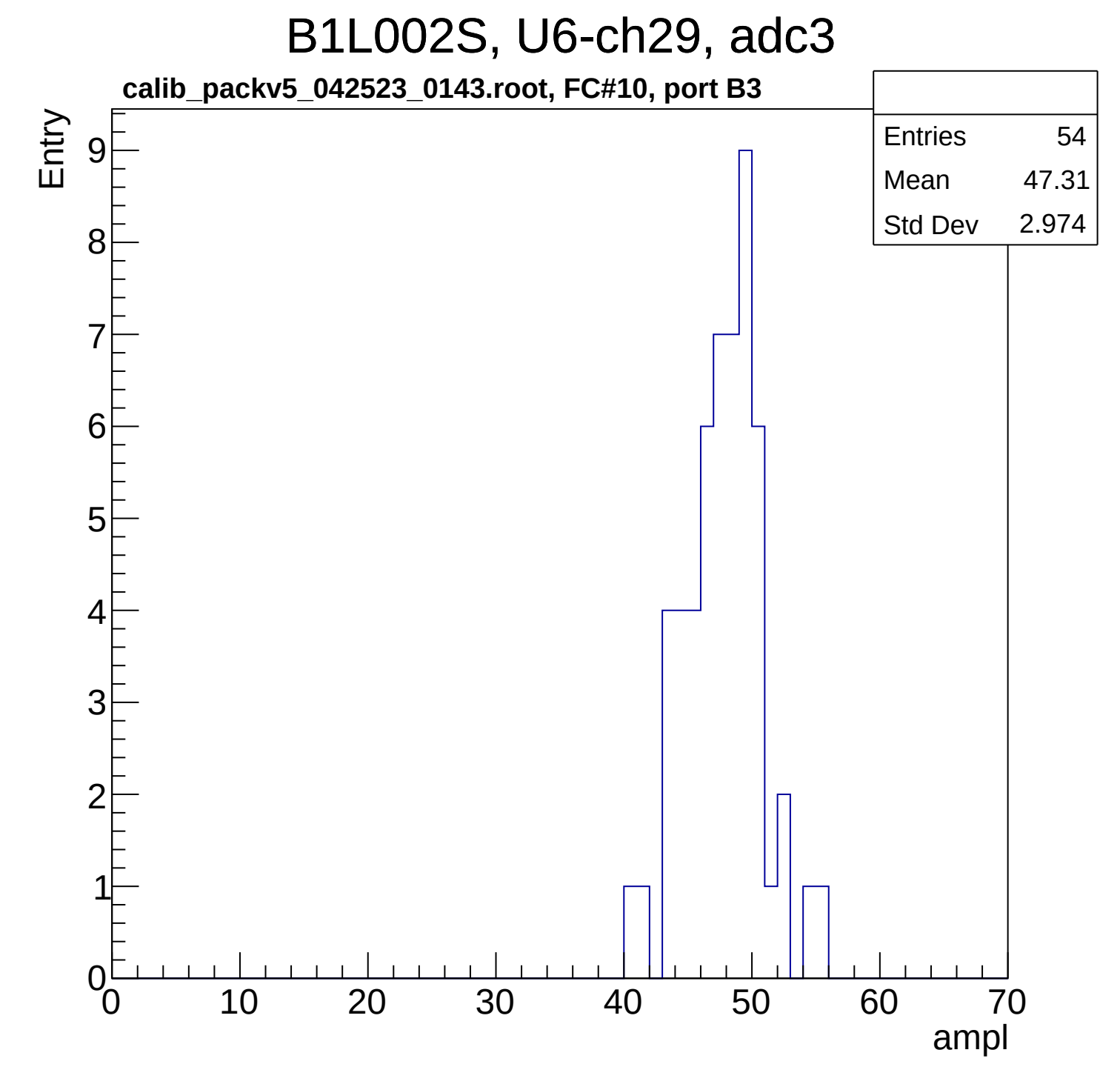

B1L002S, U6-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.25
Std Dev 13.44
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

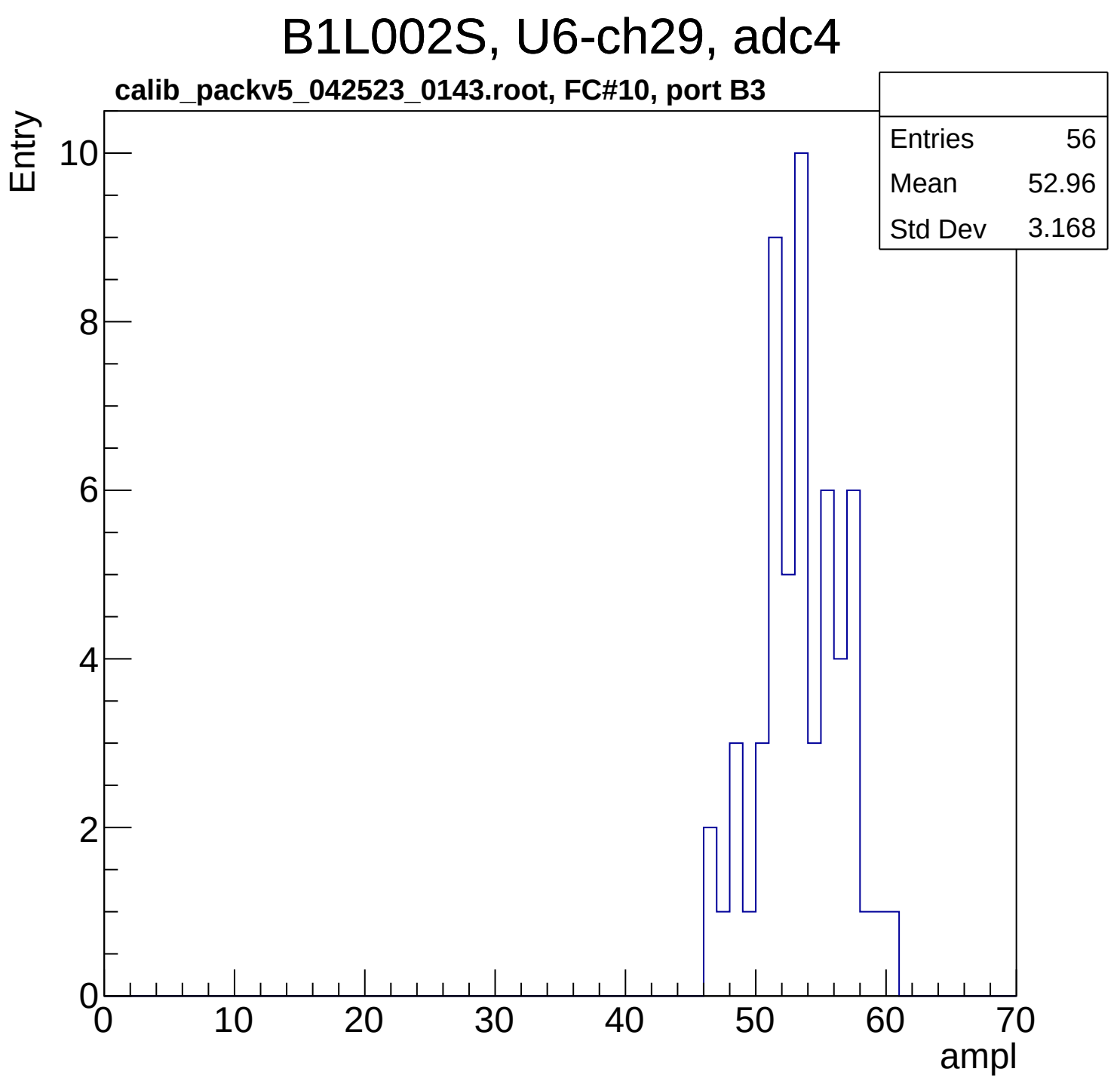

B1L002S, U6-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

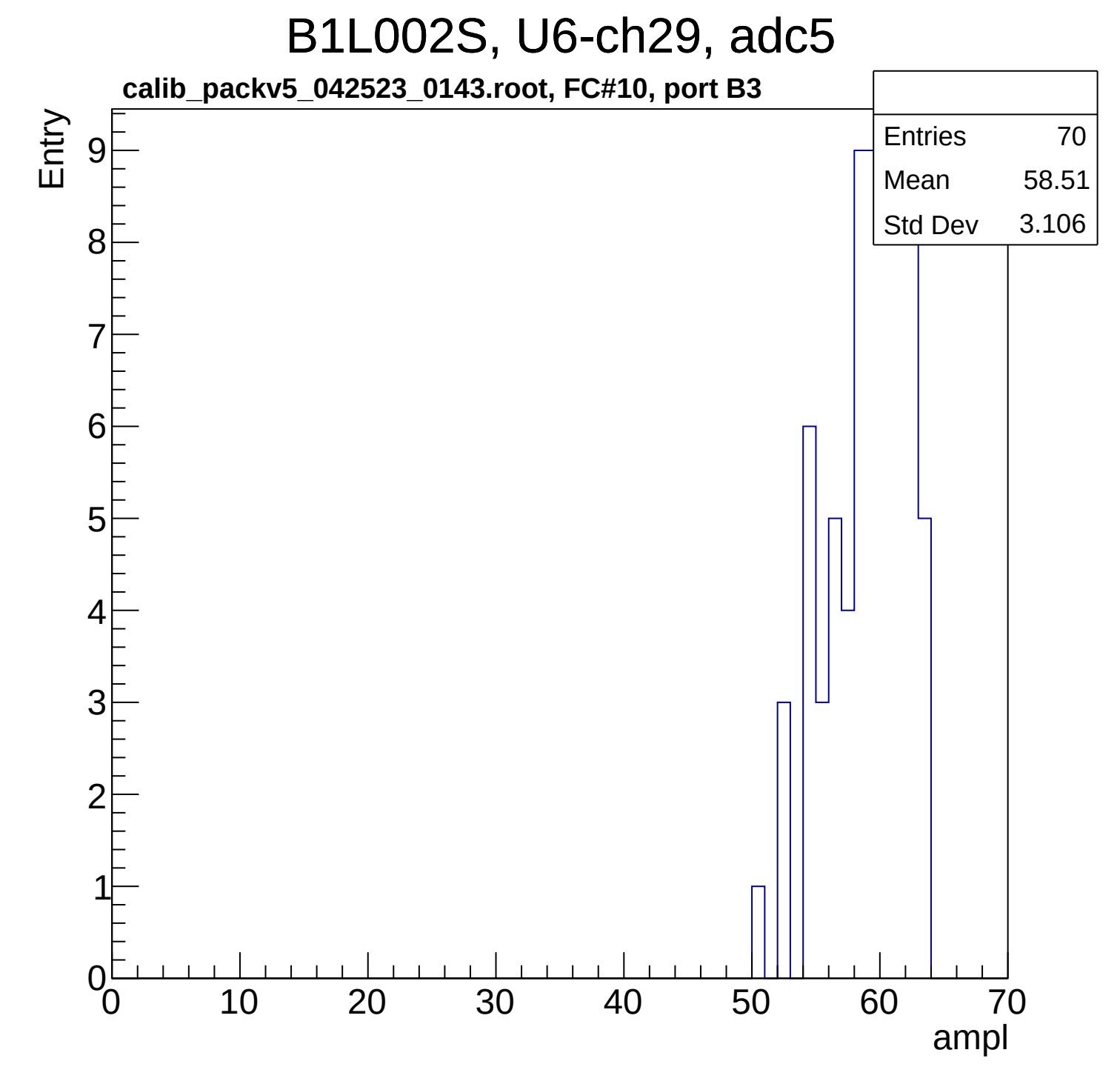

B1L002S, U6-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.63
Std Dev 4.386
Gaus mean : 28.7728
Gaus Width: 2.5155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

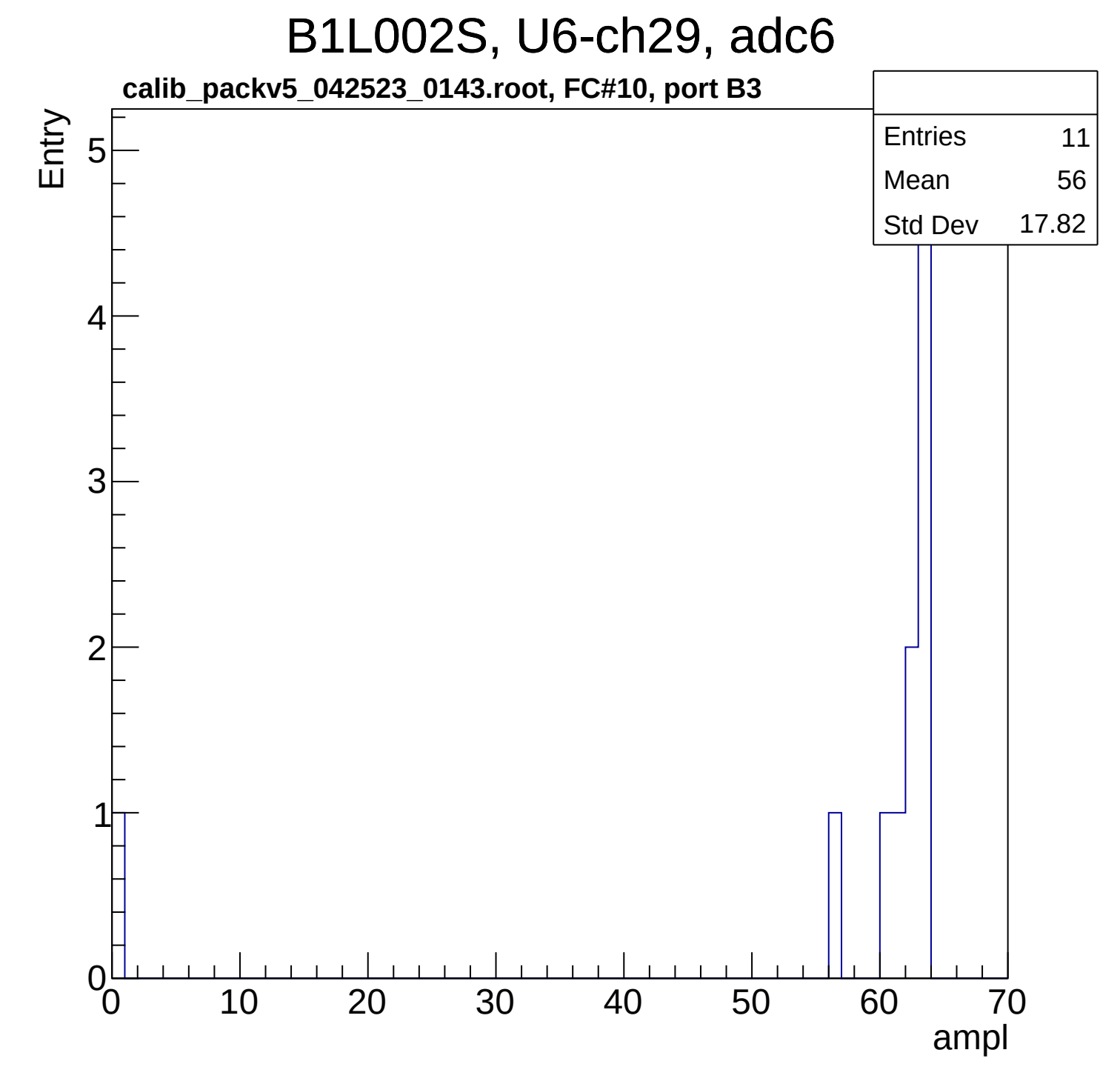

B1L002S, U6-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.48
Std Dev 2.913
Gaus mean : 35.0370
Gaus Width: 2.8164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

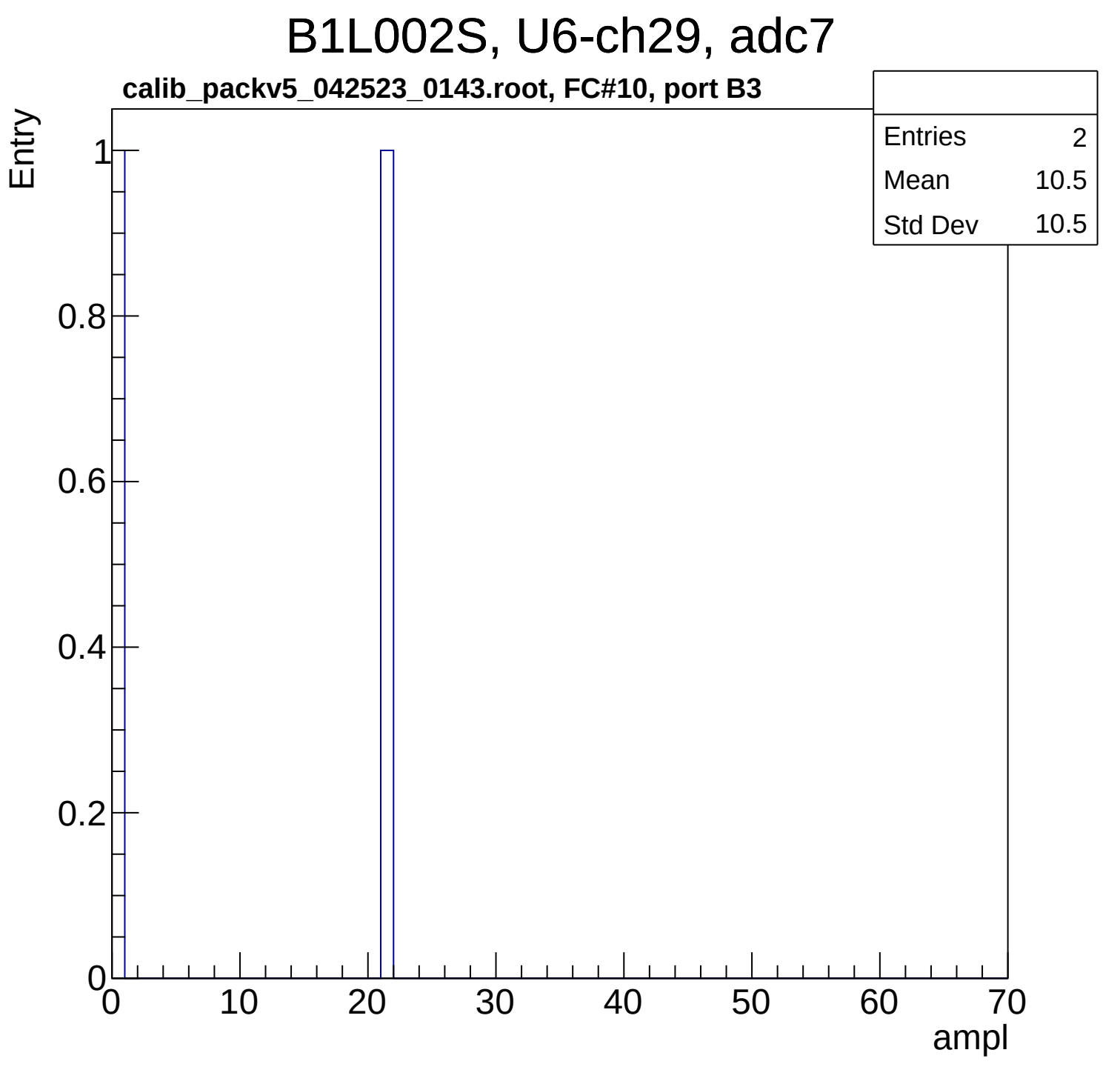

B1L002S, U6-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 40.57
Std Dev 3.267
Gaus mean : 41.2148
Gaus Width: 3.5990
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

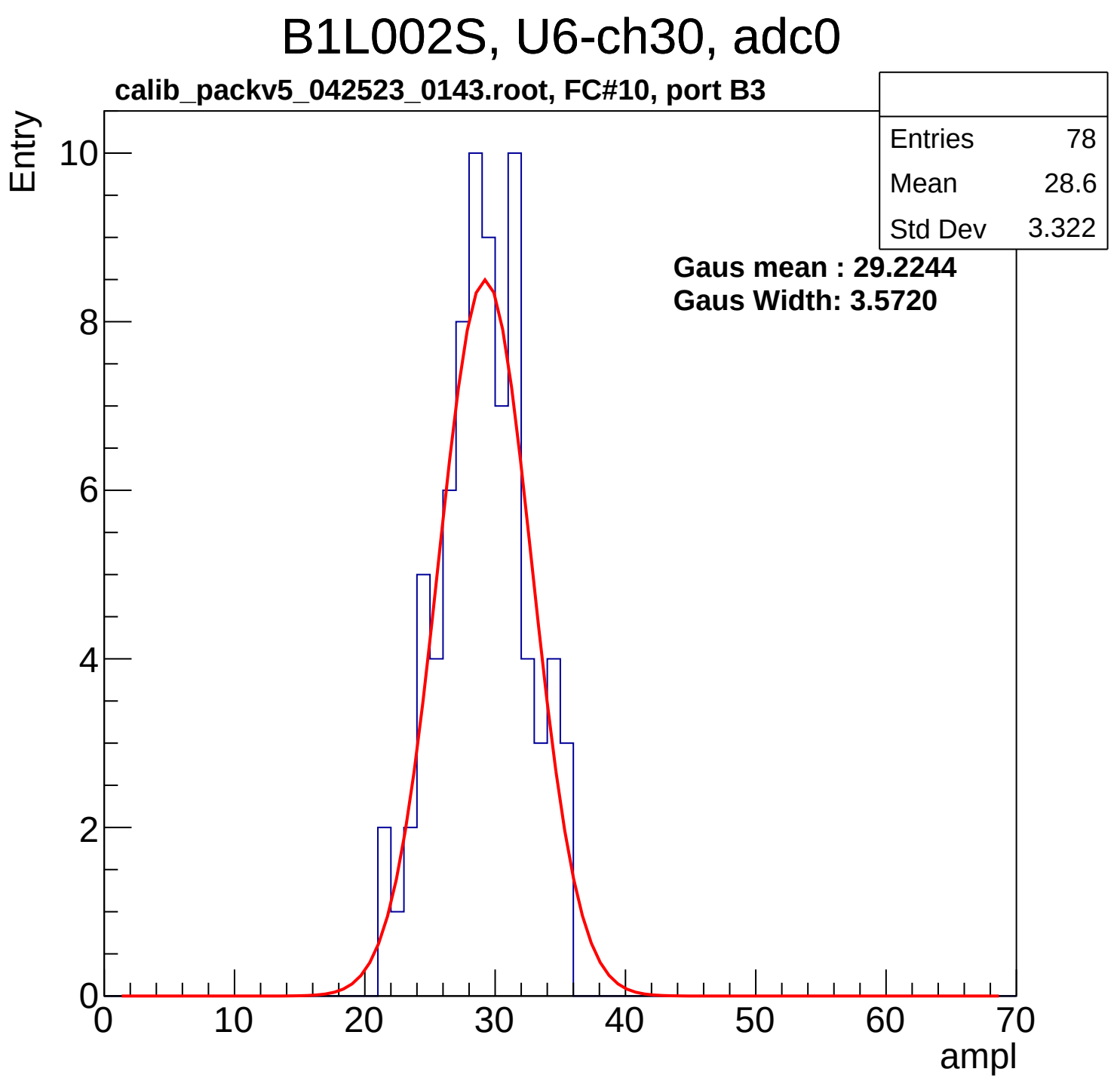

B1L002S, U6-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.31
Std Dev 2.974
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

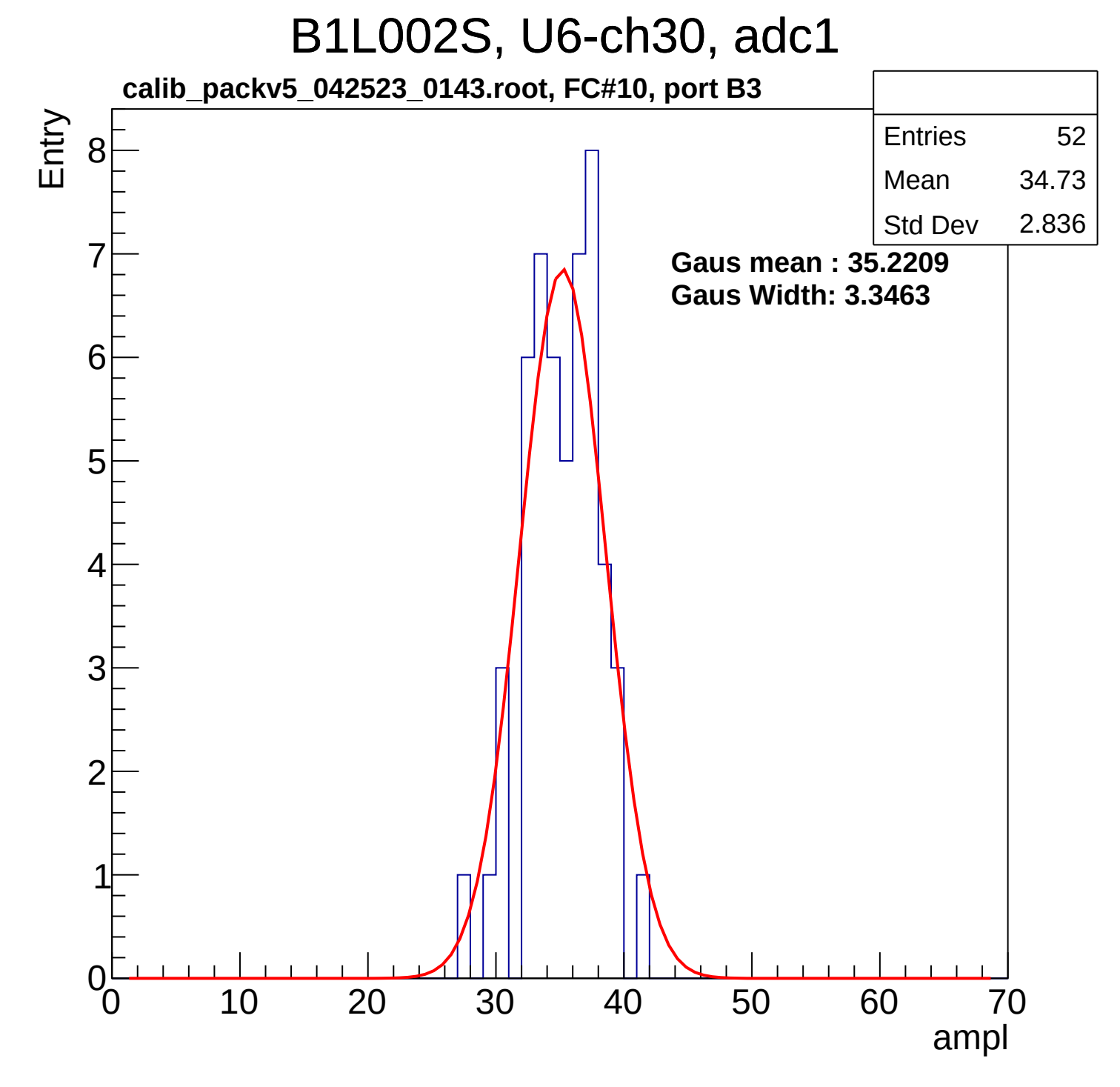

B1L002S, U6-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.96
Std Dev 3.168
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

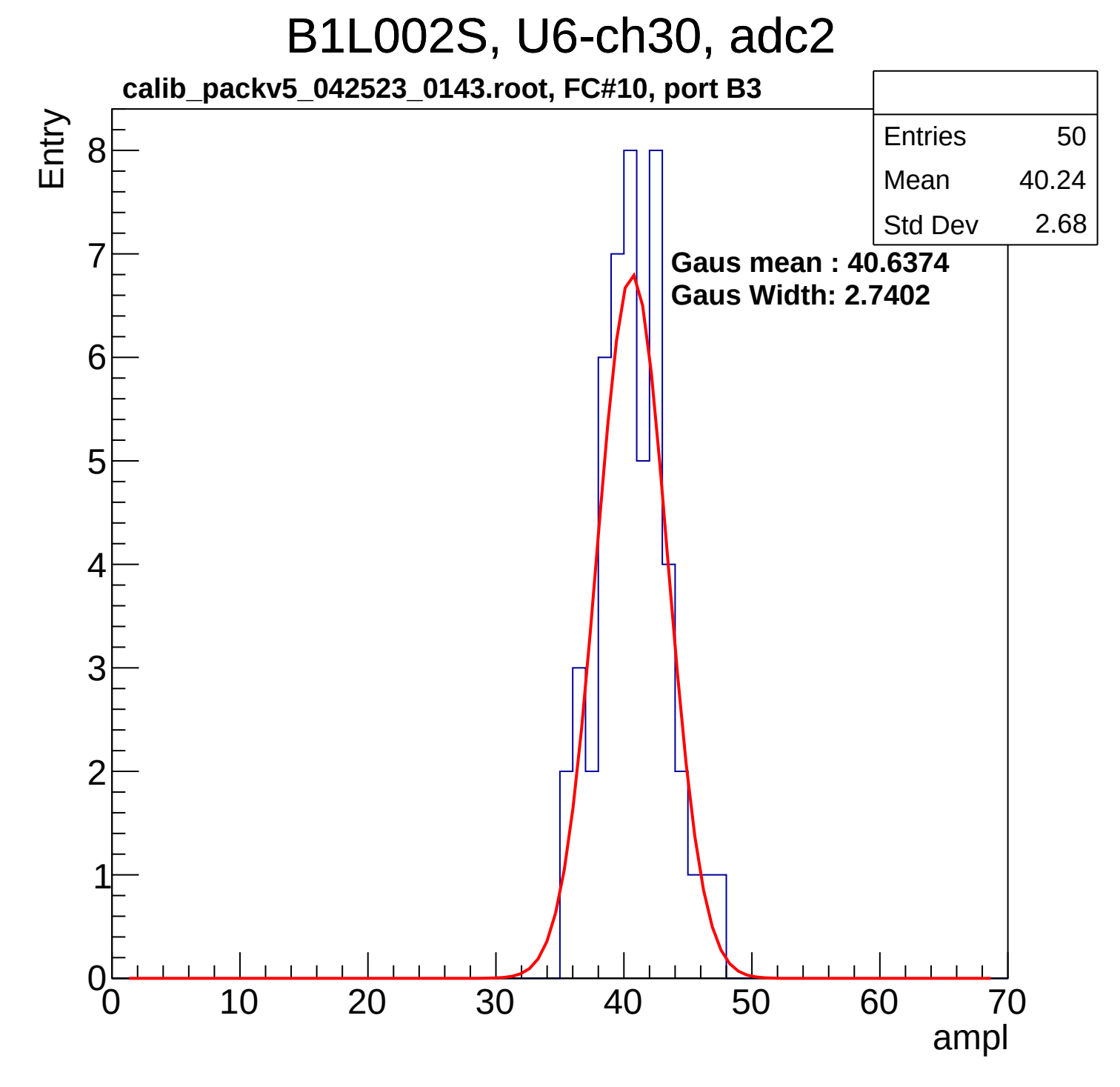

B1L002S, U6-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 58.51
Std Dev 3.106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

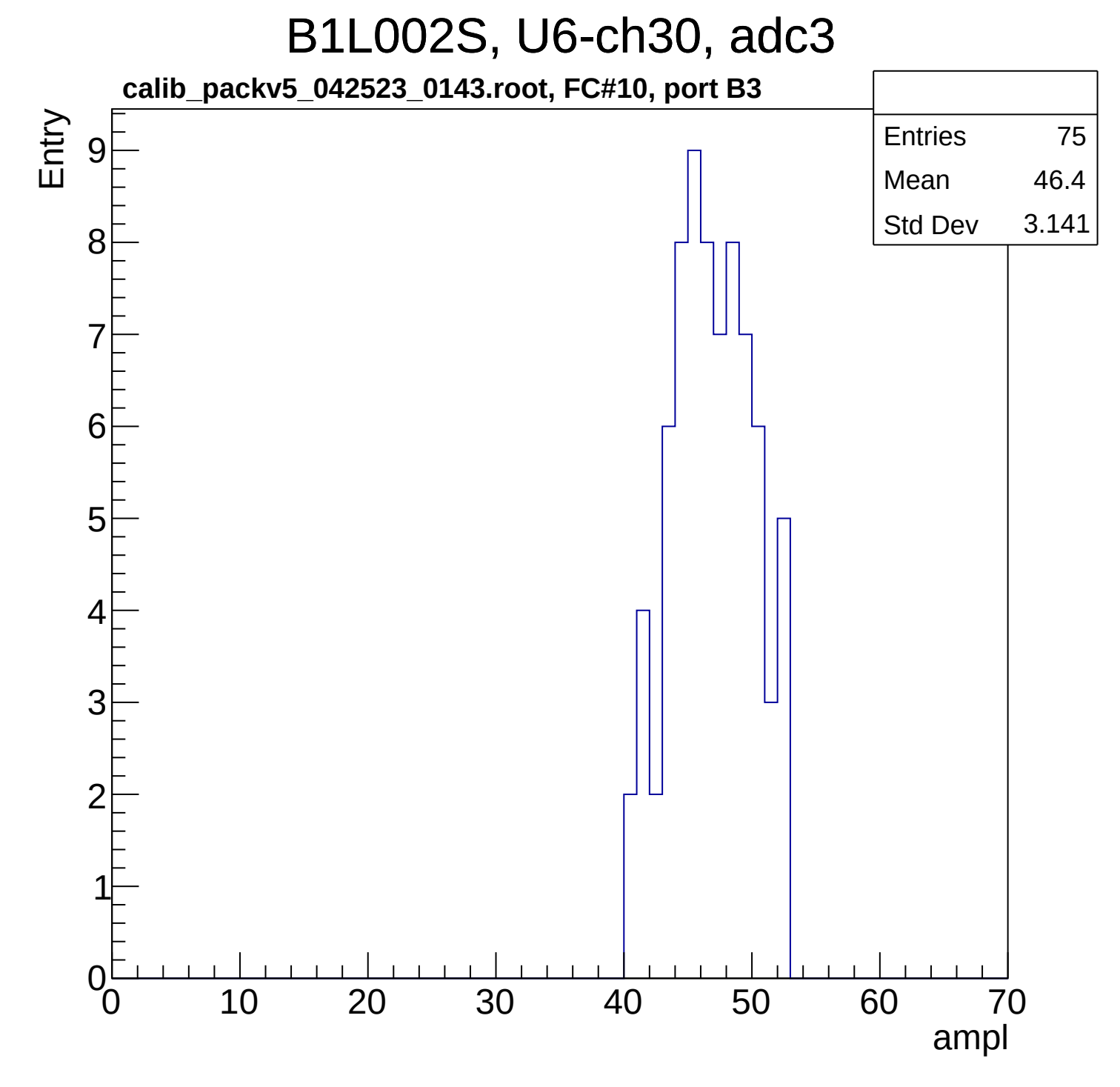

B1L002S, U6-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56
Std Dev 17.82
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

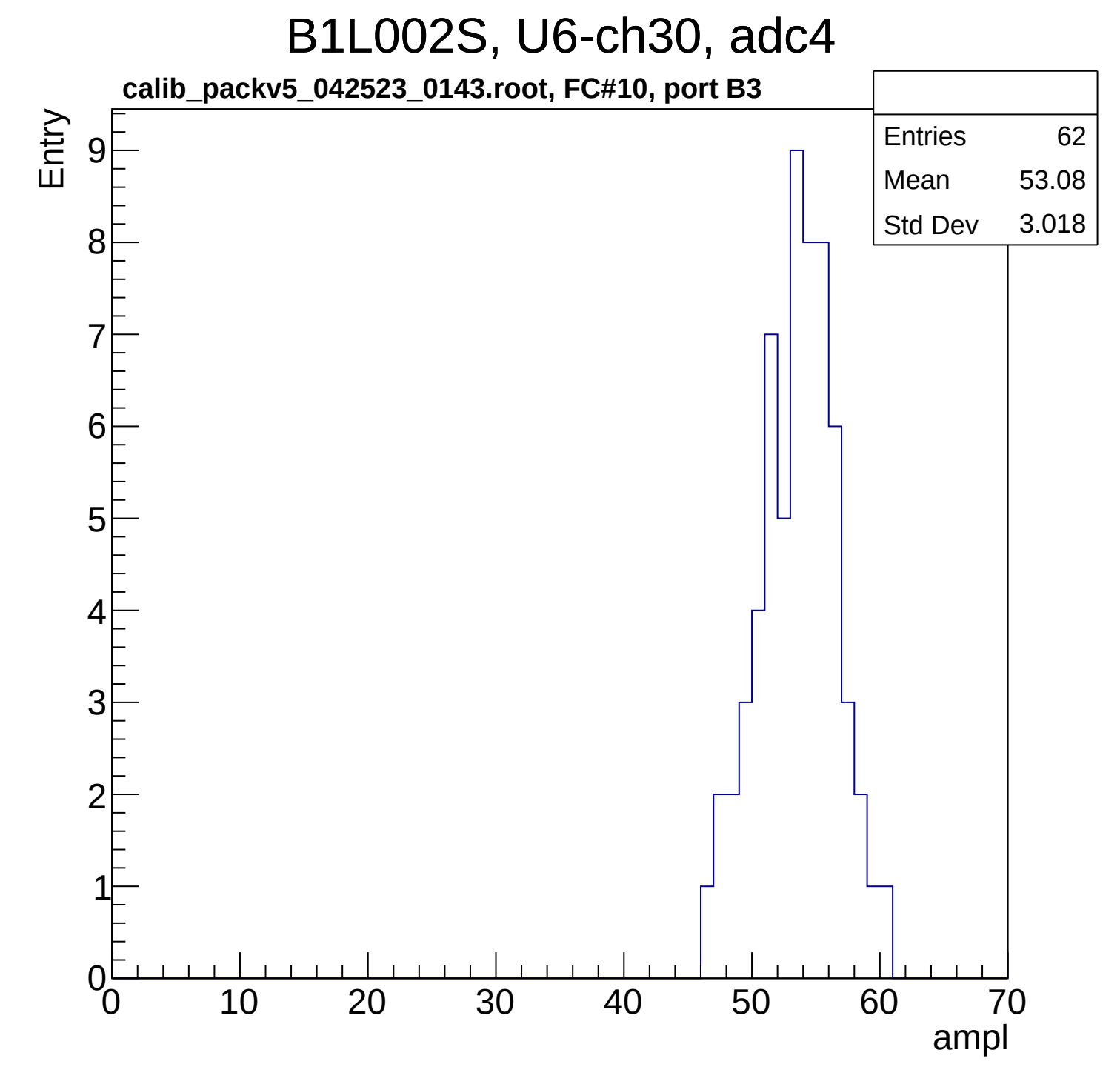

B1L002S, U6-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

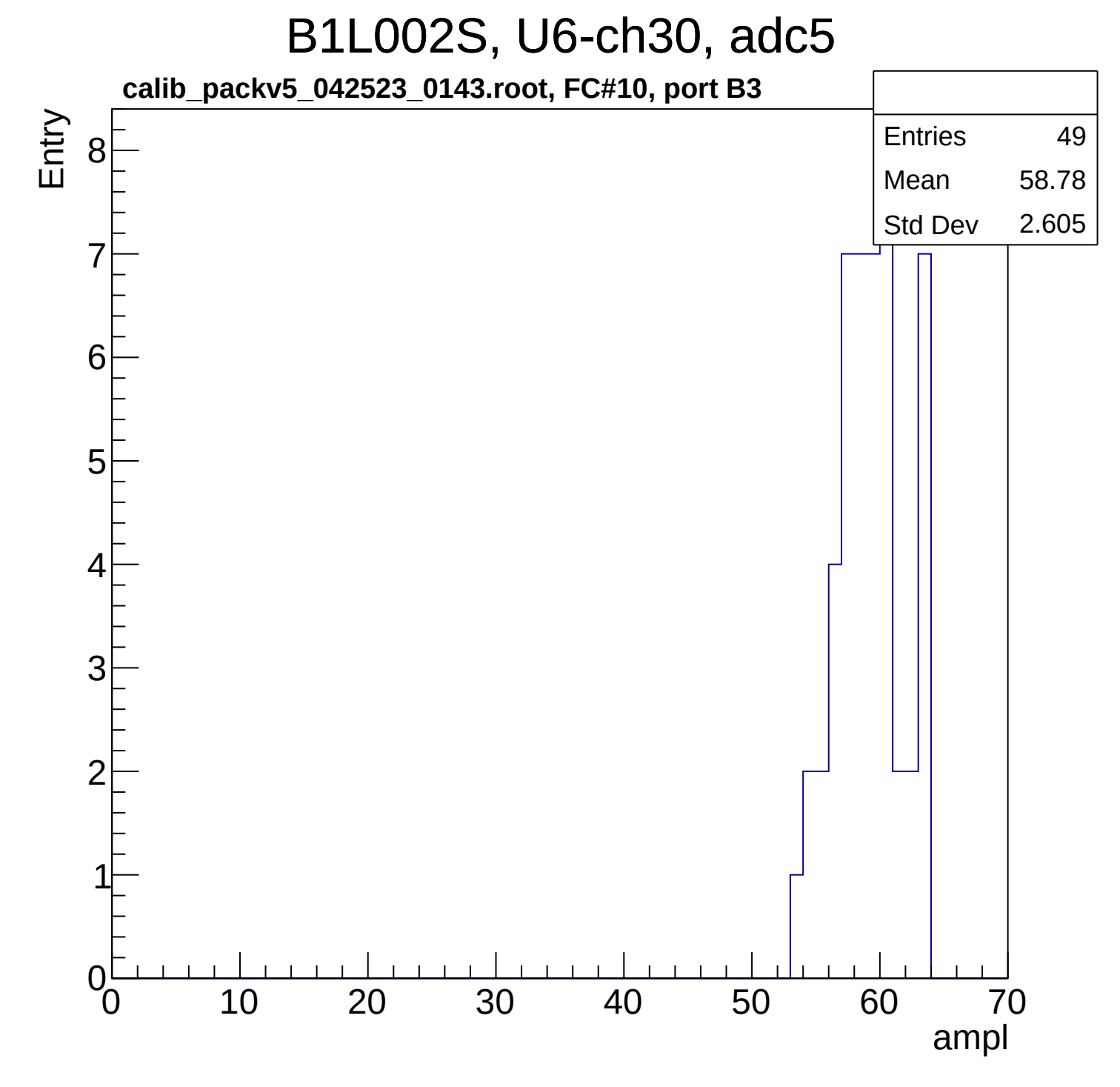

B1L002S, U6-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.6
Std Dev 3.322
Gaus mean : 29.2244
Gaus Width: 3.5720
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

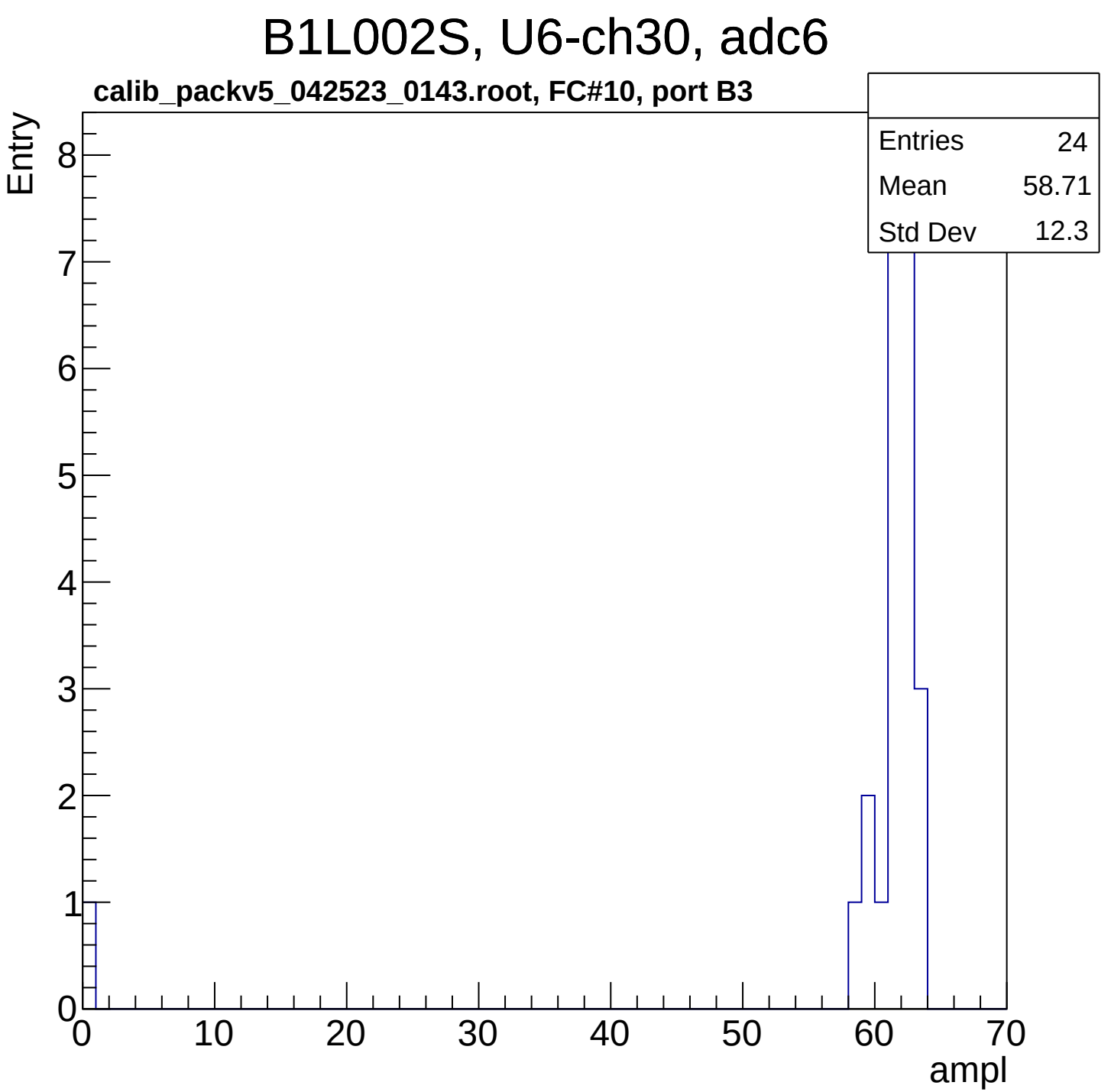

B1L002S, U6-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.73
Std Dev 2.836
Gaus mean : 35.2209
Gaus Width: 3.3463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

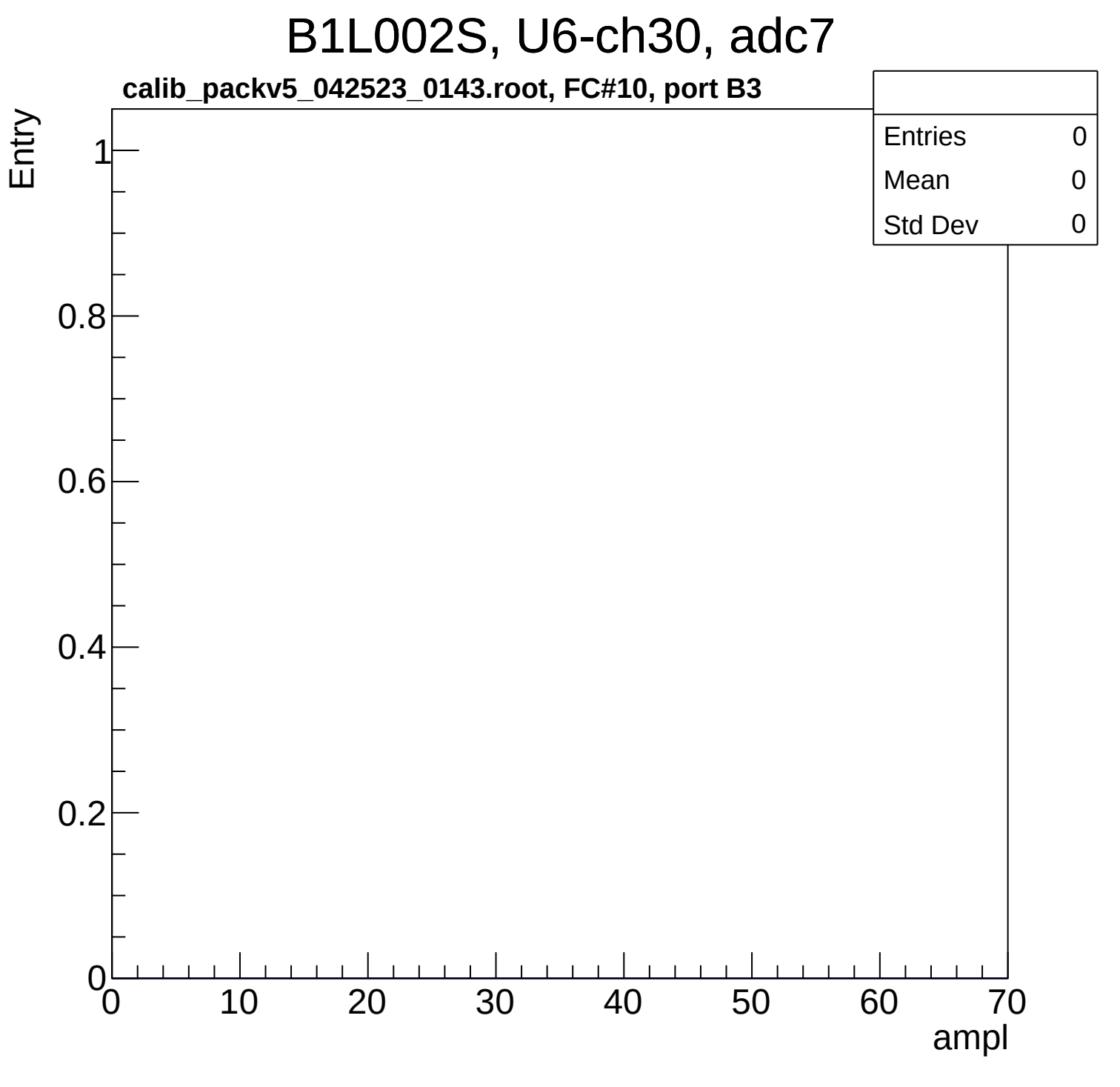

B1L002S, U6-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.24
Std Dev 2.68
Gaus mean : 40.6374
Gaus Width: 2.7402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

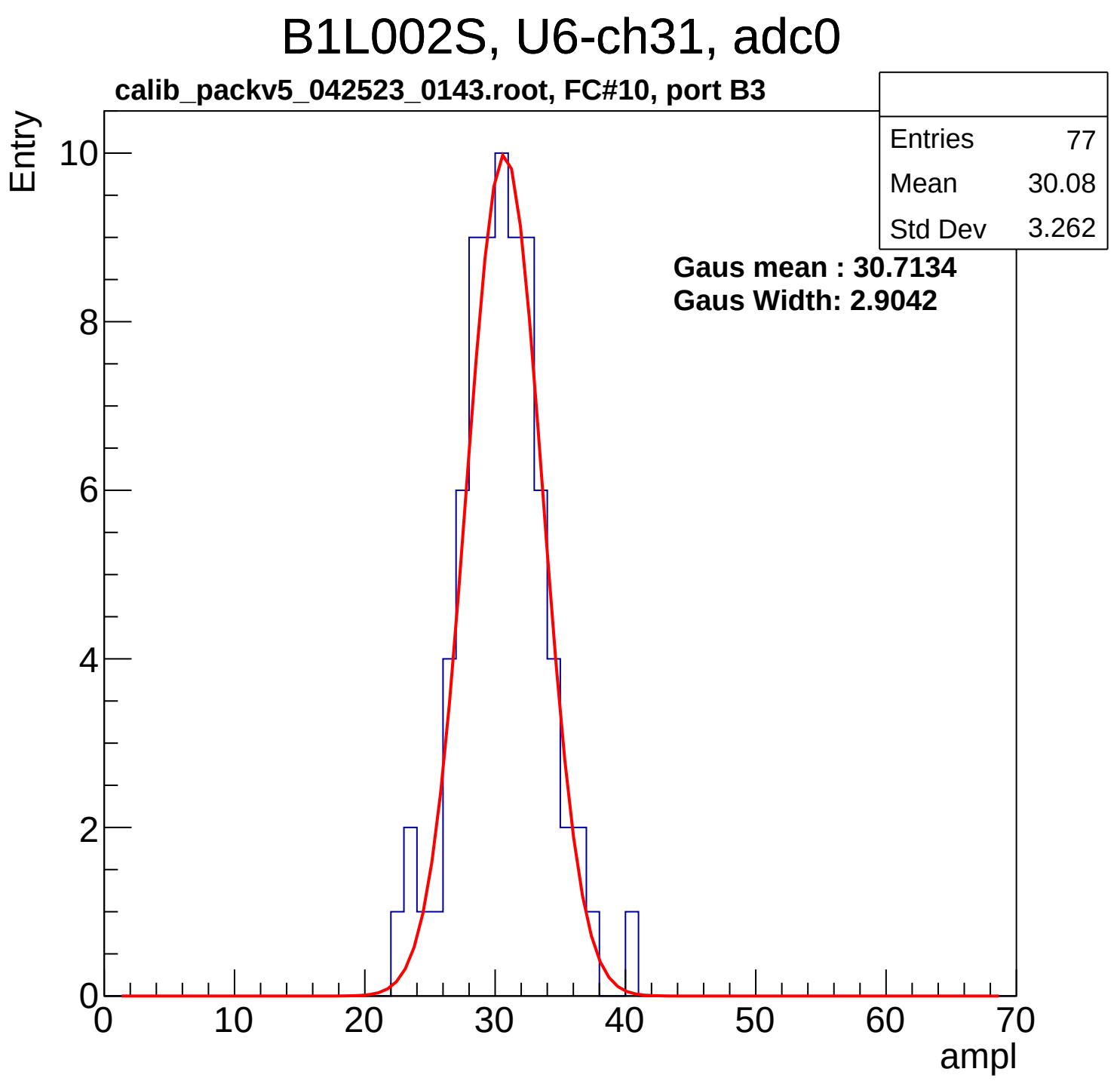

B1L002S, U6-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 46.4
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

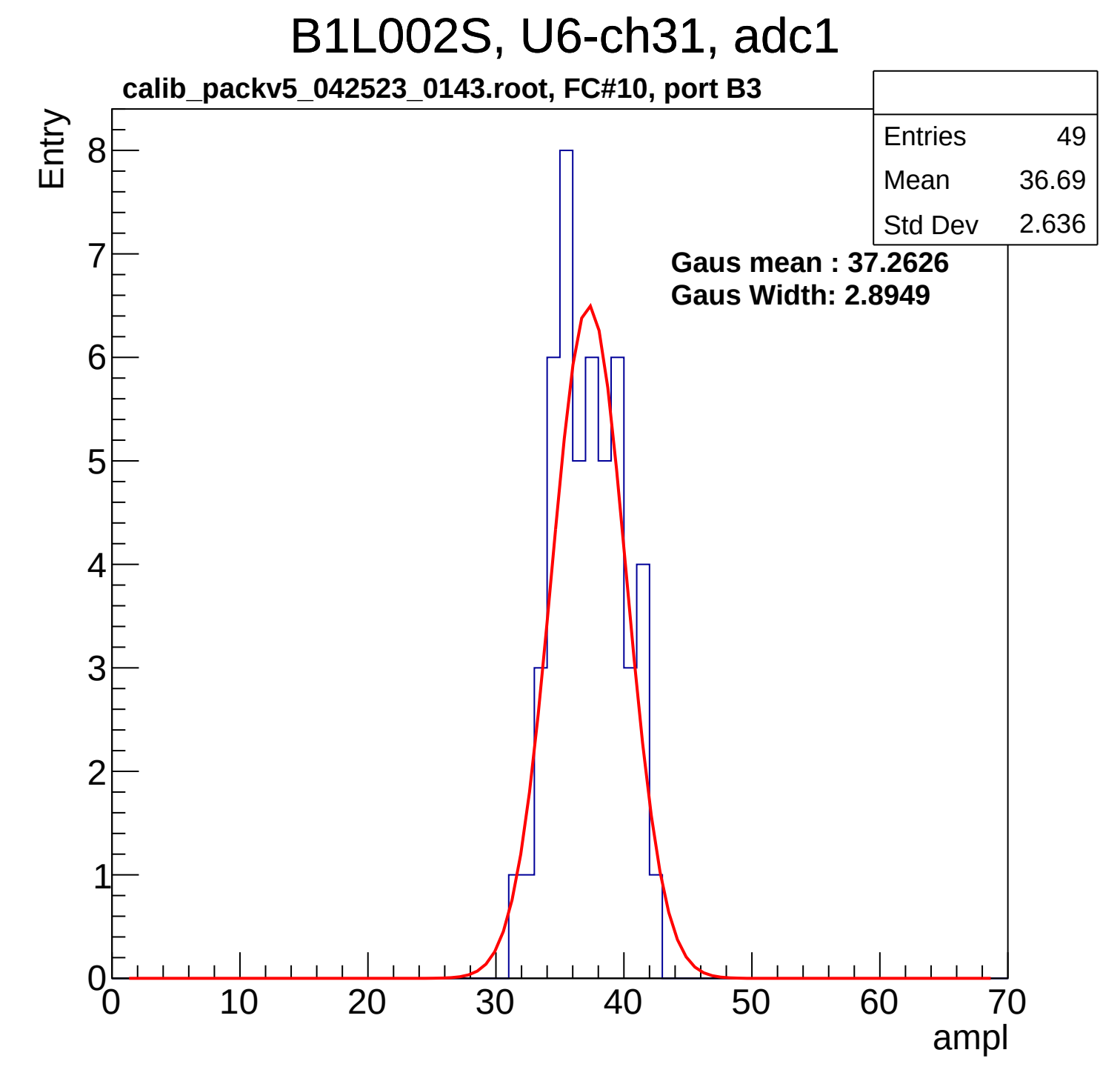

B1L002S, U6-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.08
Std Dev 3.018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

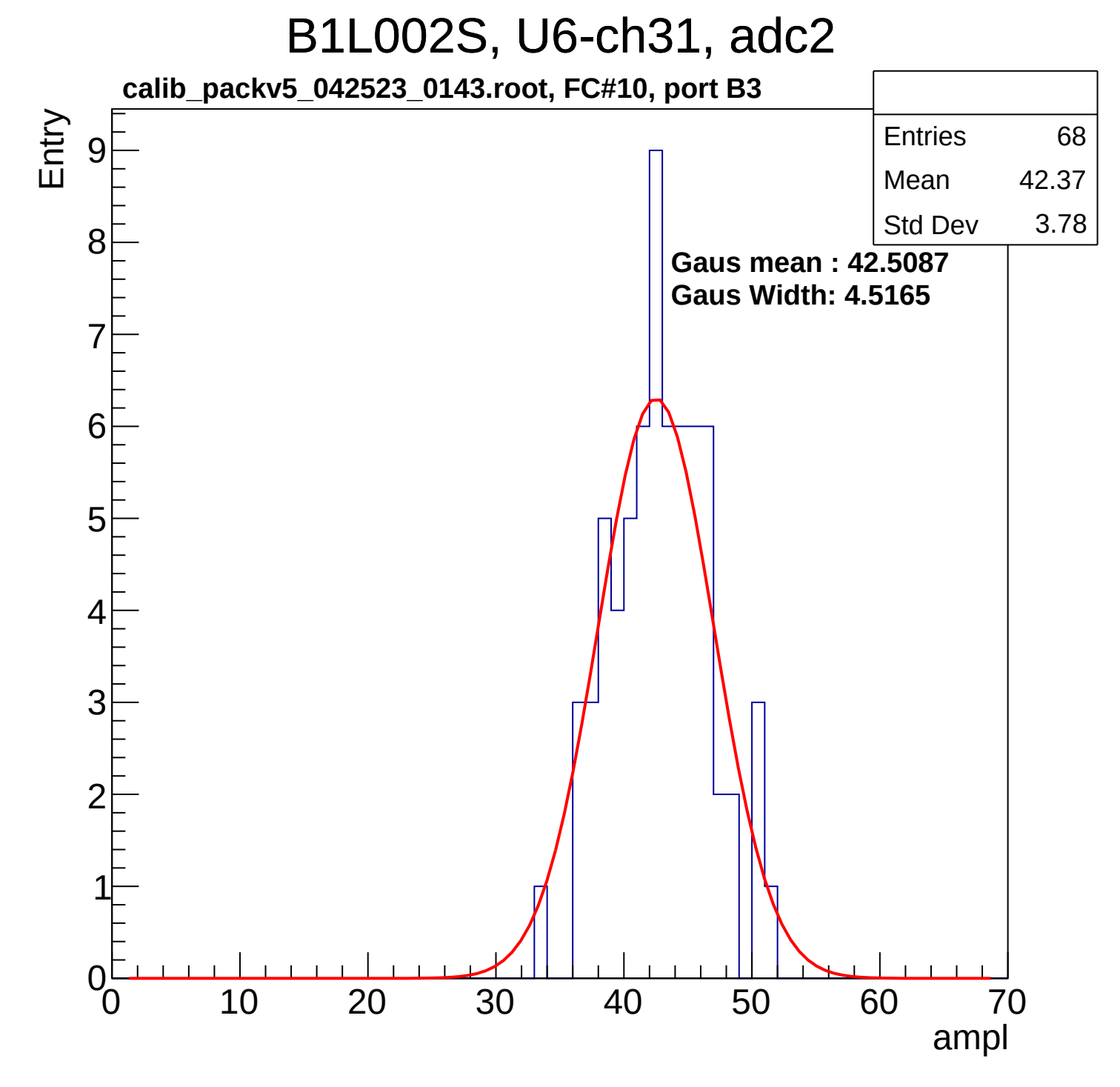

B1L002S, U6-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.78
Std Dev 2.605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

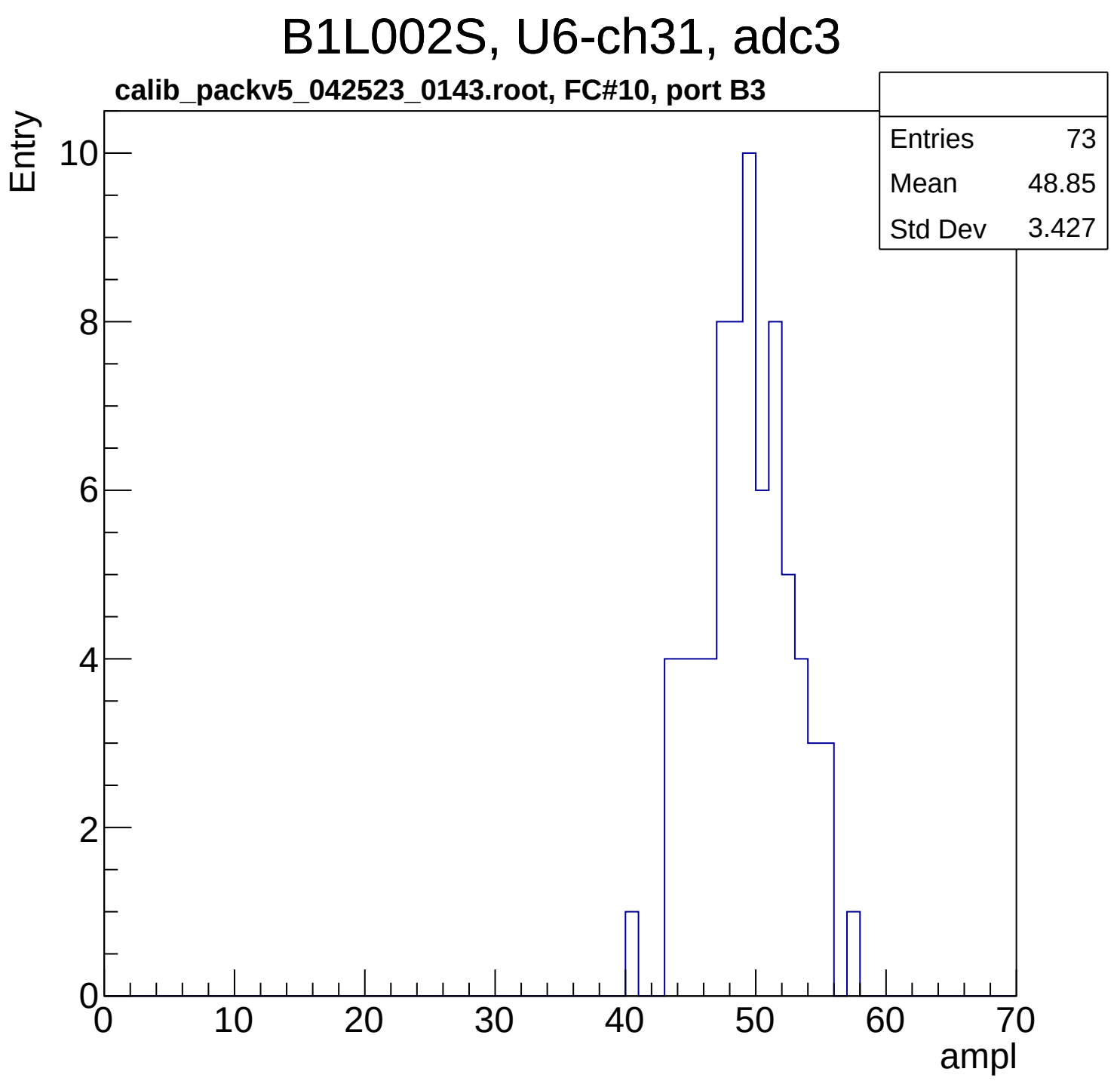

B1L002S, U6-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.71
Std Dev 12.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

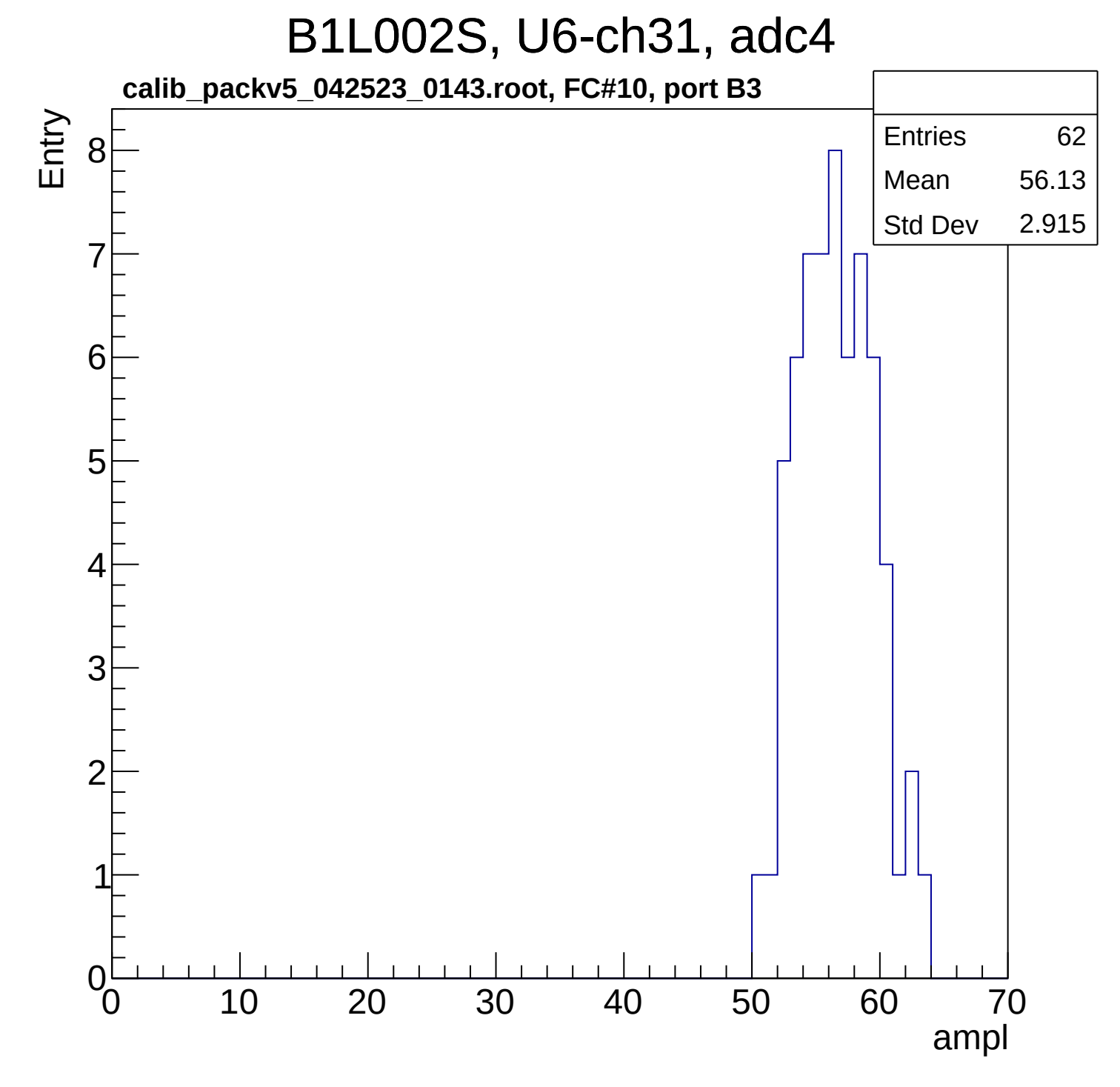

B1L002S, U6-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

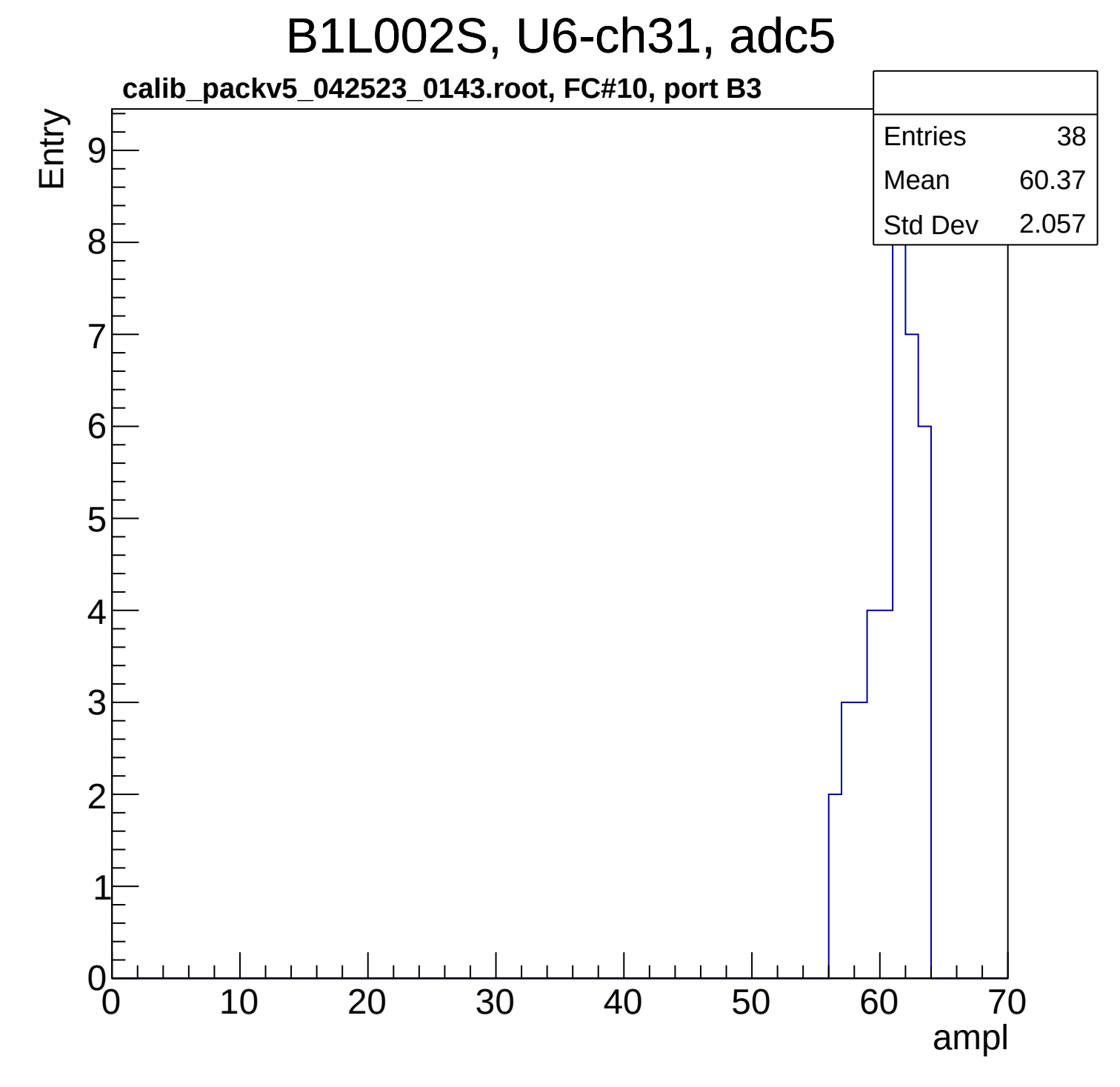

B1L002S, U6-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 30.08
Std Dev 3.262
Gaus mean : 30.7134
Gaus Width: 2.9042
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

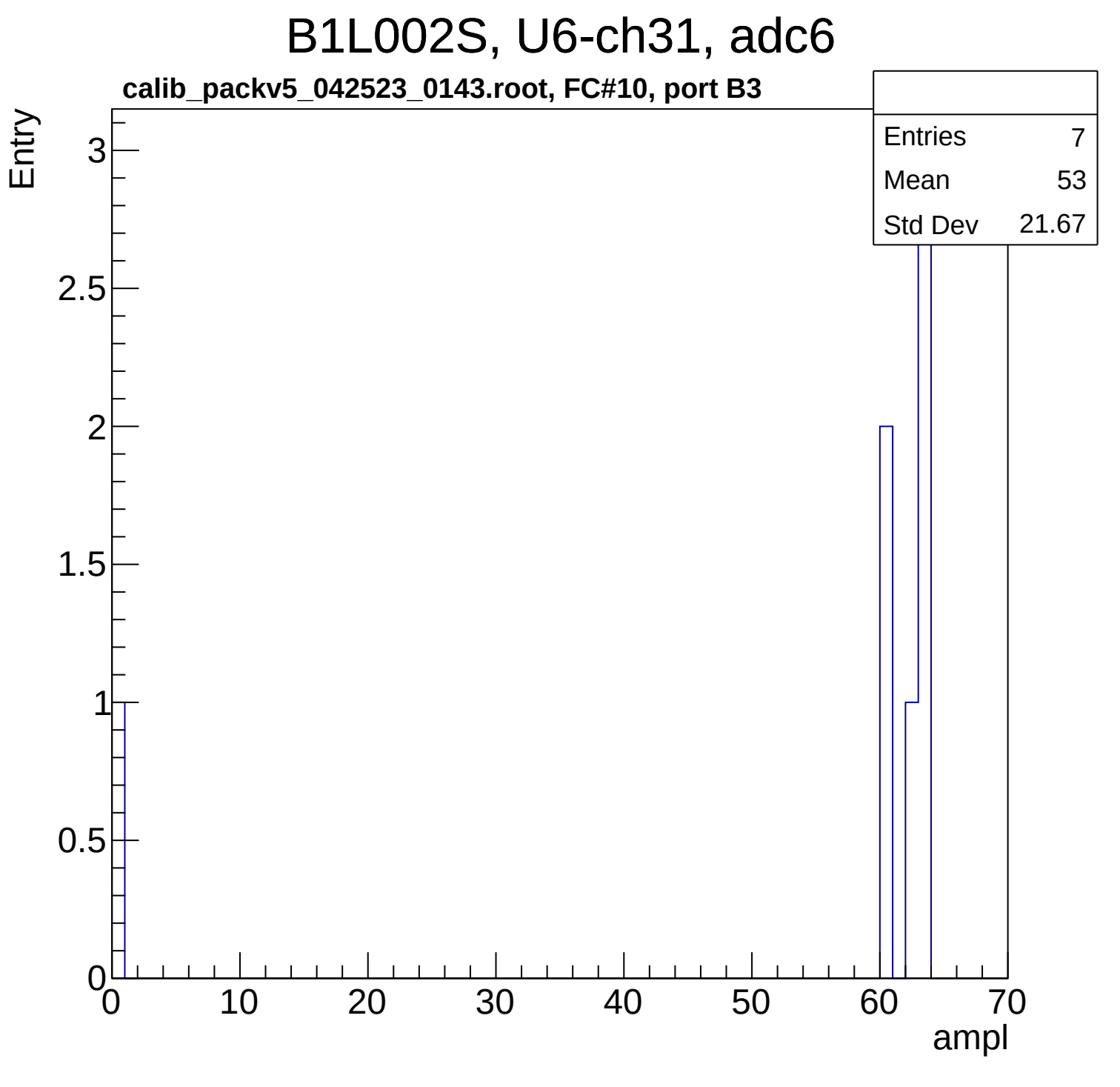

B1L002S, U6-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 36.69
Std Dev 2.636
Gaus mean : 37.2626
Gaus Width: 2.8949
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

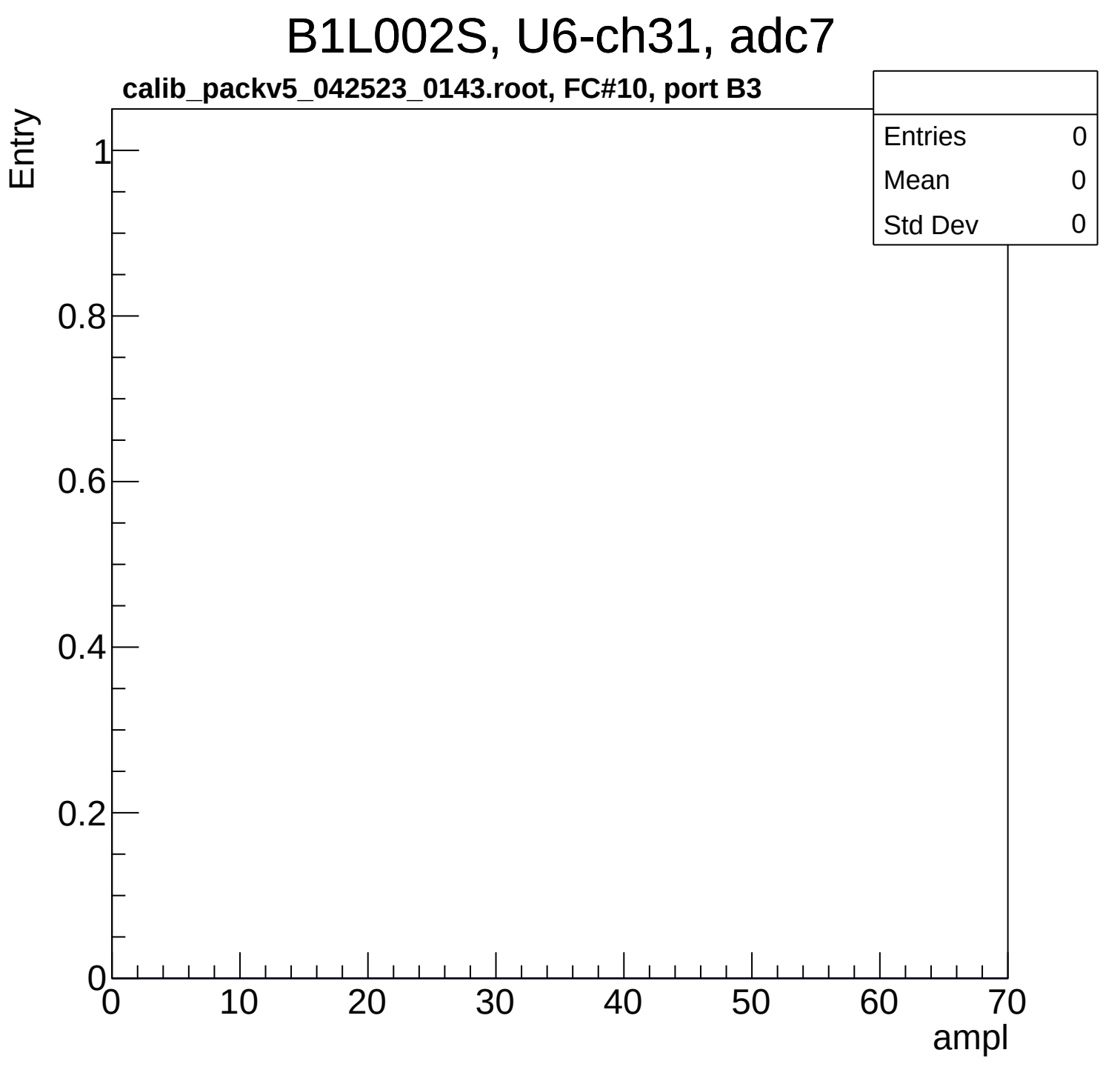

B1L002S, U6-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.37
Std Dev 3.78
Gaus mean : 42.5087
Gaus Width: 4.5165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

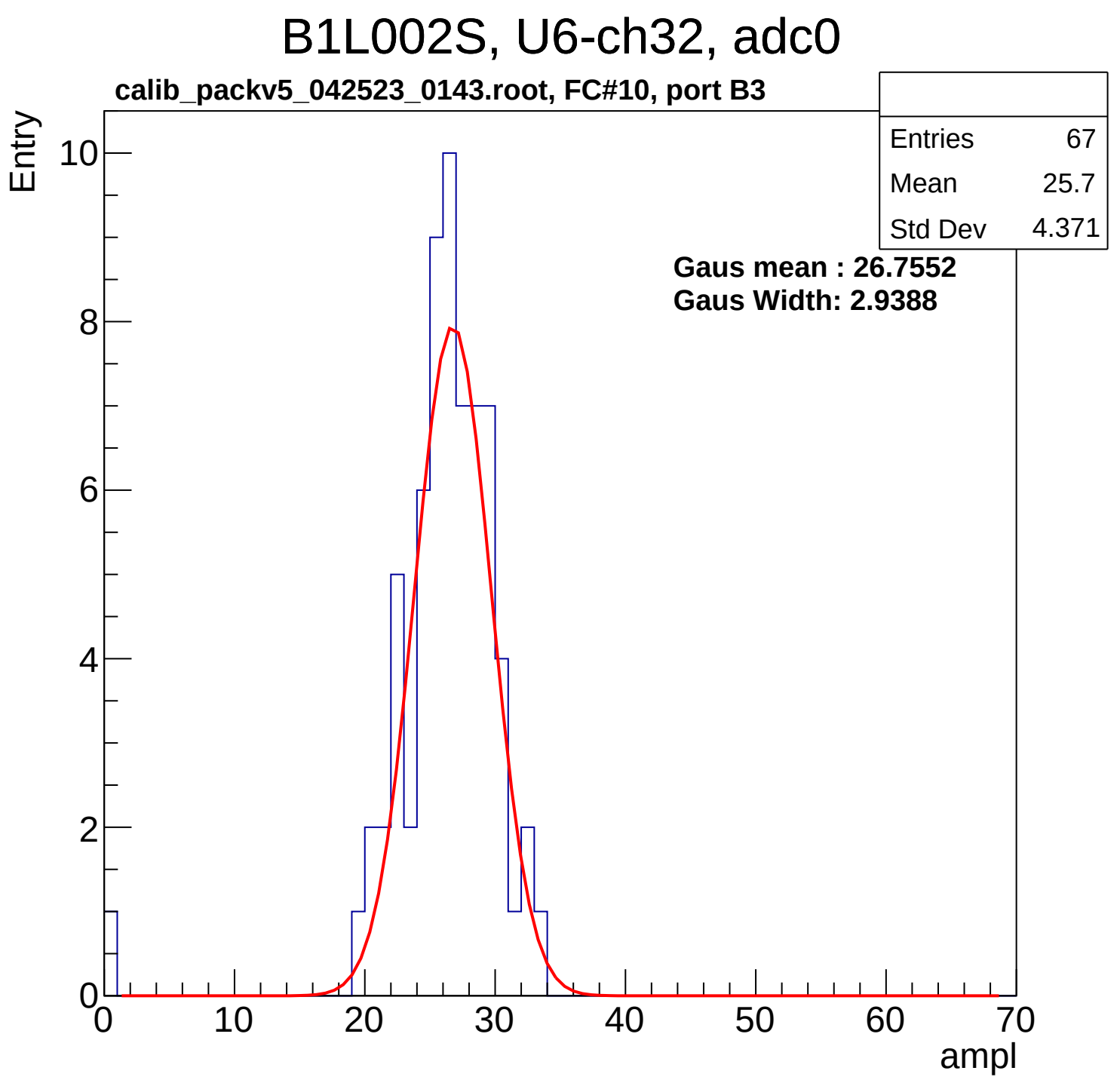

B1L002S, U6-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.85
Std Dev 3.427
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

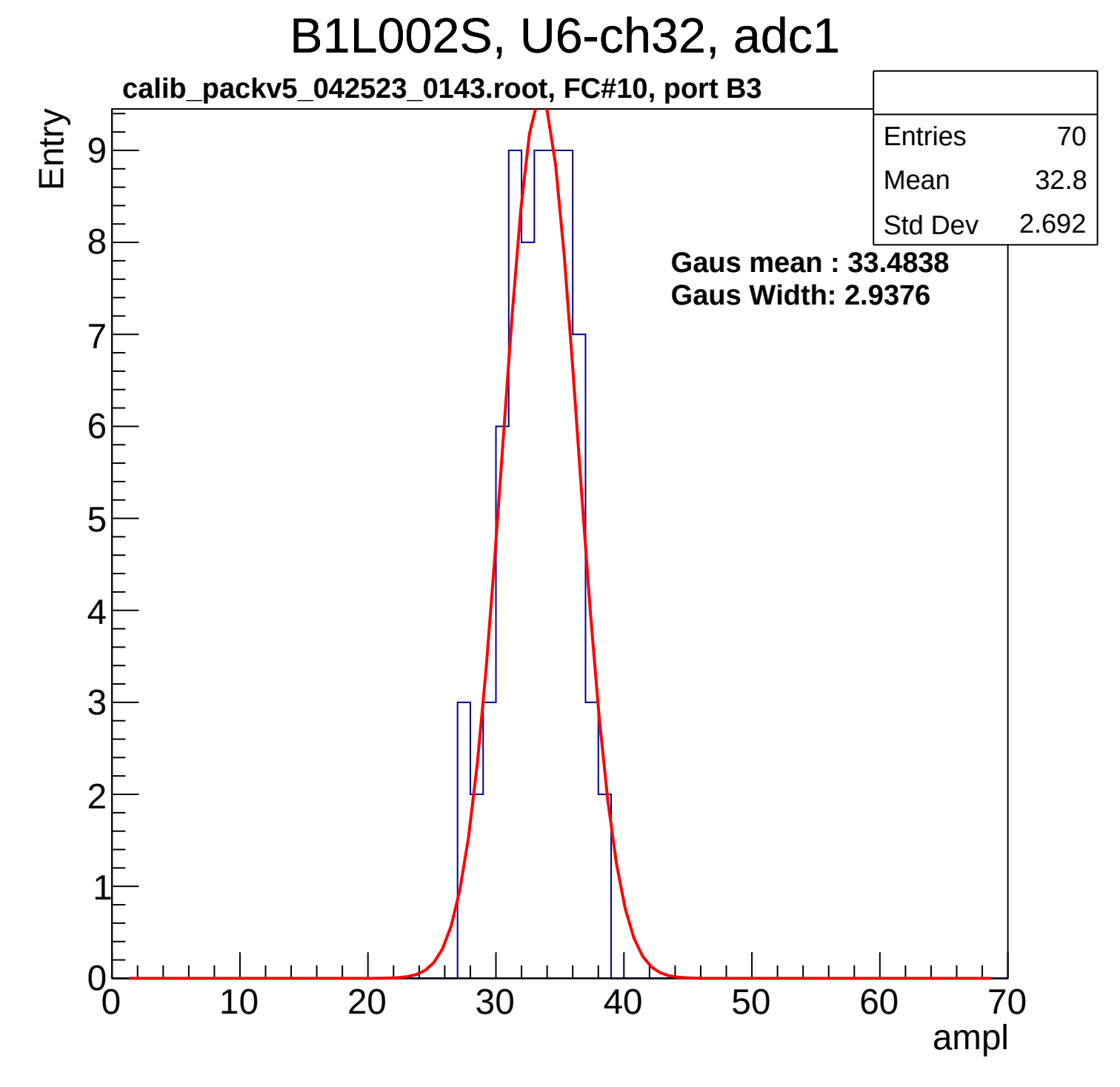

B1L002S, U6-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.13
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

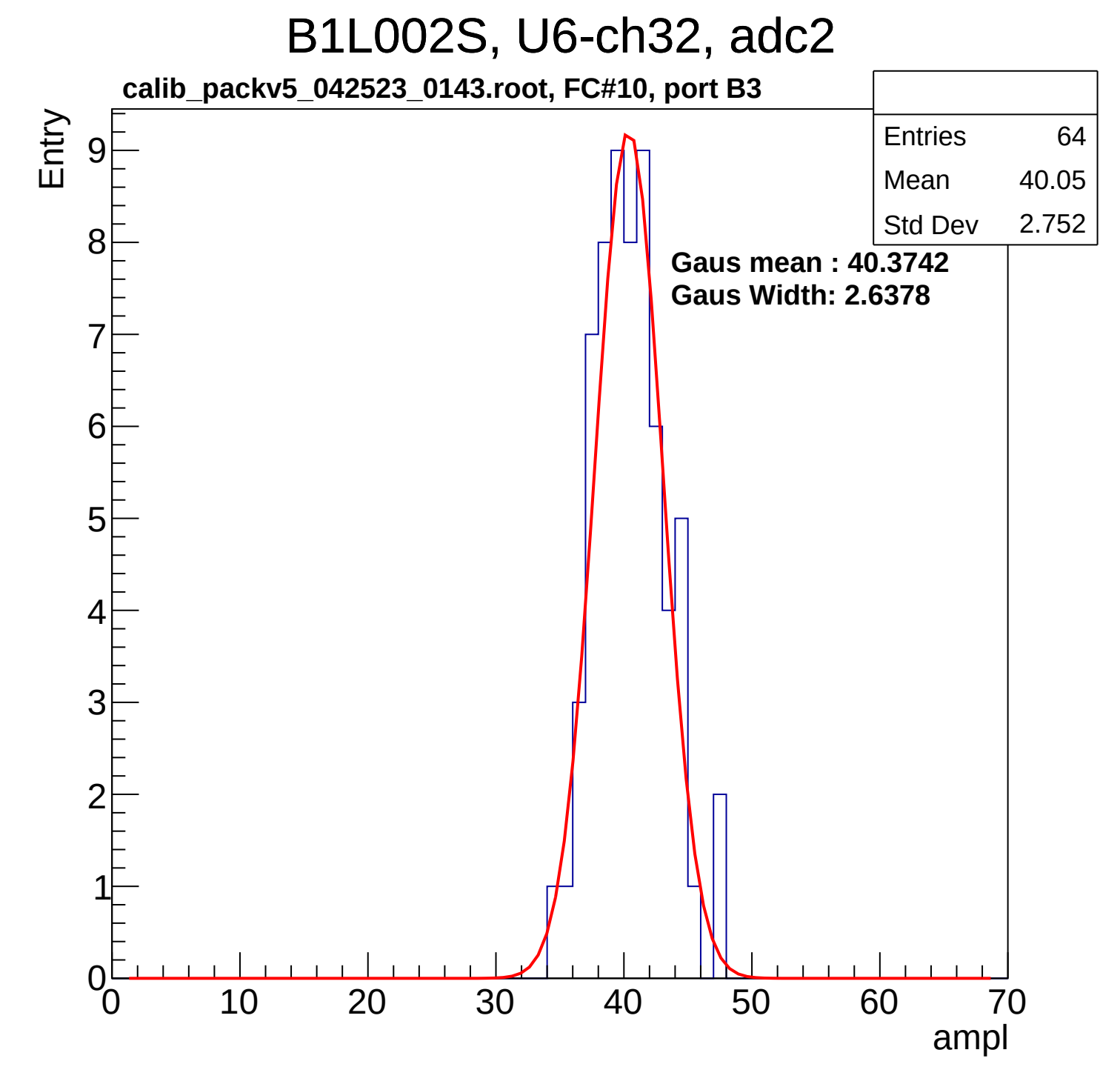

B1L002S, U6-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 60.37
Std Dev 2.057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

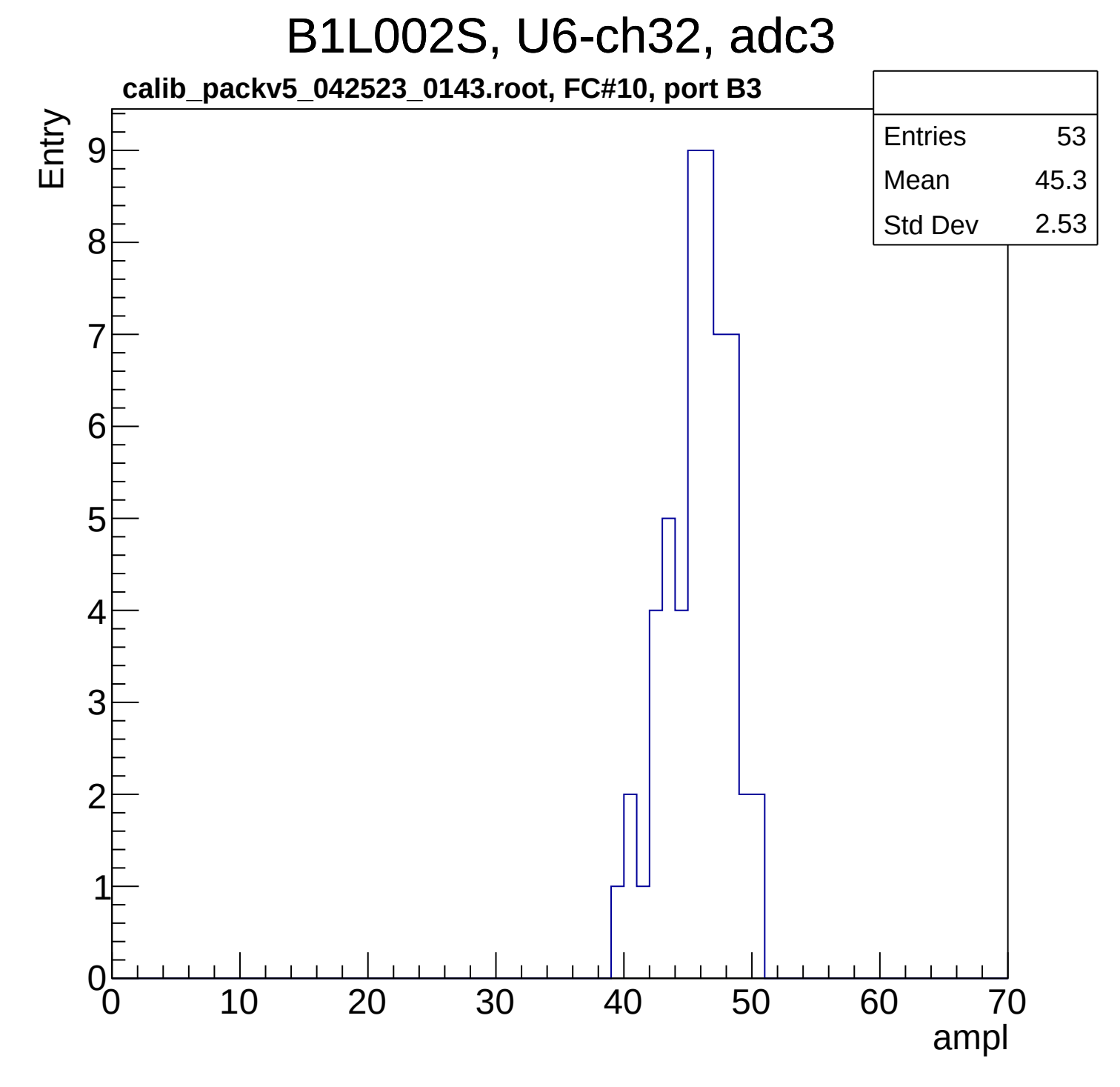

B1L002S, U6-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53
Std Dev 21.67
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

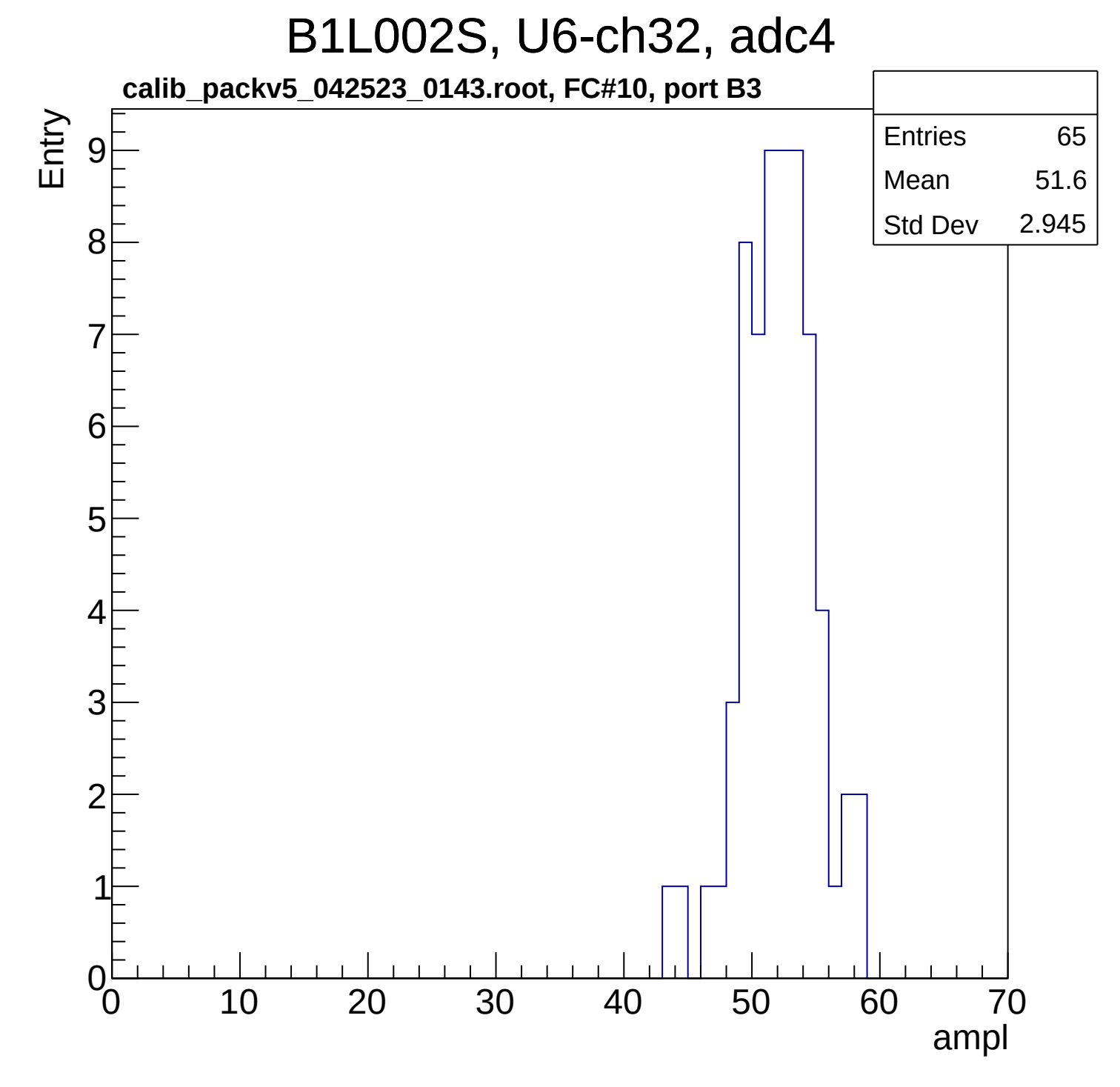

B1L002S, U6-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

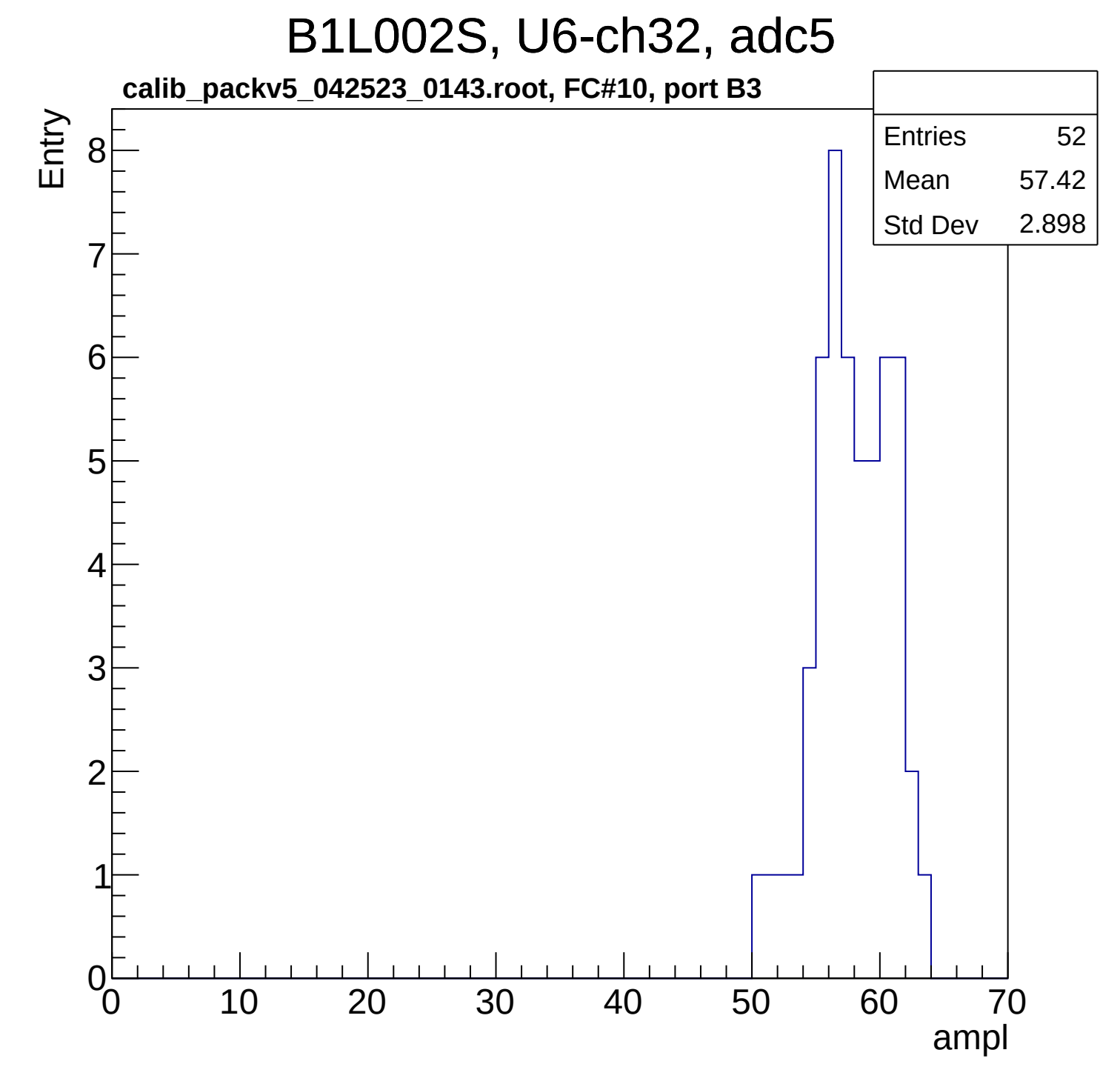

B1L002S, U6-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 25.7
Std Dev 4.371
Gaus mean : 26.7552
Gaus Width: 2.9388
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

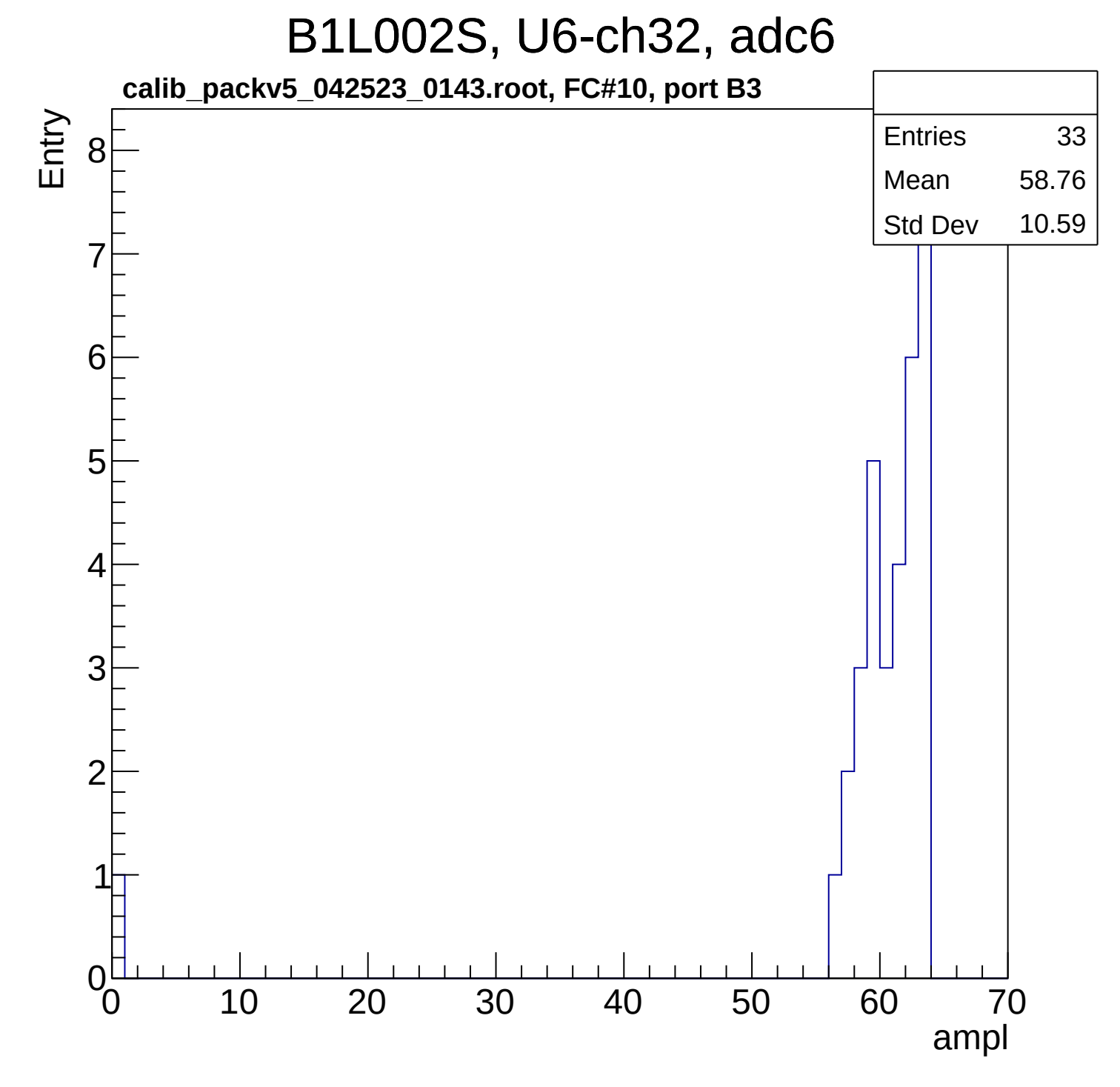

B1L002S, U6-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 32.8
Std Dev 2.692
Gaus mean : 33.4838
Gaus Width: 2.9376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

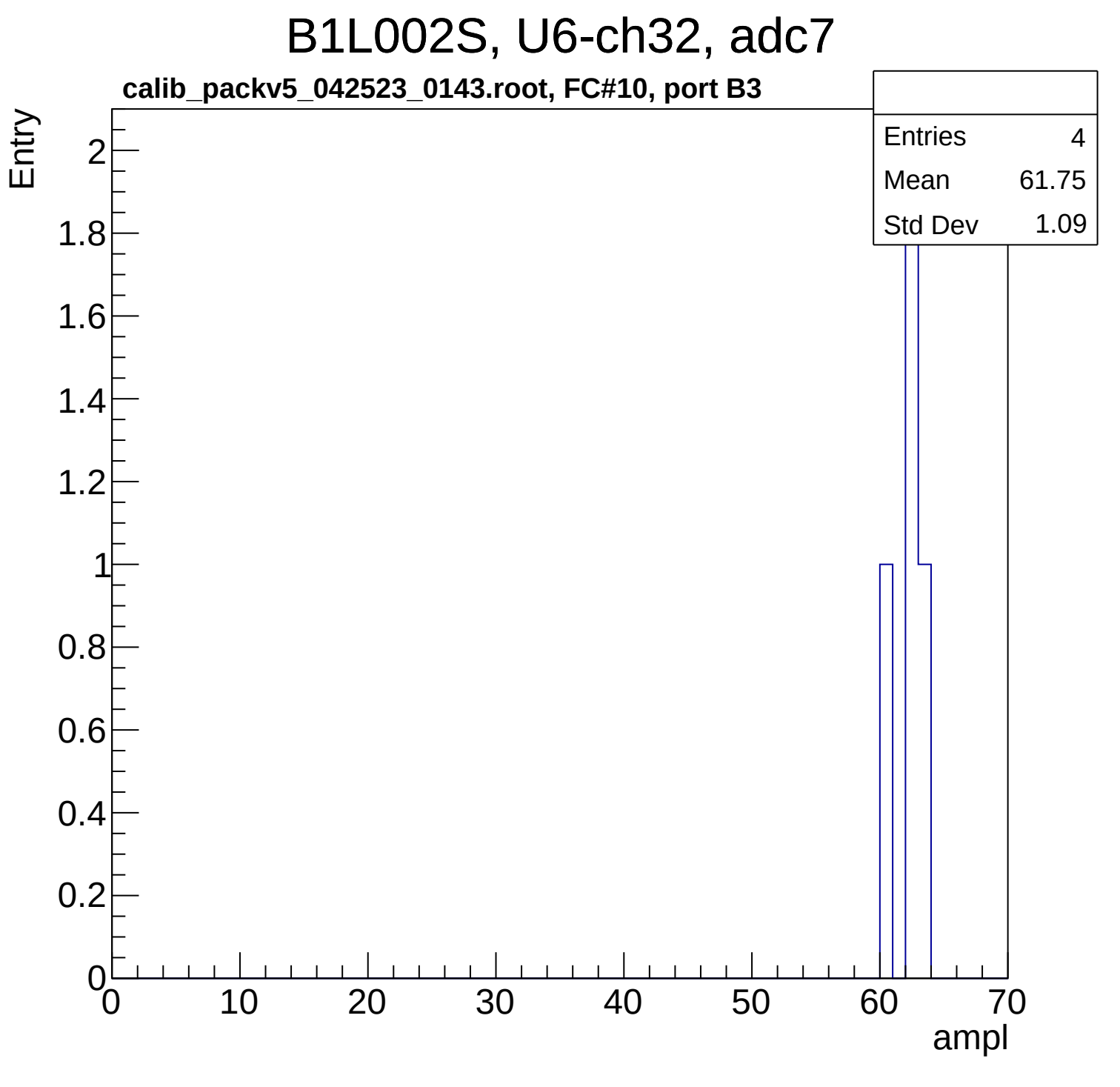

B1L002S, U6-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.05
Std Dev 2.752
Gaus mean : 40.3742
Gaus Width: 2.6378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

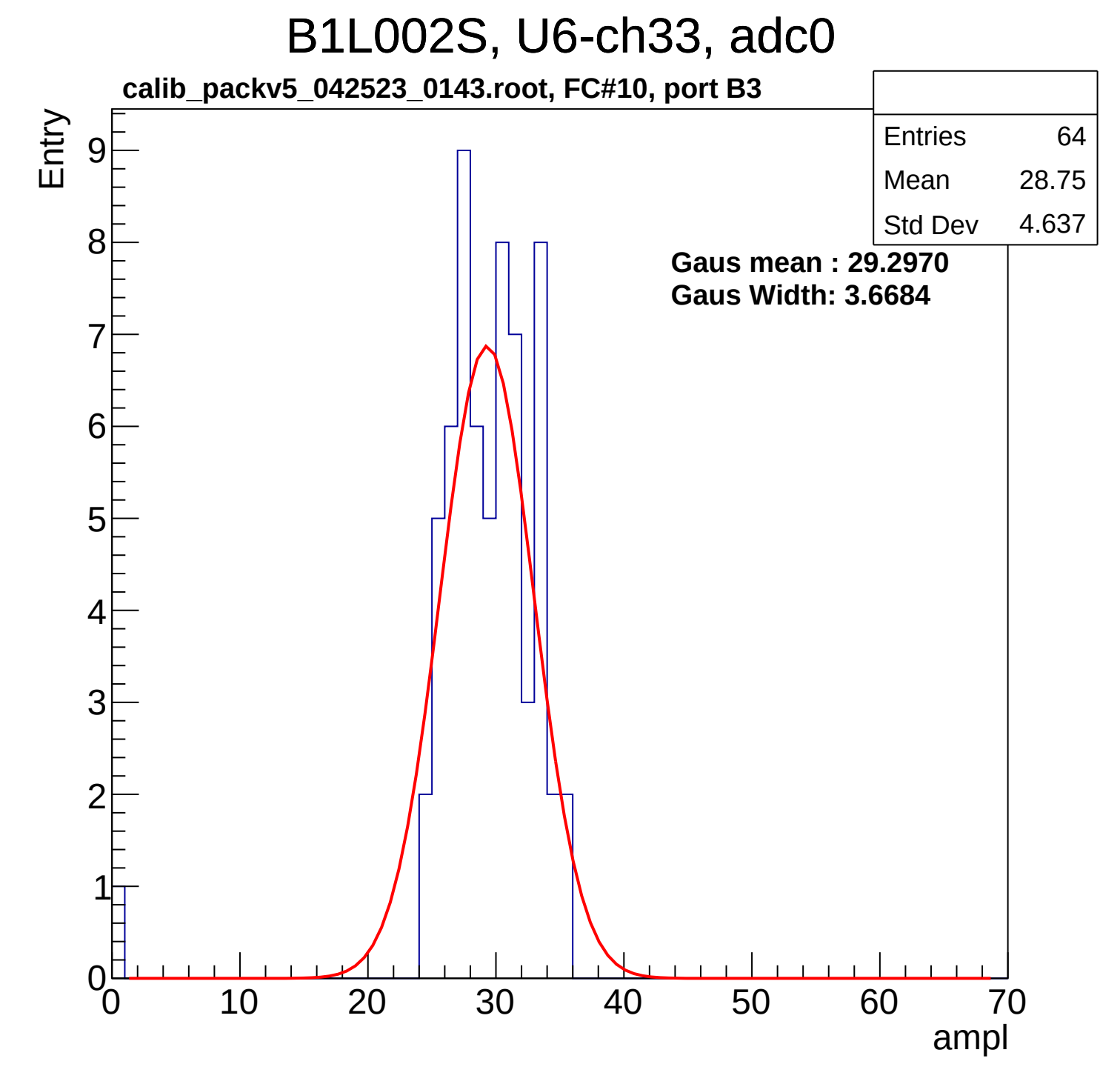

B1L002S, U6-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.3
Std Dev 2.53
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

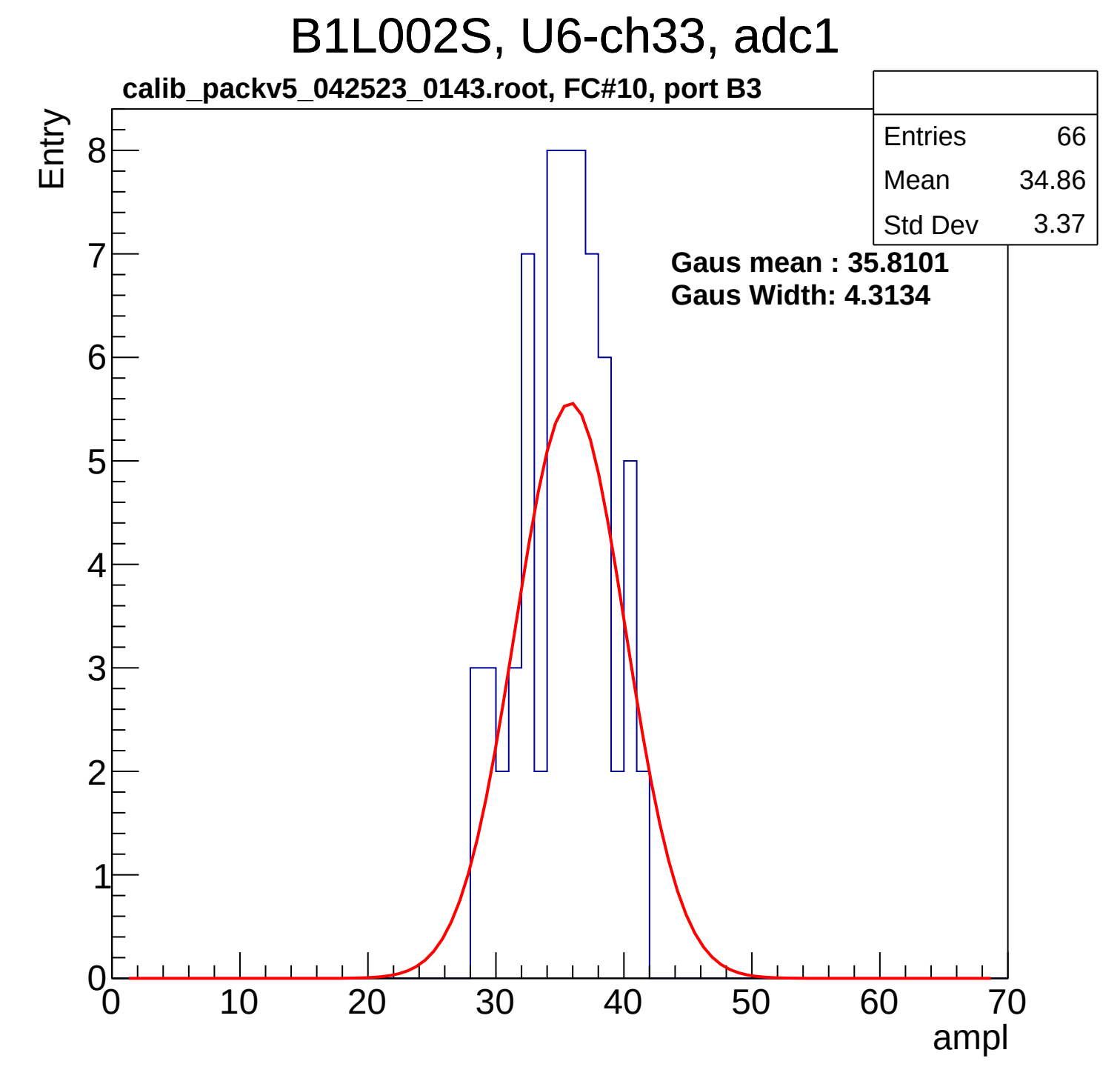

B1L002S, U6-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 51.6
Std Dev 2.945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

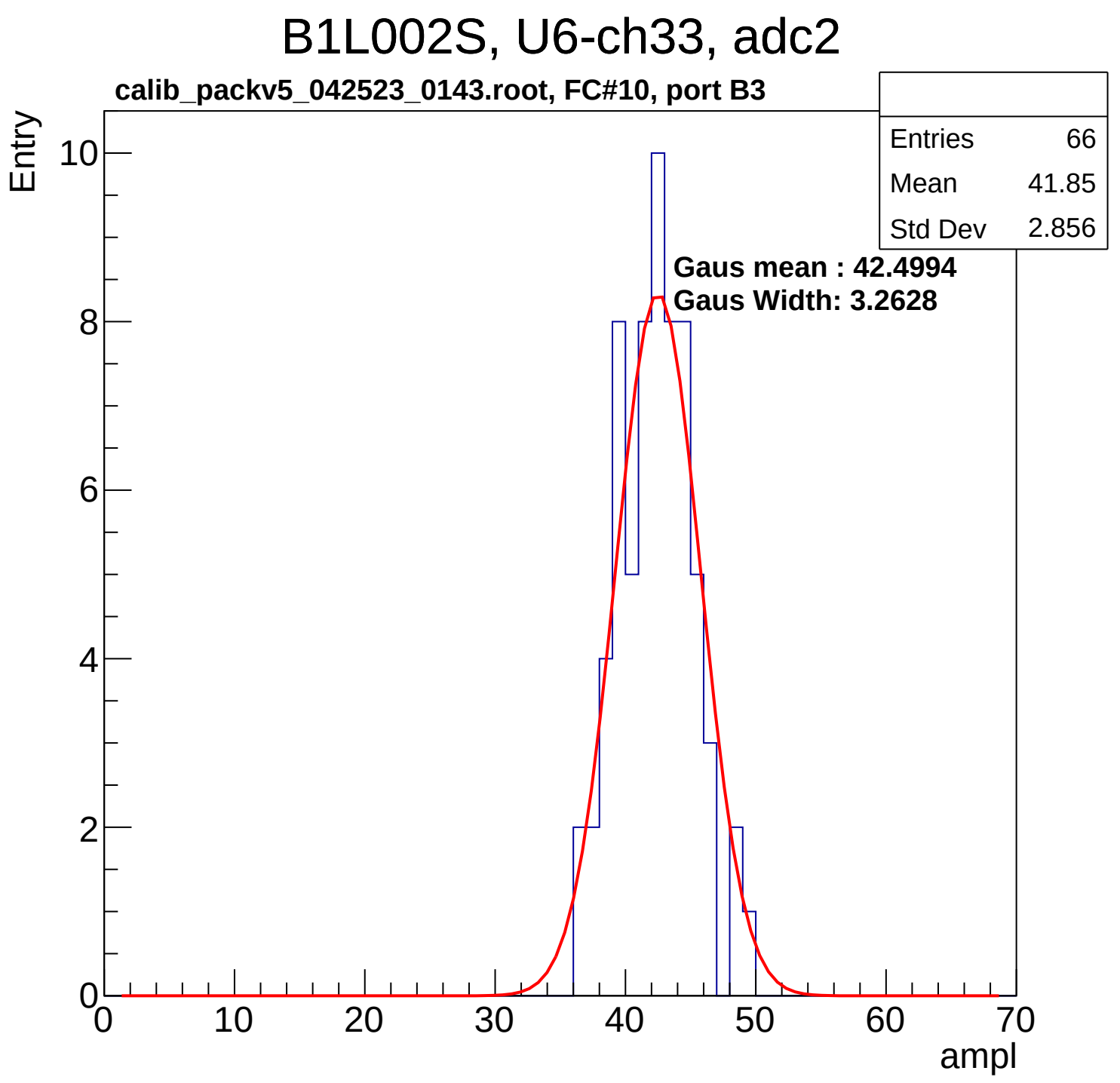

B1L002S, U6-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.42
Std Dev 2.898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

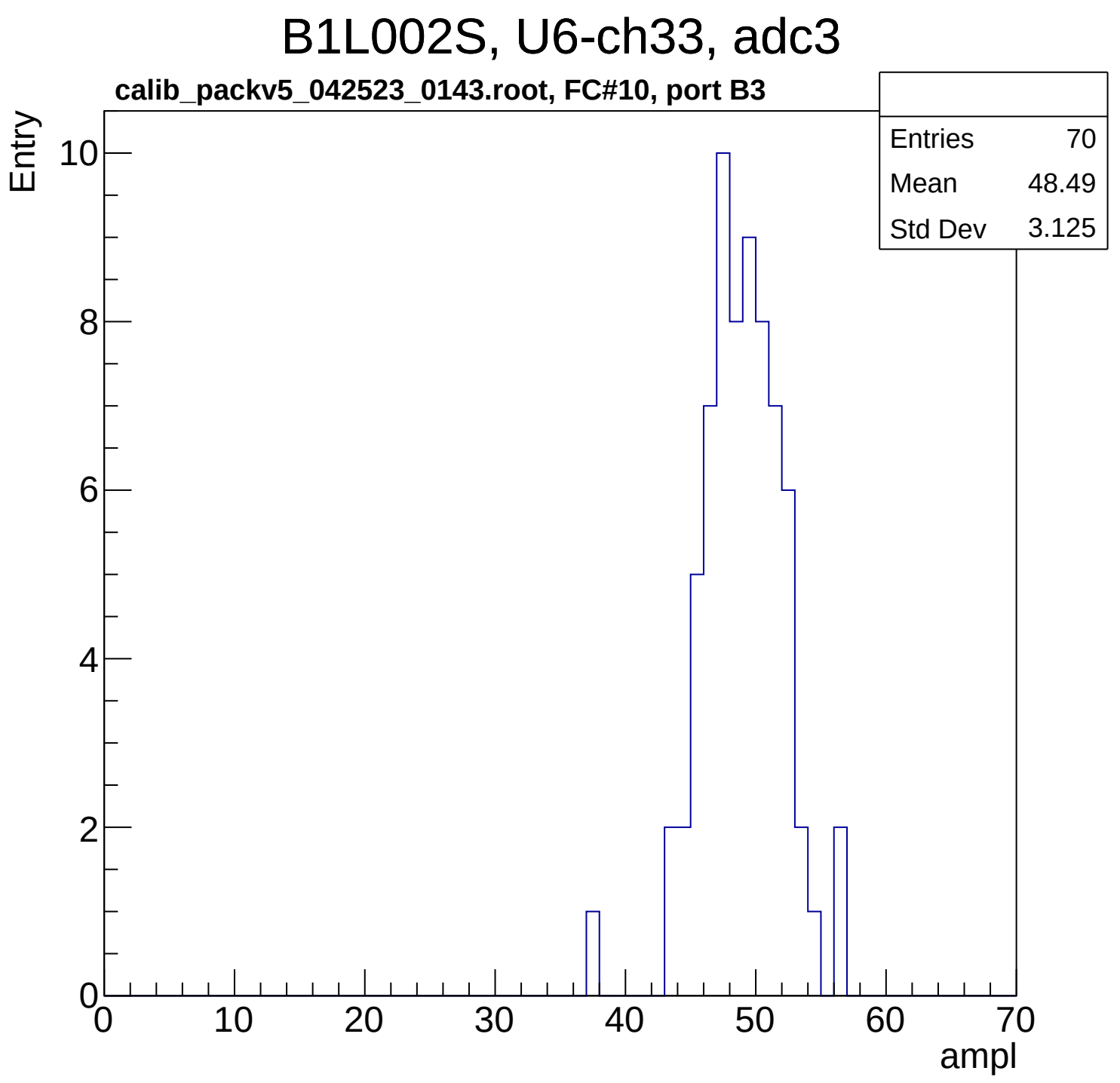

B1L002S, U6-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 58.76
Std Dev 10.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

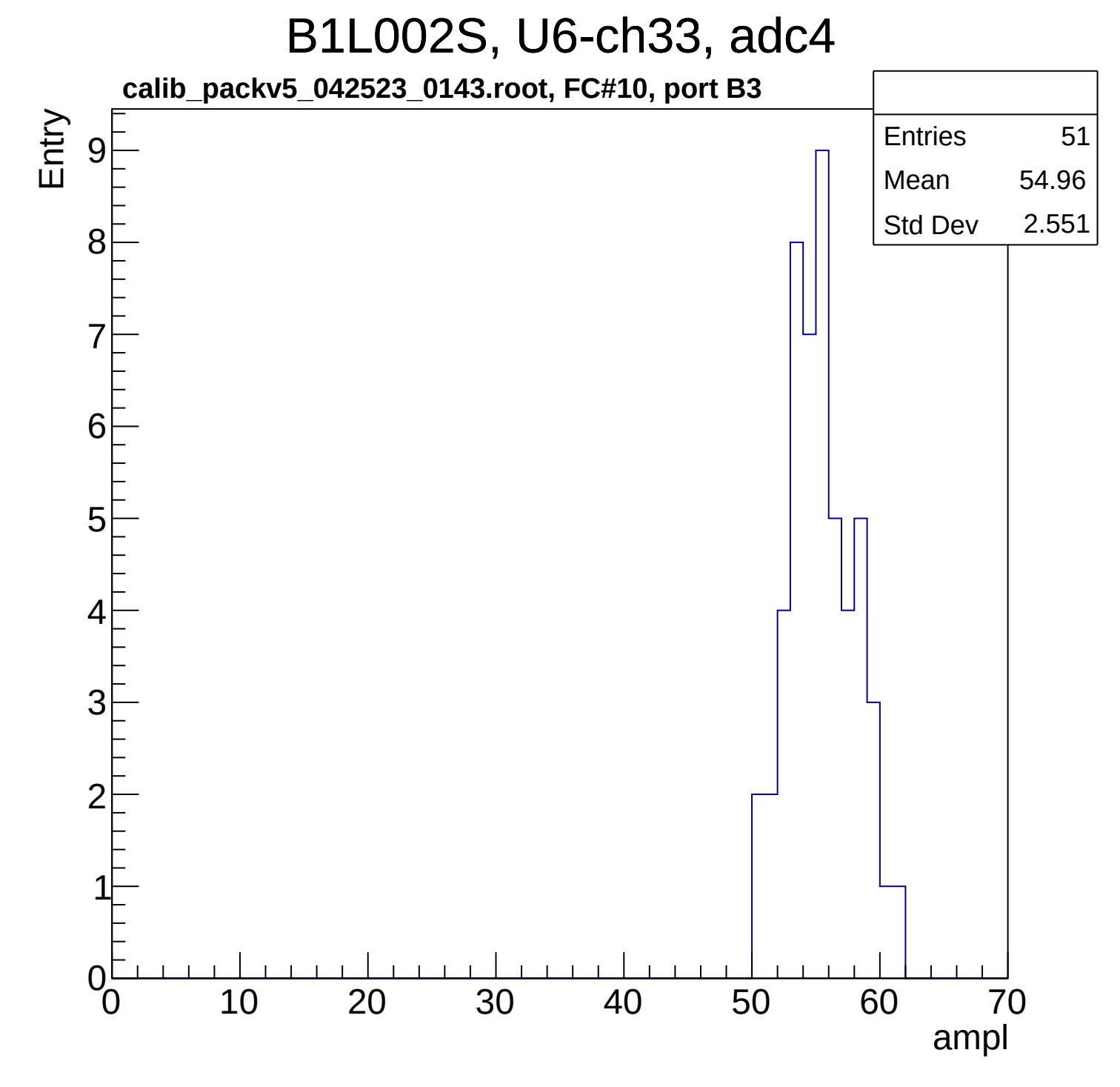

B1L002S, U6-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

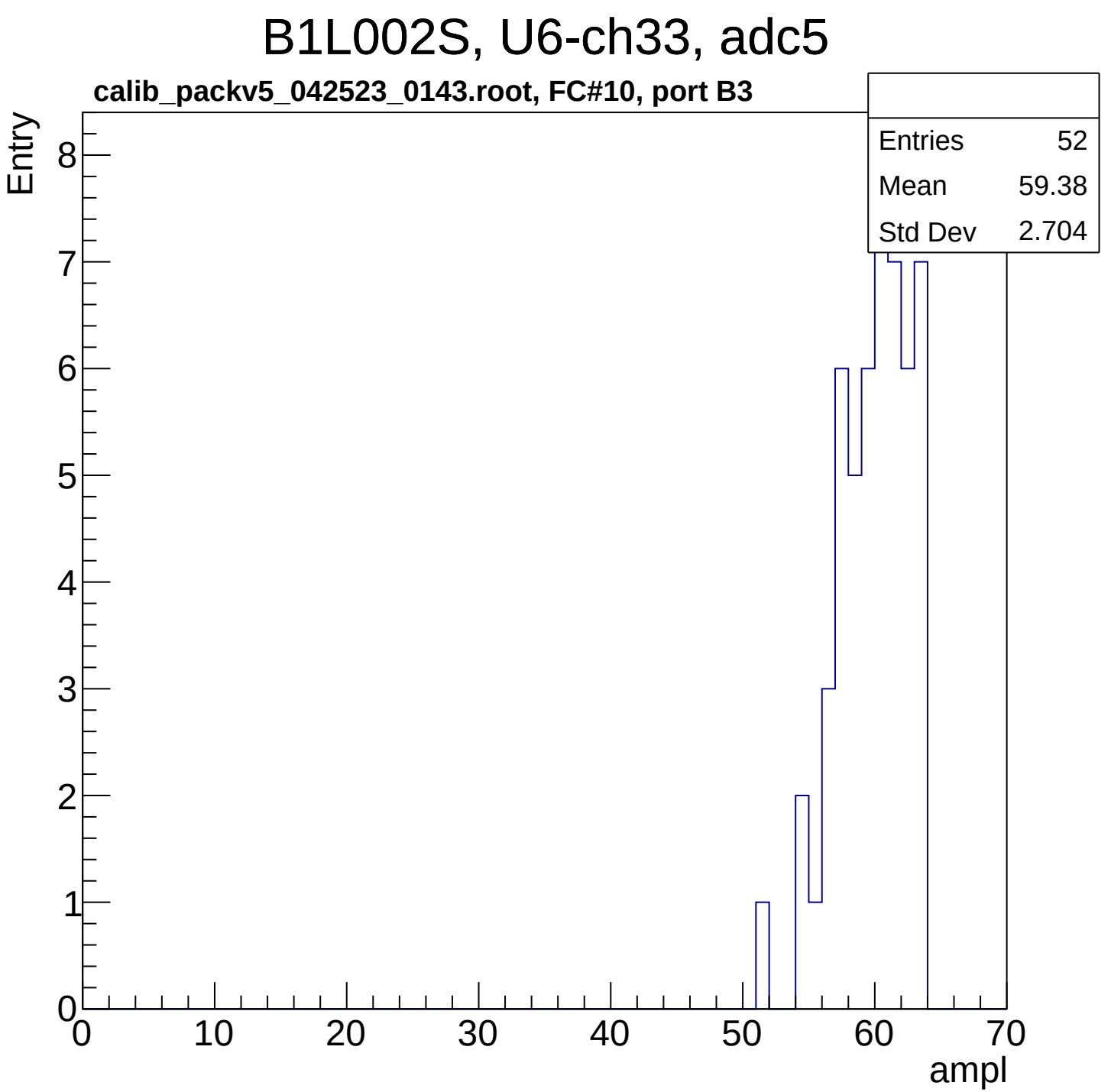

B1L002S, U6-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.75
Std Dev 4.637
Gaus mean : 29.2970
Gaus Width: 3.6684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

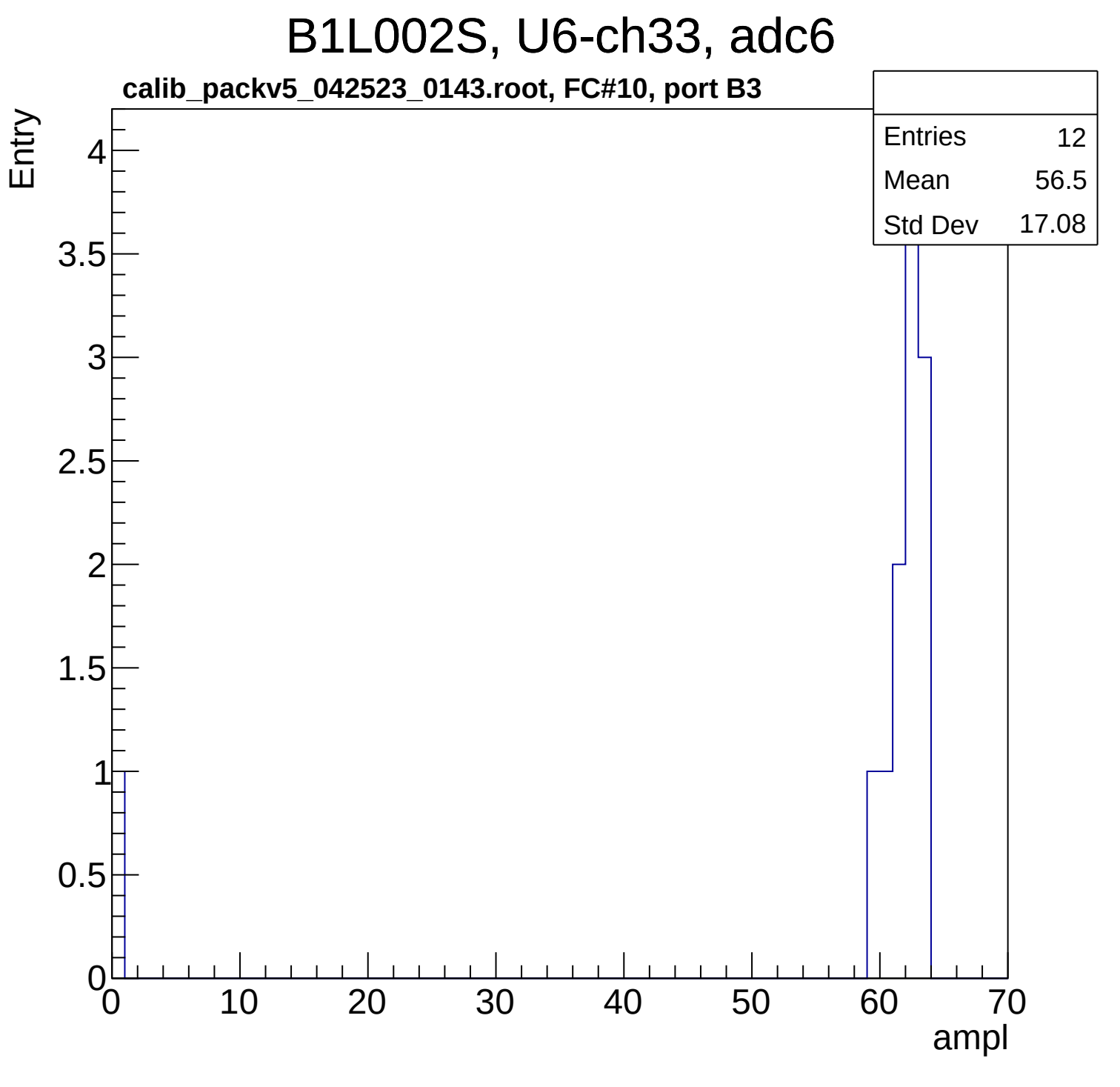

B1L002S, U6-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.86
Std Dev 3.37
Gaus mean : 35.8101
Gaus Width: 4.3134
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

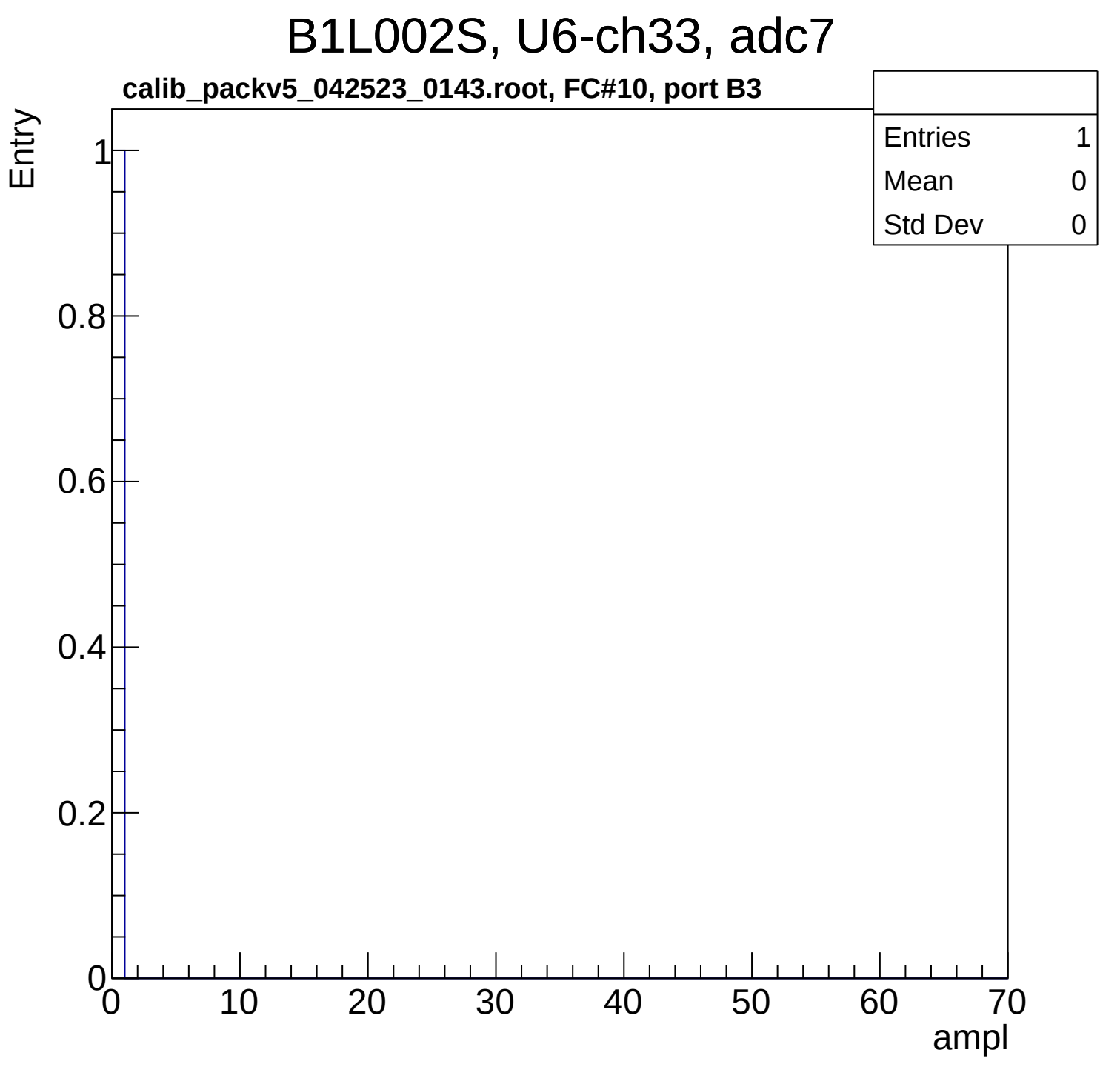

B1L002S, U6-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.85
Std Dev 2.856
Gaus mean : 42.4994
Gaus Width: 3.2628
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

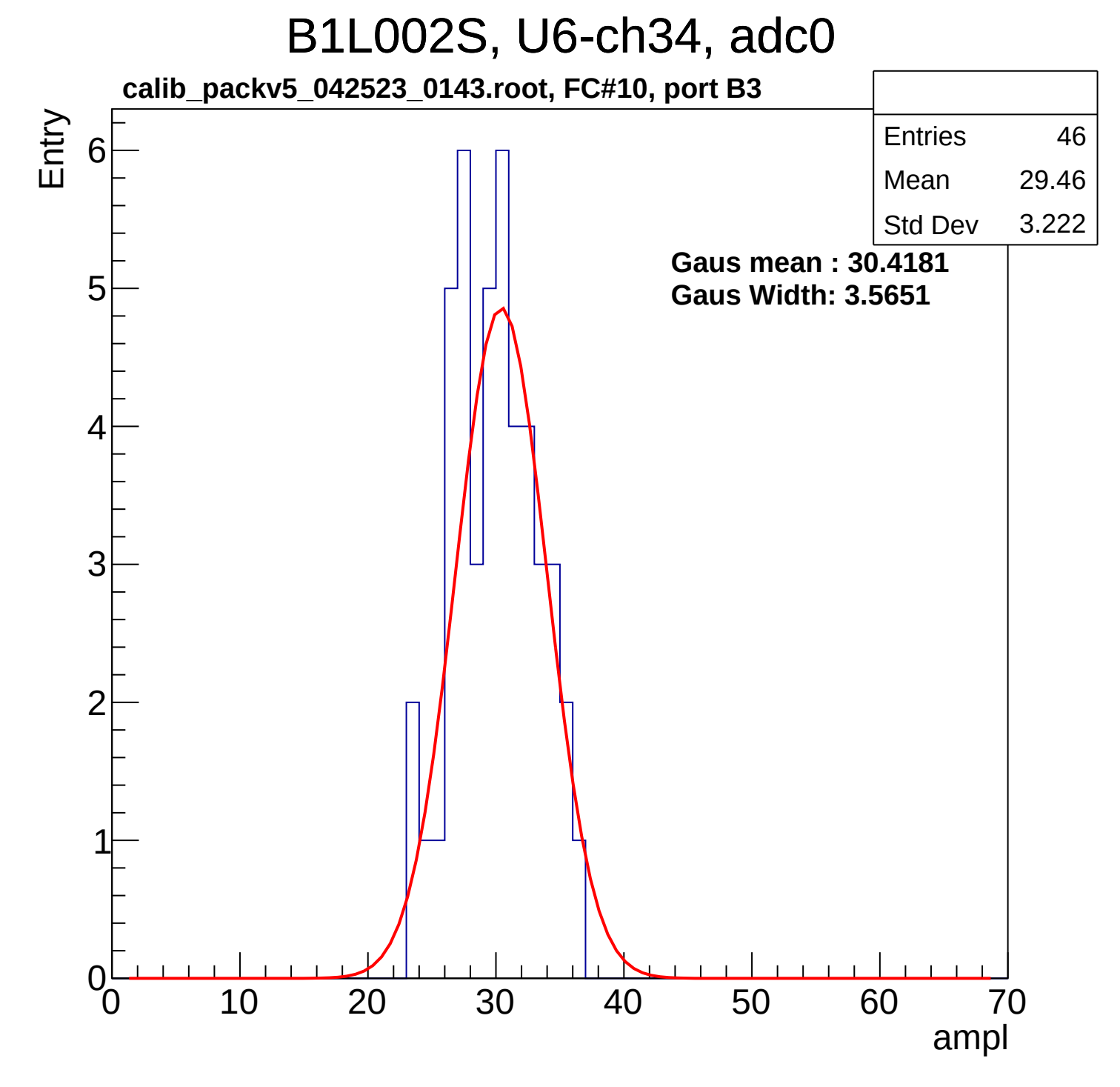

B1L002S, U6-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.49
Std Dev 3.125
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

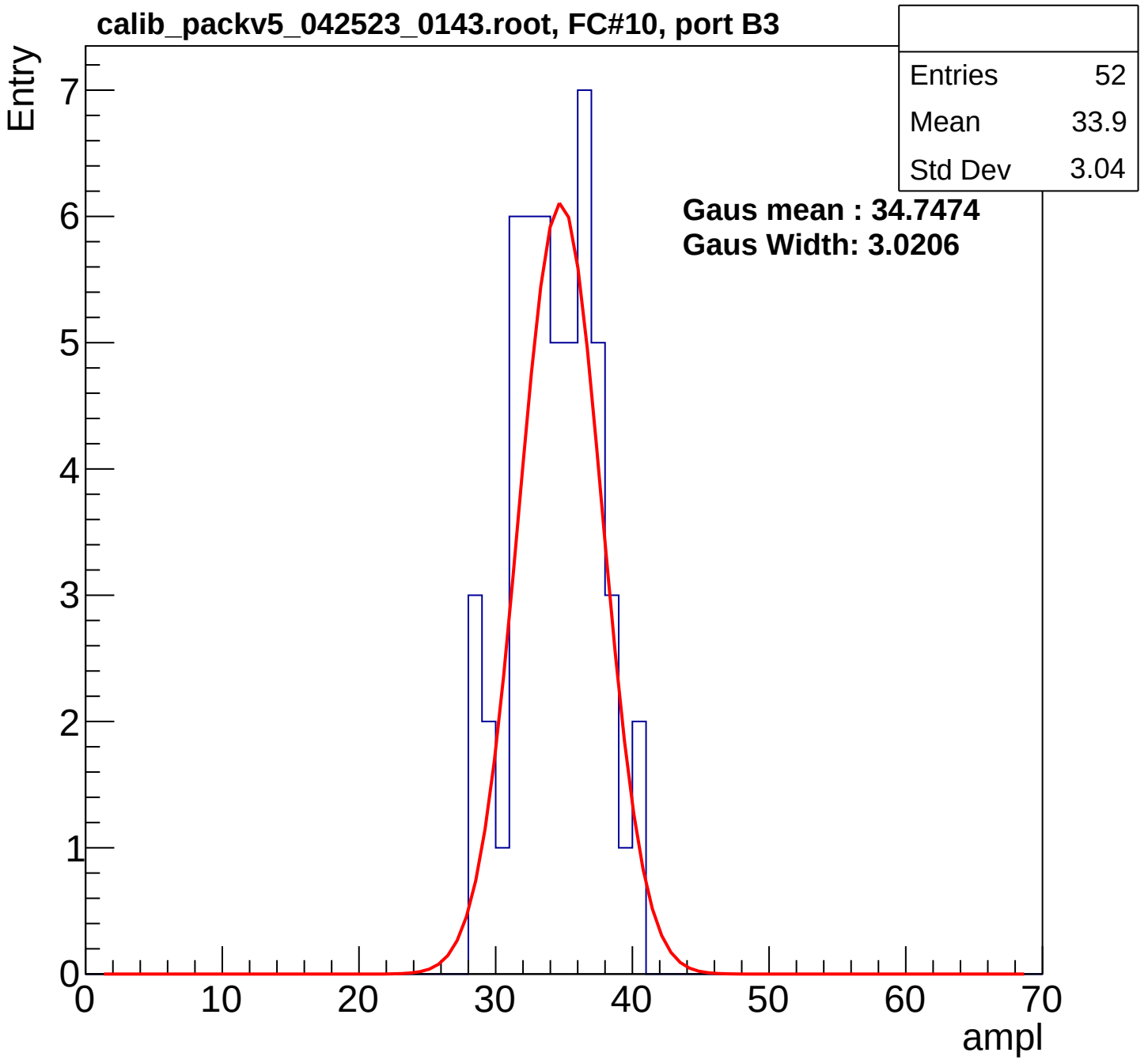

B1L002S, U6-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.96
Std Dev 2.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

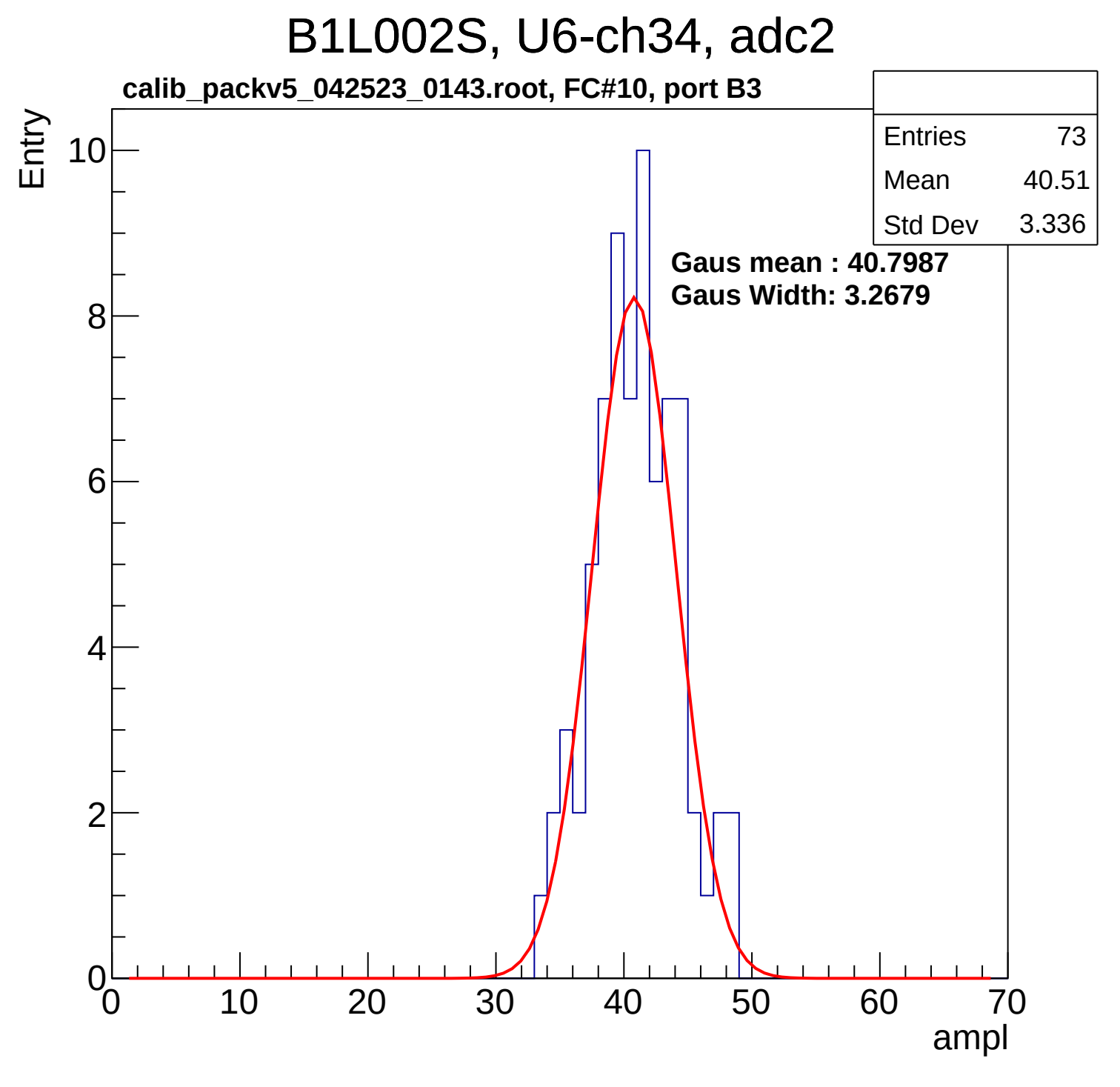

B1L002S, U6-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 59.38
Std Dev 2.704
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

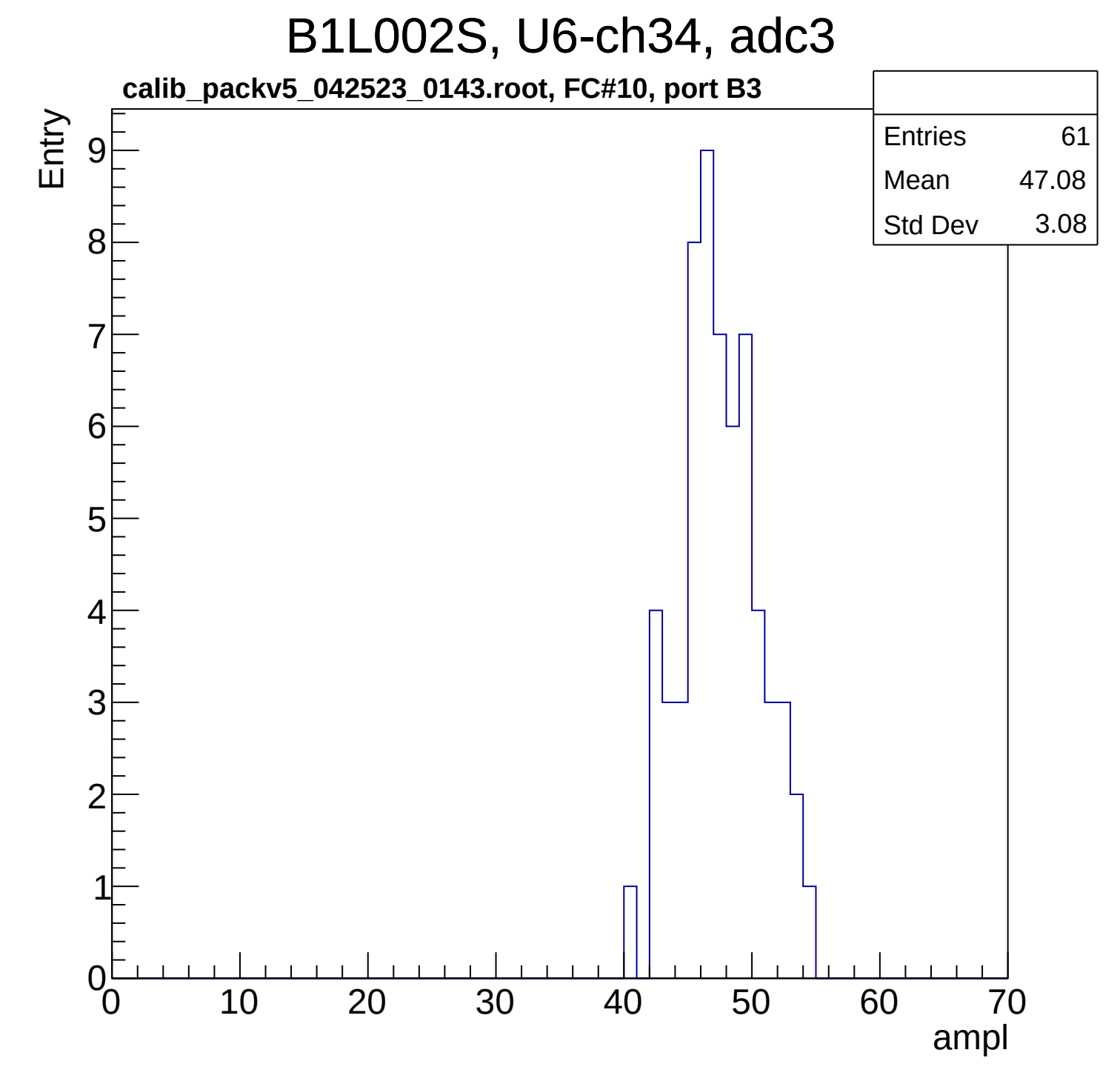

B1L002S, U6-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.5
Std Dev 17.08
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

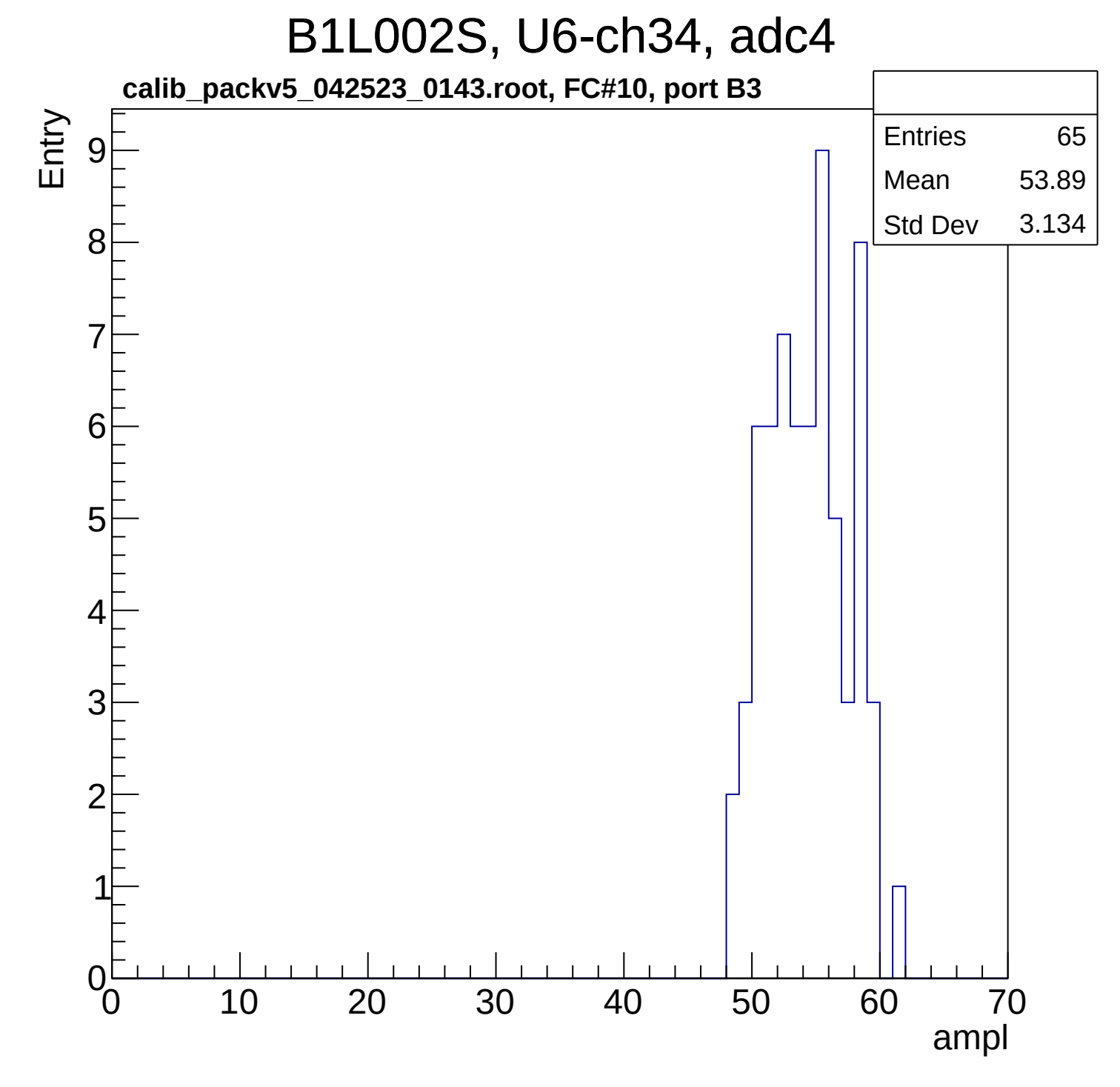

B1L002S, U6-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

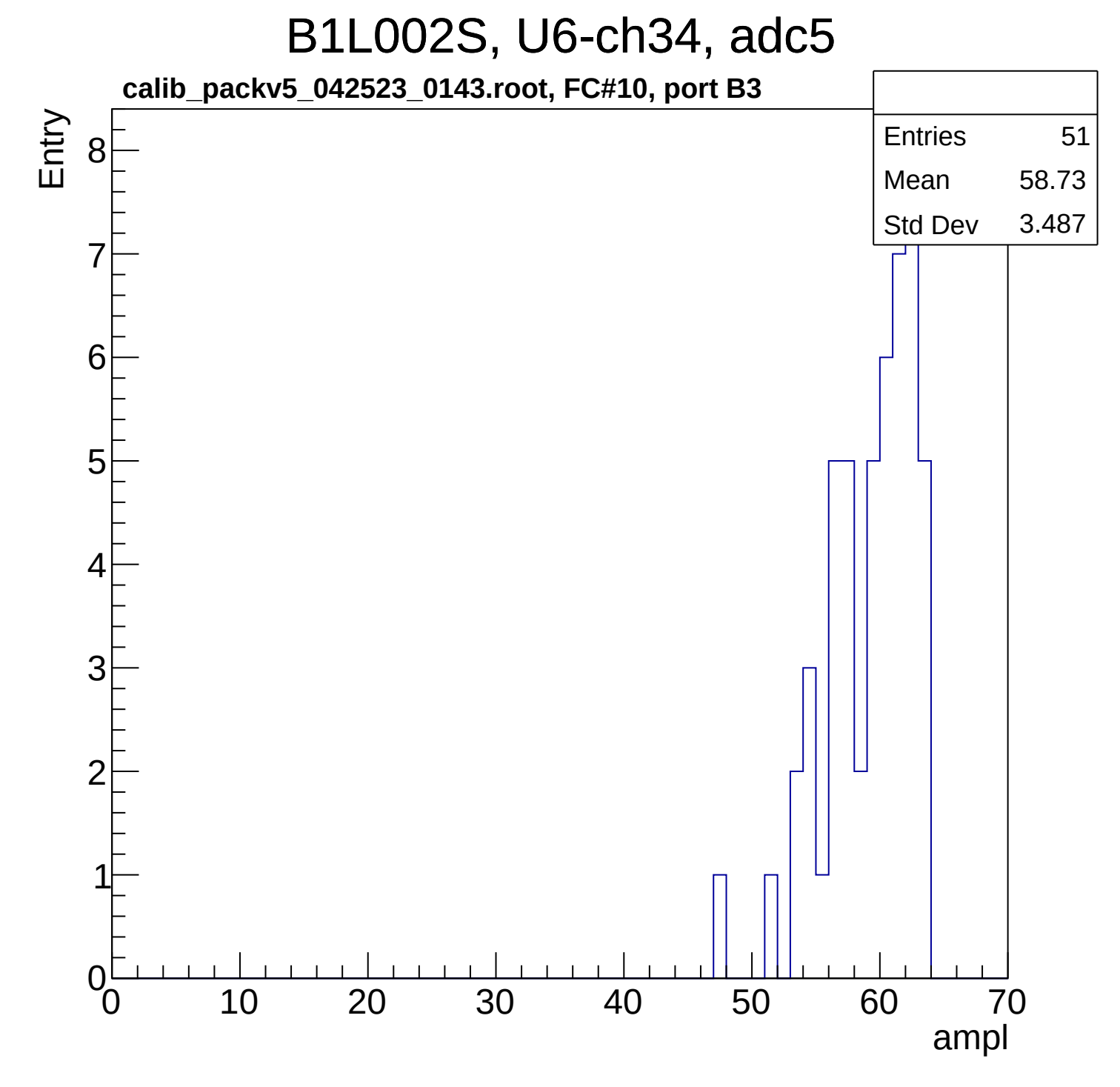

B1L002S, U6-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 29.46
Std Dev 3.222
Gaus mean : 30.4181
Gaus Width: 3.5651
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

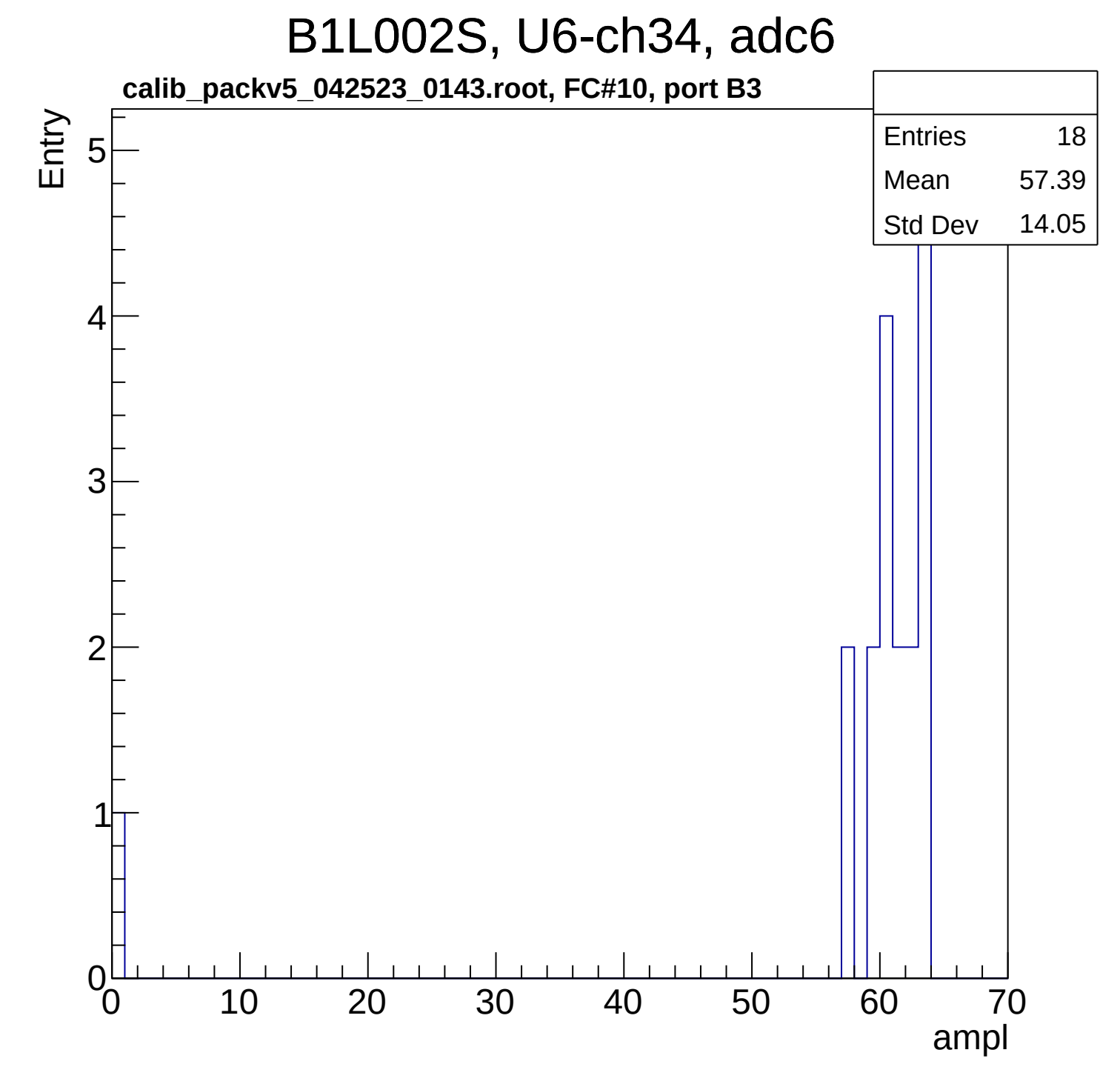

B1L002S, U6-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 33.9
Std Dev 3.04
Gaus mean : 34.7474
Gaus Width: 3.0206
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

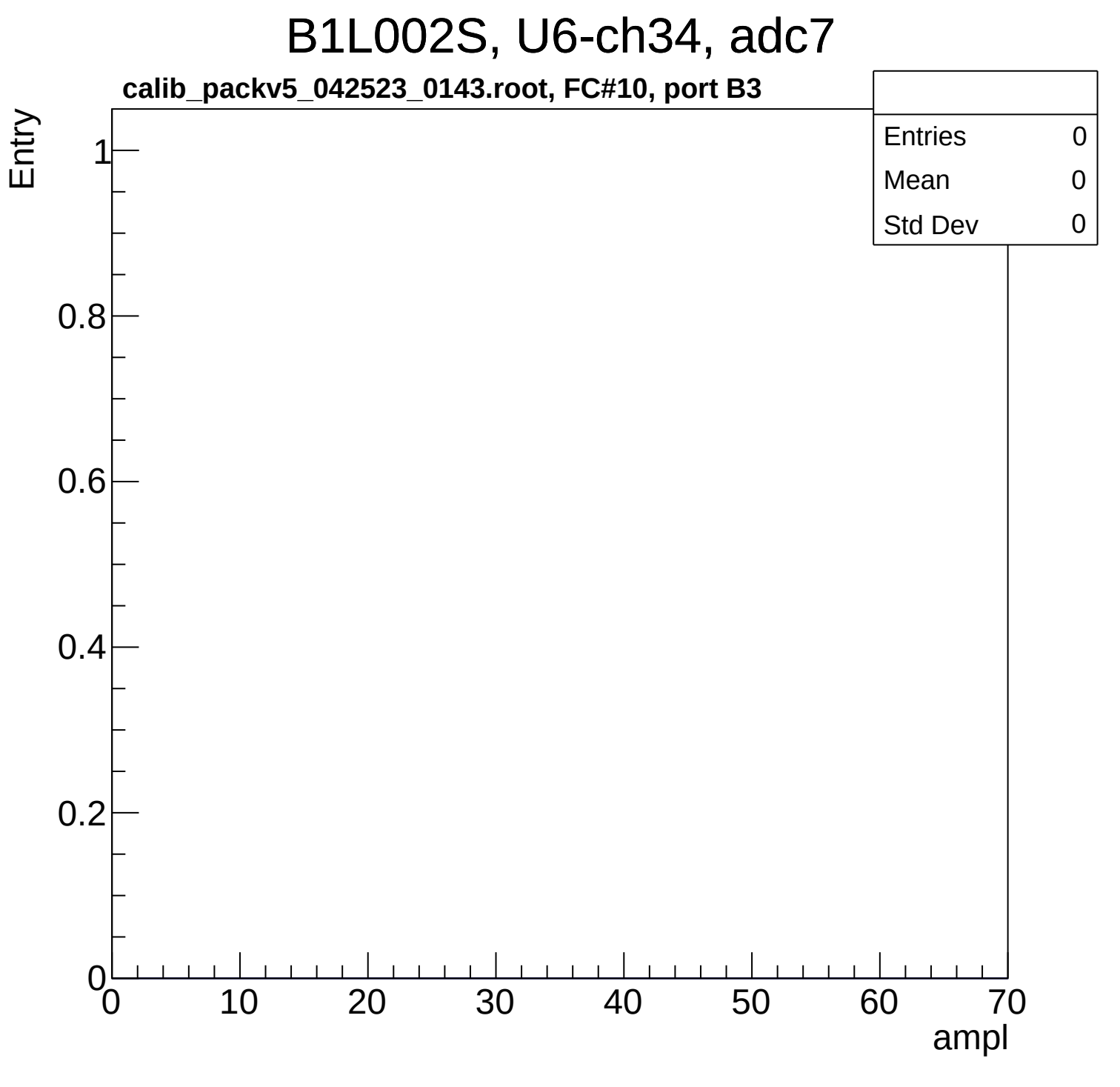

B1L002S, U6-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.51
Std Dev 3.336
Gaus mean : 40.7987
Gaus Width: 3.2679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

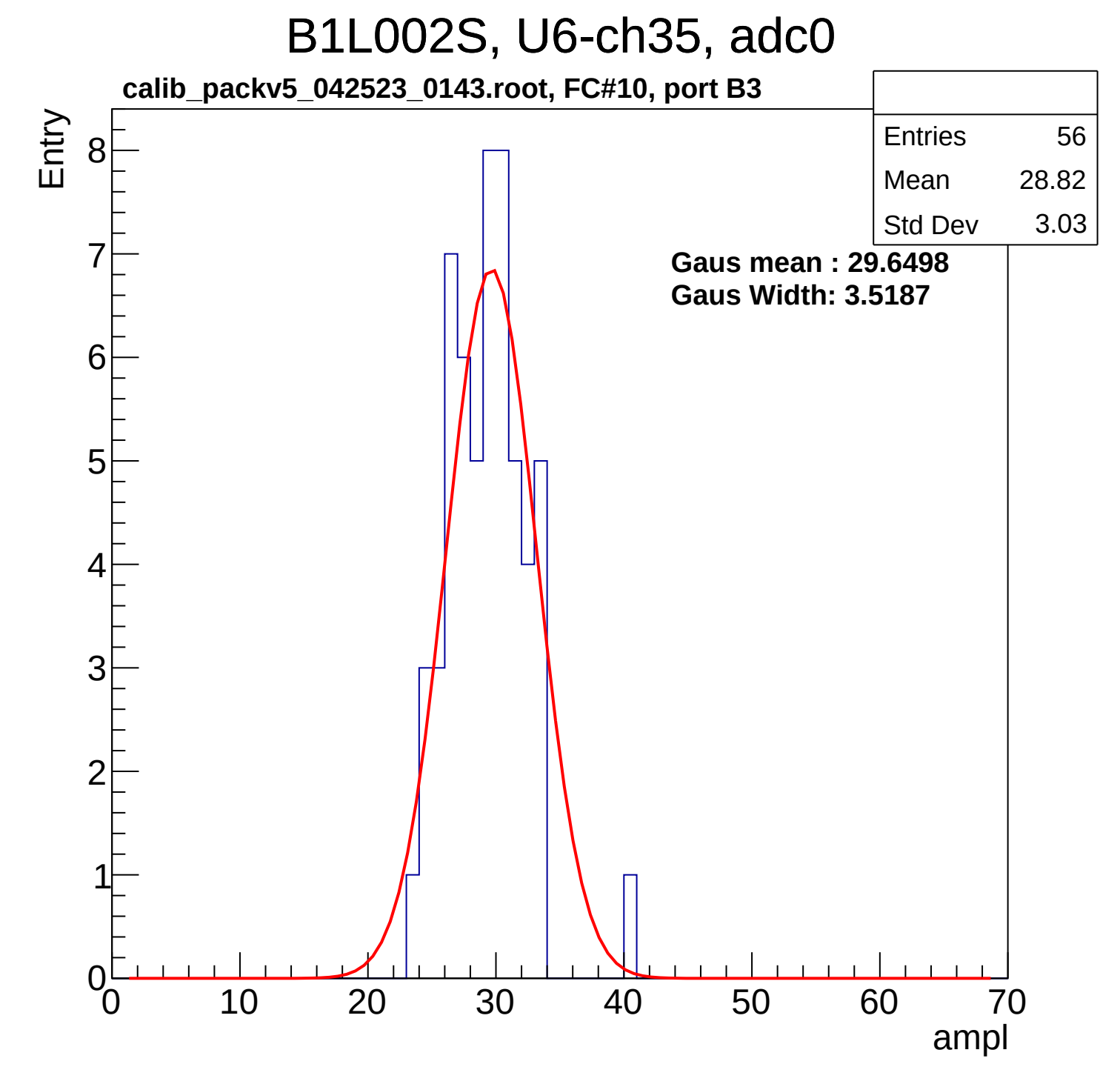

B1L002S, U6-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.08
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

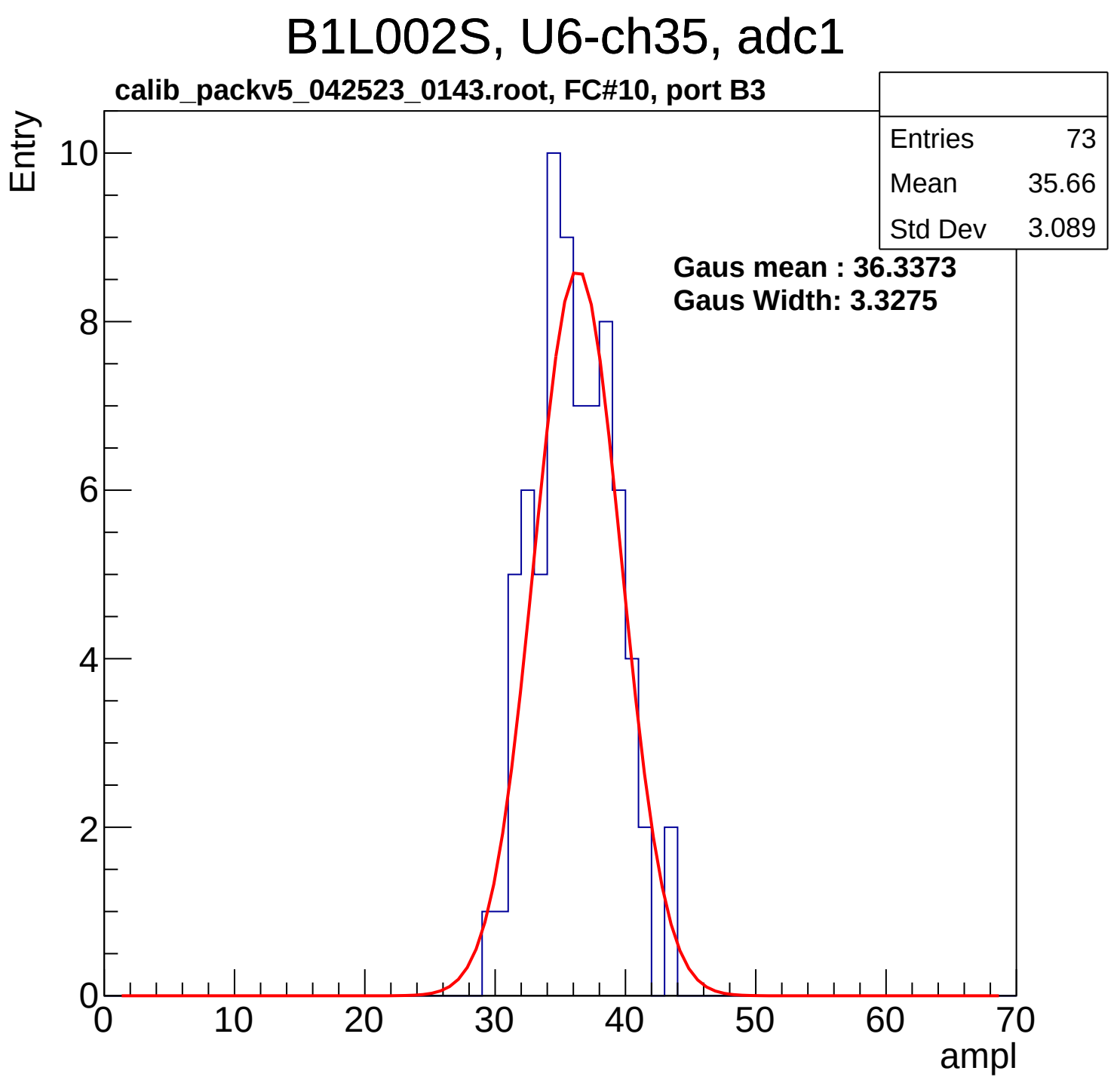

B1L002S, U6-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.89
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

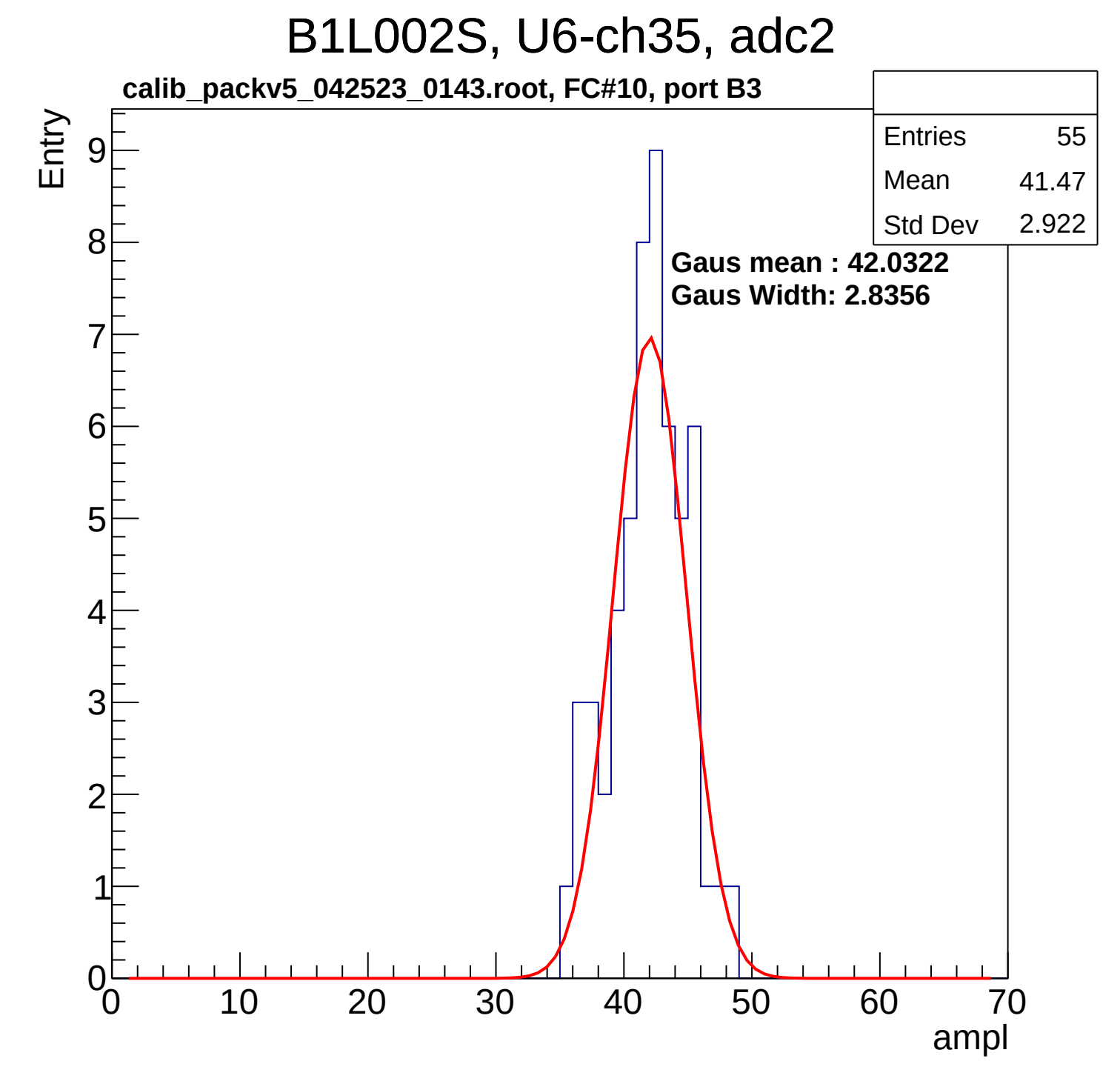

B1L002S, U6-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.73
Std Dev 3.487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

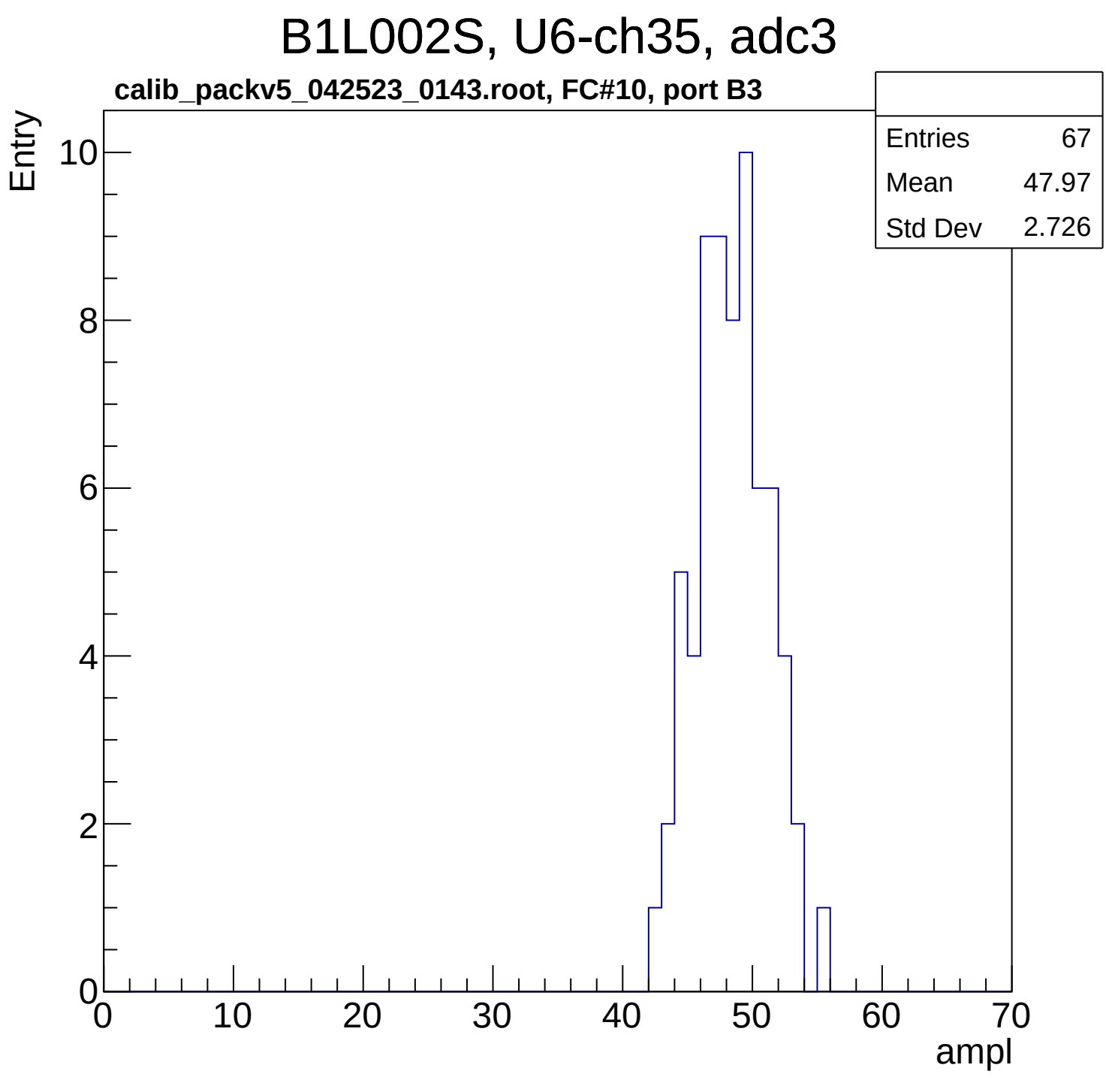

B1L002S, U6-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.39
Std Dev 14.05
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

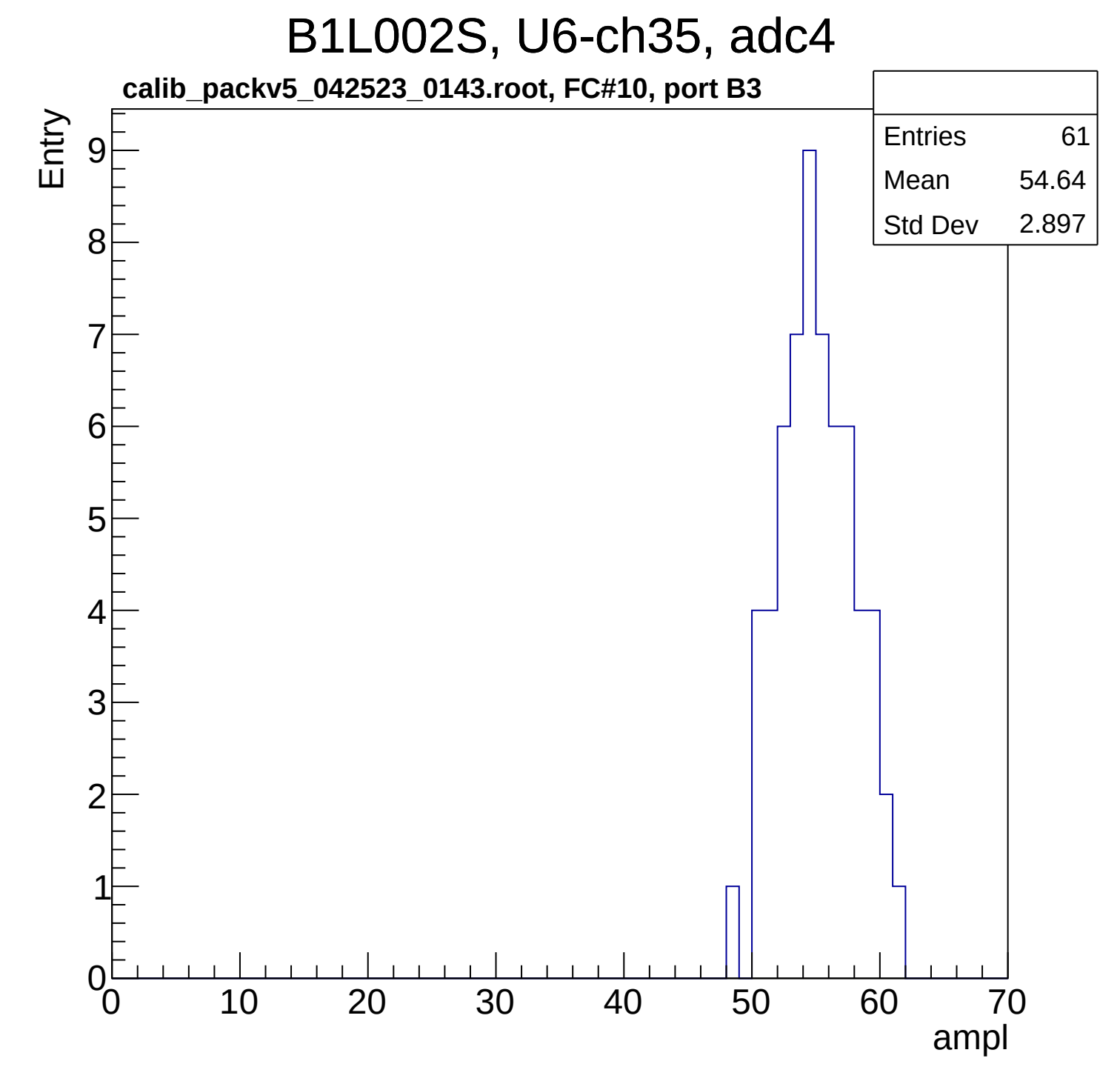

B1L002S, U6-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

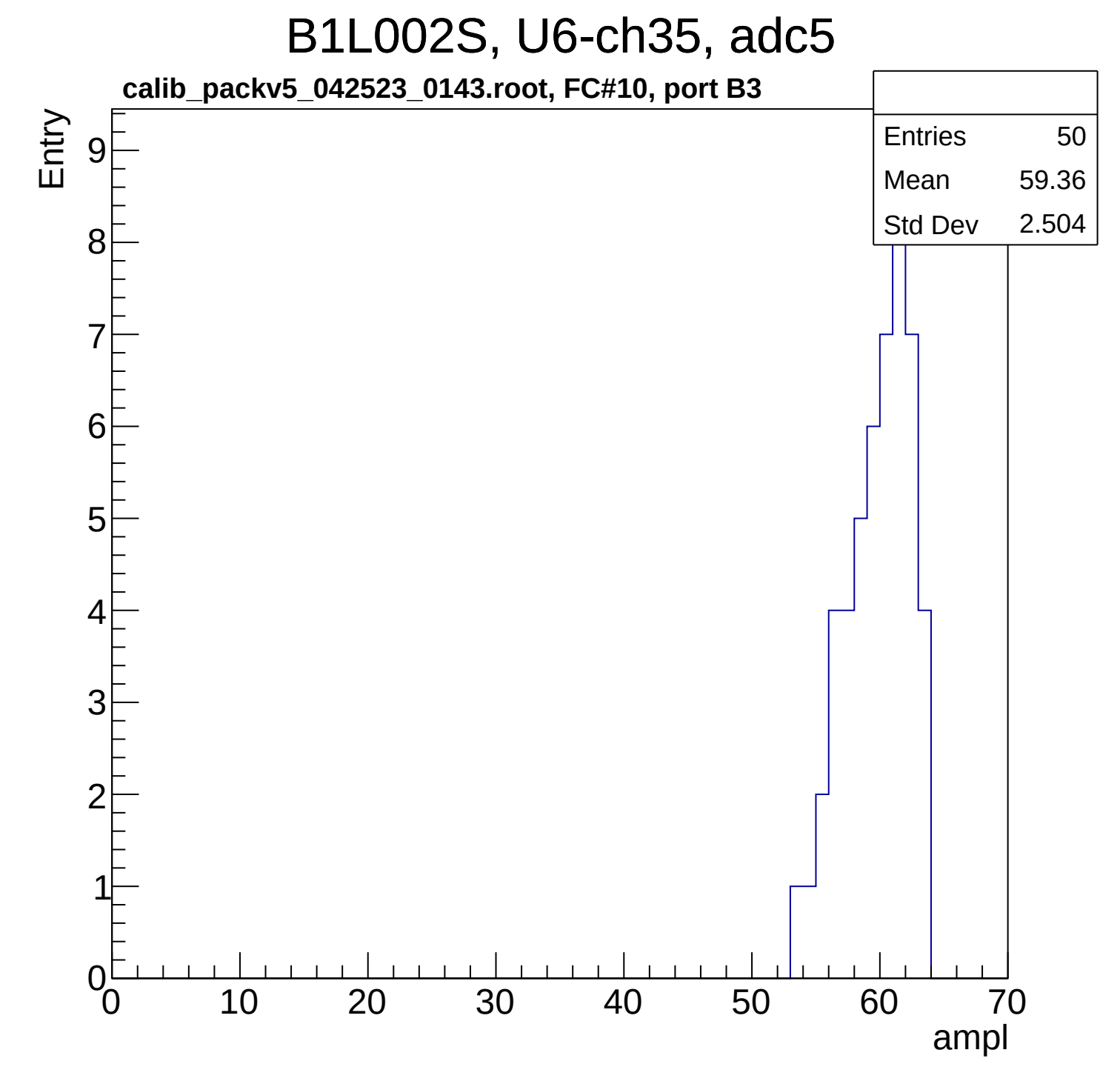

B1L002S, U6-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.82
Std Dev 3.03
Gaus mean : 29.6498
Gaus Width: 3.5187
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

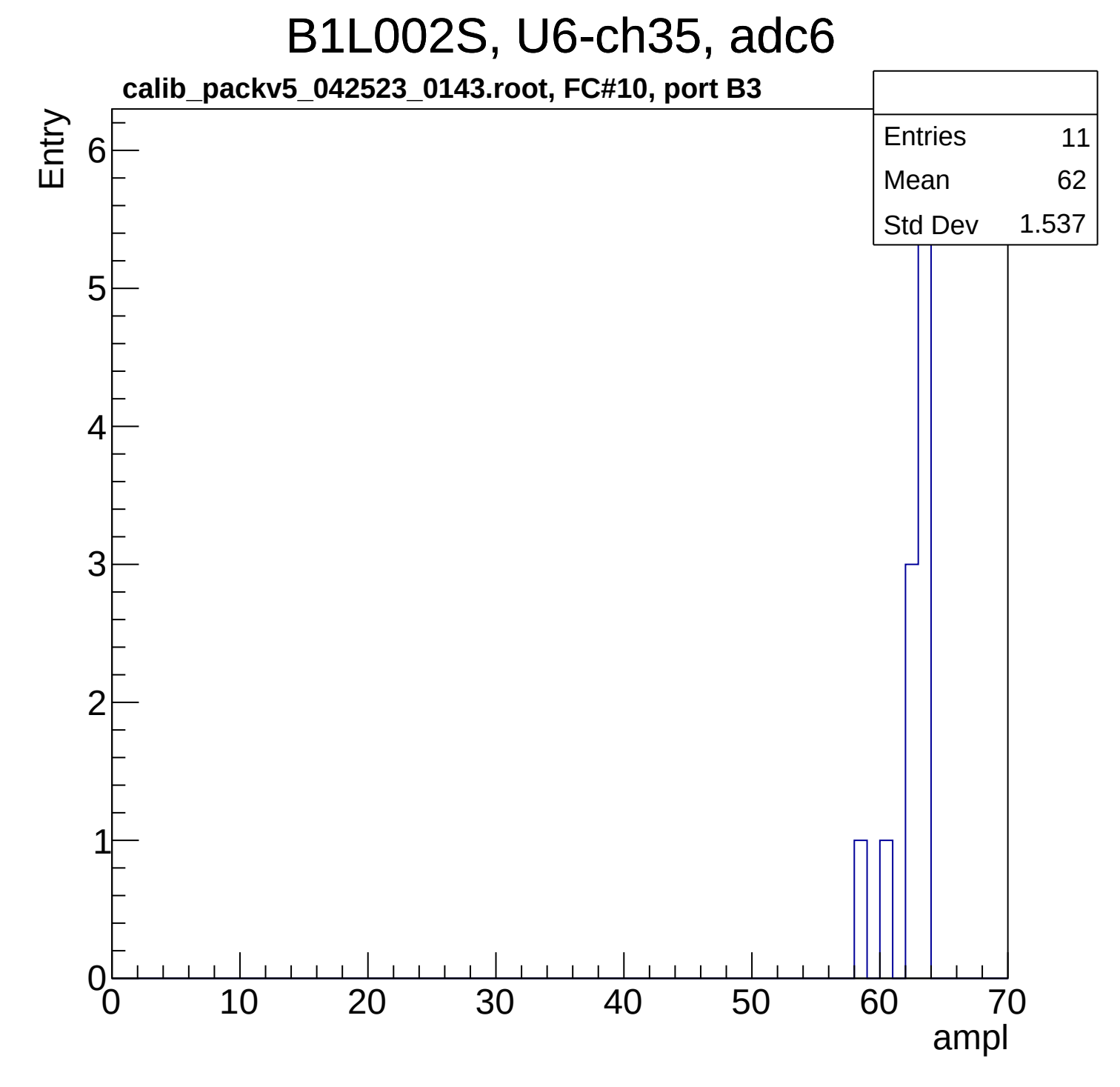

B1L002S, U6-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.66
Std Dev 3.089
Gaus mean : 36.3373
Gaus Width: 3.3275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

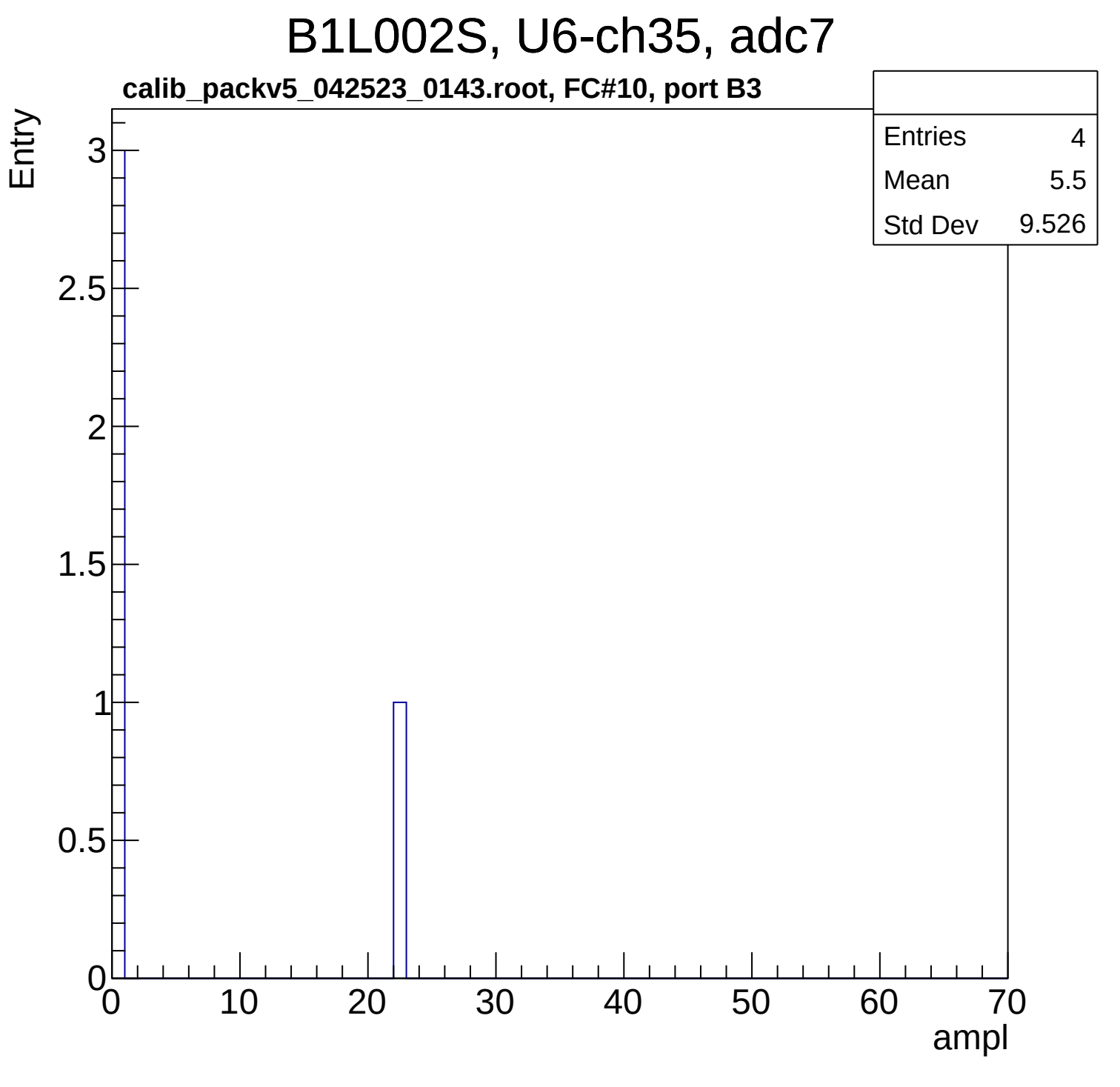

B1L002S, U6-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.47
Std Dev 2.922
Gaus mean : 42.0322
Gaus Width: 2.8356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

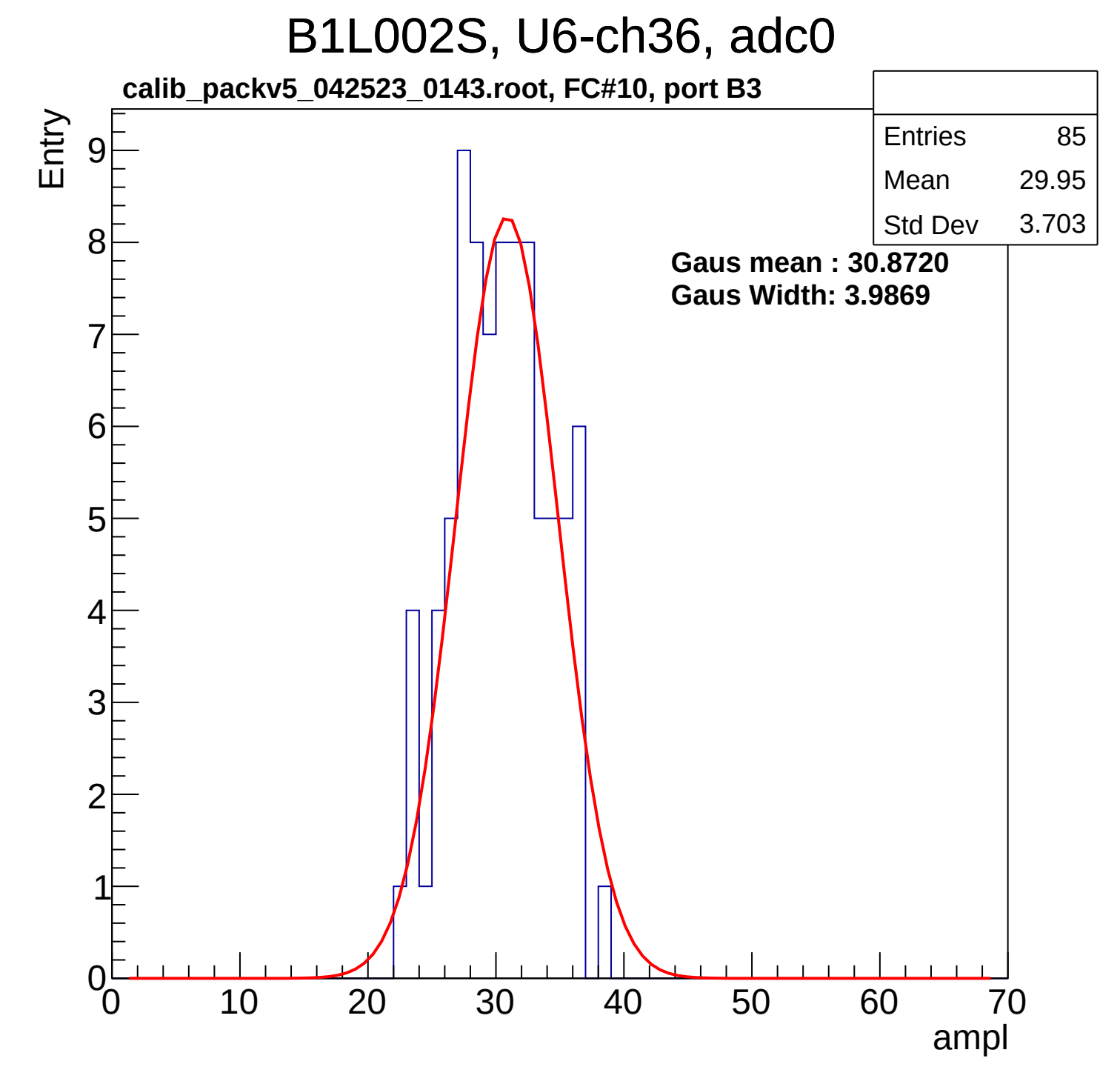

B1L002S, U6-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.97
Std Dev 2.726
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

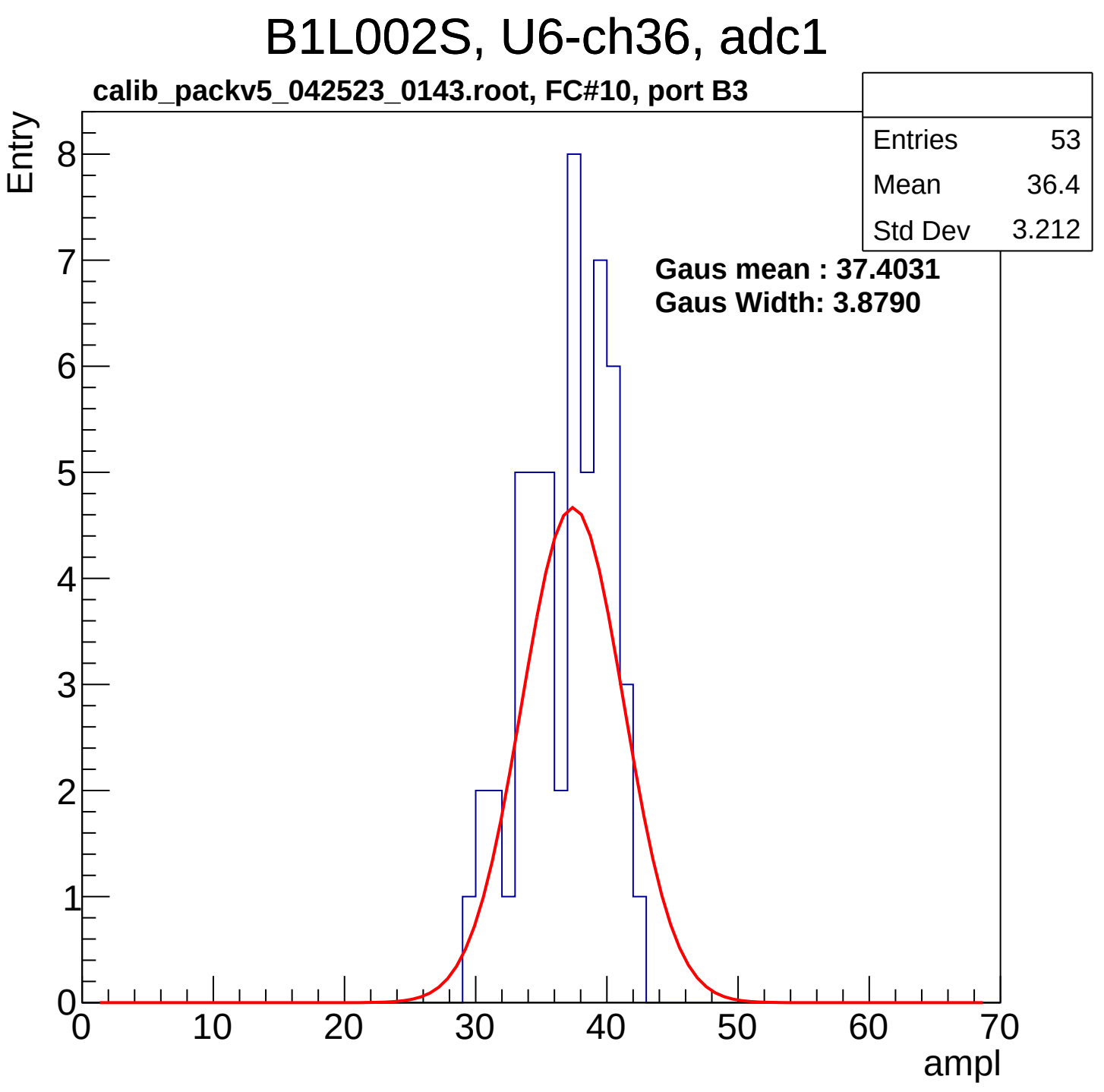

B1L002S, U6-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.64
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

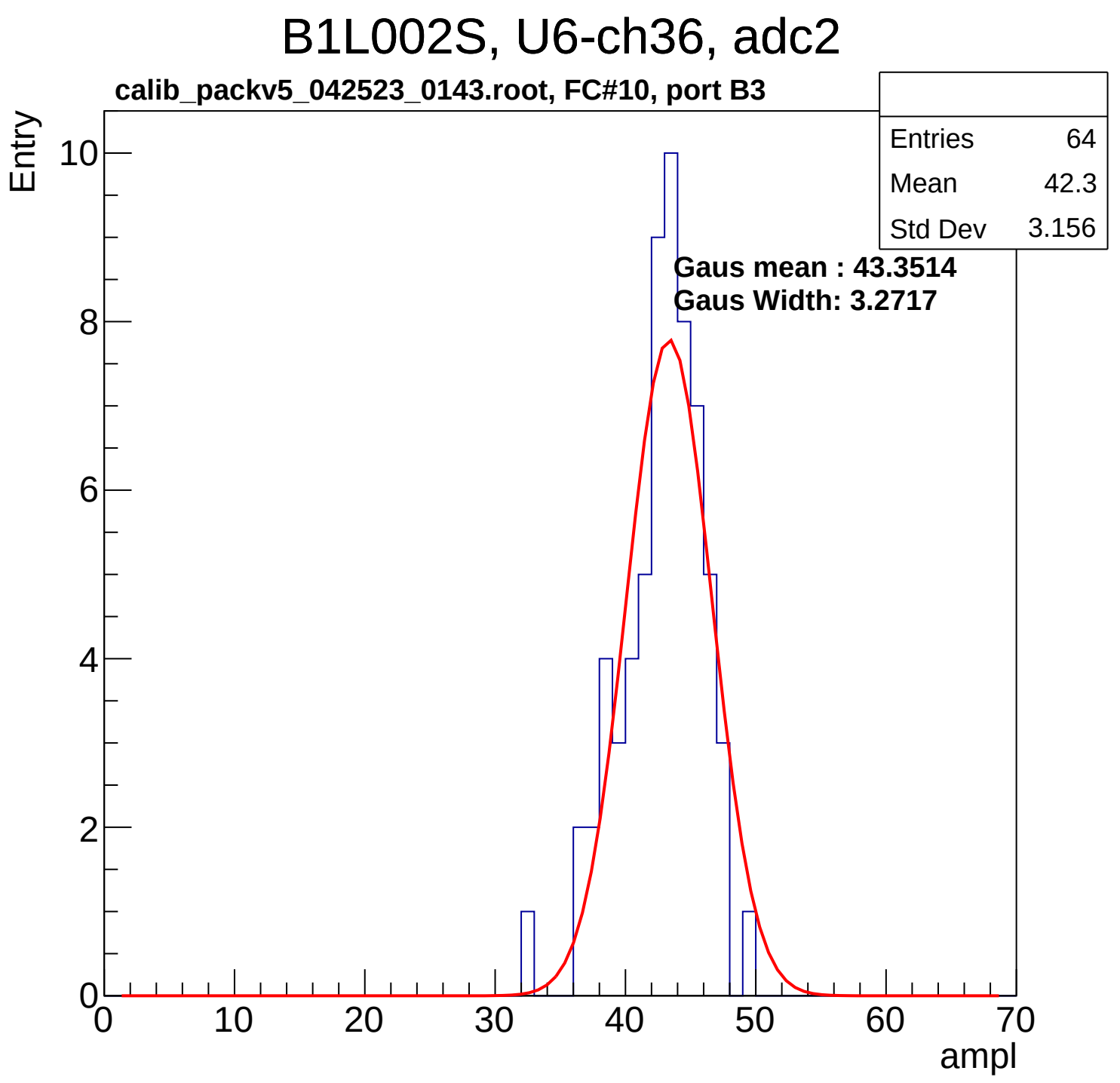

B1L002S, U6-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.36
Std Dev 2.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

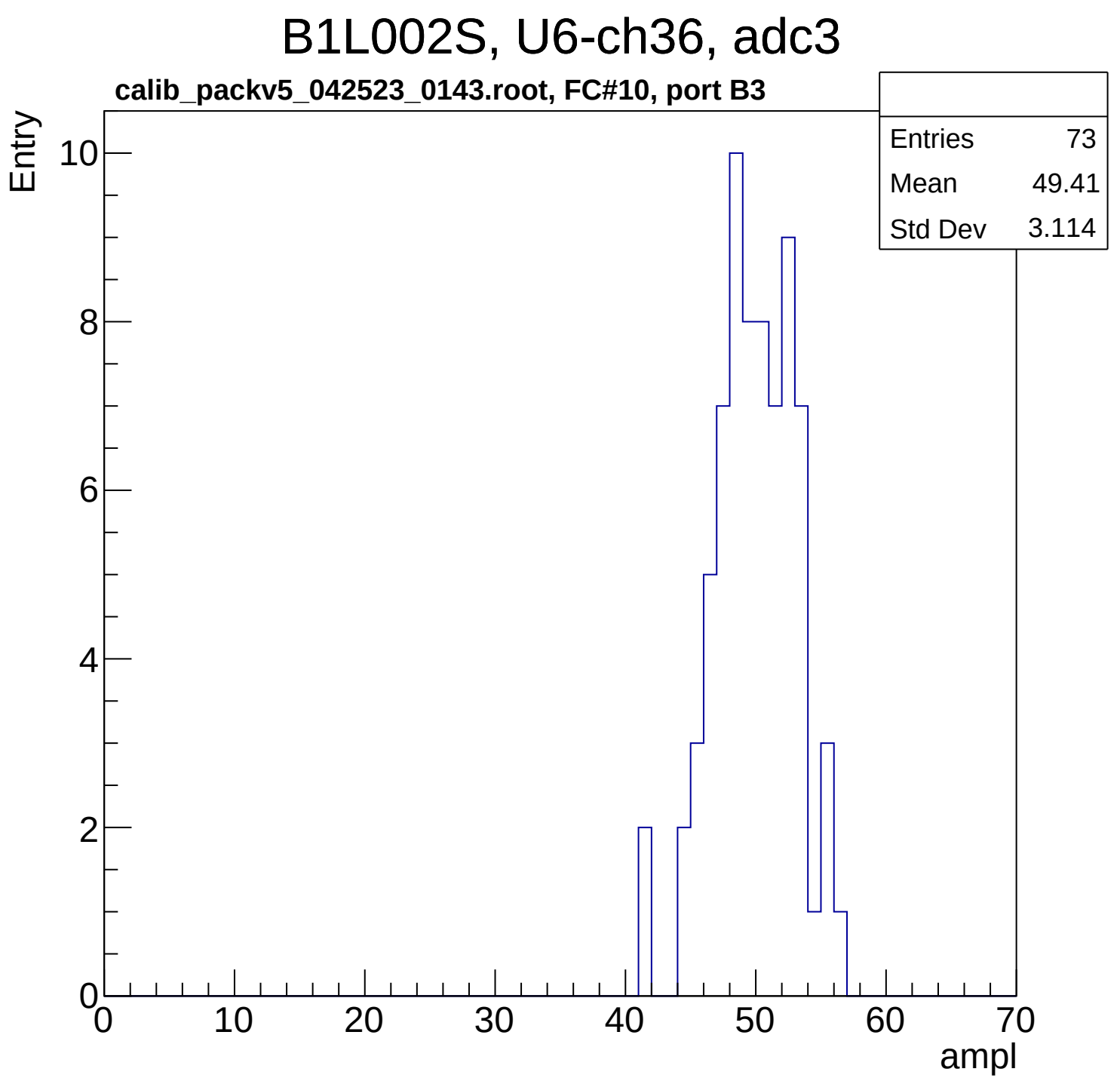

B1L002S, U6-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62
Std Dev 1.537
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

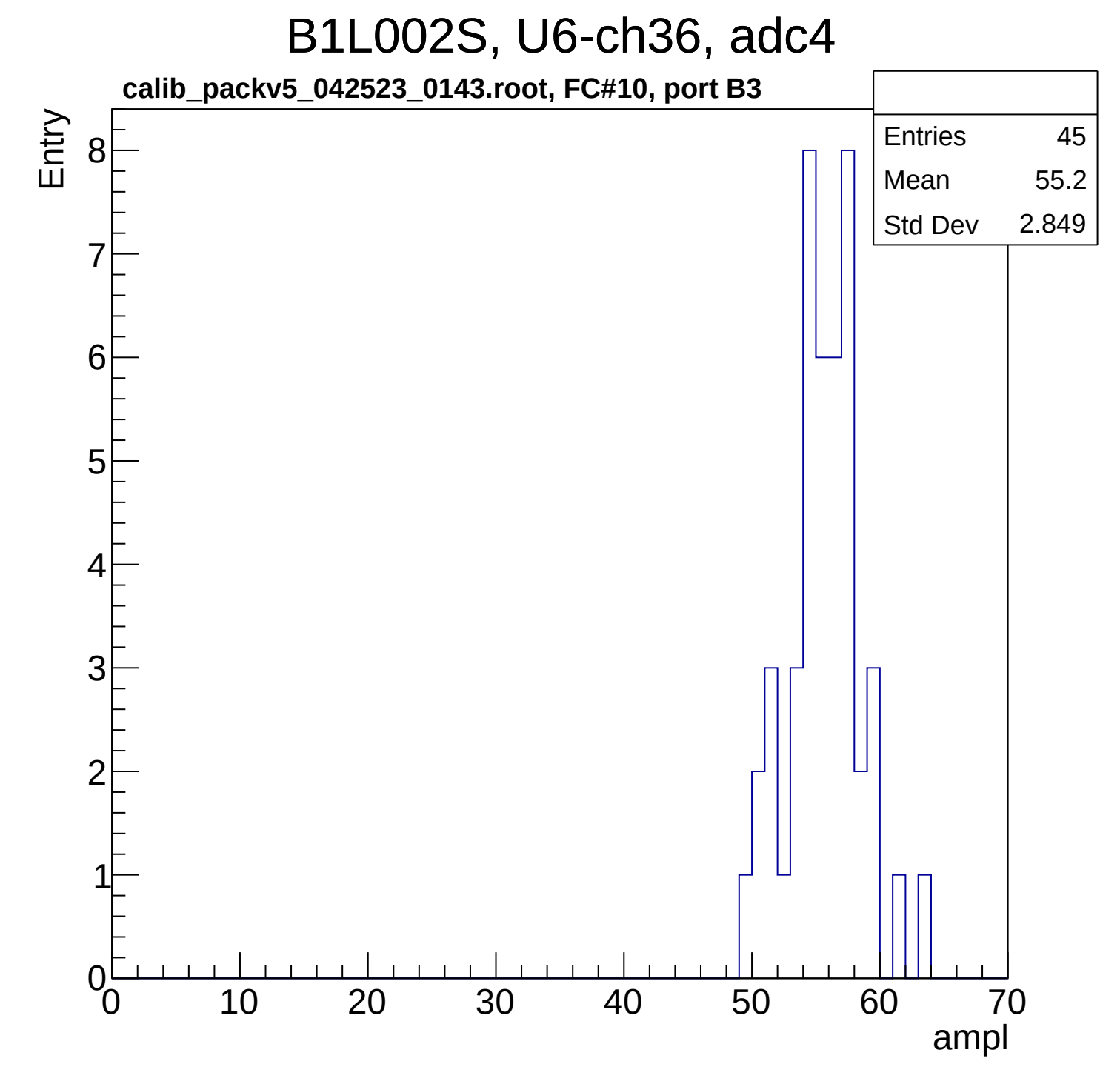

B1L002S, U6-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 5.5
Std Dev 9.526
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

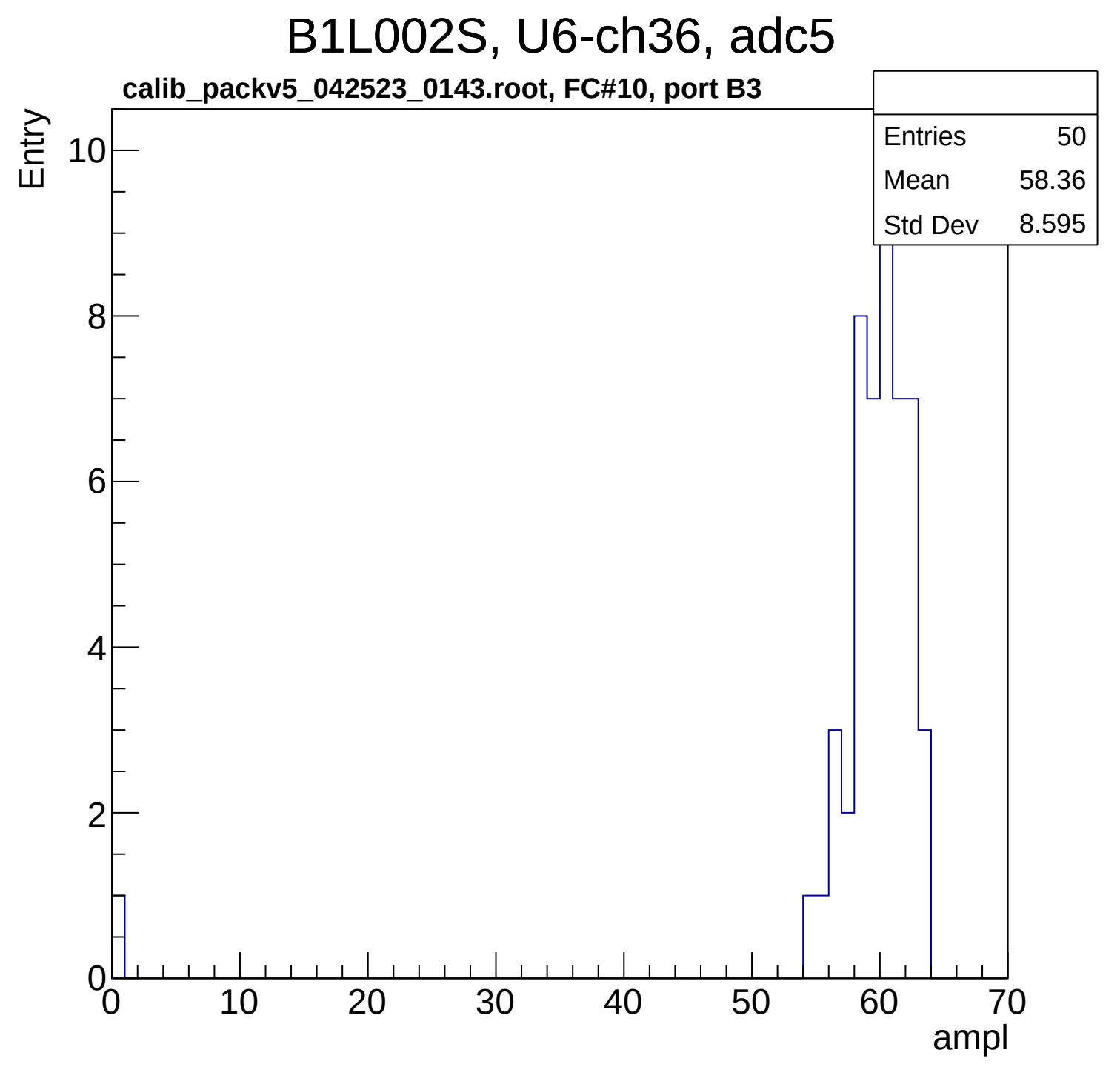

B1L002S, U6-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 29.95
Std Dev 3.703
Gaus mean : 30.8720
Gaus Width: 3.9869
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

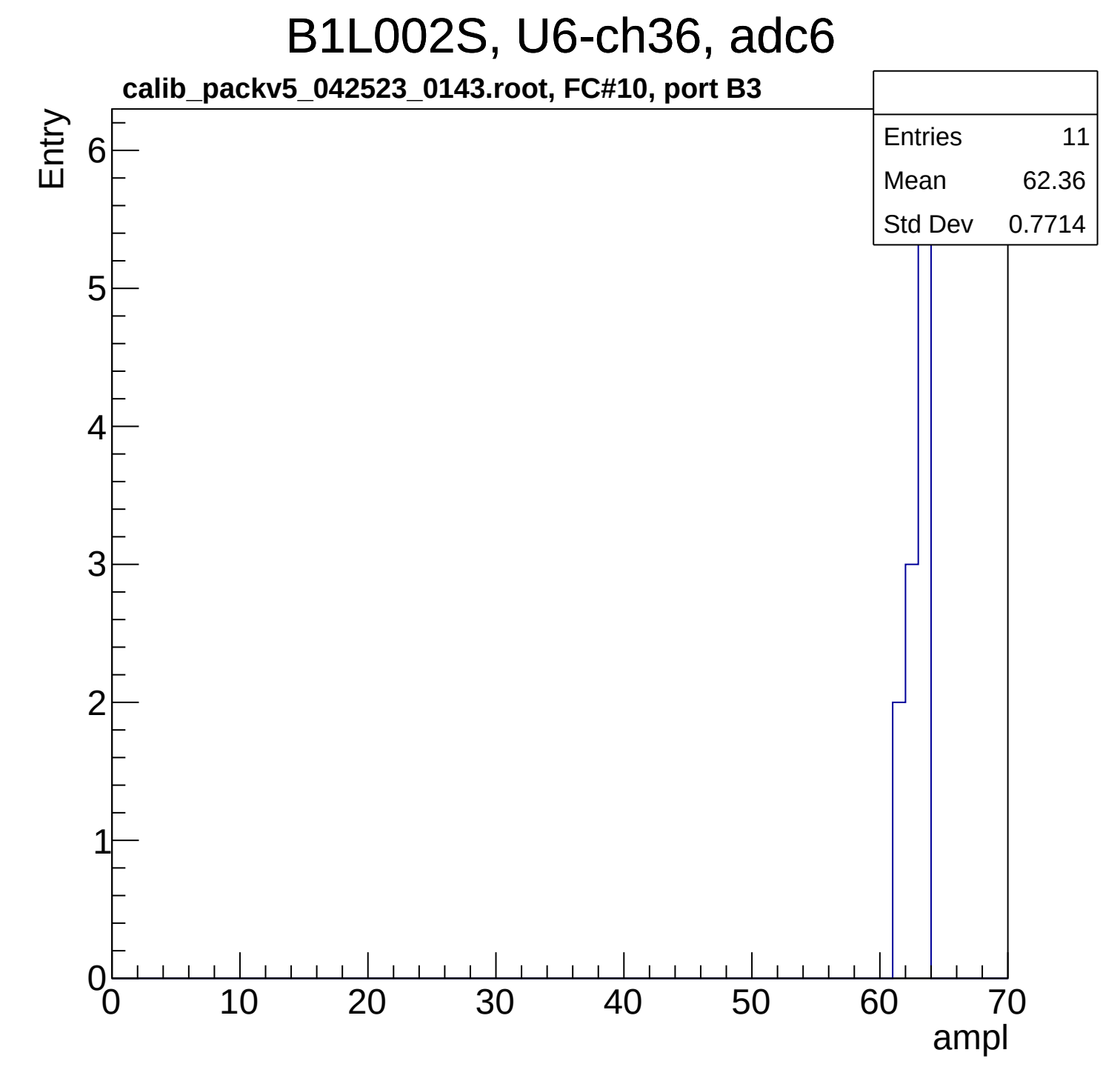

B1L002S, U6-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 36.4
Std Dev 3.212
Gaus mean : 37.4031
Gaus Width: 3.8790
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

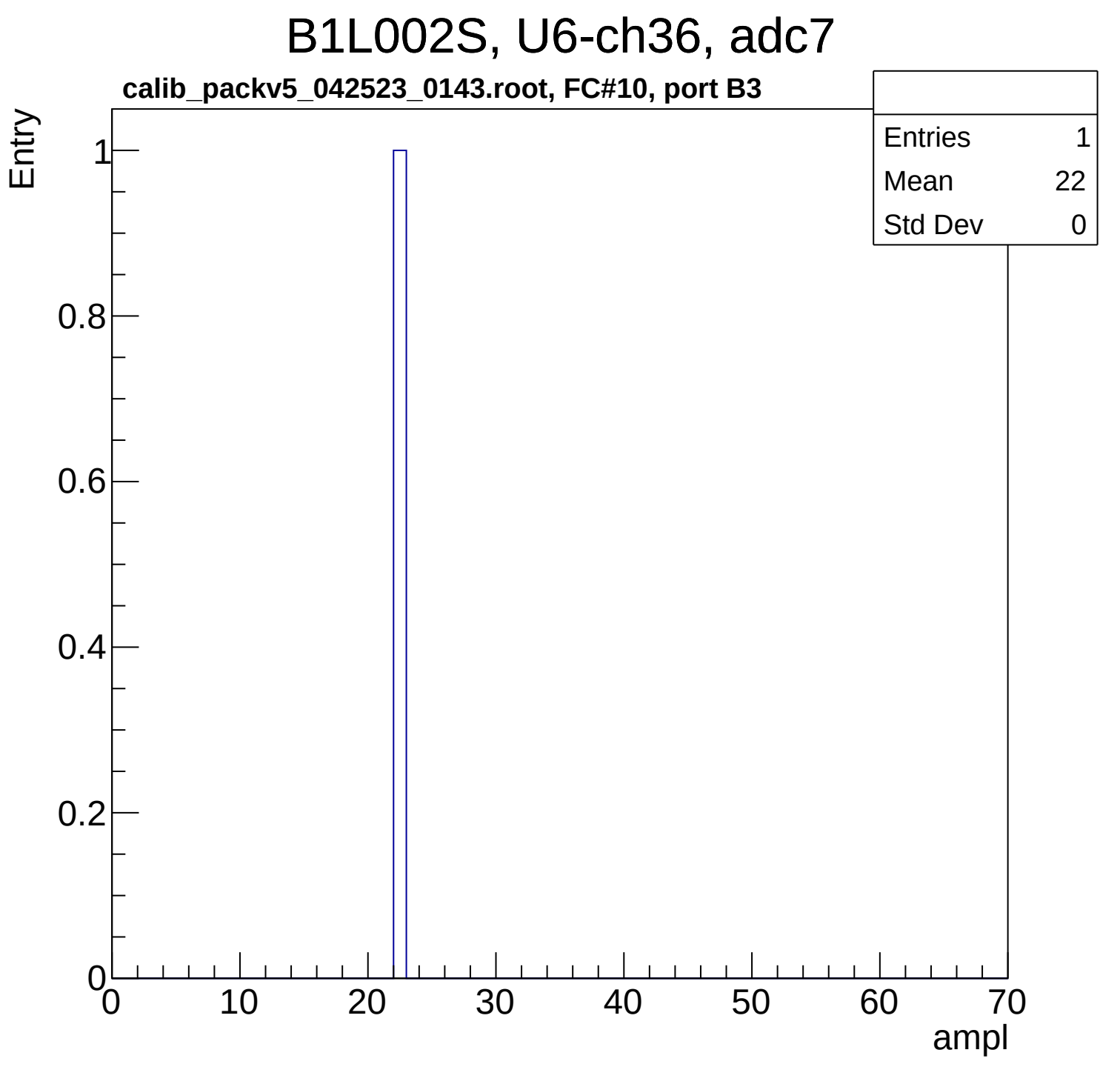

B1L002S, U6-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.3
Std Dev 3.156
Gaus mean : 43.3514
Gaus Width: 3.2717
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

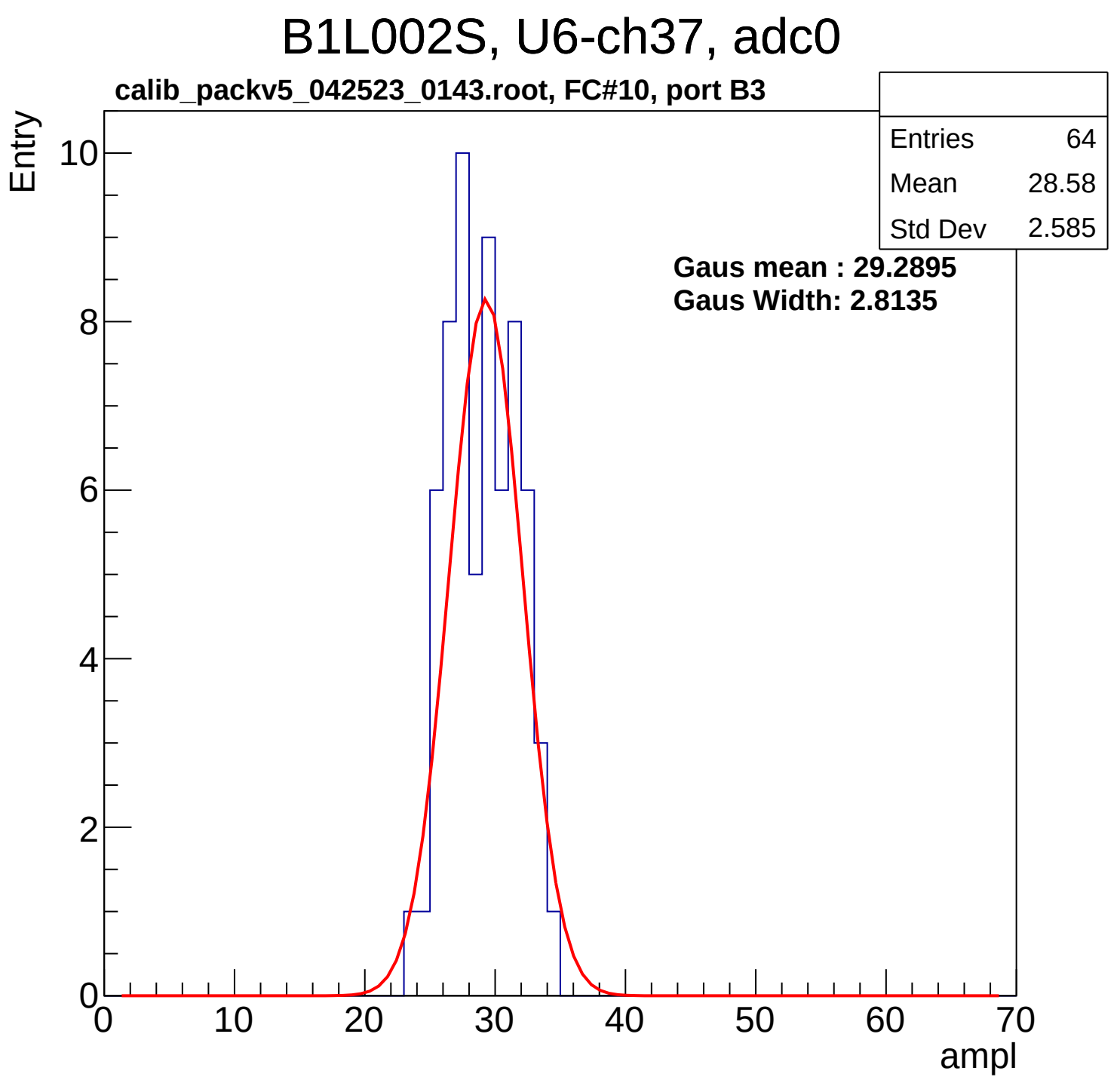

B1L002S, U6-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 49.41
Std Dev 3.114
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

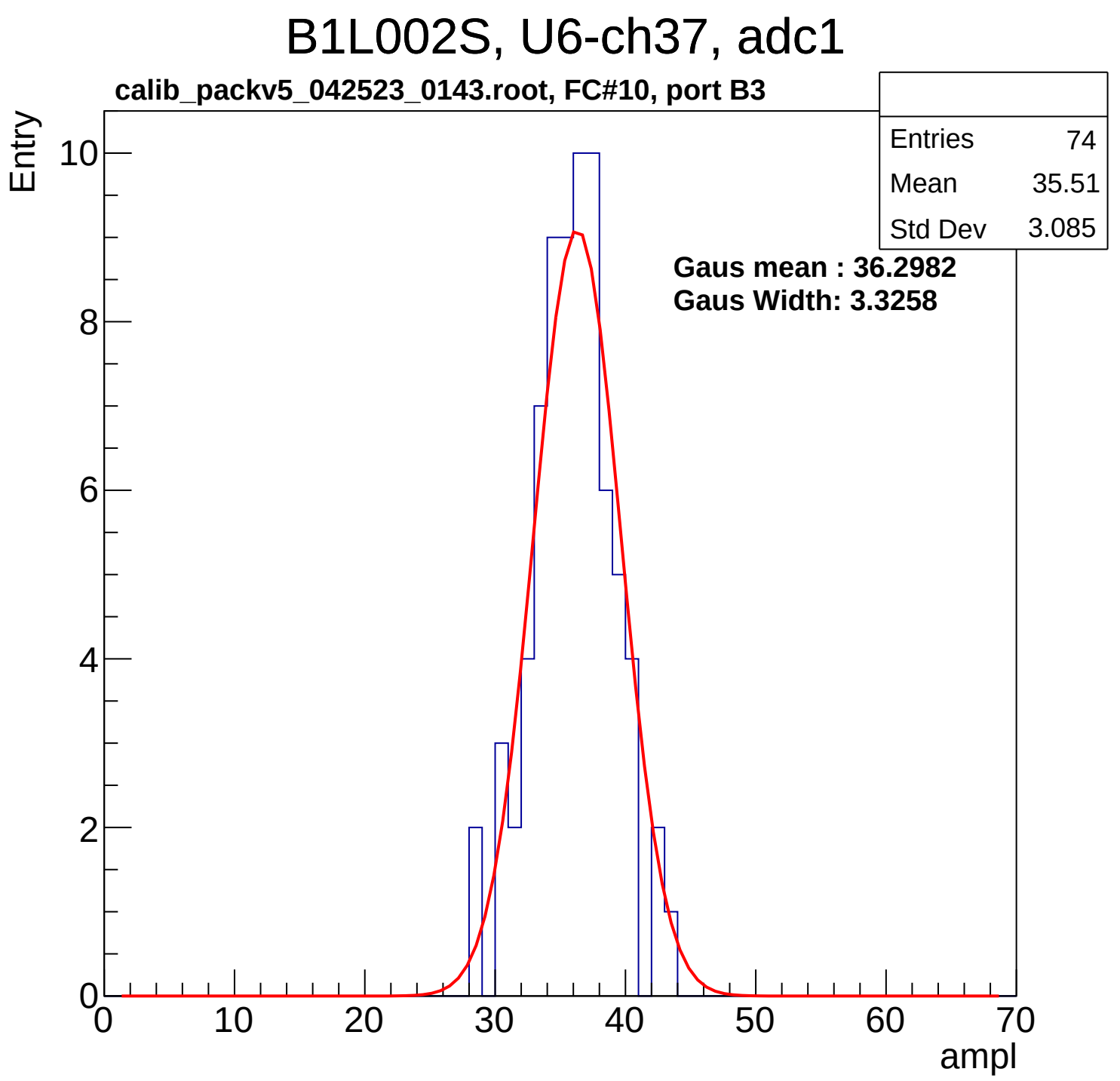

B1L002S, U6-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 55.2
Std Dev 2.849
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

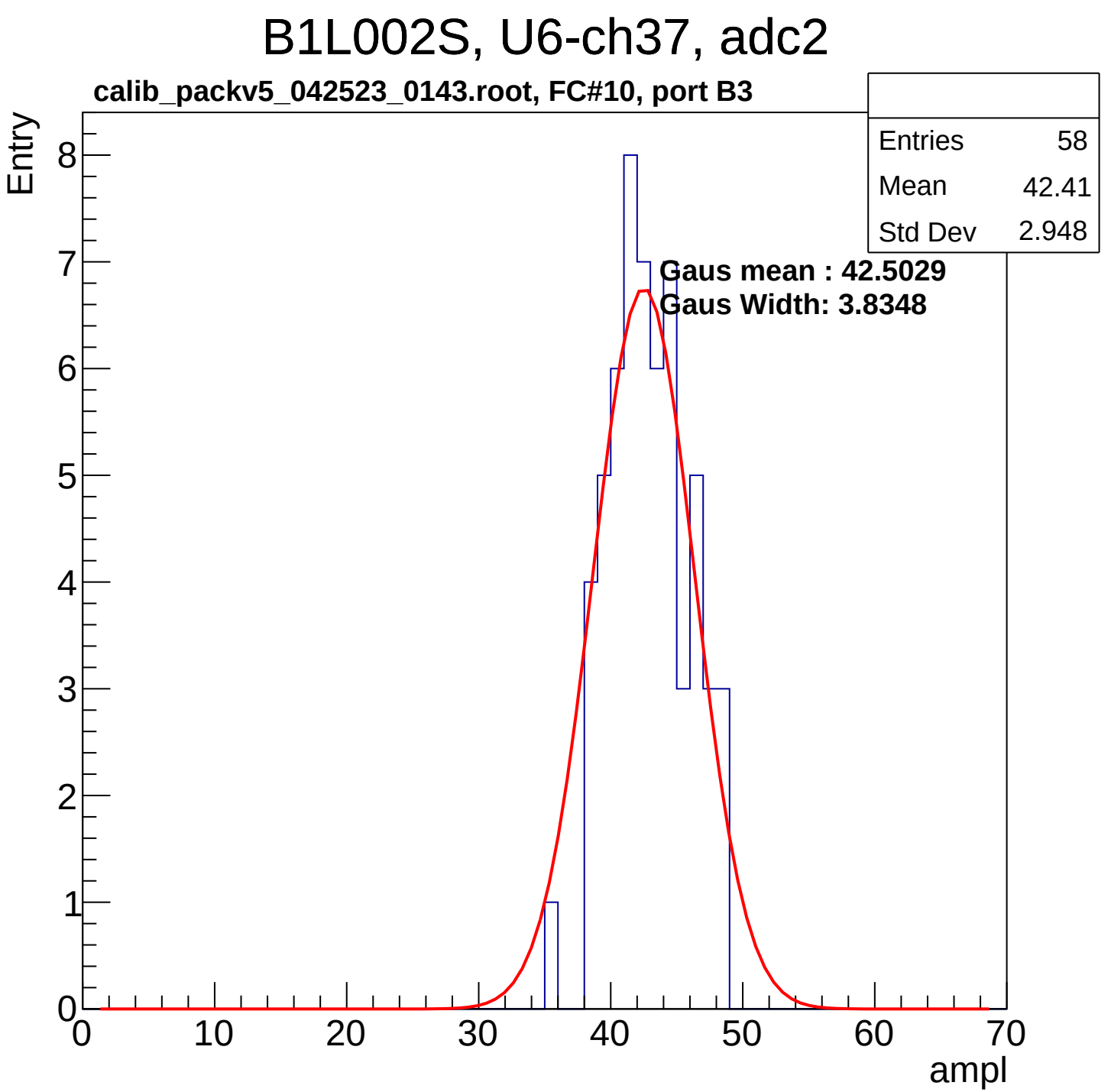

B1L002S, U6-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.36
Std Dev 8.595
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

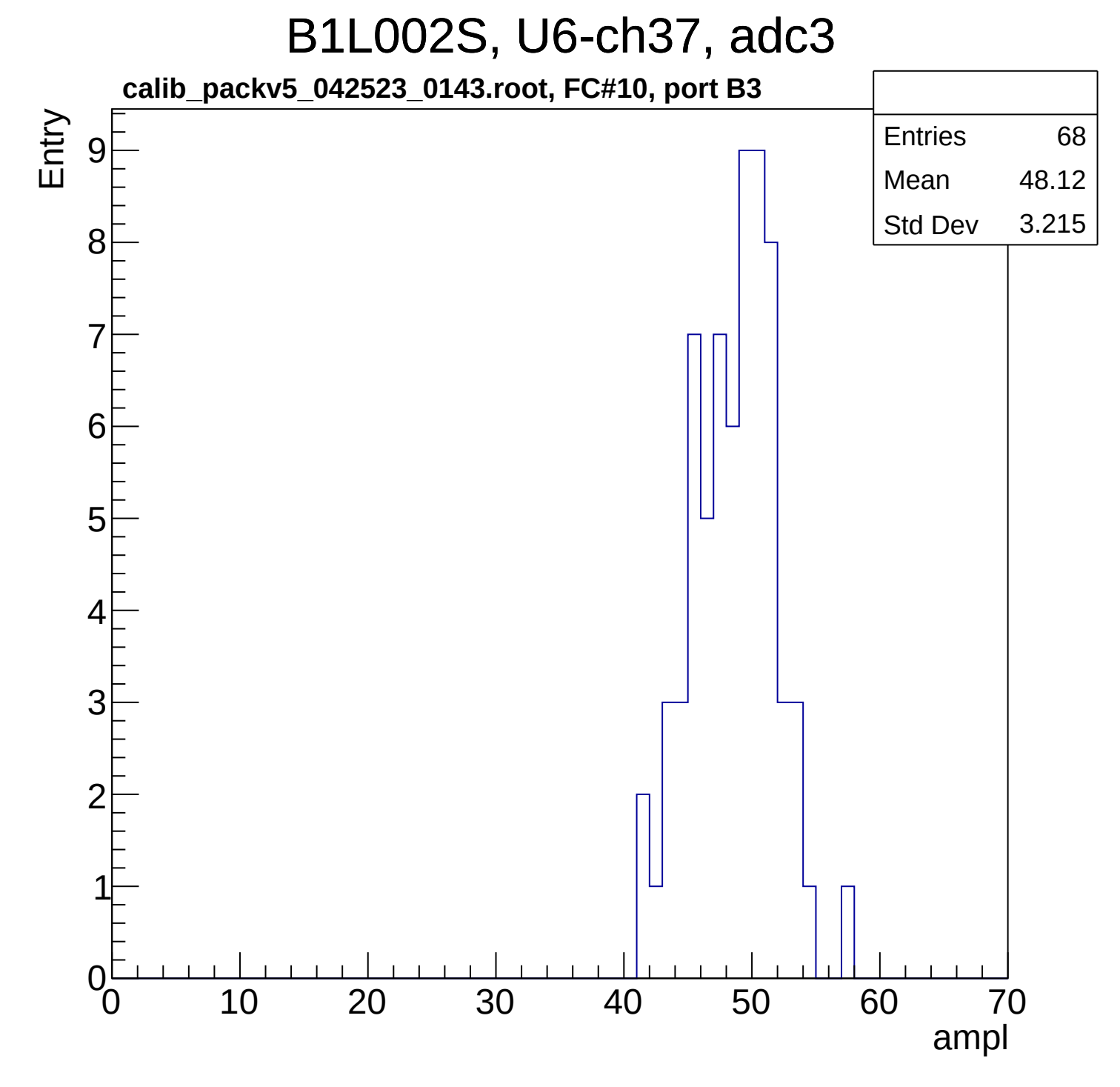

B1L002S, U6-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.36
Std Dev 0.7714
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

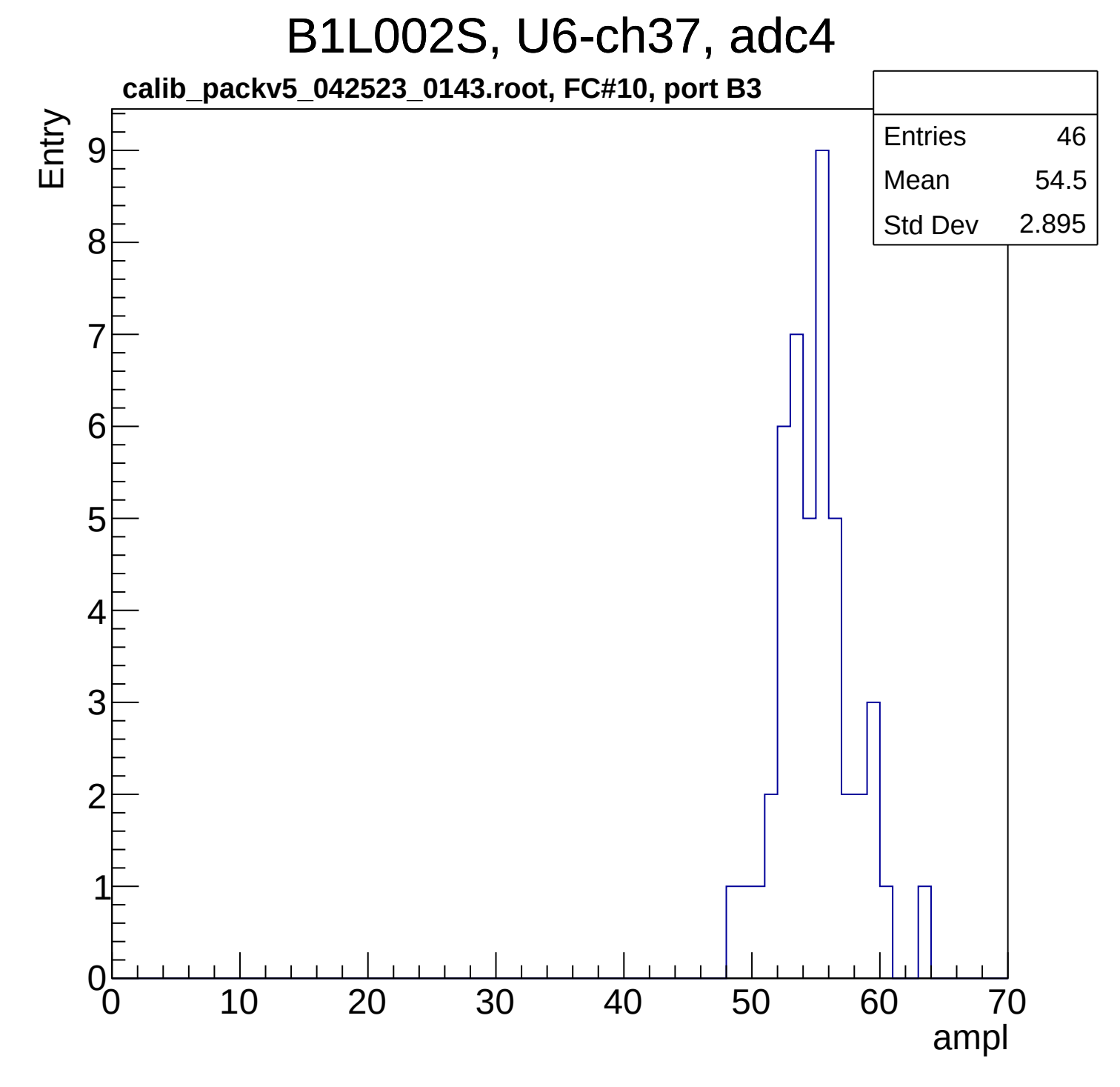

B1L002S, U6-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

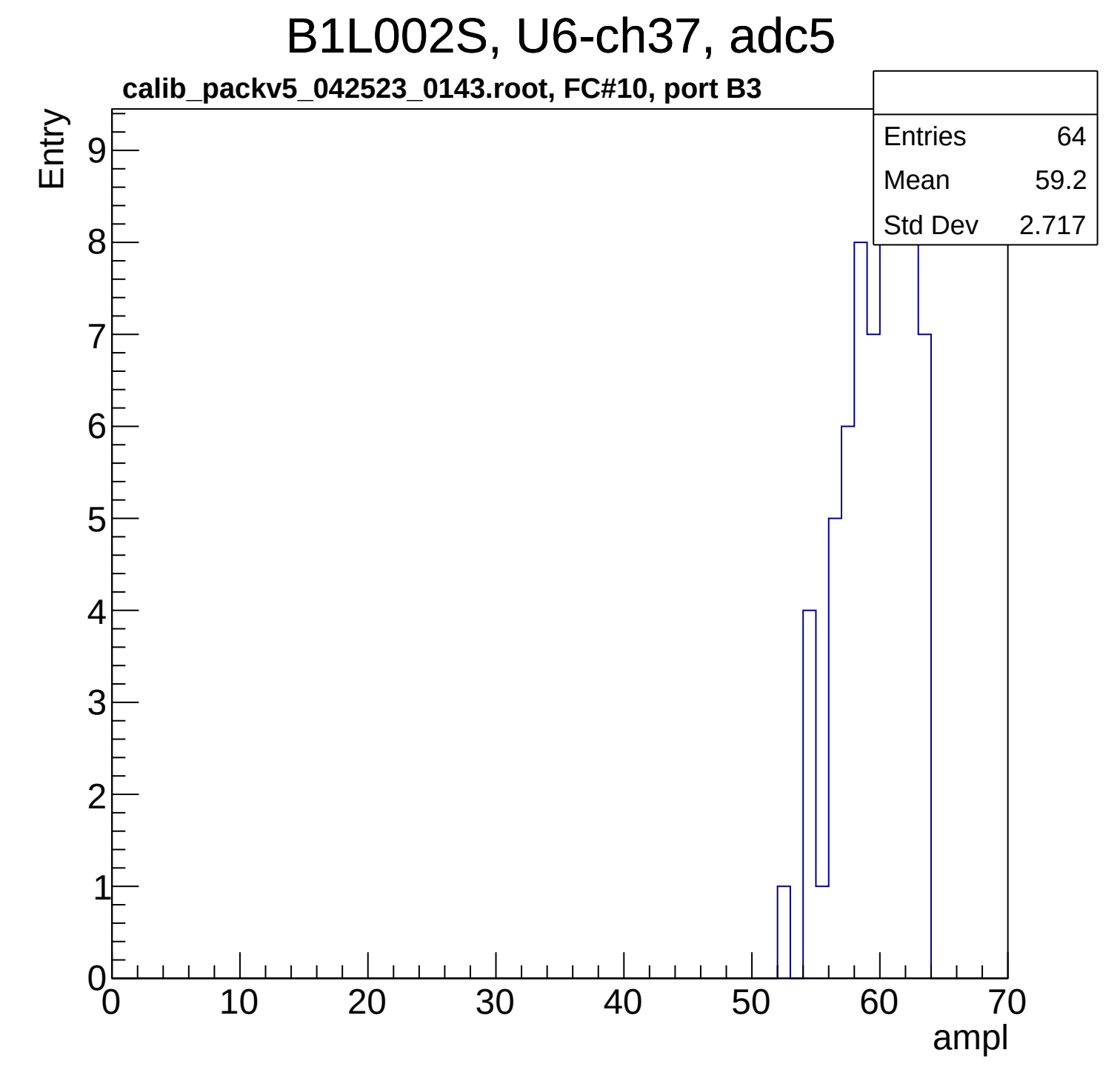

B1L002S, U6-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.58
Std Dev 2.585
Gaus mean : 29.2895
Gaus Width: 2.8135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

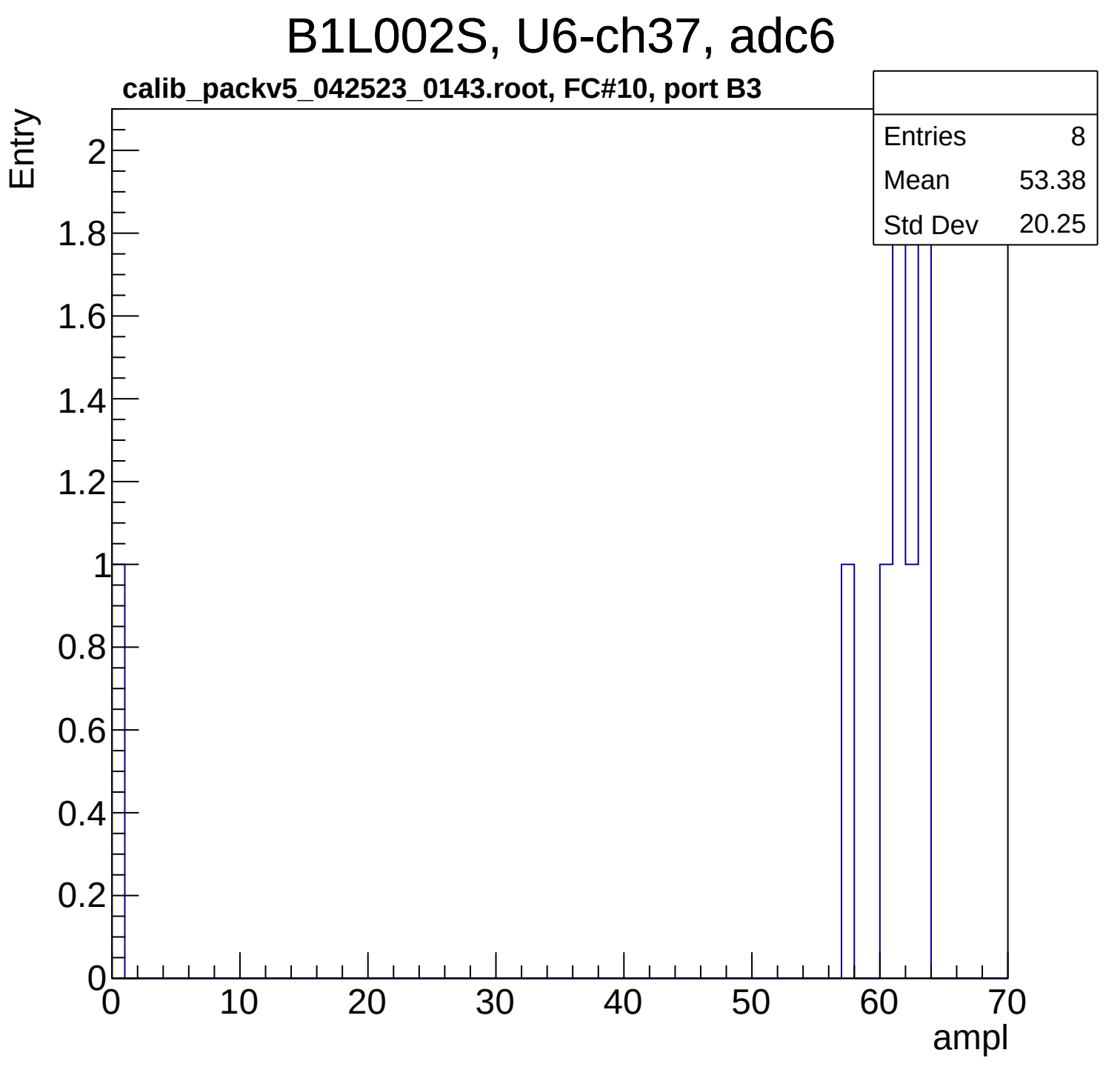

B1L002S, U6-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.51
Std Dev 3.085
Gaus mean : 36.2982
Gaus Width: 3.3258
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

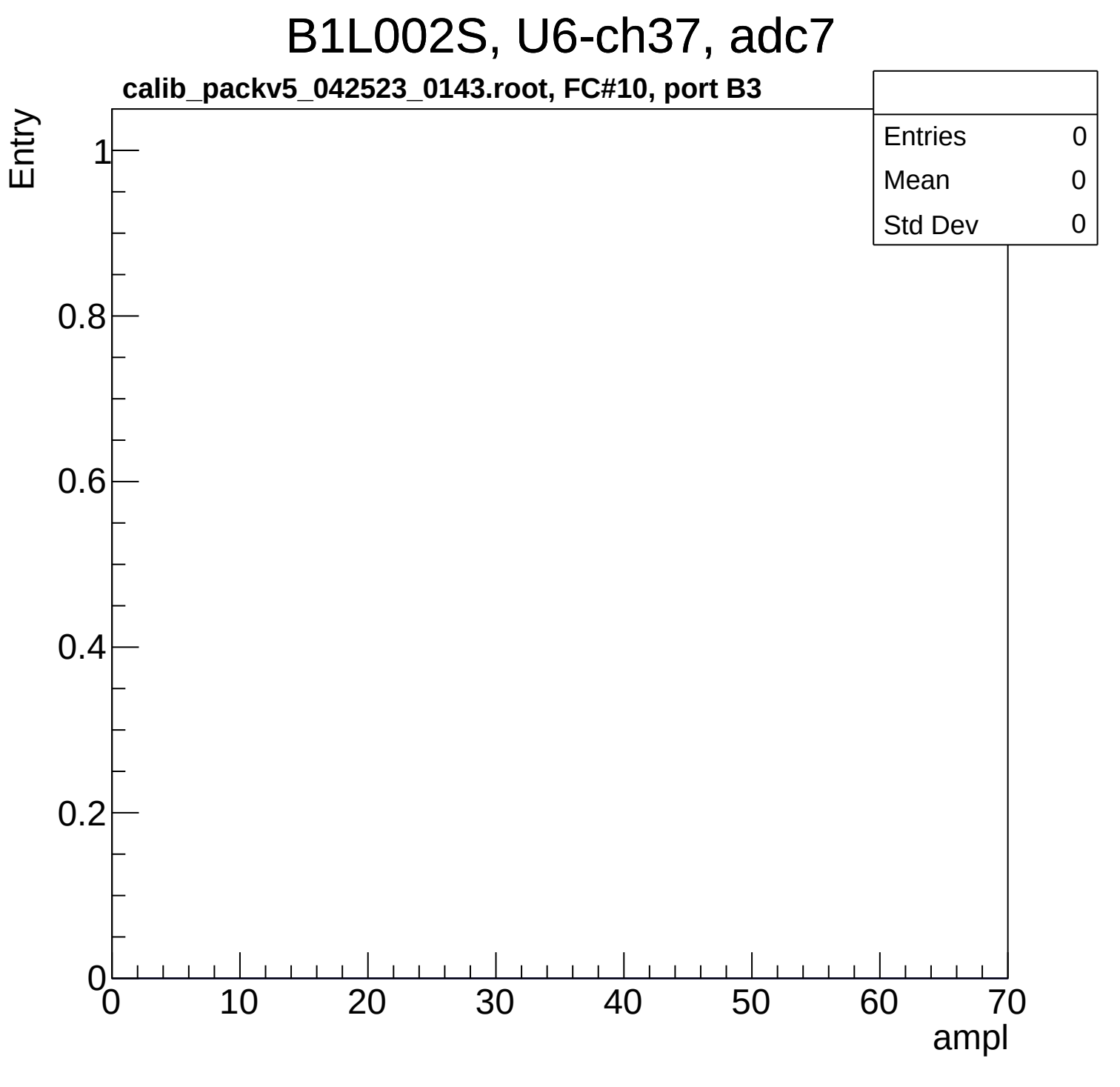

B1L002S, U6-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.41
Std Dev 2.948
Gaus mean : 42.5029
Gaus Width: 3.8348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

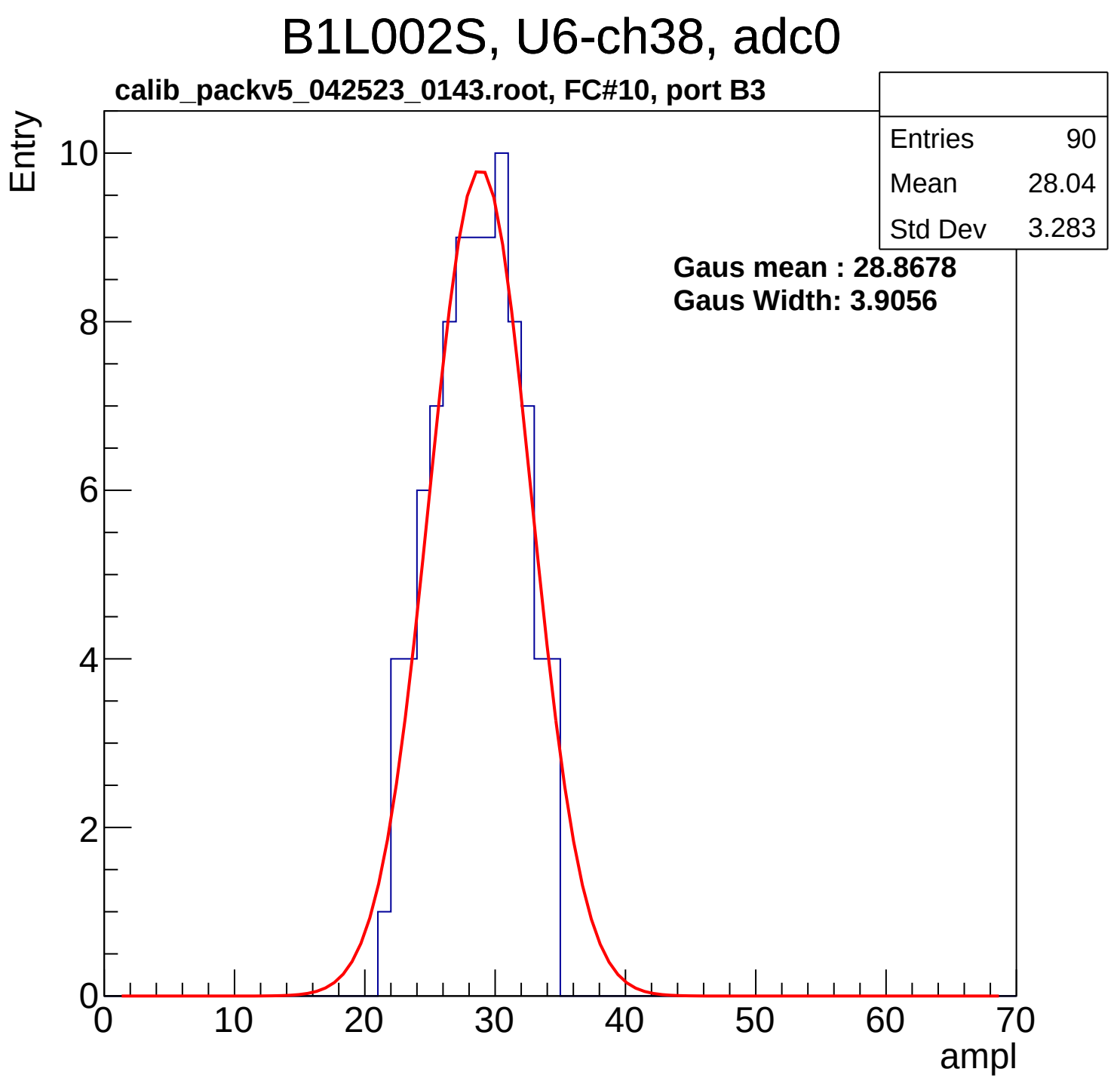

B1L002S, U6-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.12
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

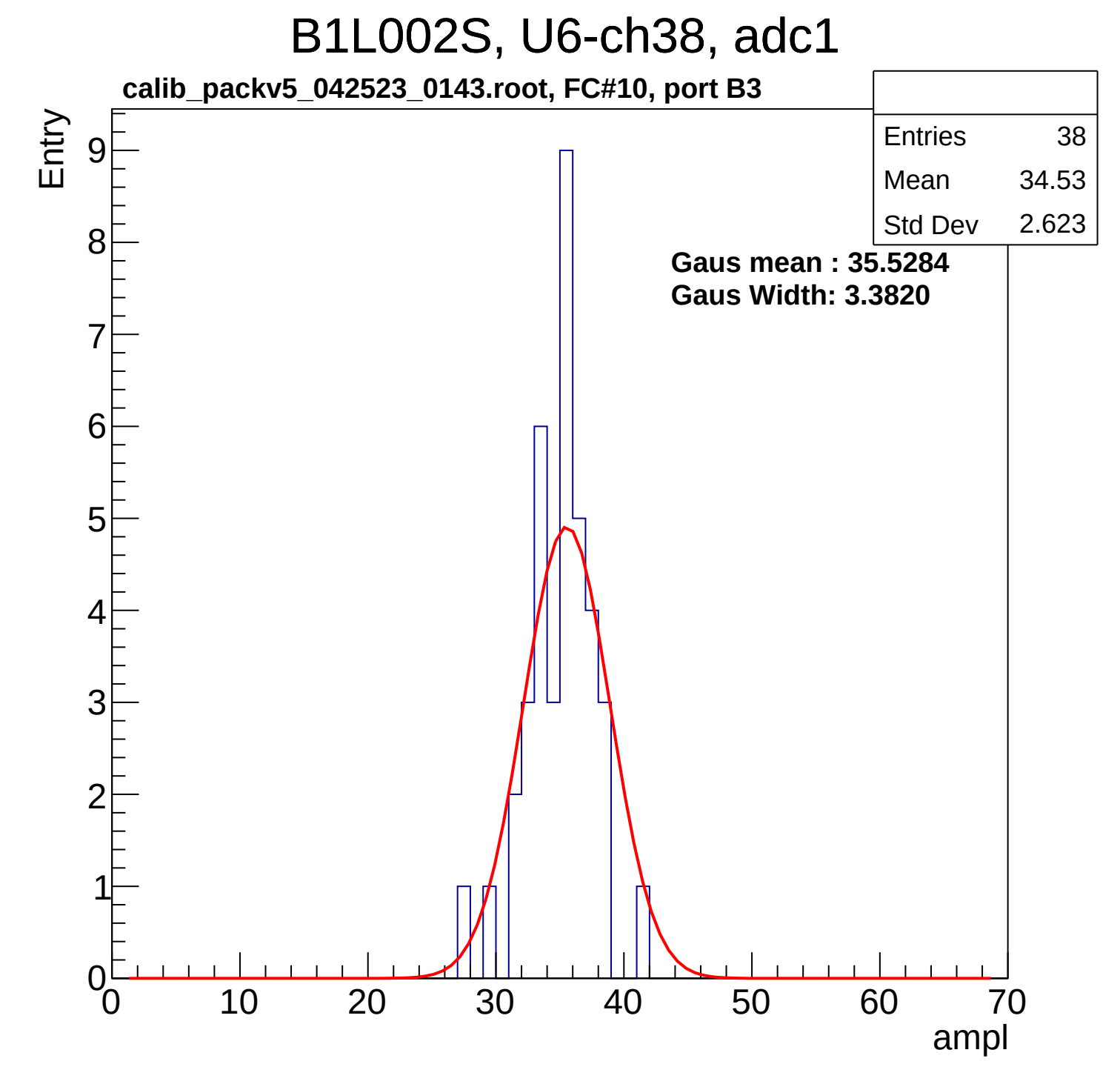

B1L002S, U6-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 54.5
Std Dev 2.895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

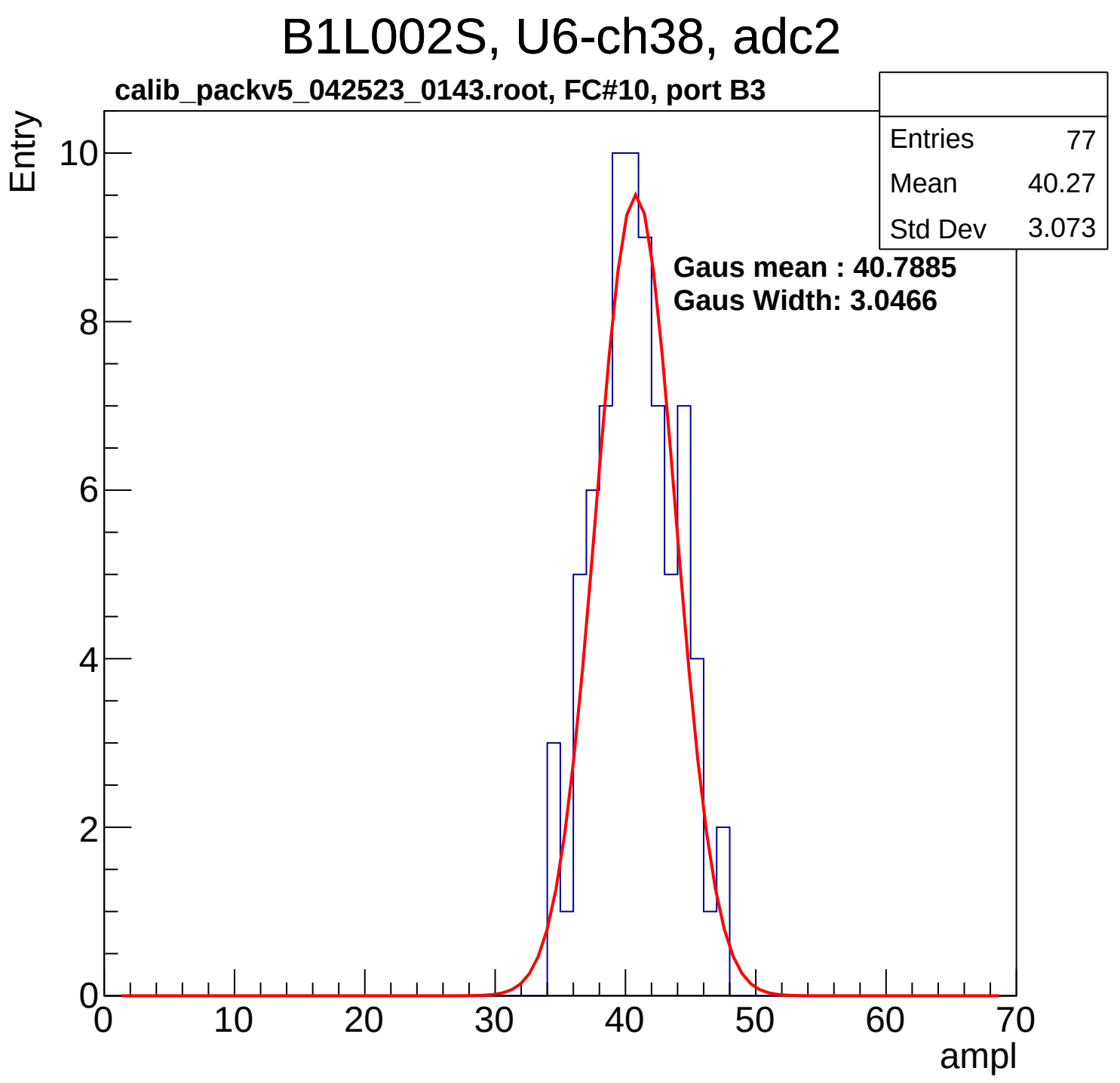

B1L002S, U6-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 59.2
Std Dev 2.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

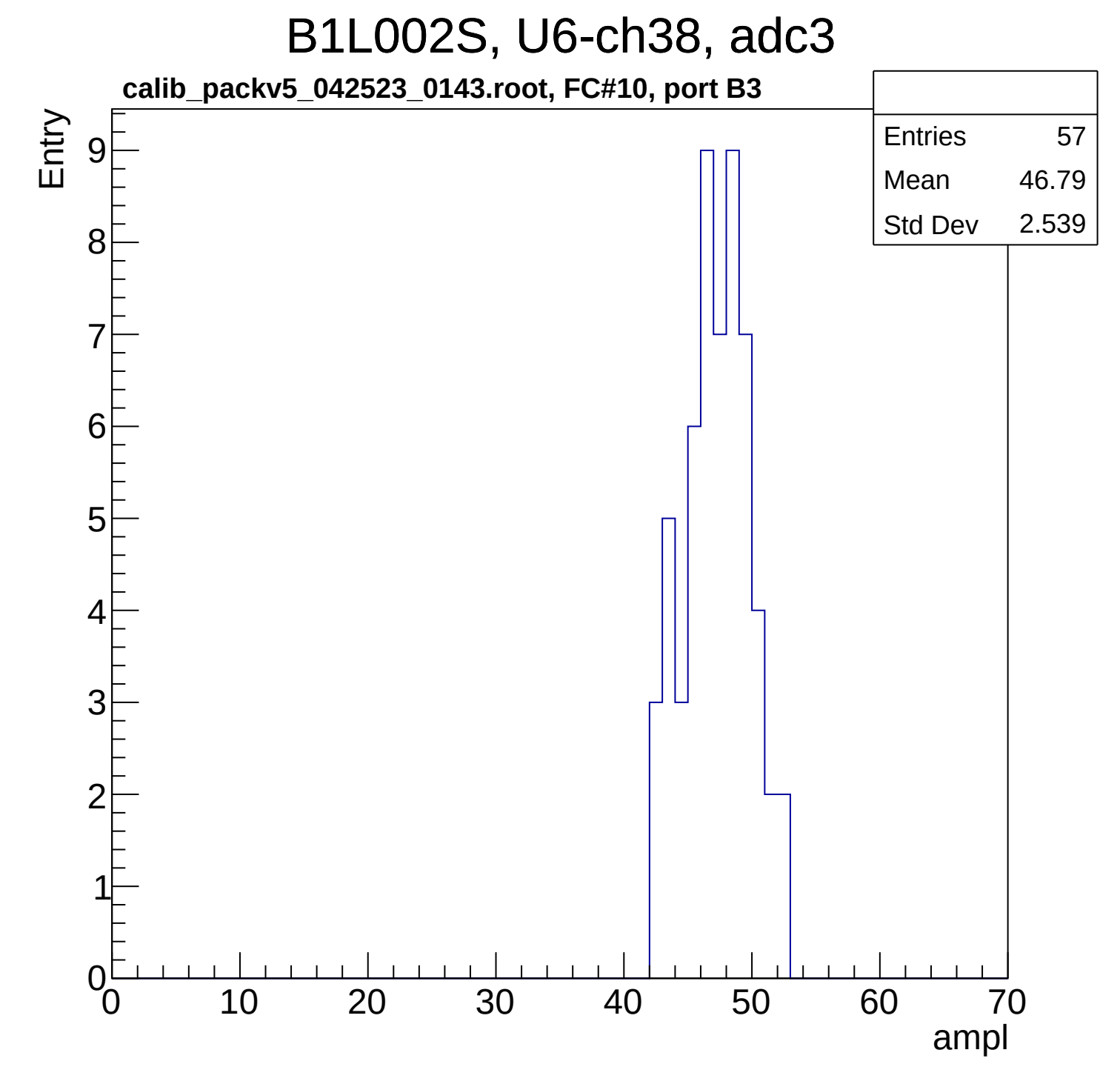

B1L002S, U6-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 53.38
Std Dev 20.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

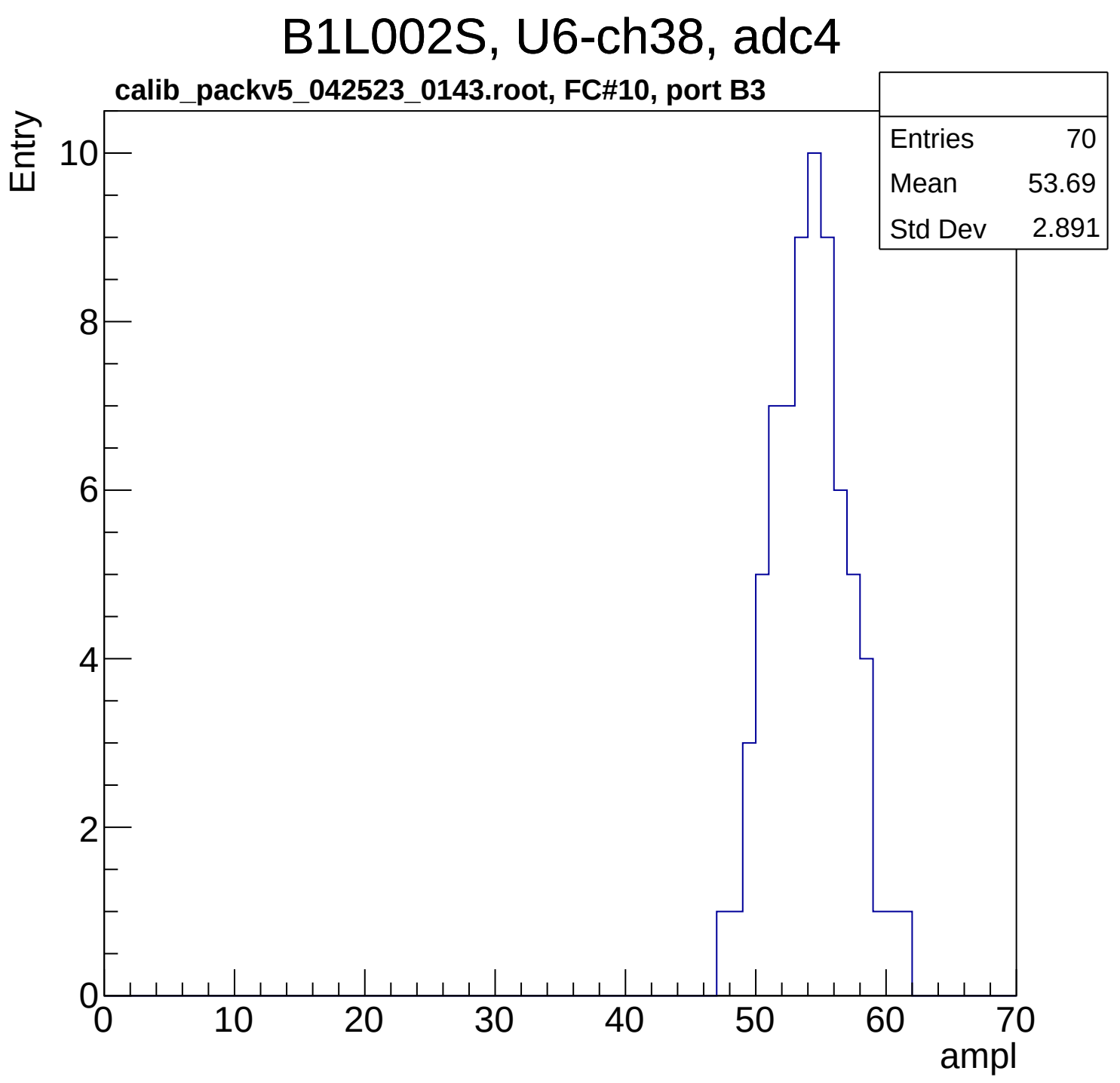

B1L002S, U6-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

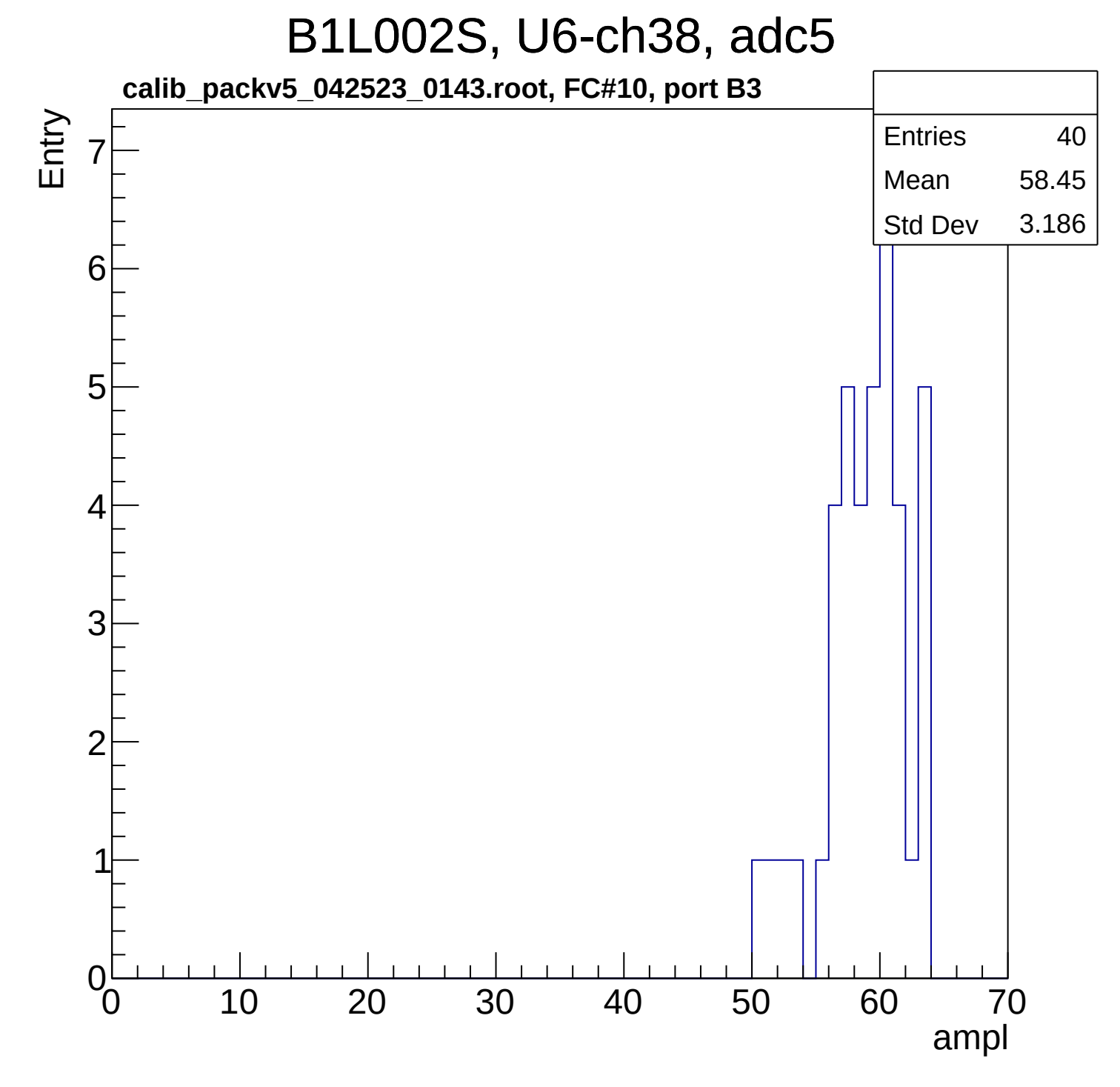

B1L002S, U6-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 28.04
Std Dev 3.283
Gaus mean : 28.8678
Gaus Width: 3.9056
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

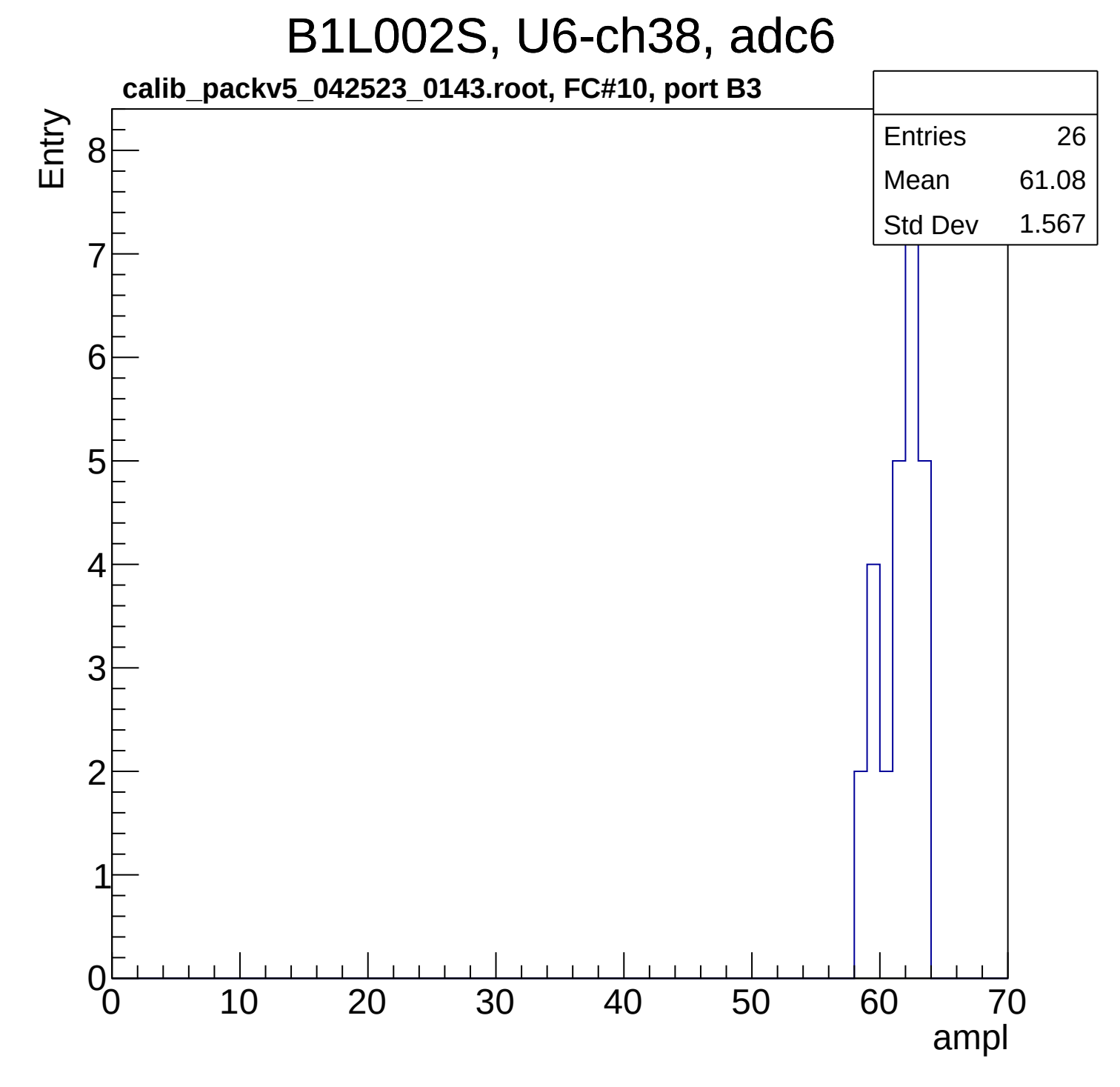

B1L002S, U6-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 34.53
Std Dev 2.623
Gaus mean : 35.5284
Gaus Width: 3.3820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

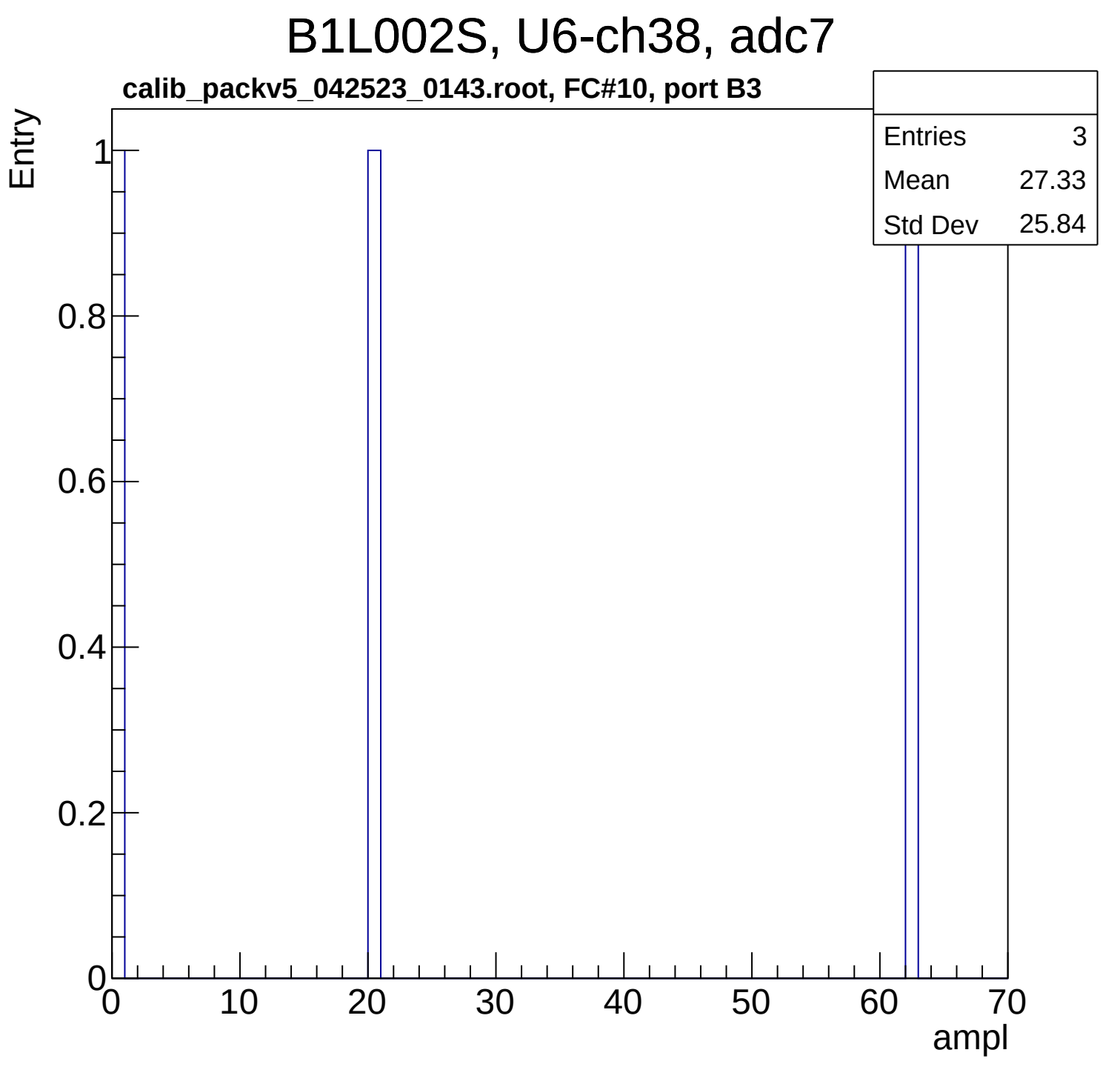

B1L002S, U6-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 40.27
Std Dev 3.073
Gaus mean : 40.7885
Gaus Width: 3.0466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

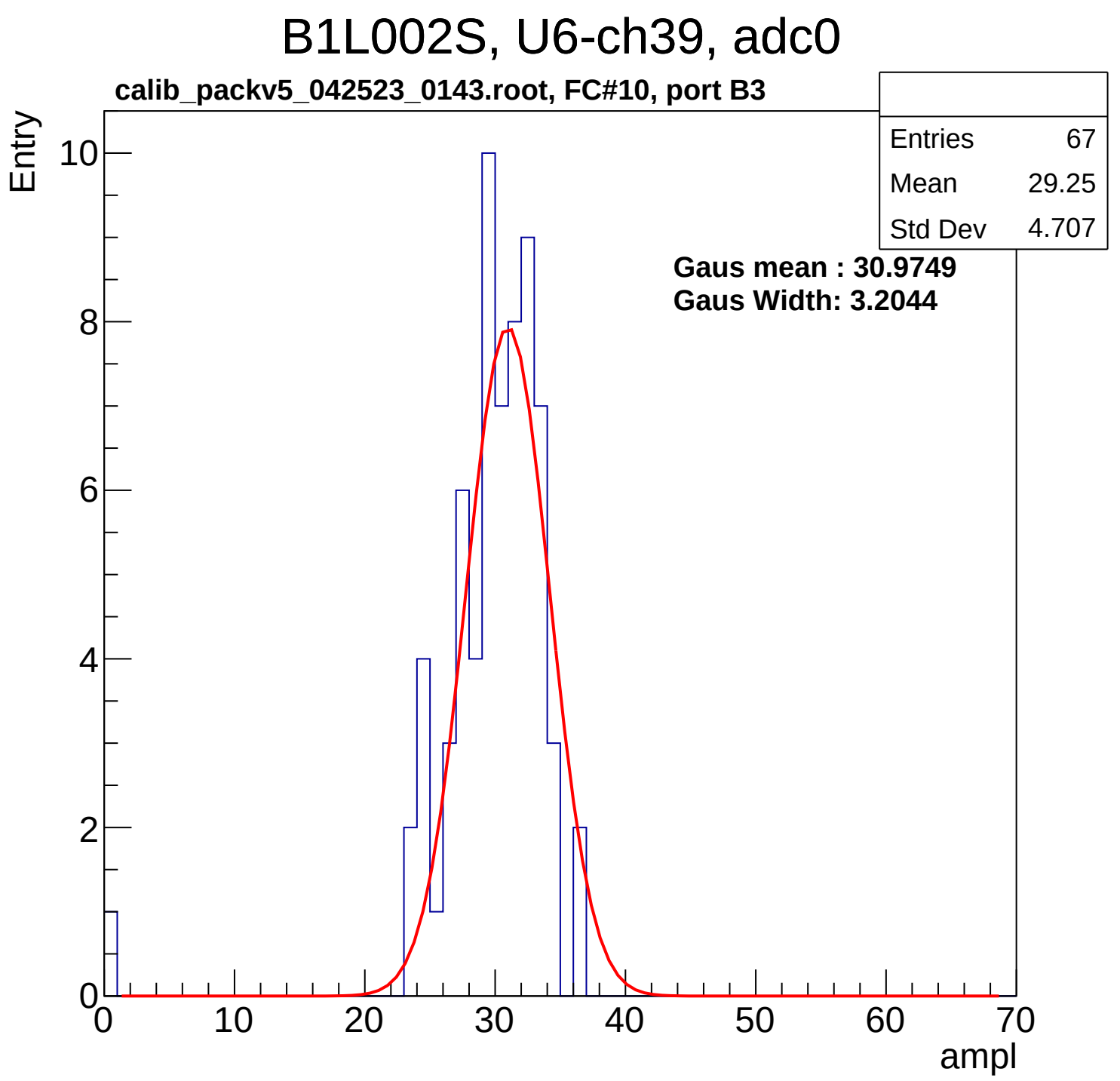

B1L002S, U6-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.79
Std Dev 2.539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

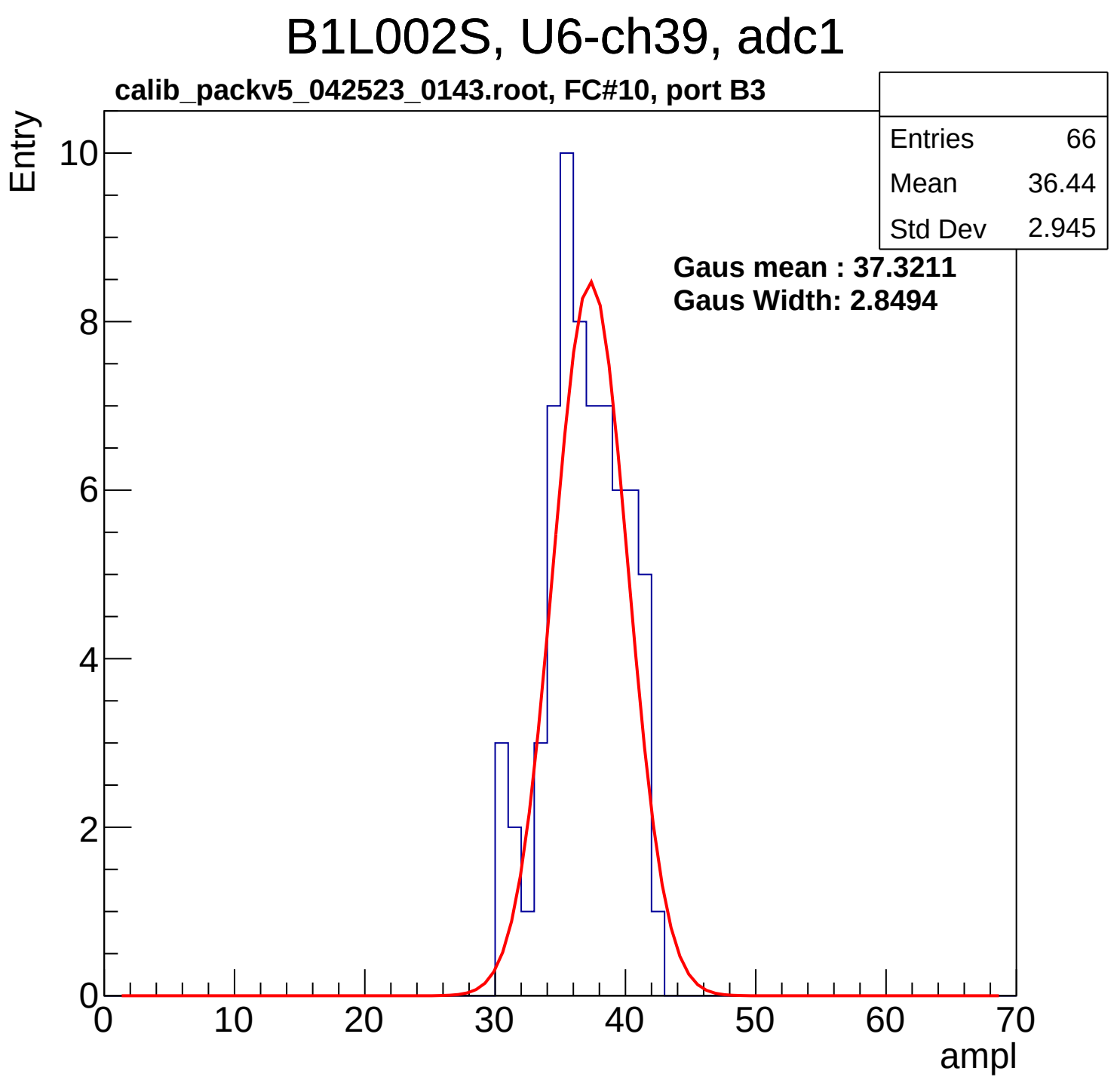

B1L002S, U6-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 53.69
Std Dev 2.891
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

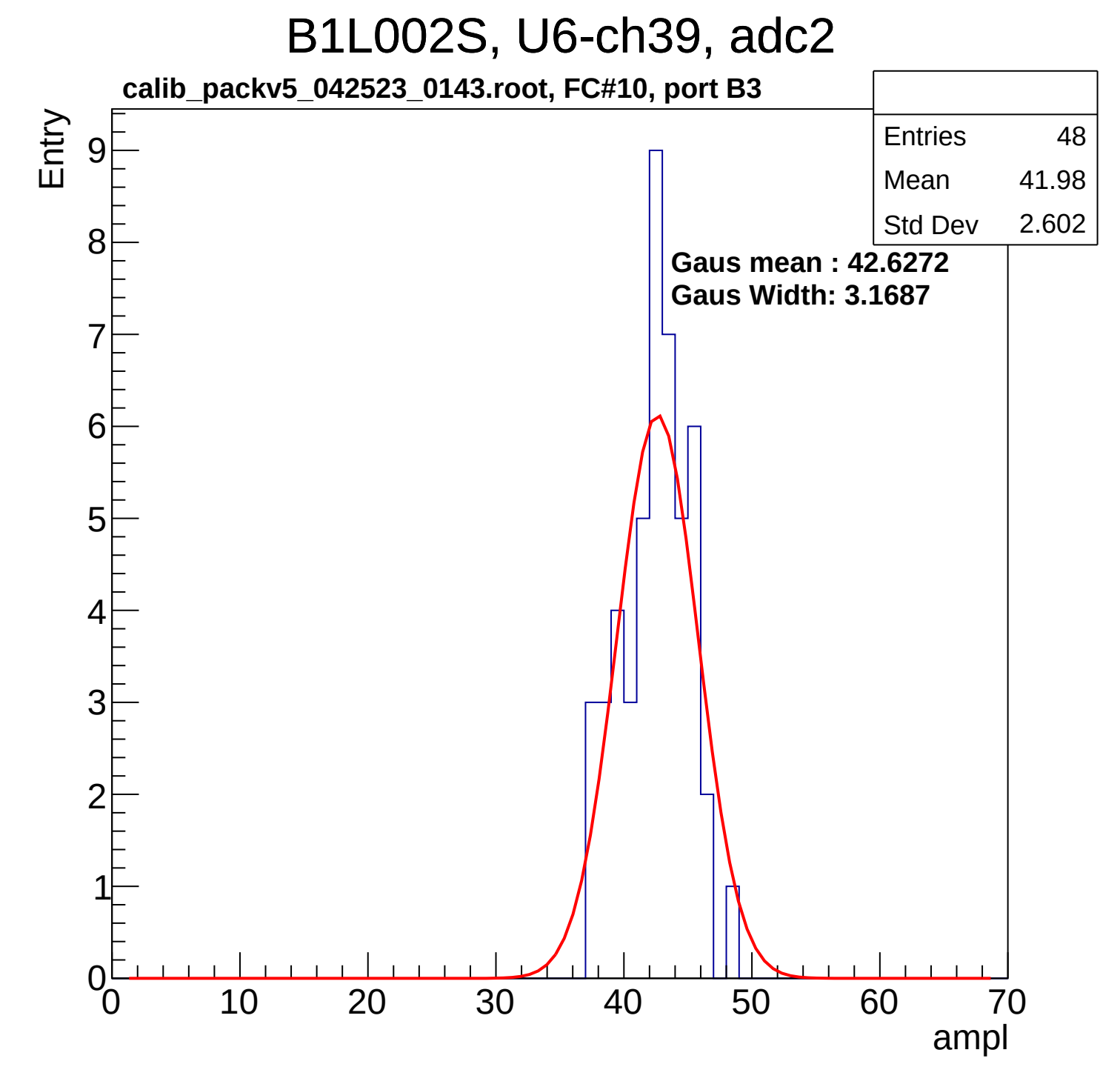

B1L002S, U6-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 58.45
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

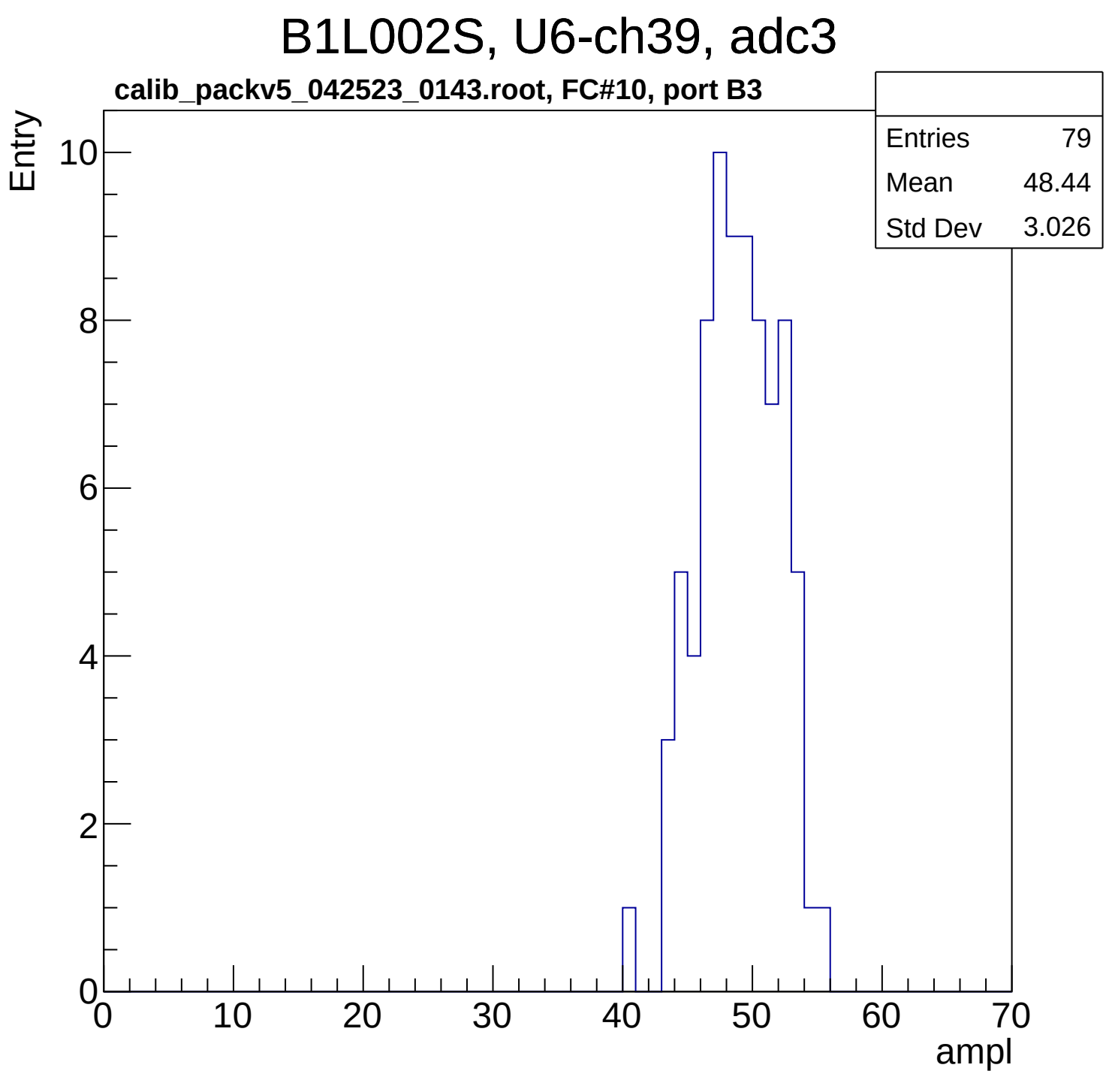

B1L002S, U6-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.08
Std Dev 1.567
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

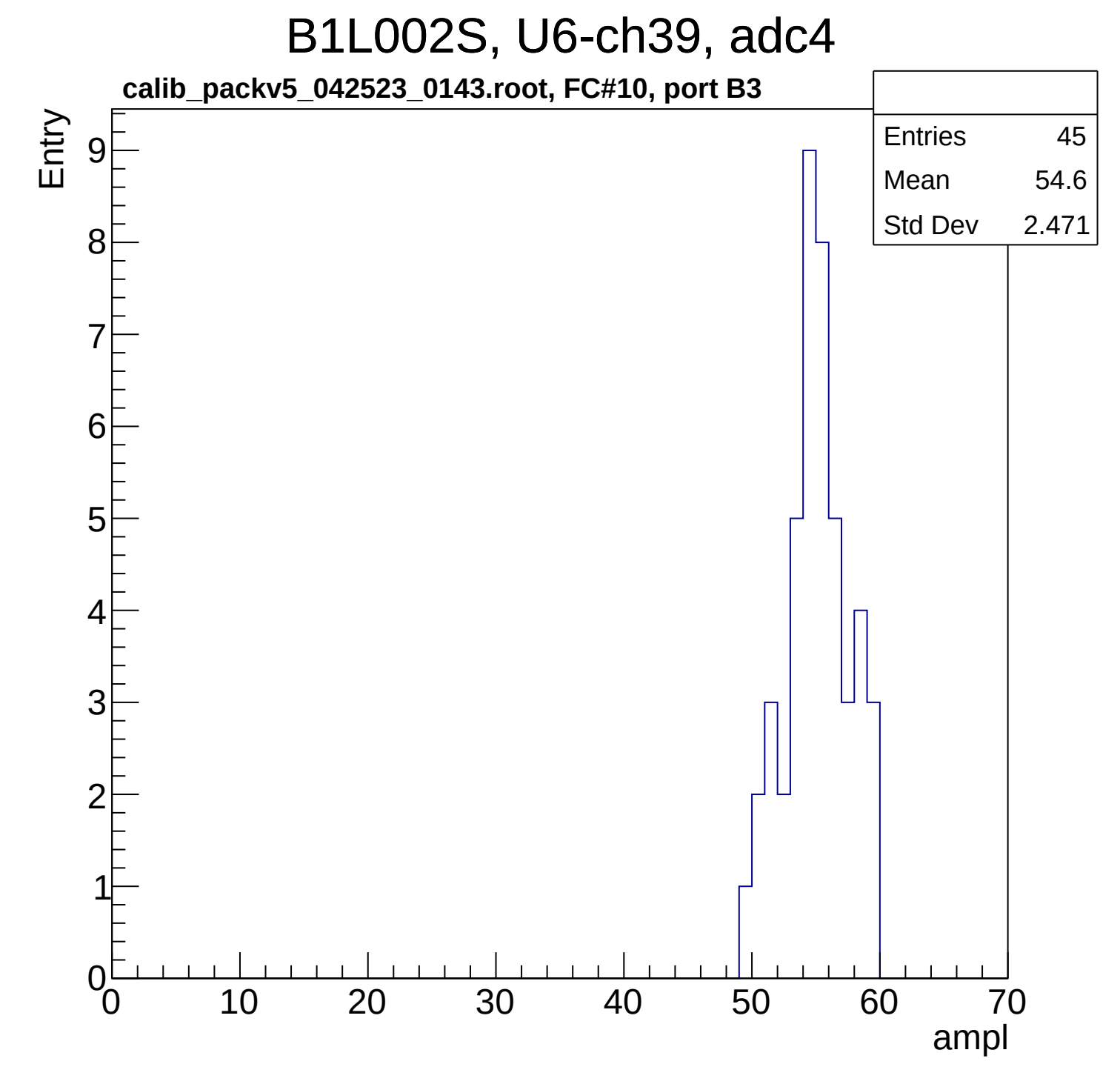

B1L002S, U6-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

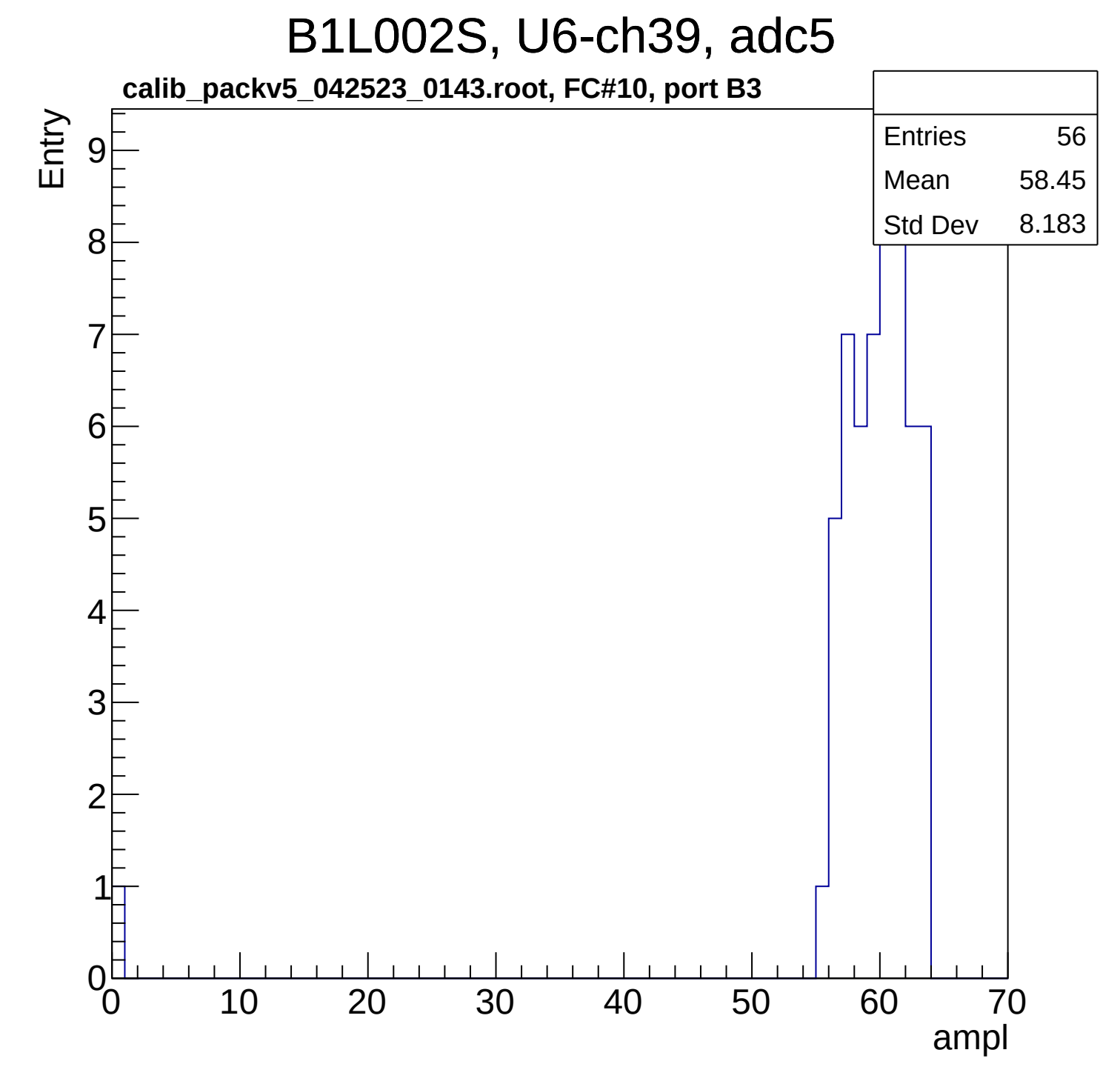

B1L002S, U6-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.25
Std Dev 4.707
Gaus mean : 30.9749
Gaus Width: 3.2044
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

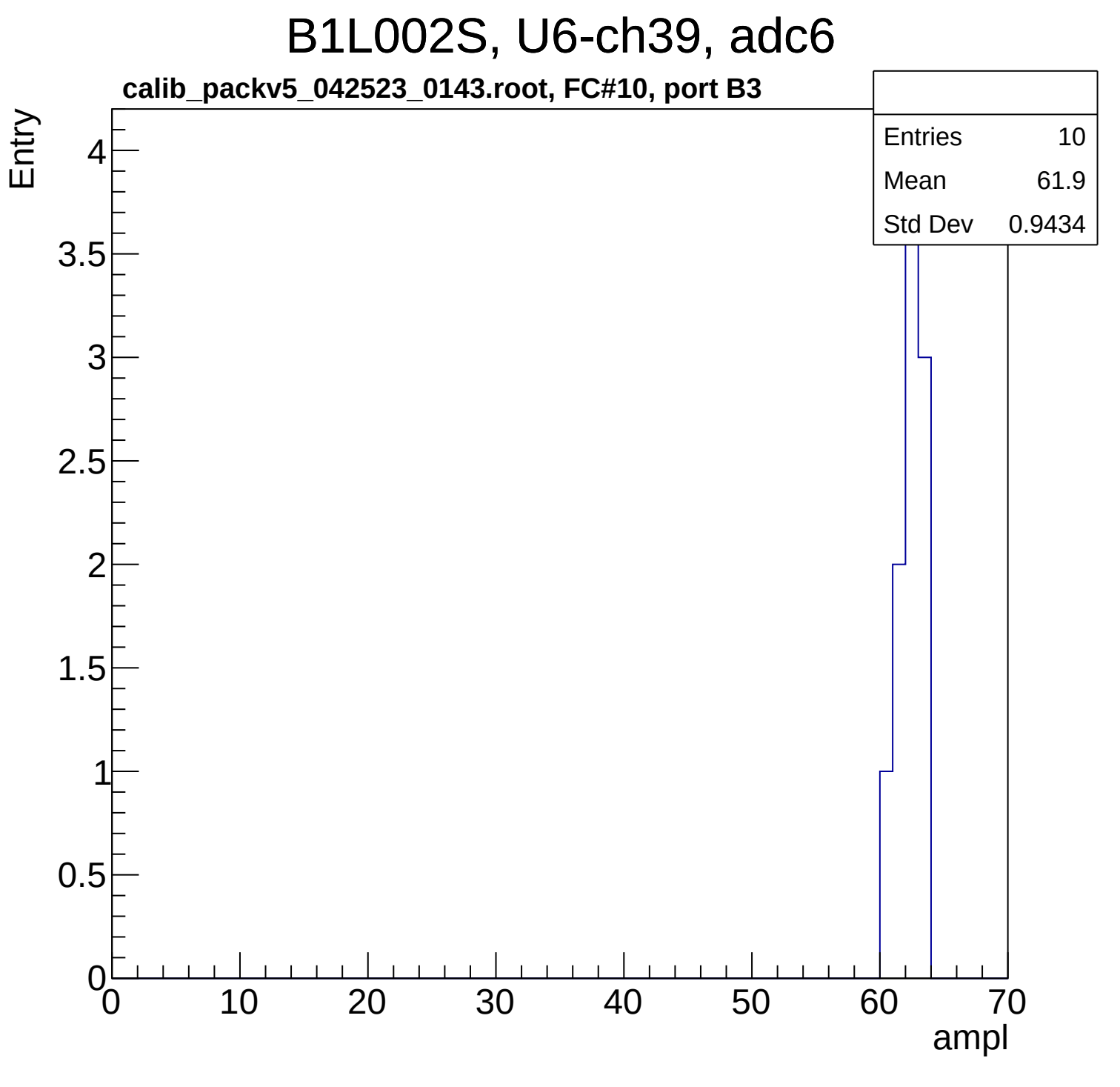

B1L002S, U6-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 36.44
Std Dev 2.945
Gaus mean : 37.3211
Gaus Width: 2.8494
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

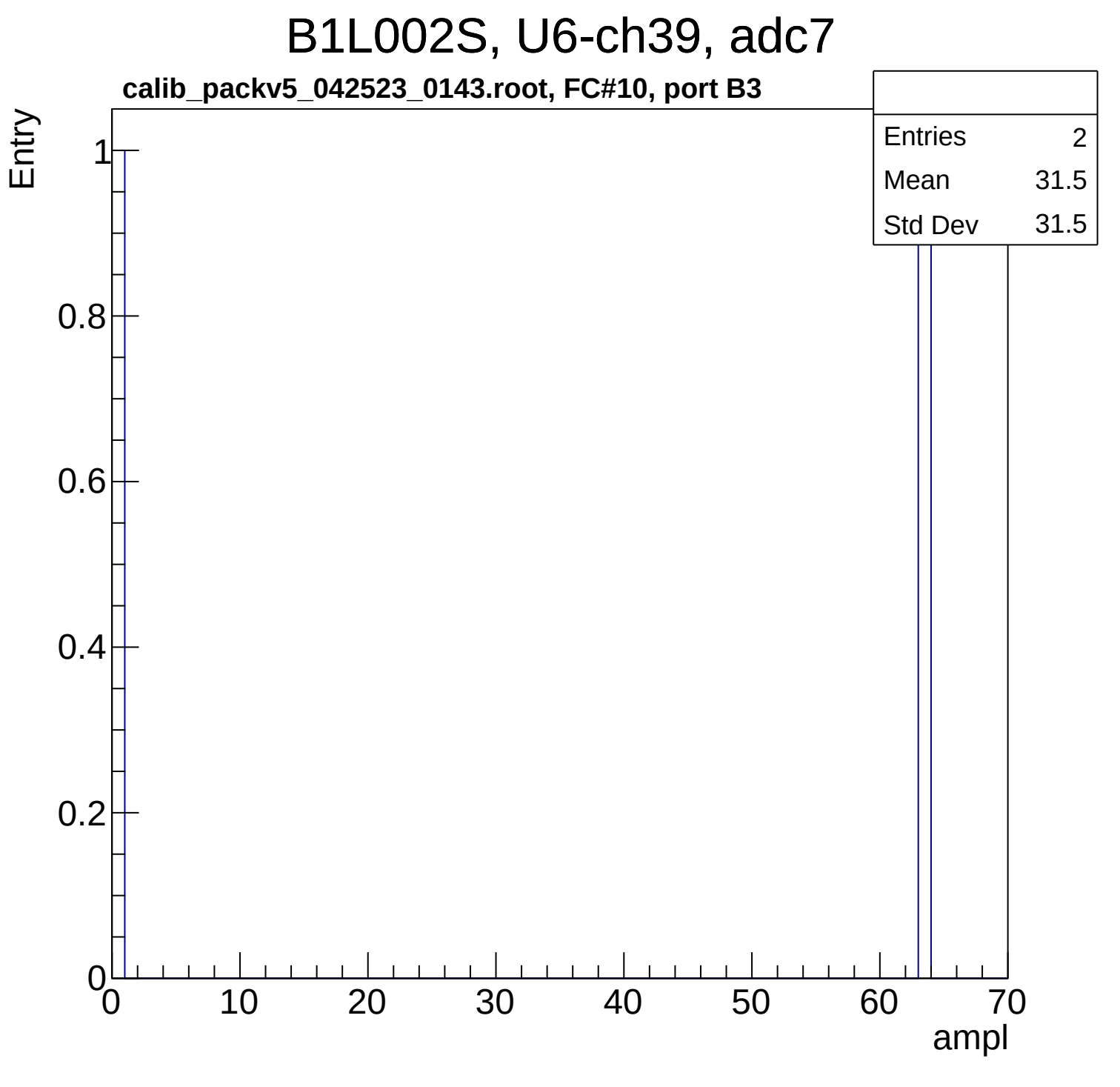

B1L002S, U6-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.98
Std Dev 2.602
Gaus mean : 42.6272
Gaus Width: 3.1687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

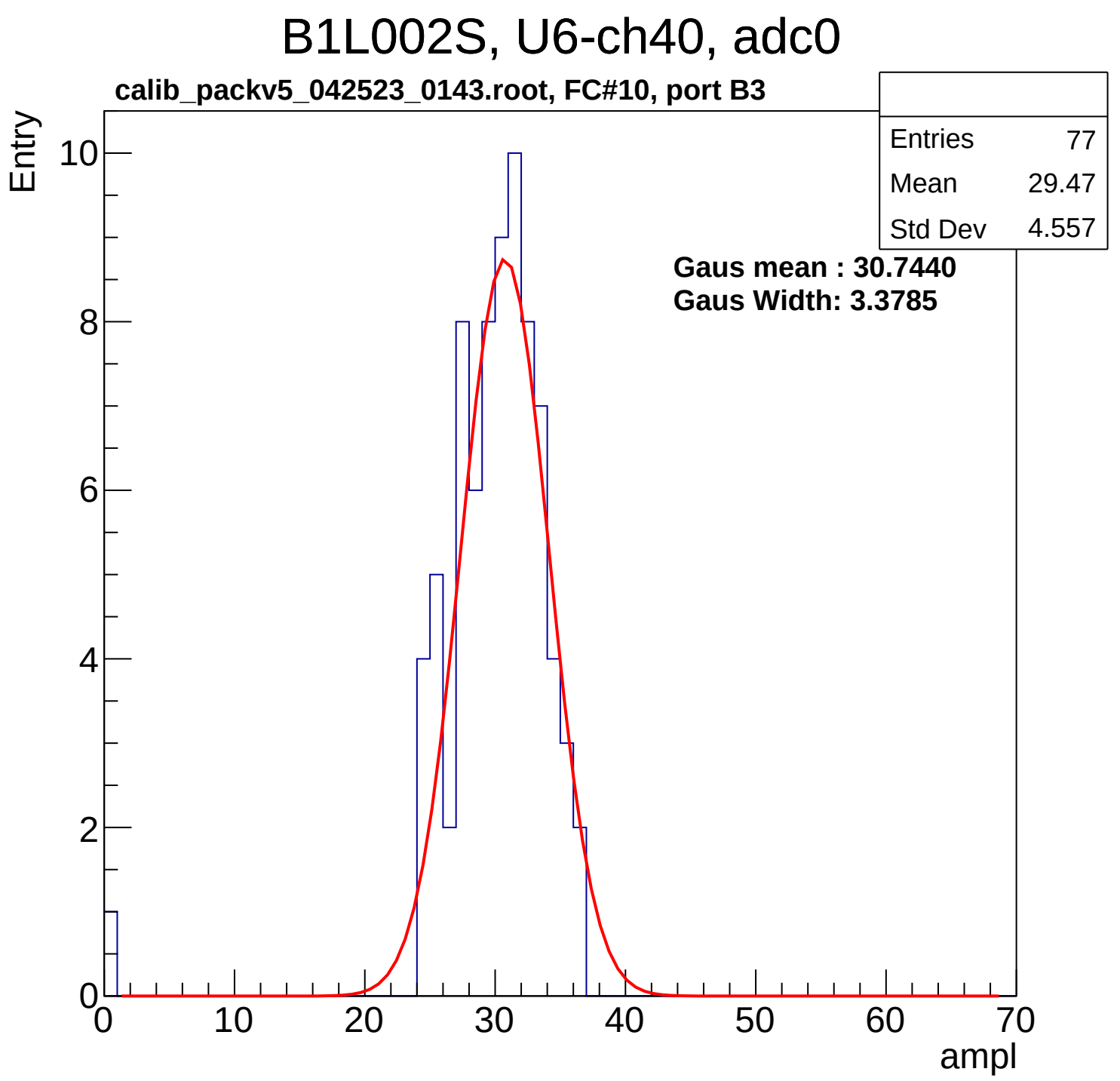

B1L002S, U6-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 48.44
Std Dev 3.026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

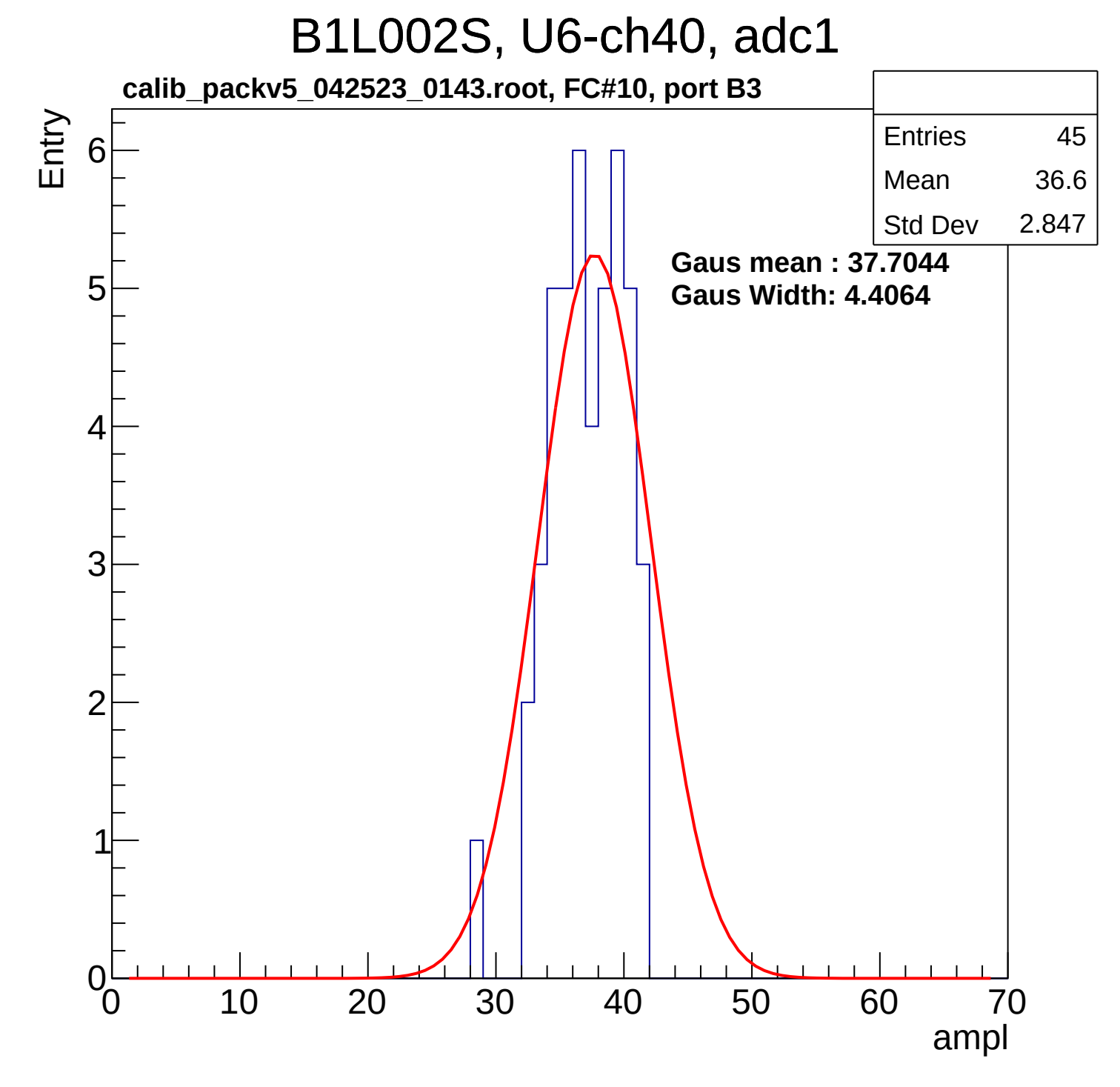

B1L002S, U6-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 54.6
Std Dev 2.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

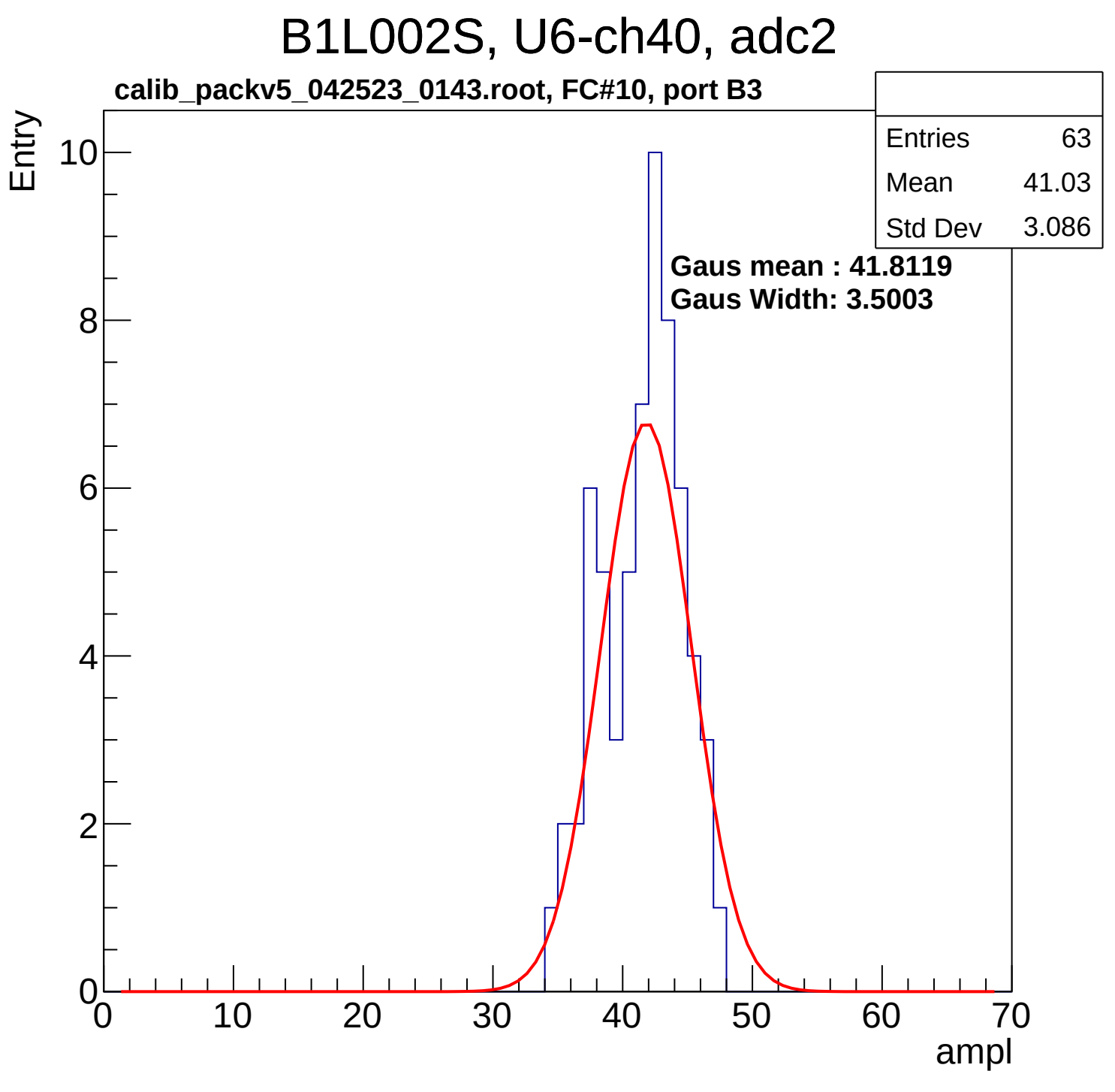

B1L002S, U6-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.45
Std Dev 8.183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

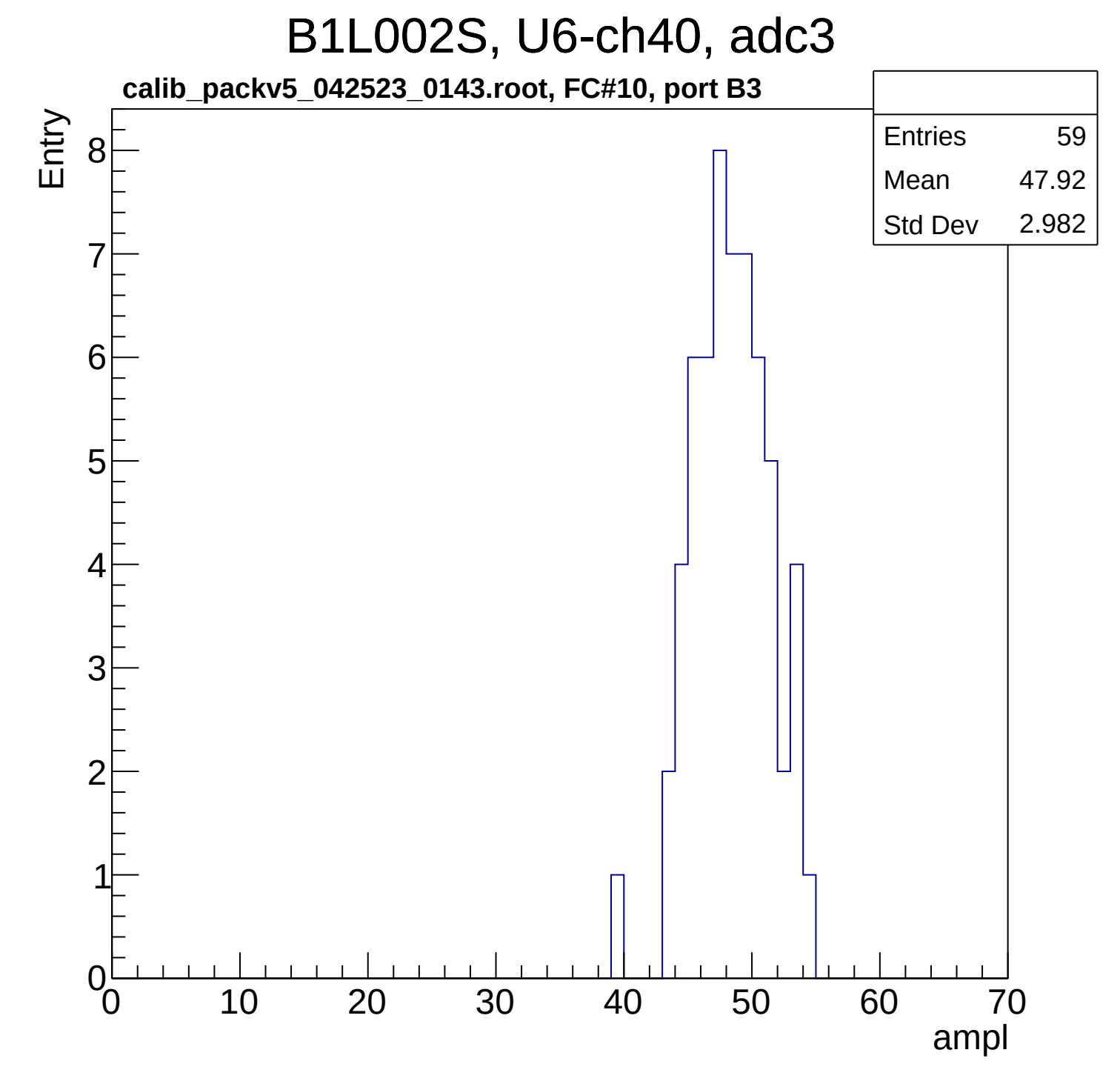

B1L002S, U6-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.9
Std Dev 0.9434
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

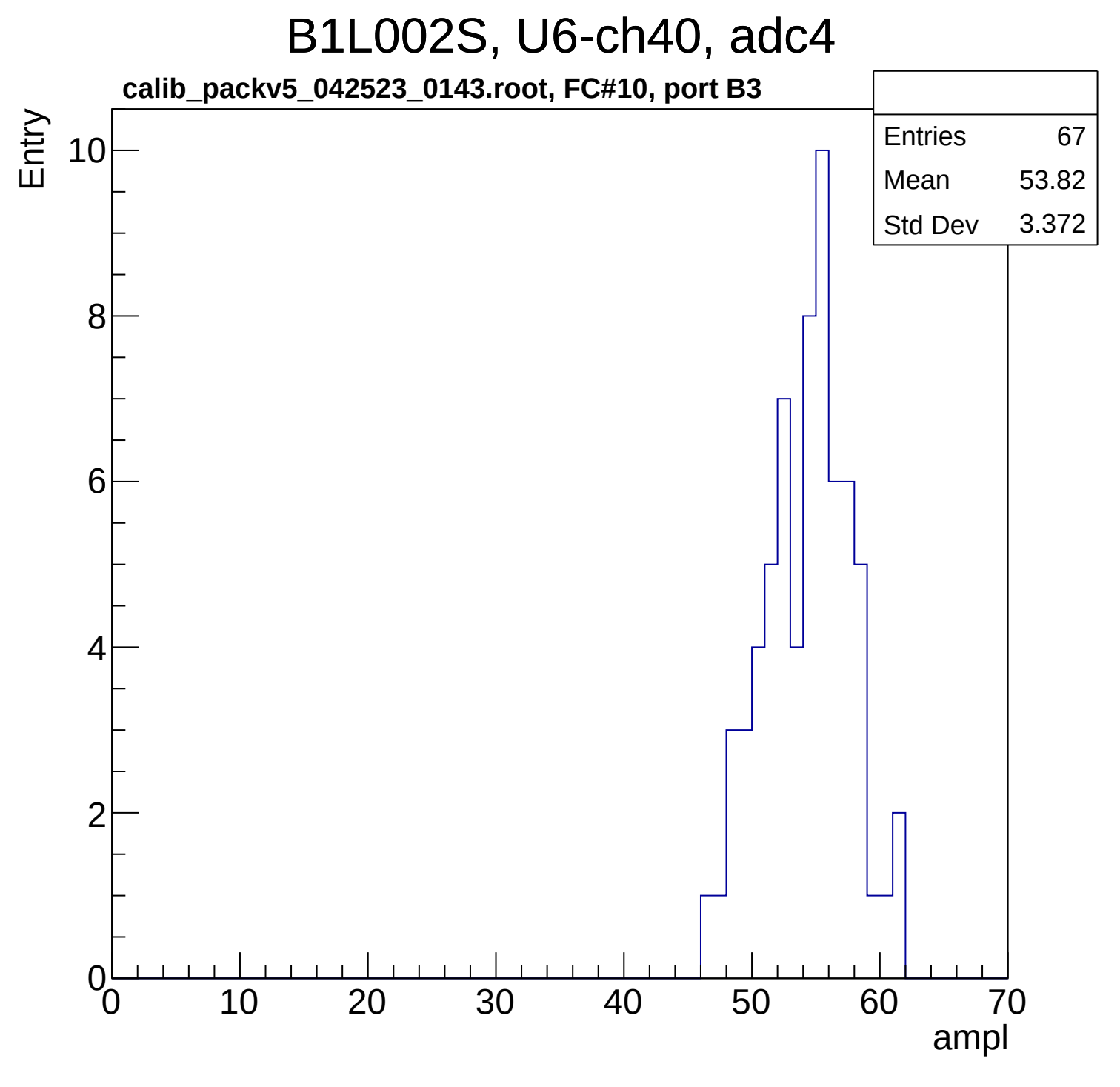

B1L002S, U6-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

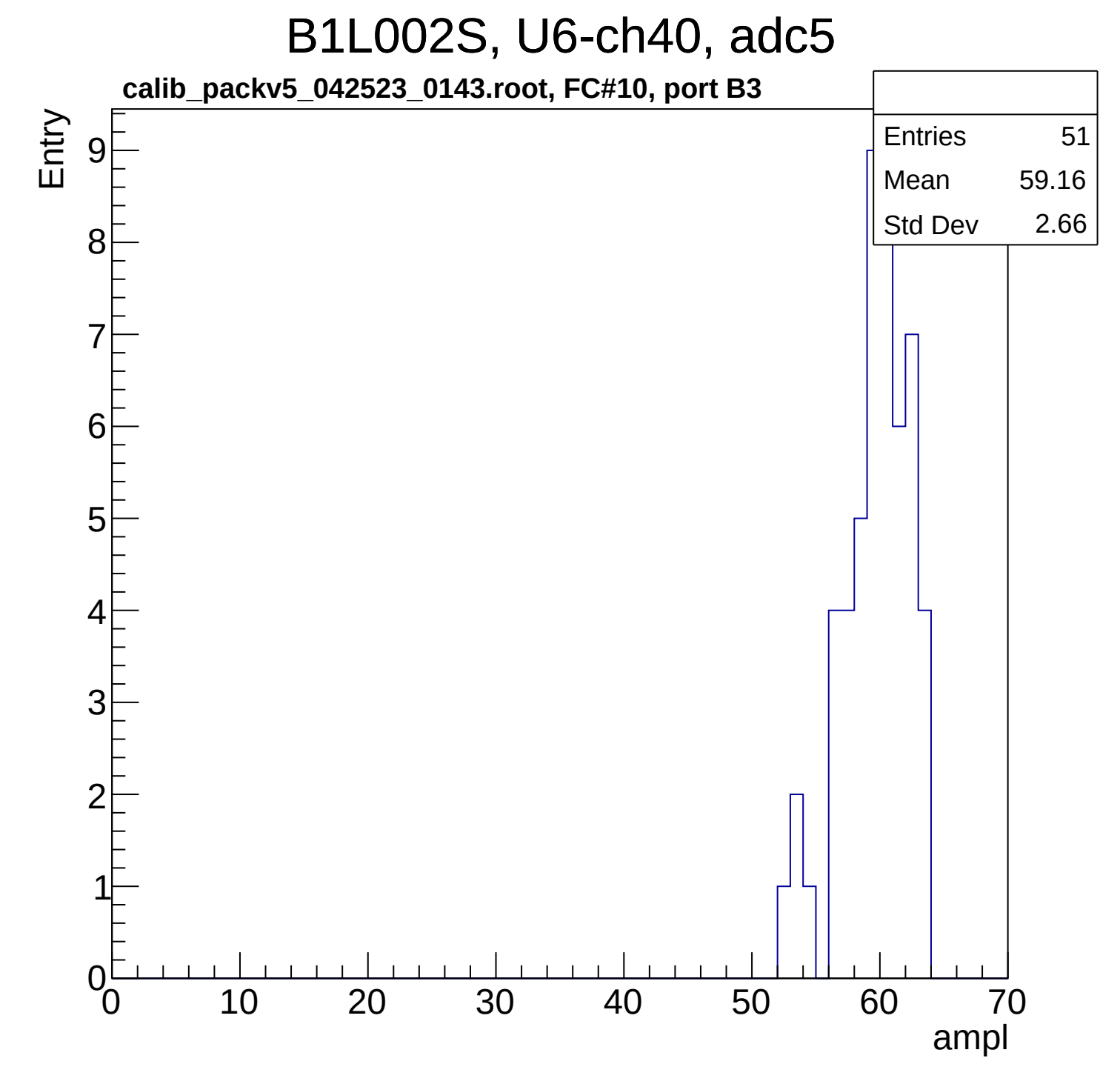

B1L002S, U6-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.47
Std Dev 4.557
Gaus mean : 30.7440
Gaus Width: 3.3785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

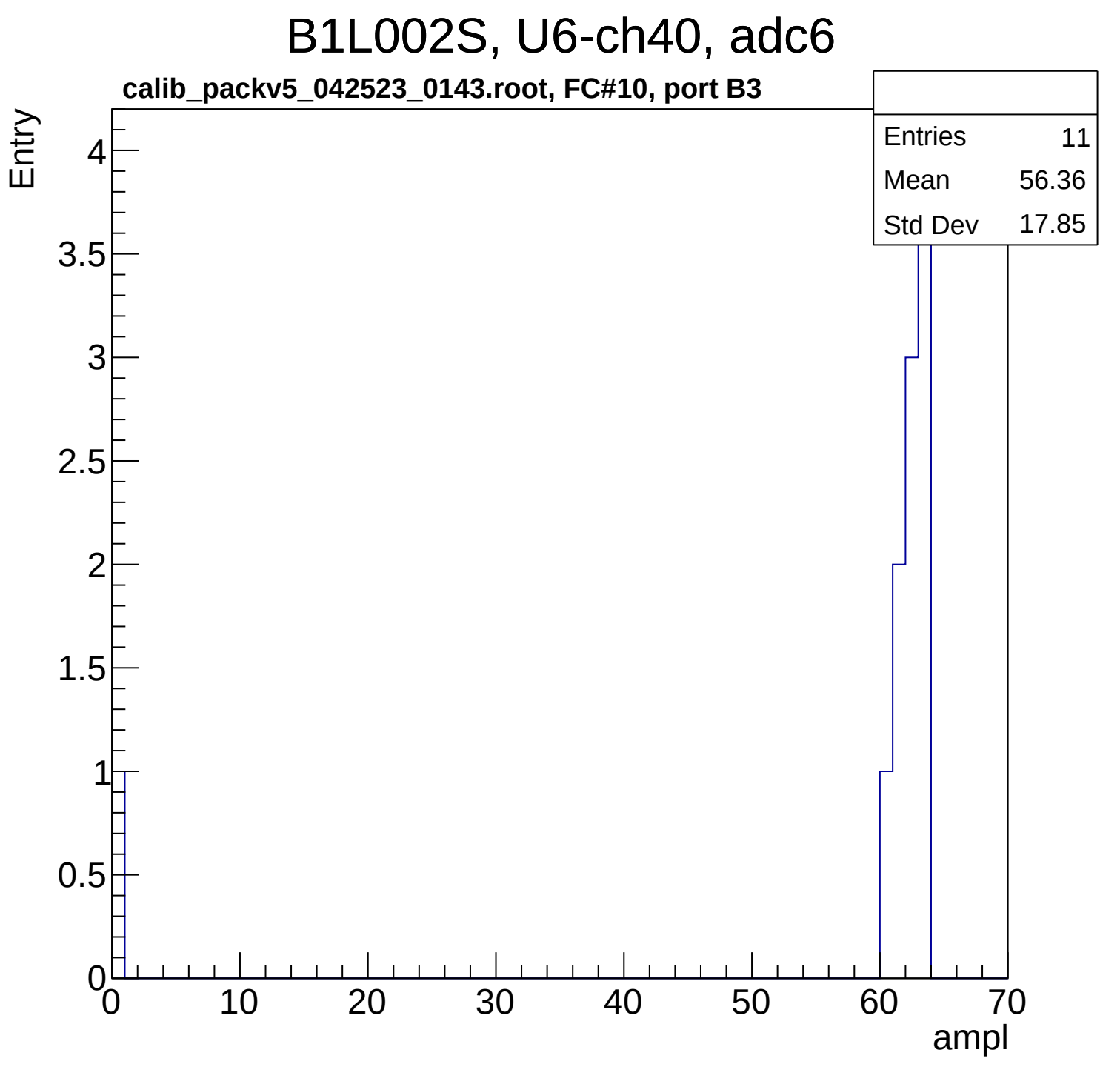

B1L002S, U6-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 36.6
Std Dev 2.847
Gaus mean : 37.7044
Gaus Width: 4.4064
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

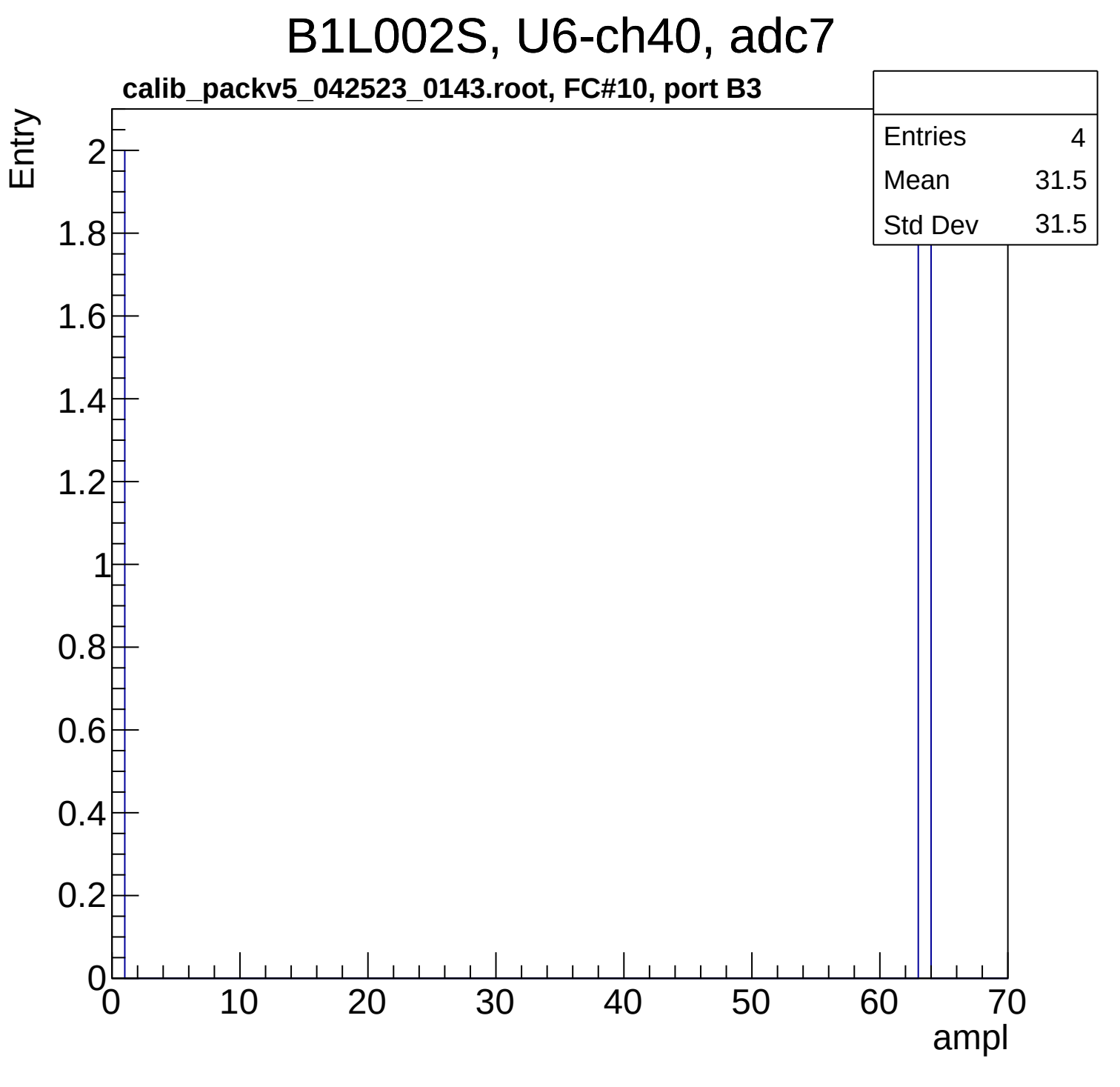

B1L002S, U6-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.03
Std Dev 3.086
Gaus mean : 41.8119
Gaus Width: 3.5003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

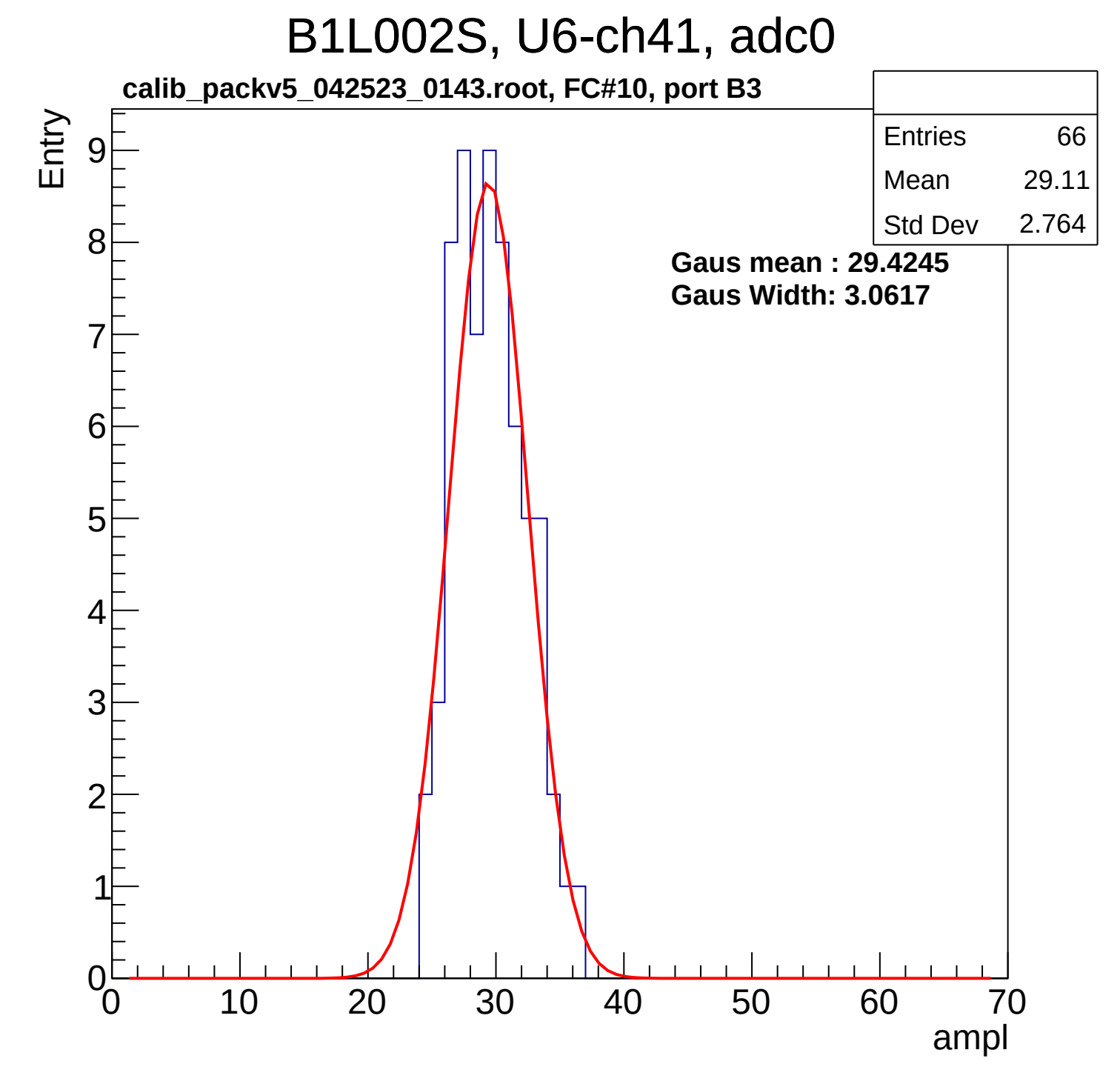

B1L002S, U6-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.92
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

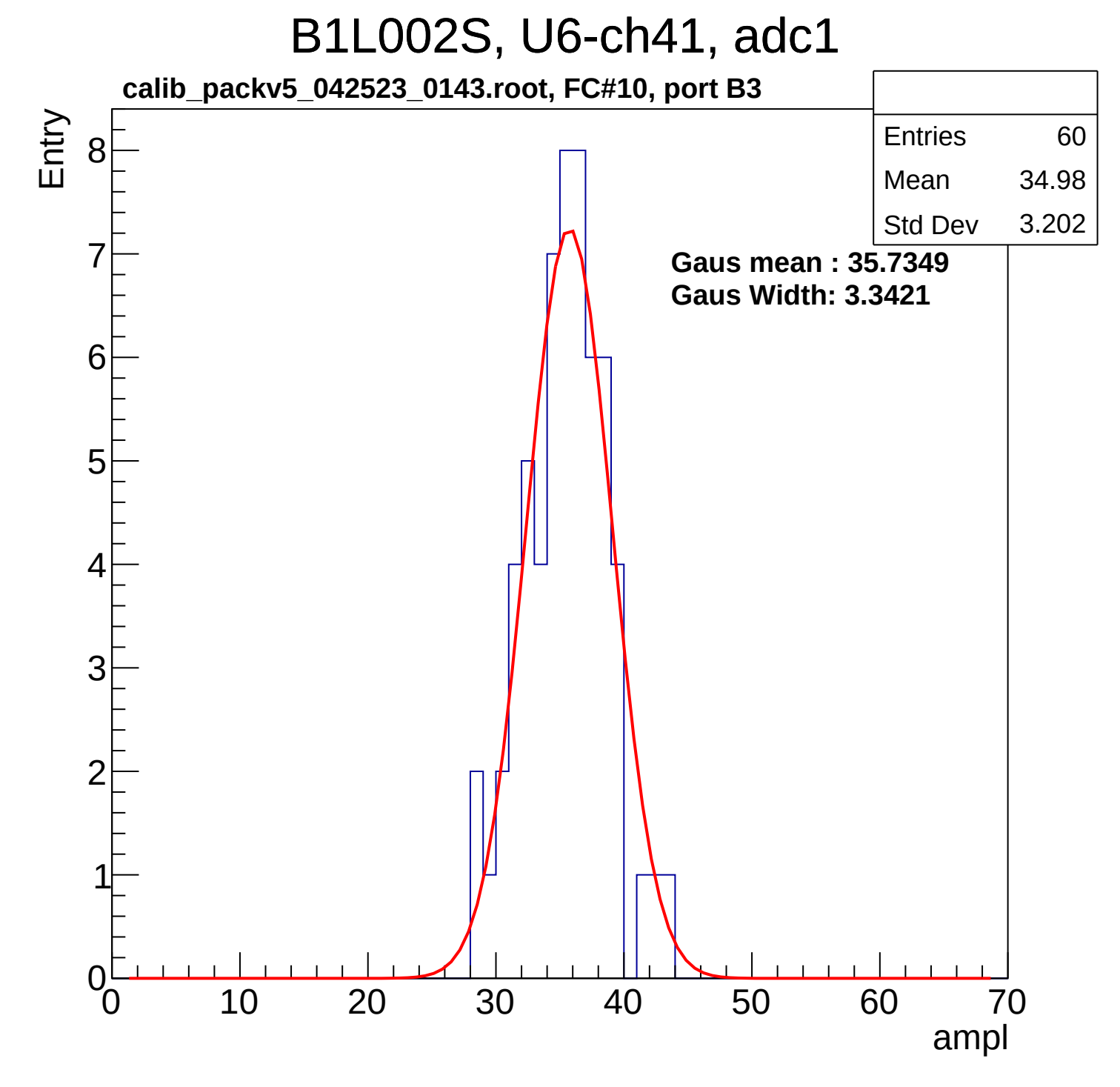

B1L002S, U6-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.82
Std Dev 3.372
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

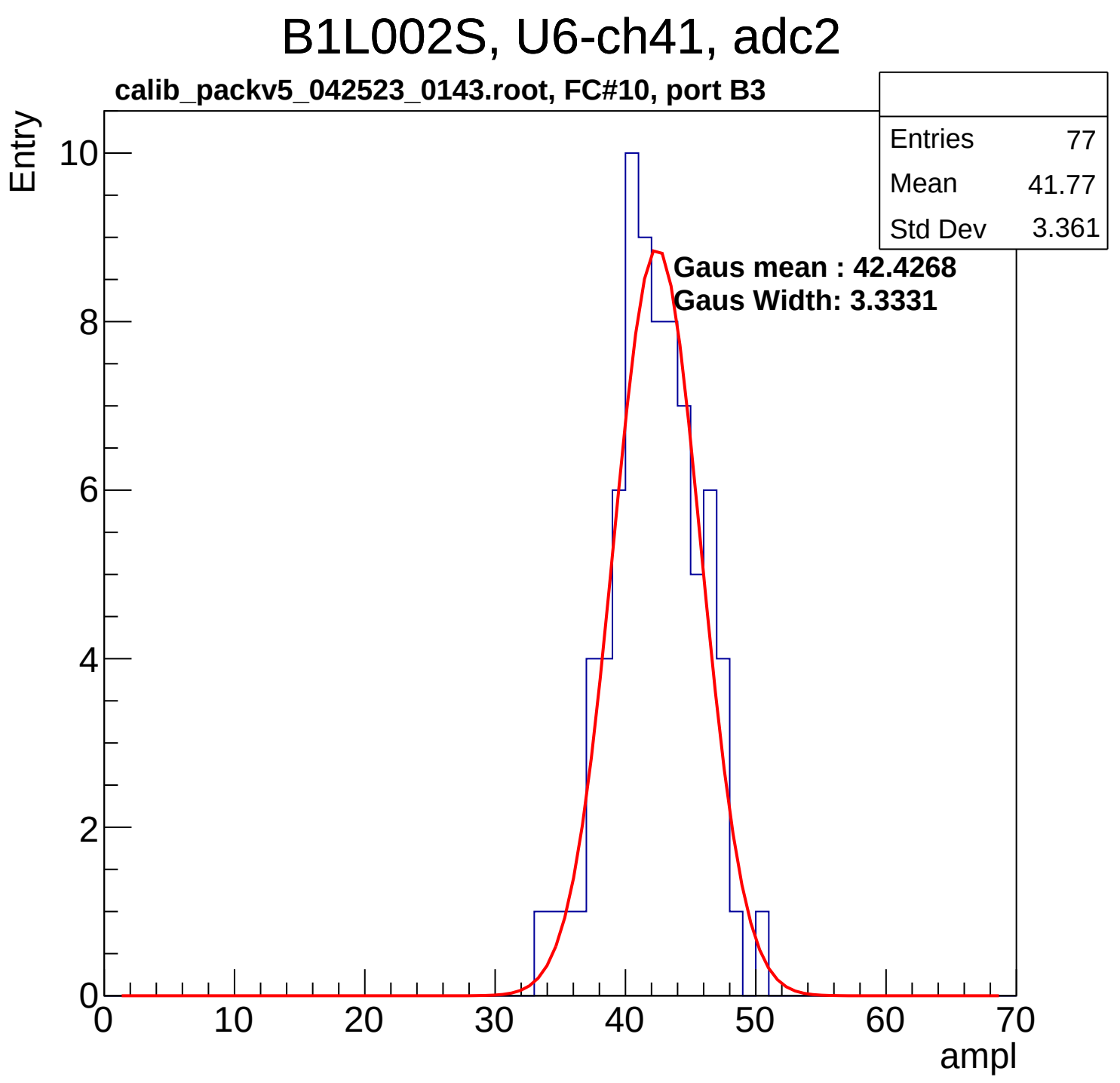

B1L002S, U6-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.16
Std Dev 2.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

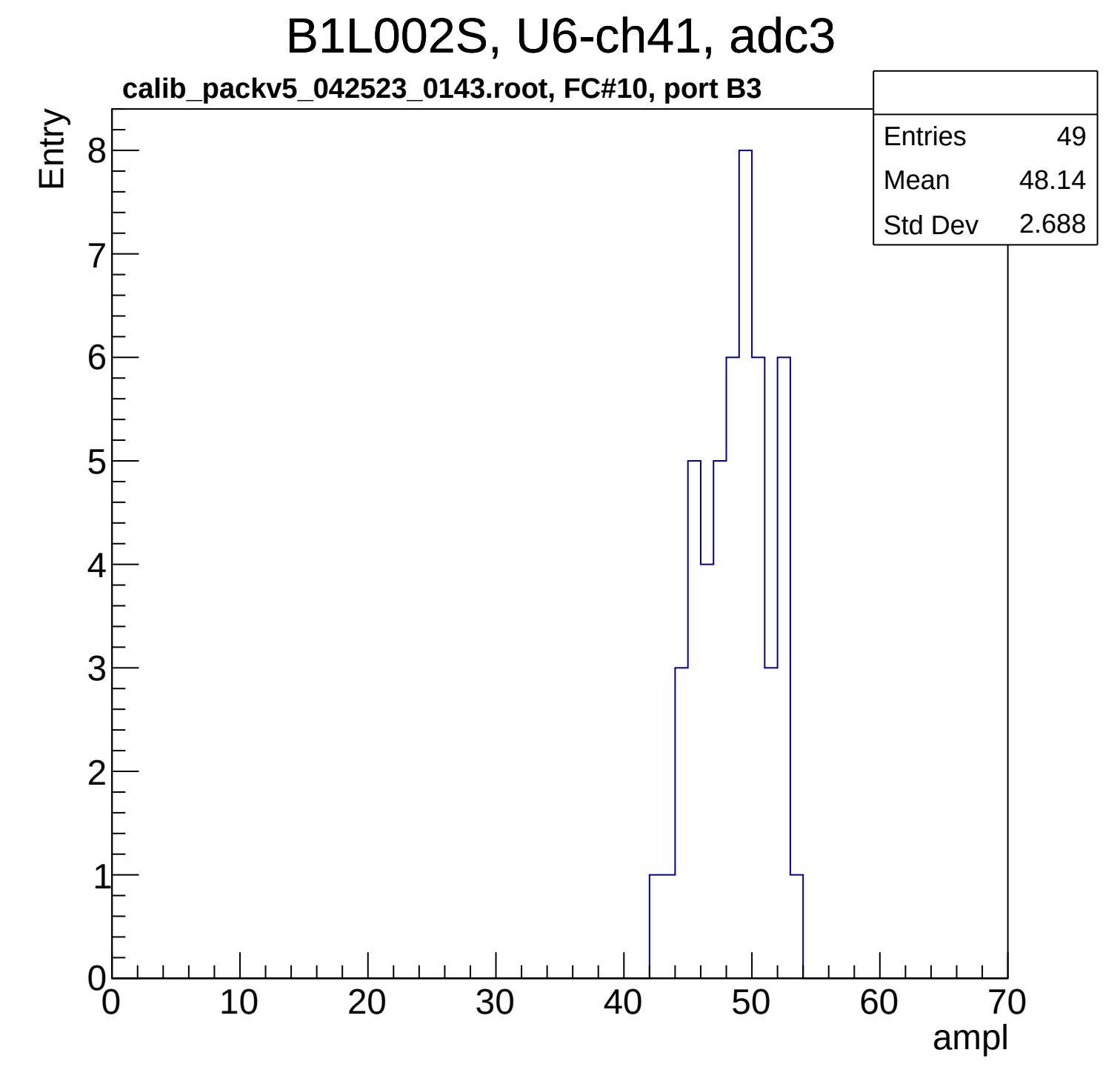

B1L002S, U6-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.36
Std Dev 17.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

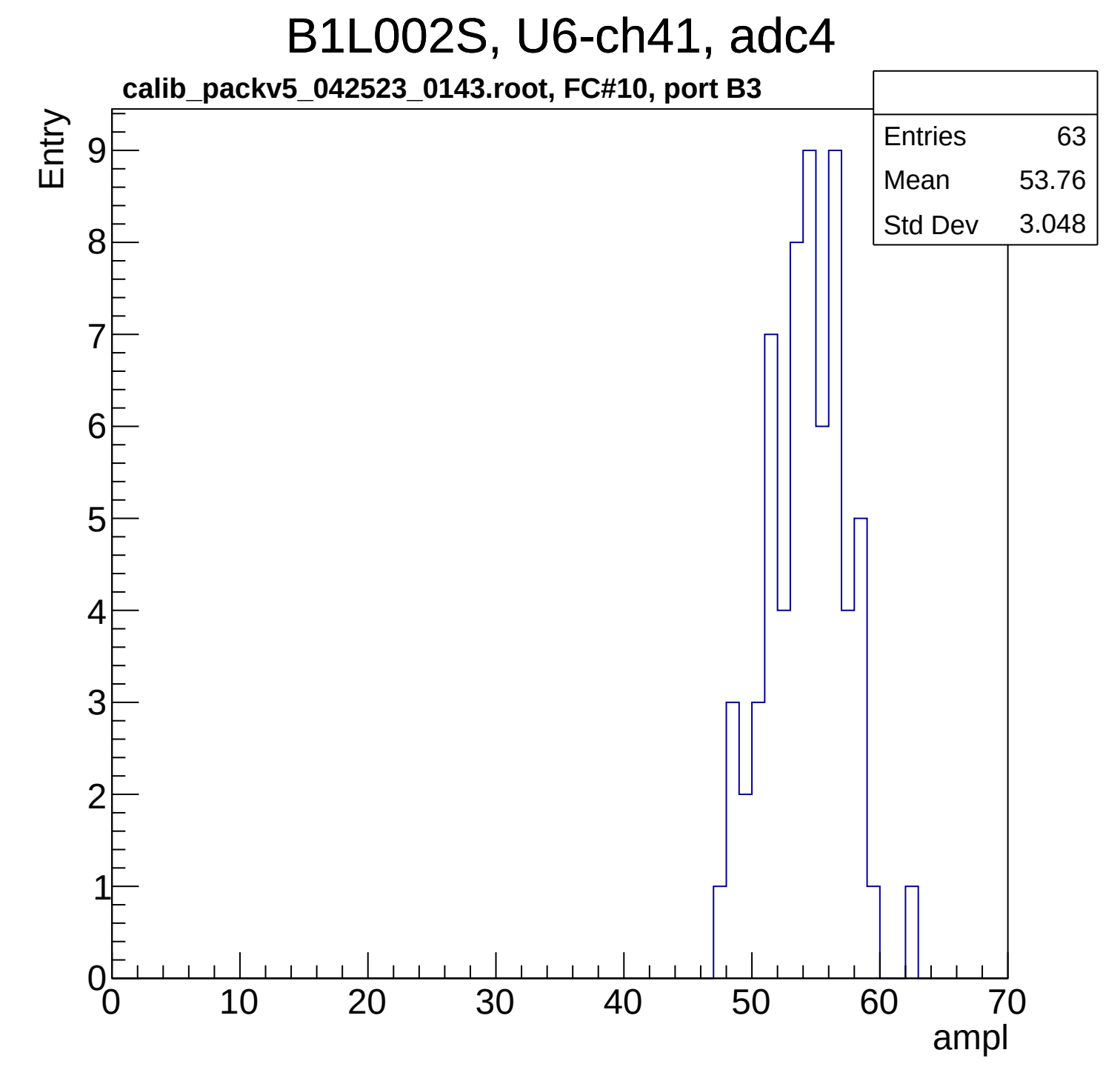

B1L002S, U6-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

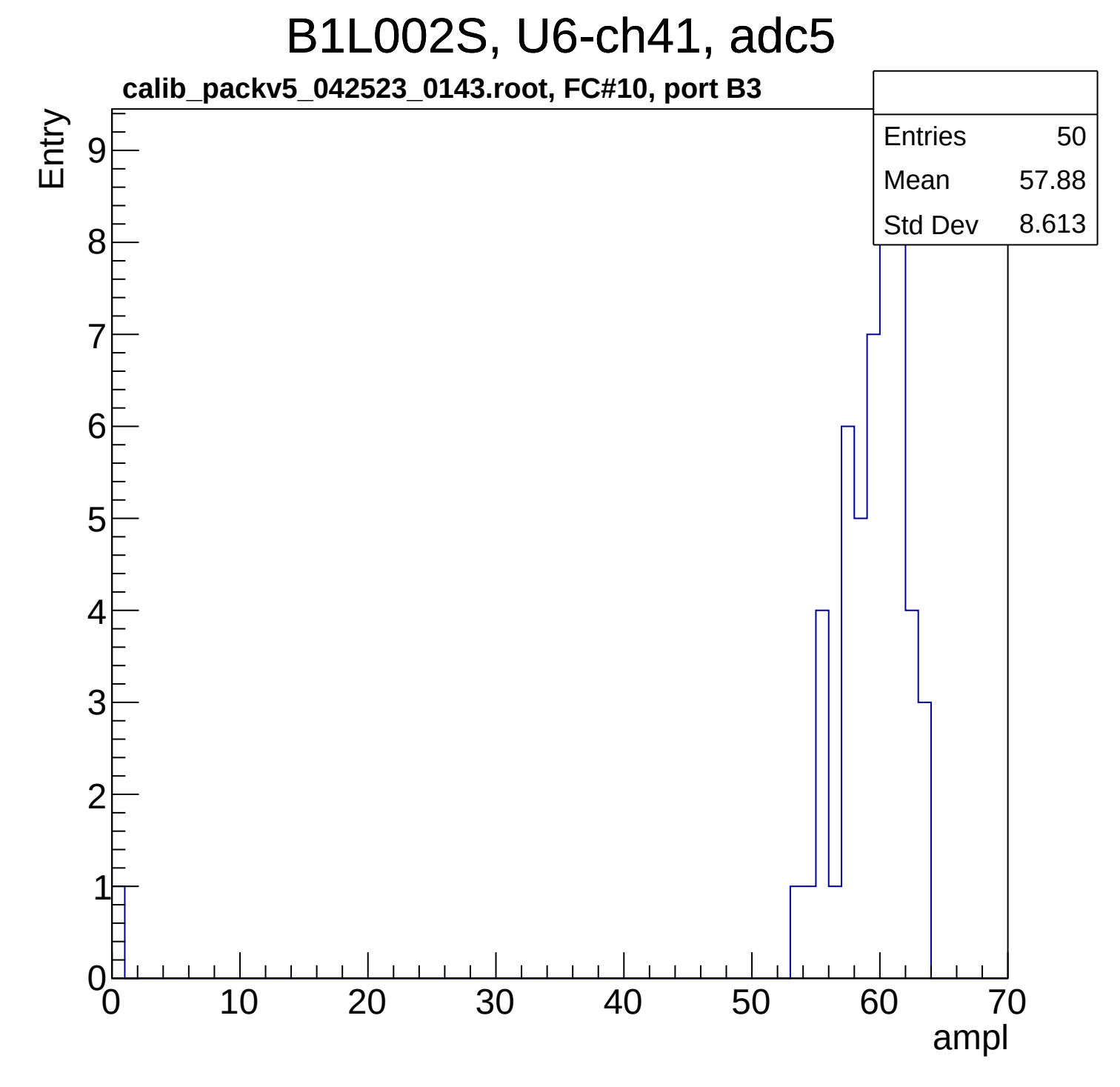

B1L002S, U6-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.11
Std Dev 2.764
Gaus mean : 29.4245
Gaus Width: 3.0617
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

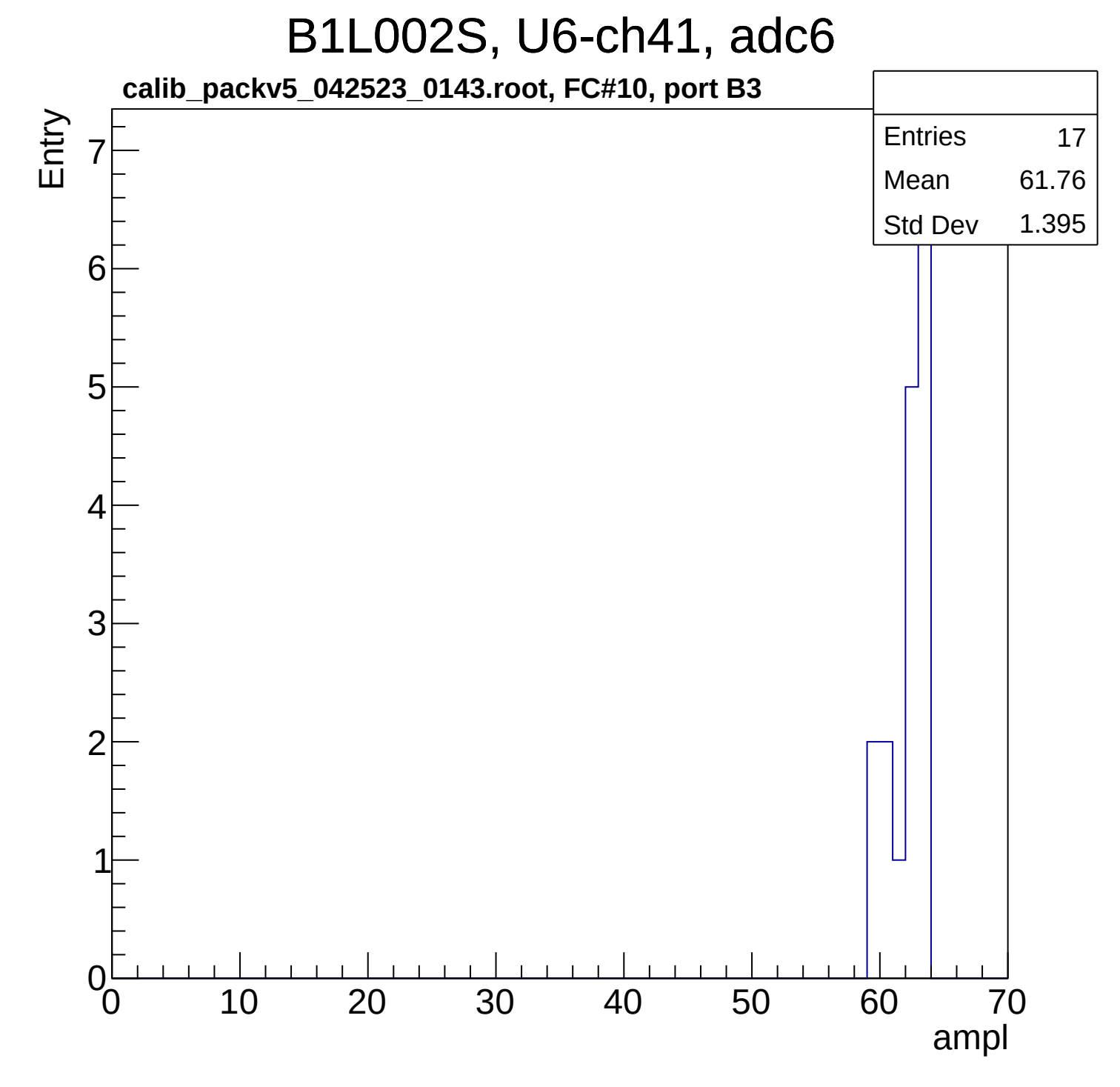

B1L002S, U6-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.98
Std Dev 3.202
Gaus mean : 35.7349
Gaus Width: 3.3421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

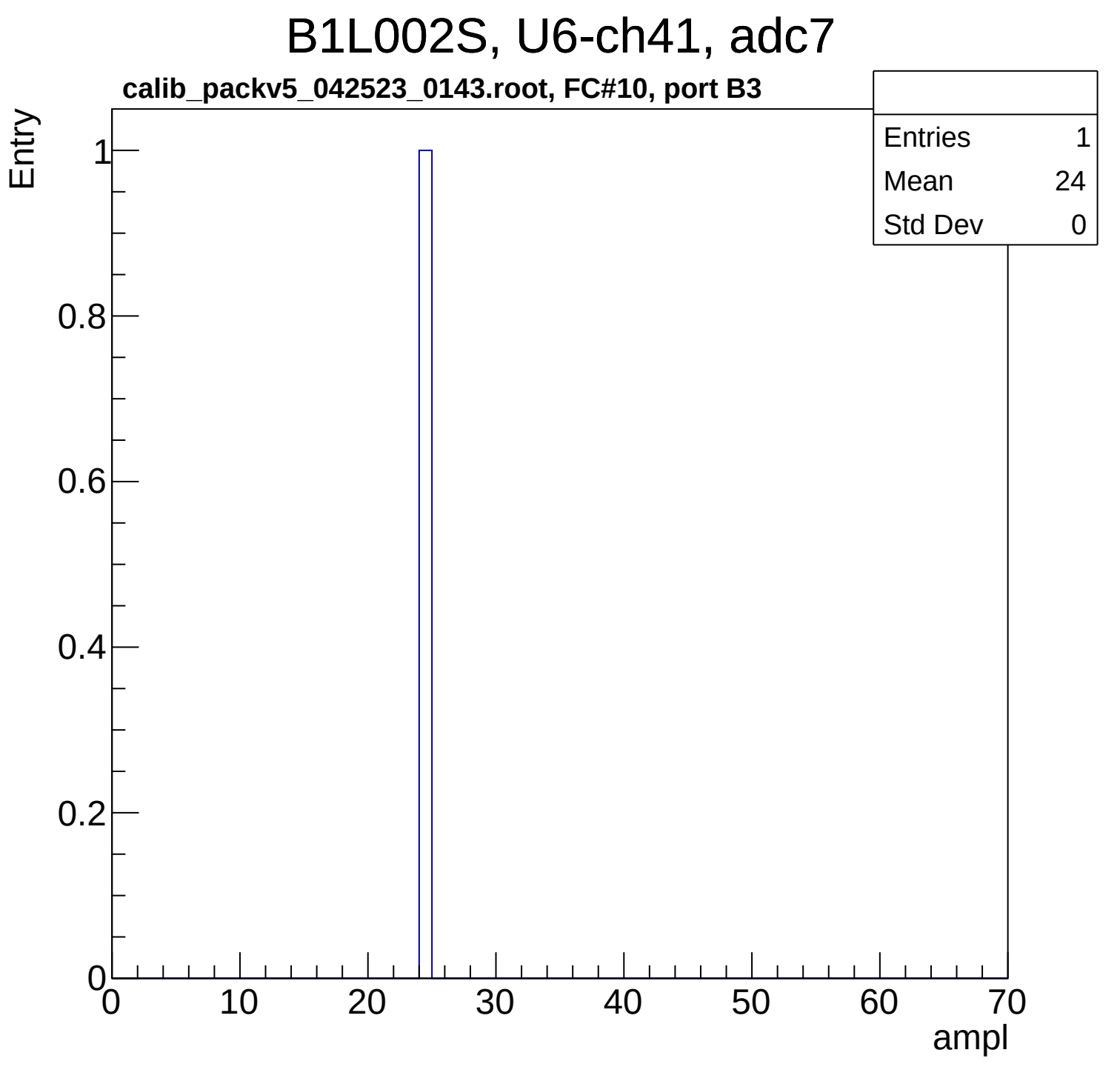

B1L002S, U6-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41.77
Std Dev 3.361
Gaus mean : 42.4268
Gaus Width: 3.3331
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

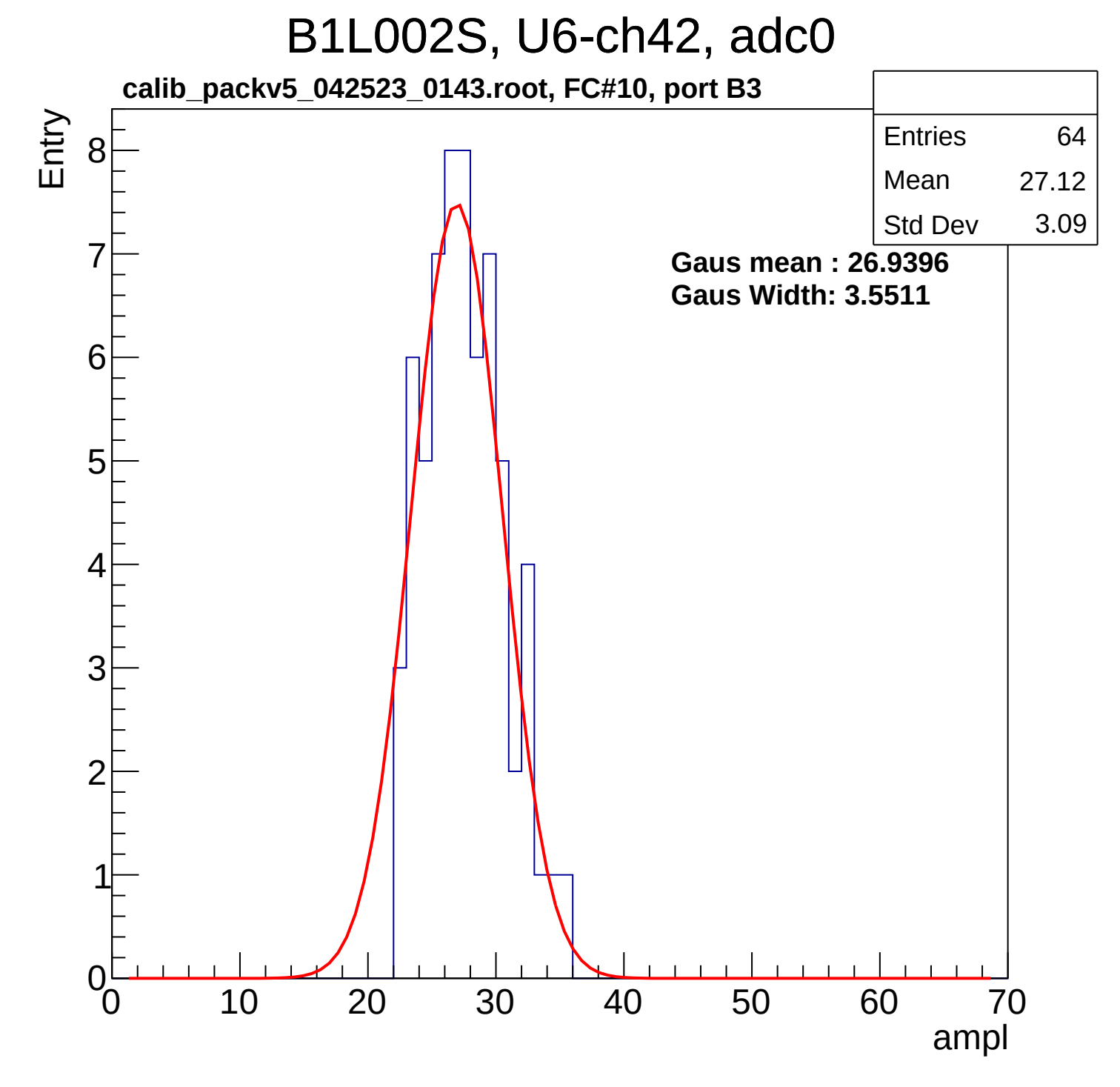

B1L002S, U6-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 48.14
Std Dev 2.688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

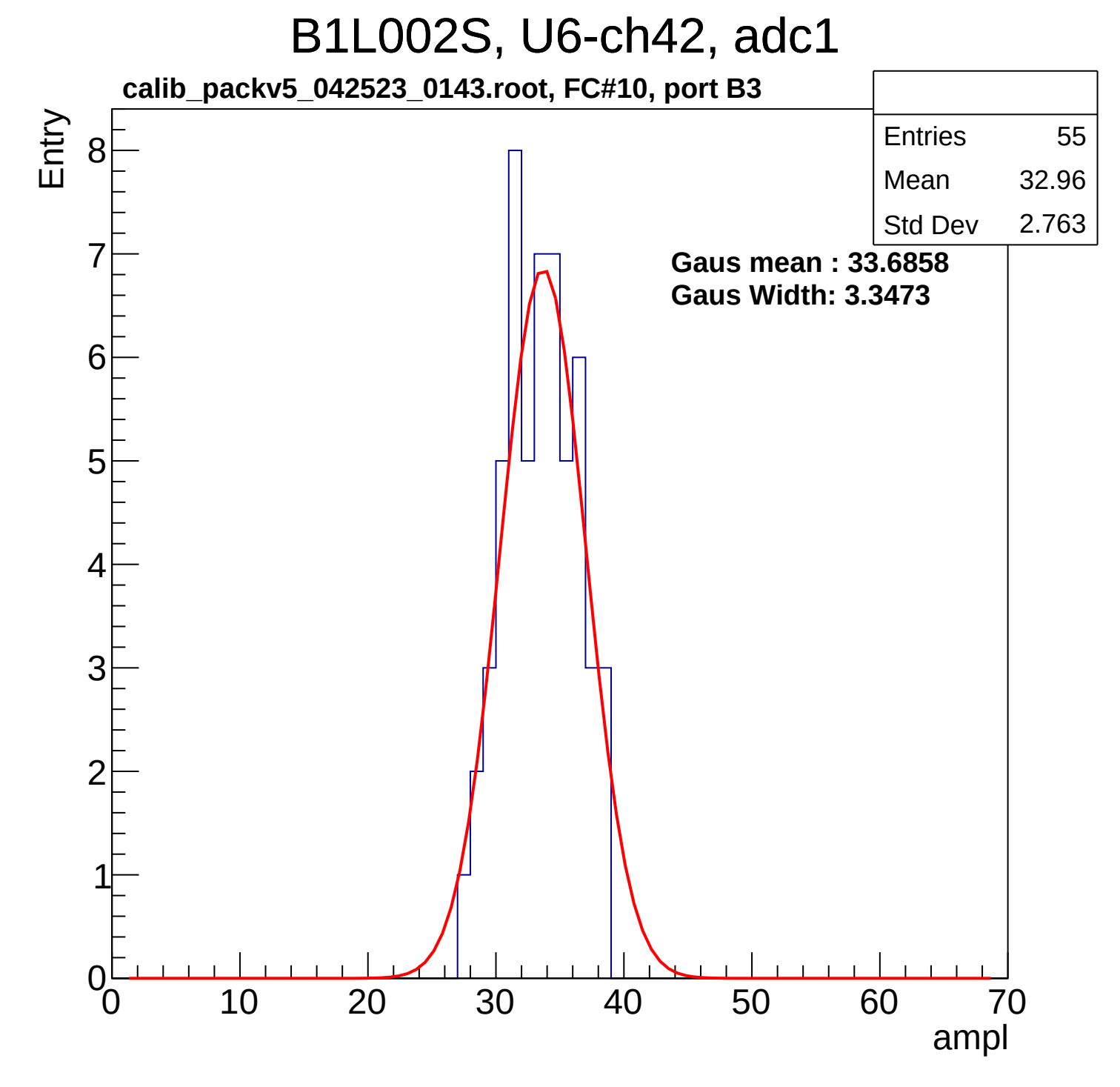

B1L002S, U6-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.76
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

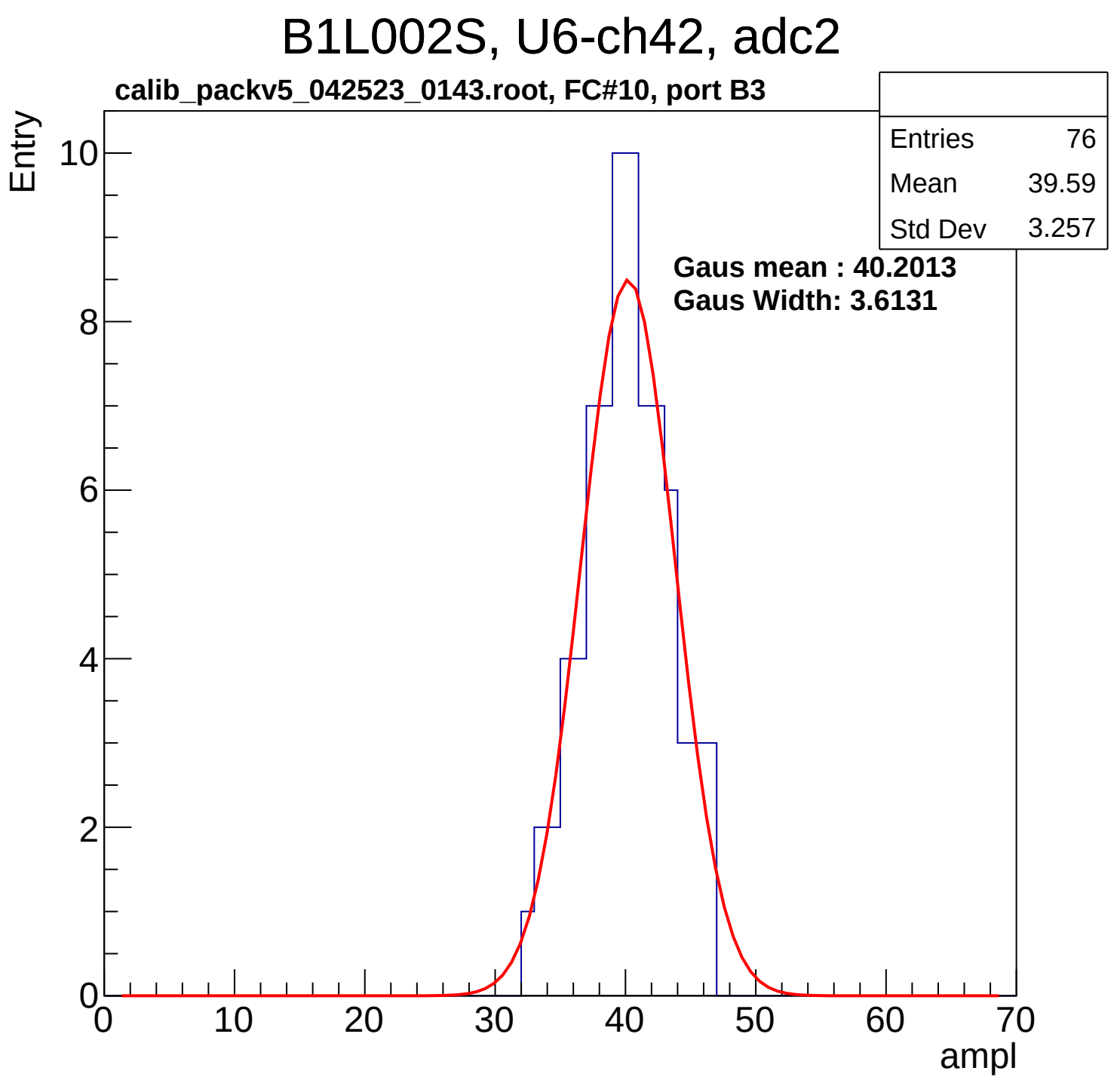

B1L002S, U6-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.88
Std Dev 8.613
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

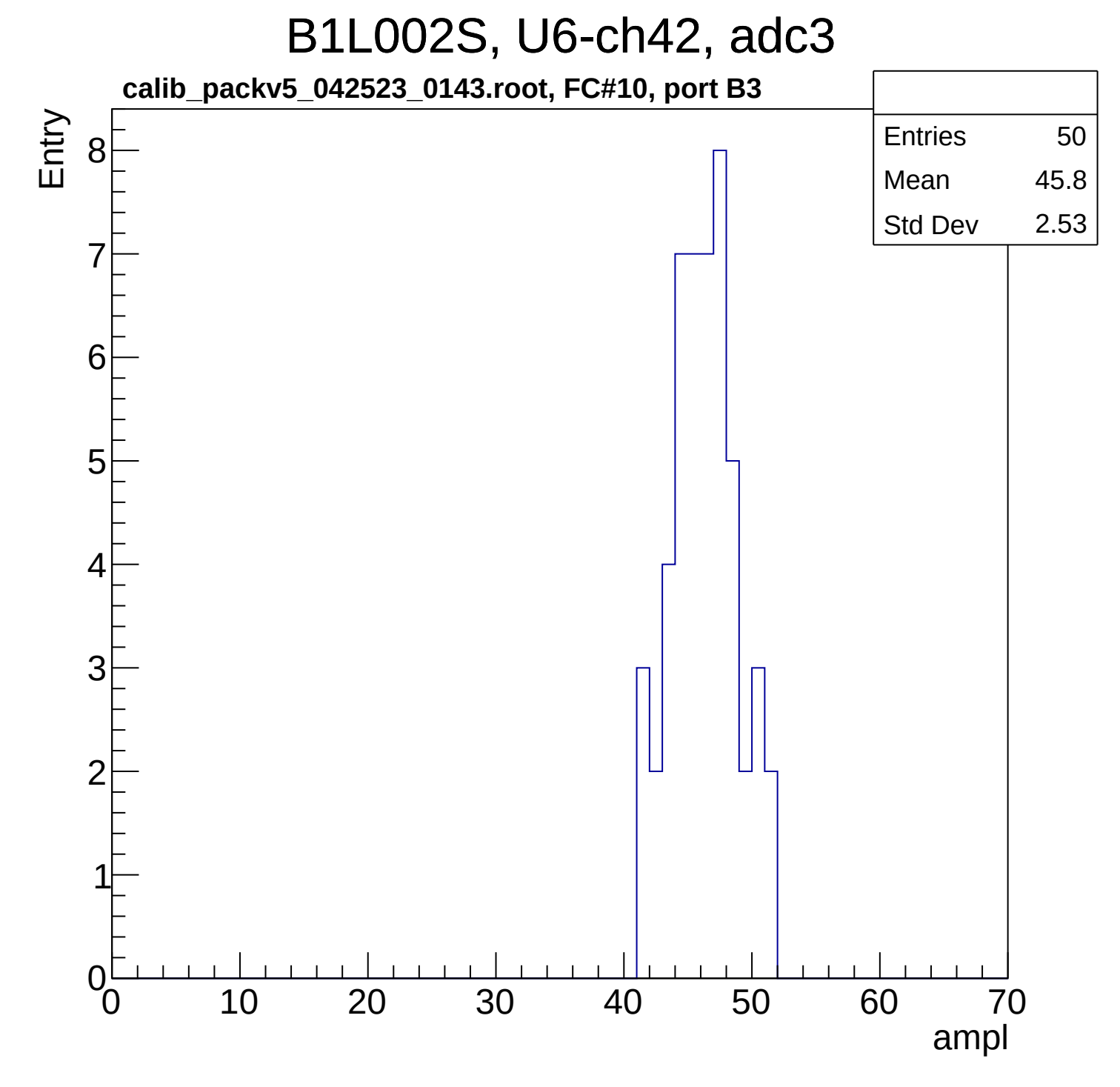

B1L002S, U6-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.76
Std Dev 1.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

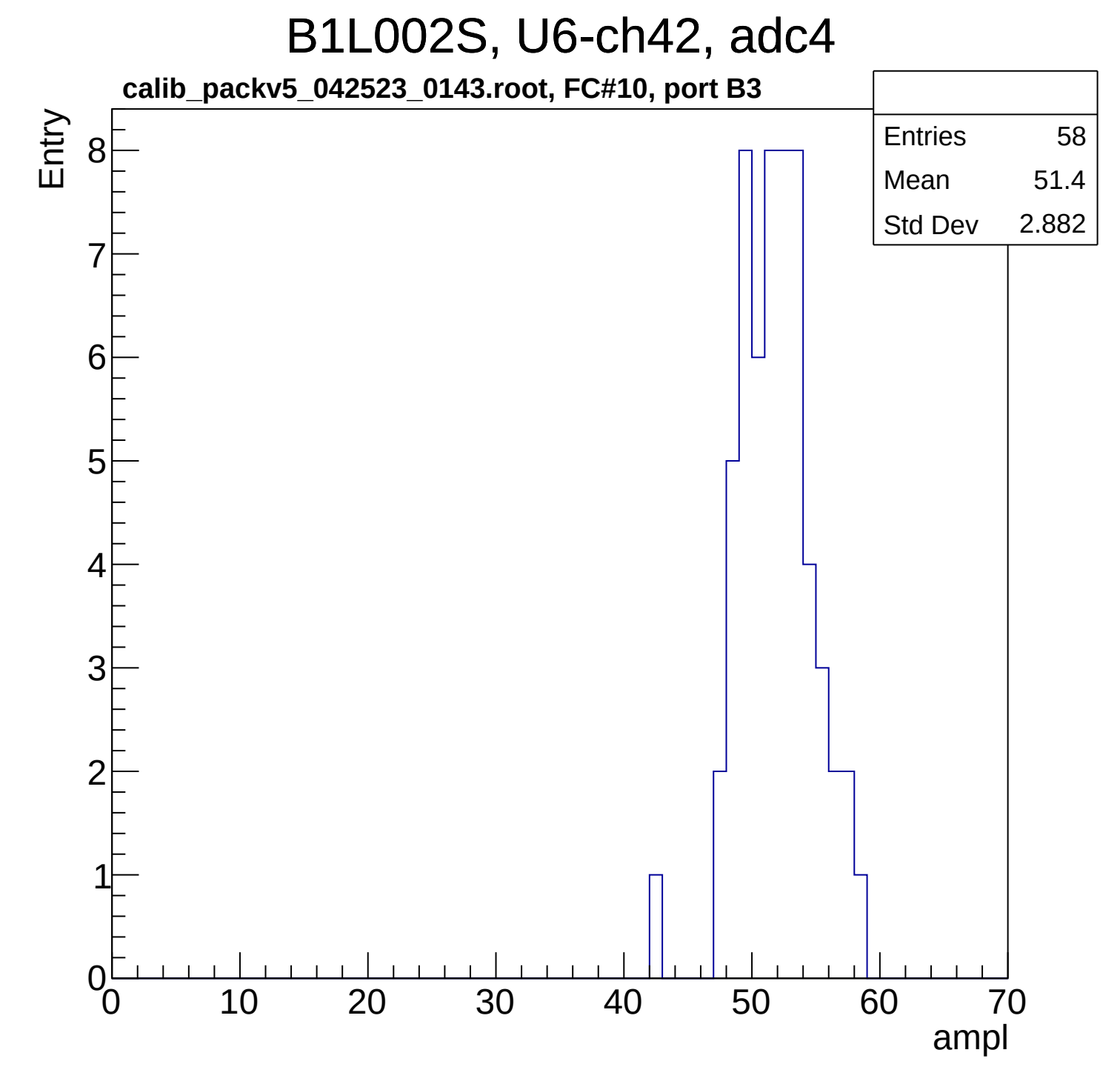

B1L002S, U6-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

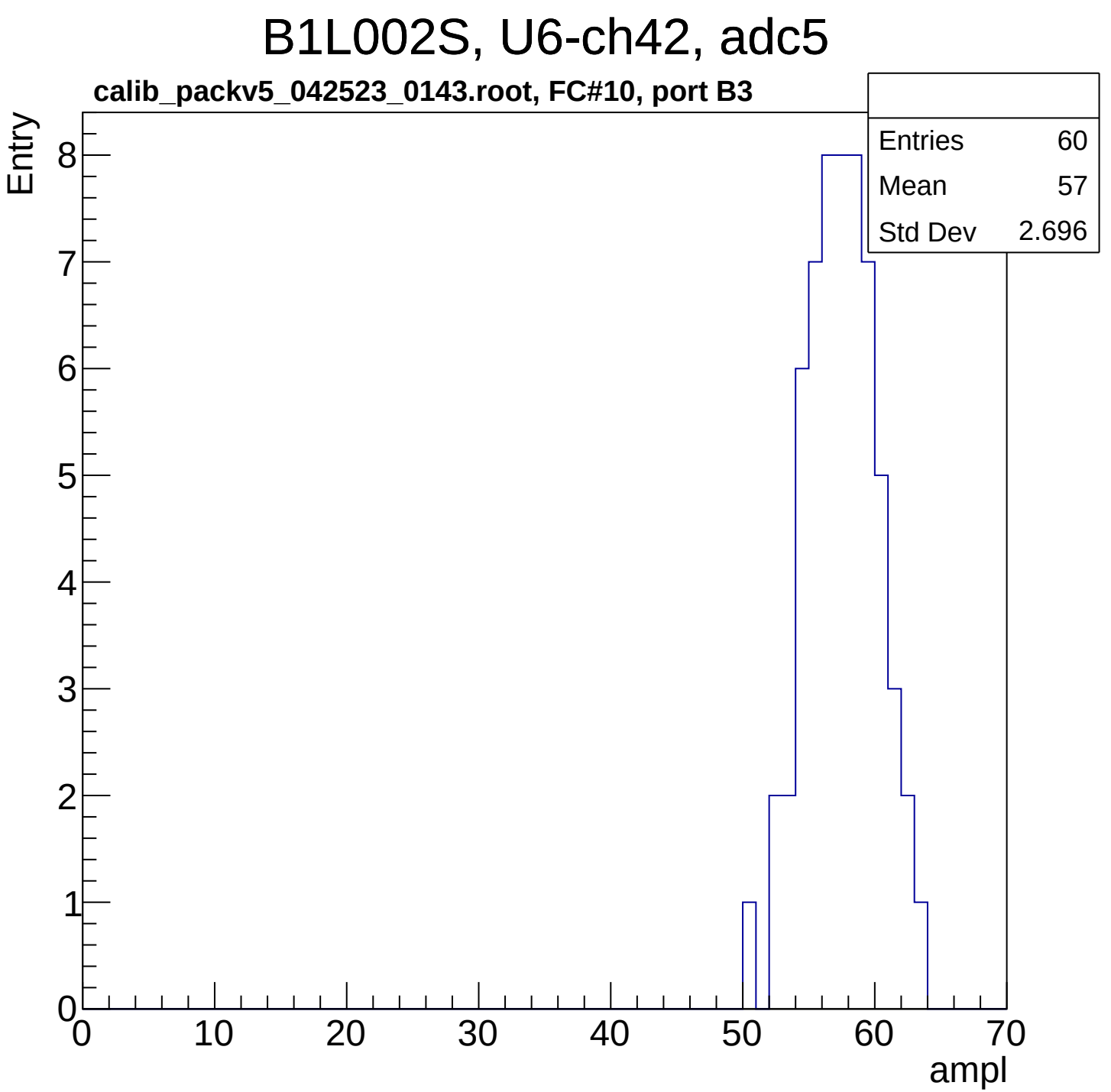

B1L002S, U6-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.12
Std Dev 3.09
Gaus mean : 26.9396
Gaus Width: 3.5511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

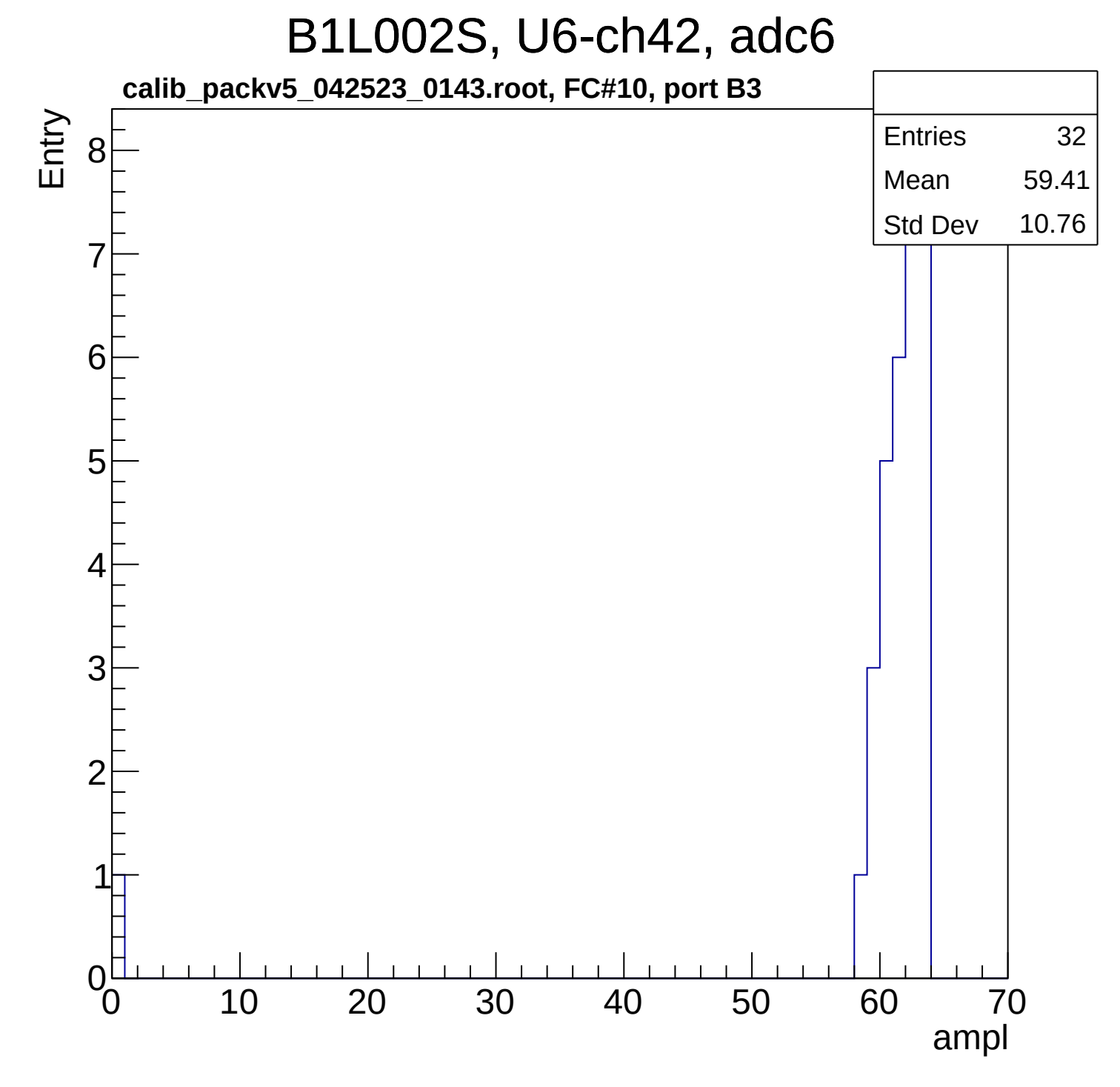

B1L002S, U6-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 32.96
Std Dev 2.763
Gaus mean : 33.6858
Gaus Width: 3.3473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

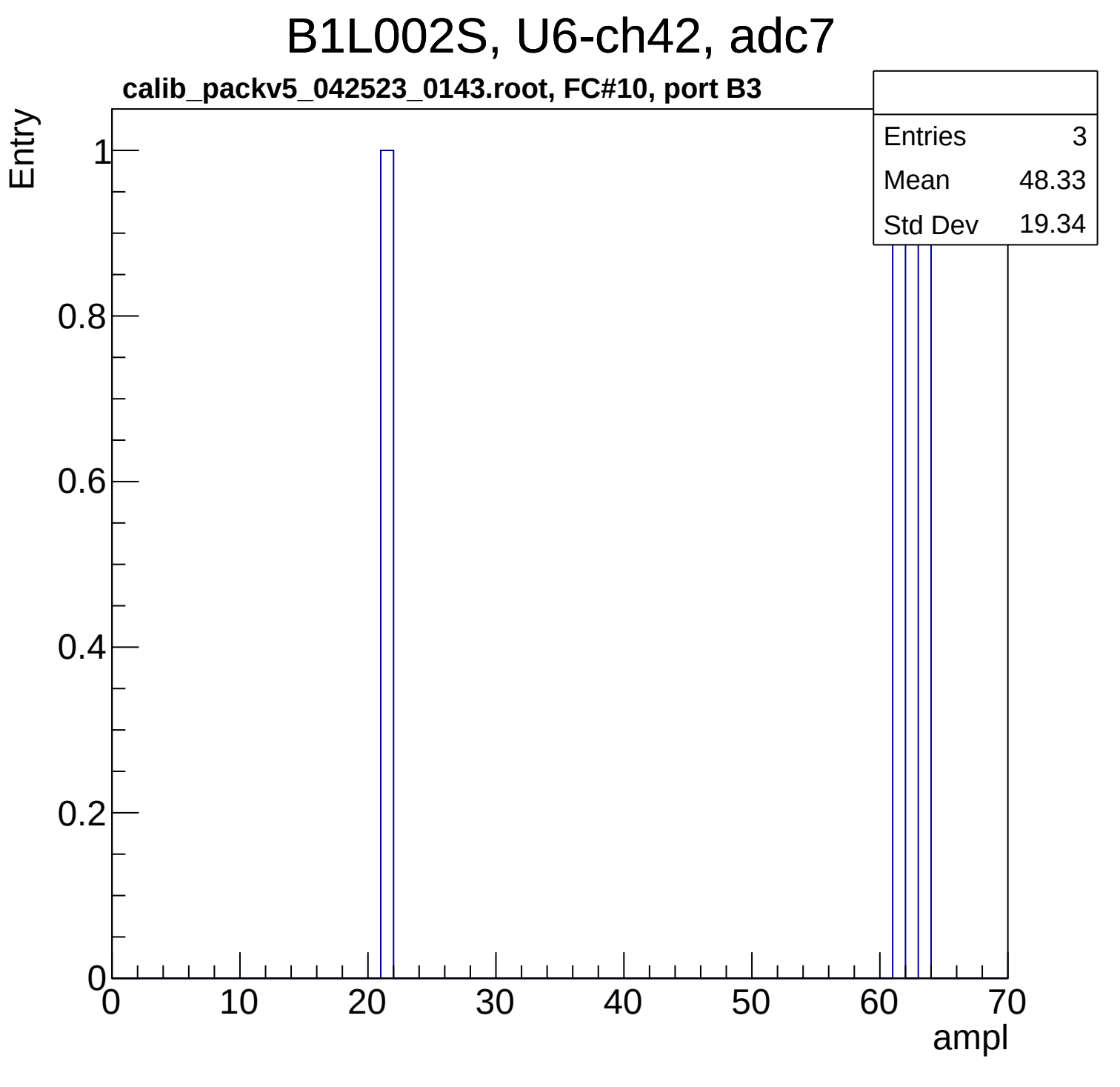

B1L002S, U6-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 39.59
Std Dev 3.257
Gaus mean : 40.2013
Gaus Width: 3.6131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

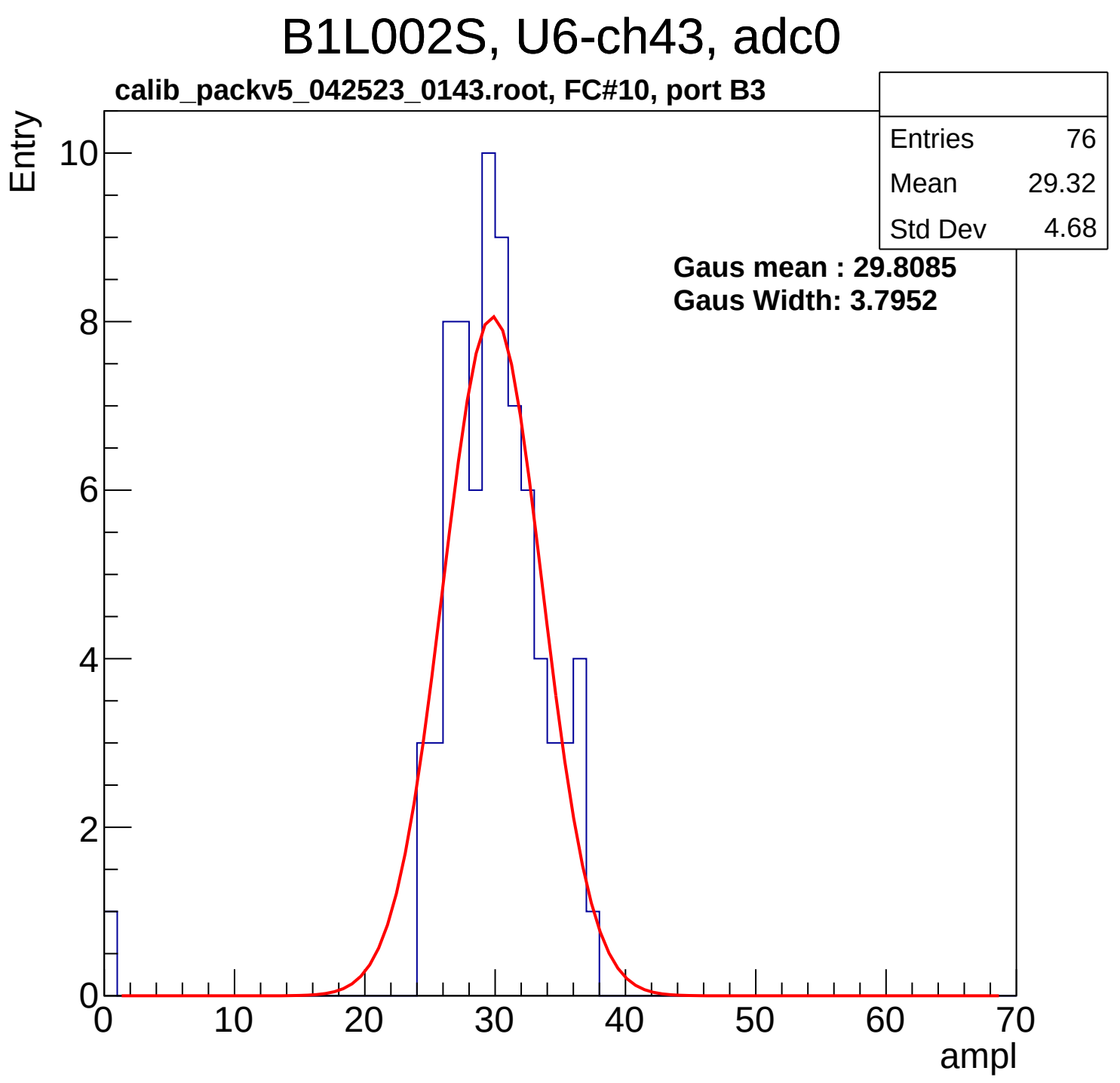

B1L002S, U6-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 45.8
Std Dev 2.53
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

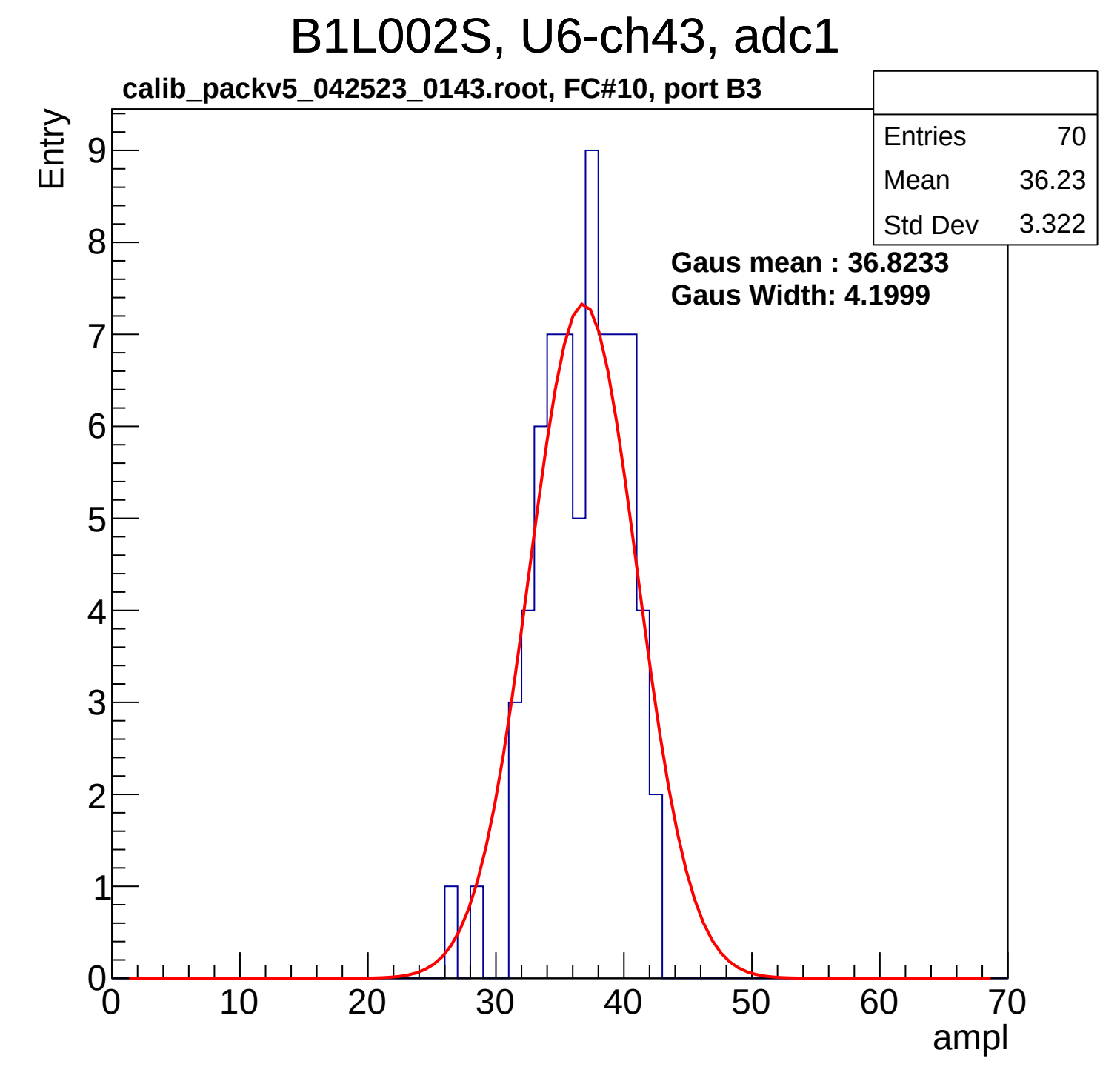

B1L002S, U6-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 51.4
Std Dev 2.882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

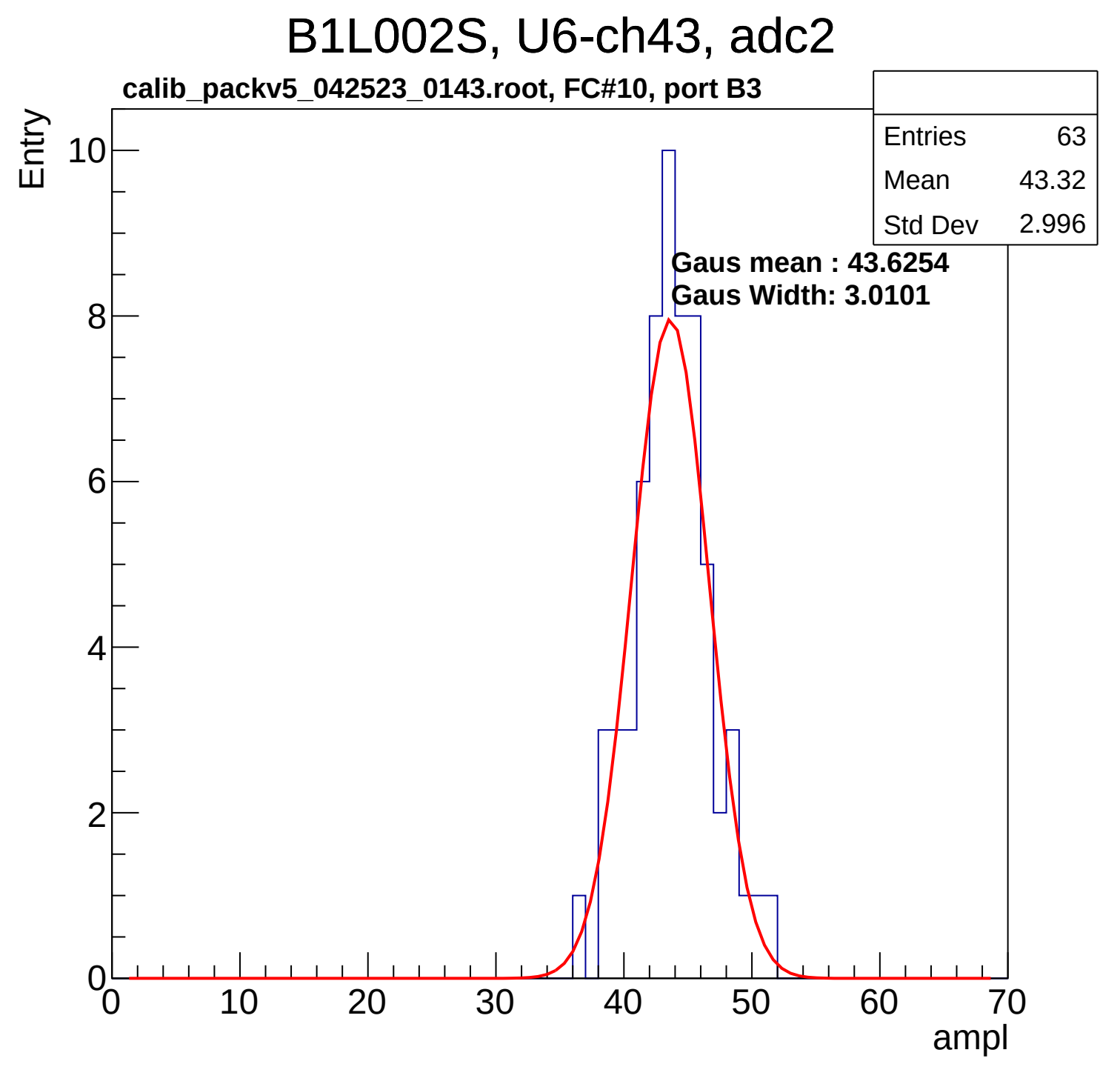

B1L002S, U6-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57
Std Dev 2.696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

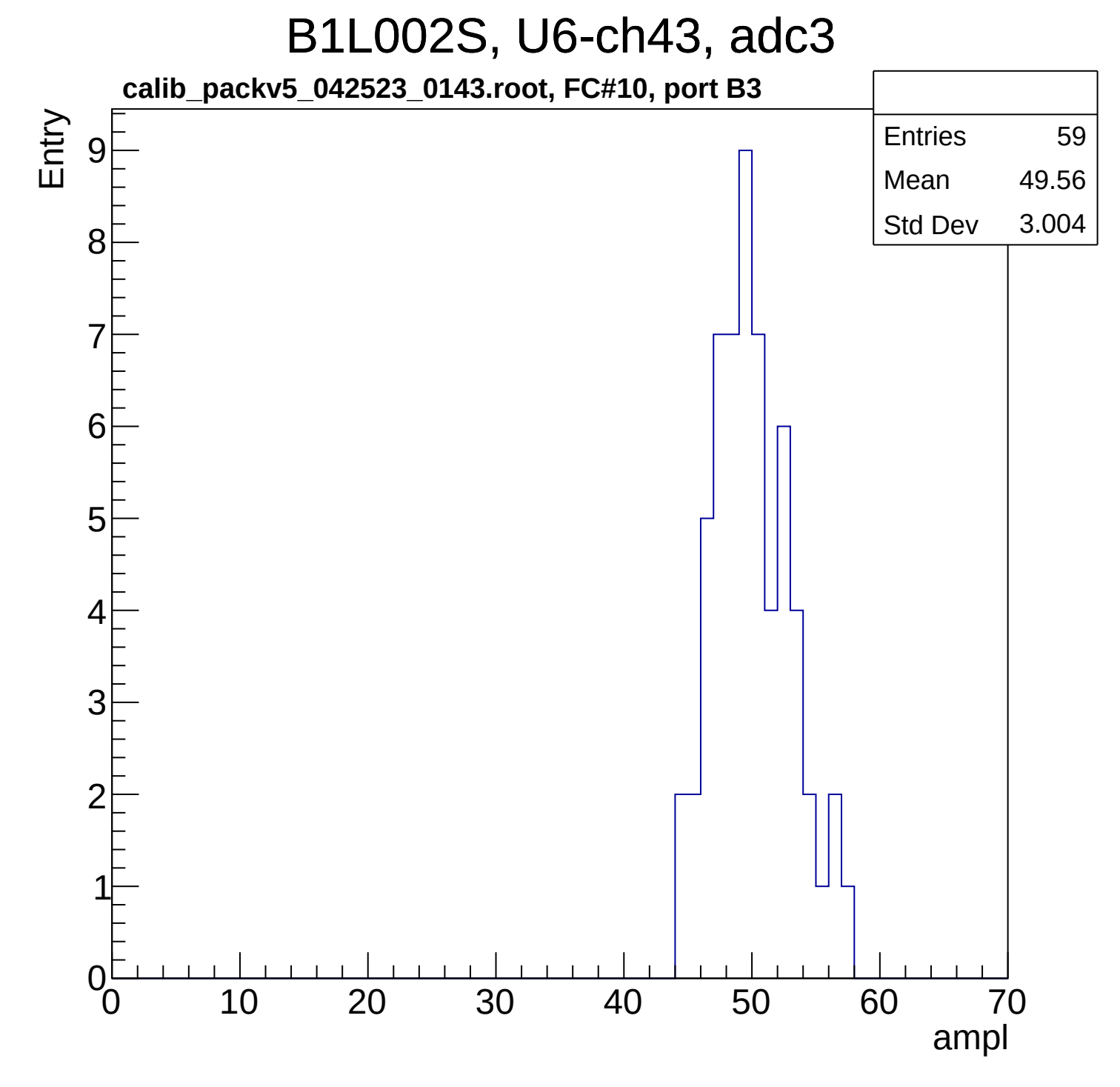

B1L002S, U6-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 59.41
Std Dev 10.76
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

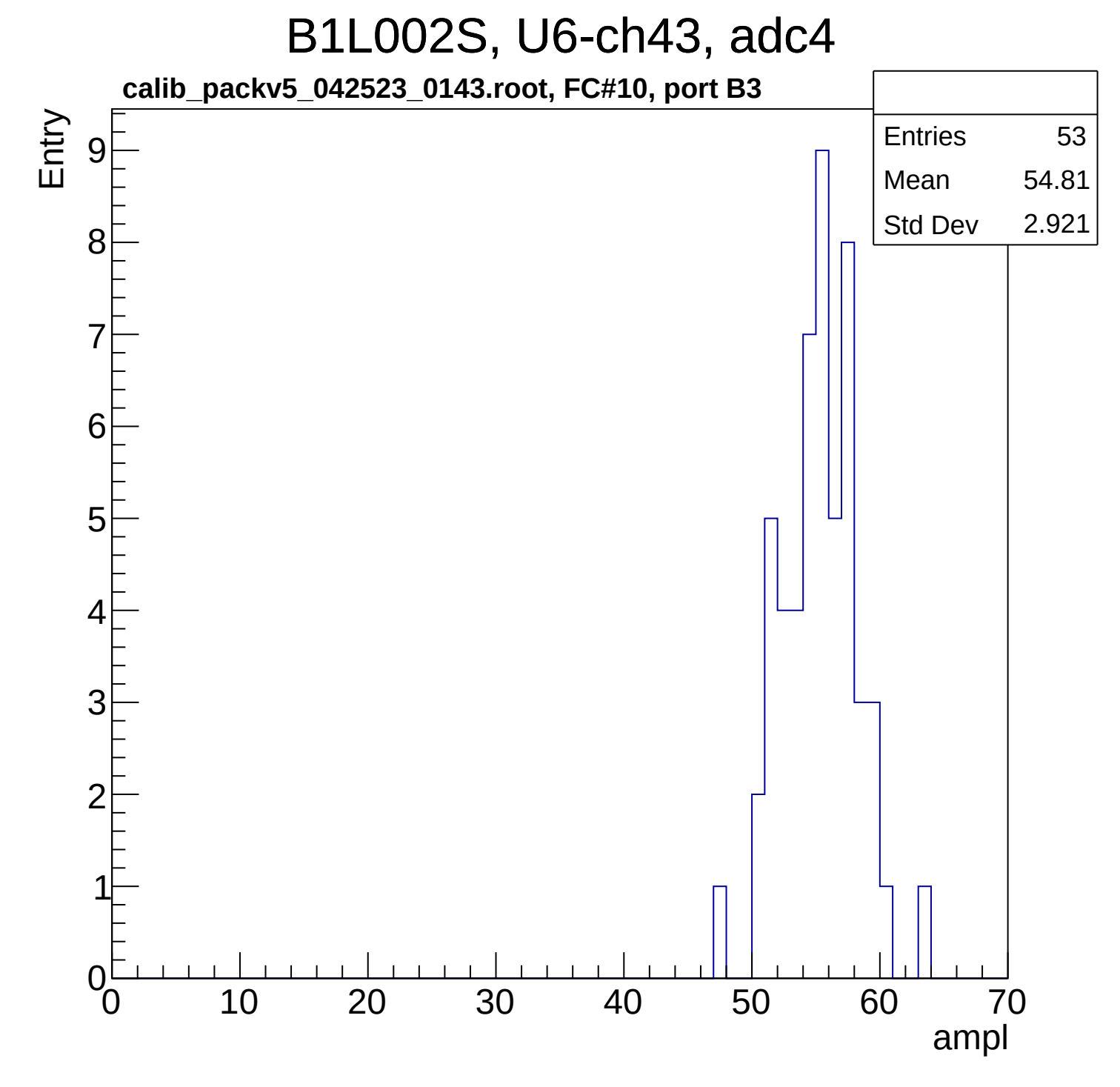

B1L002S, U6-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 19.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

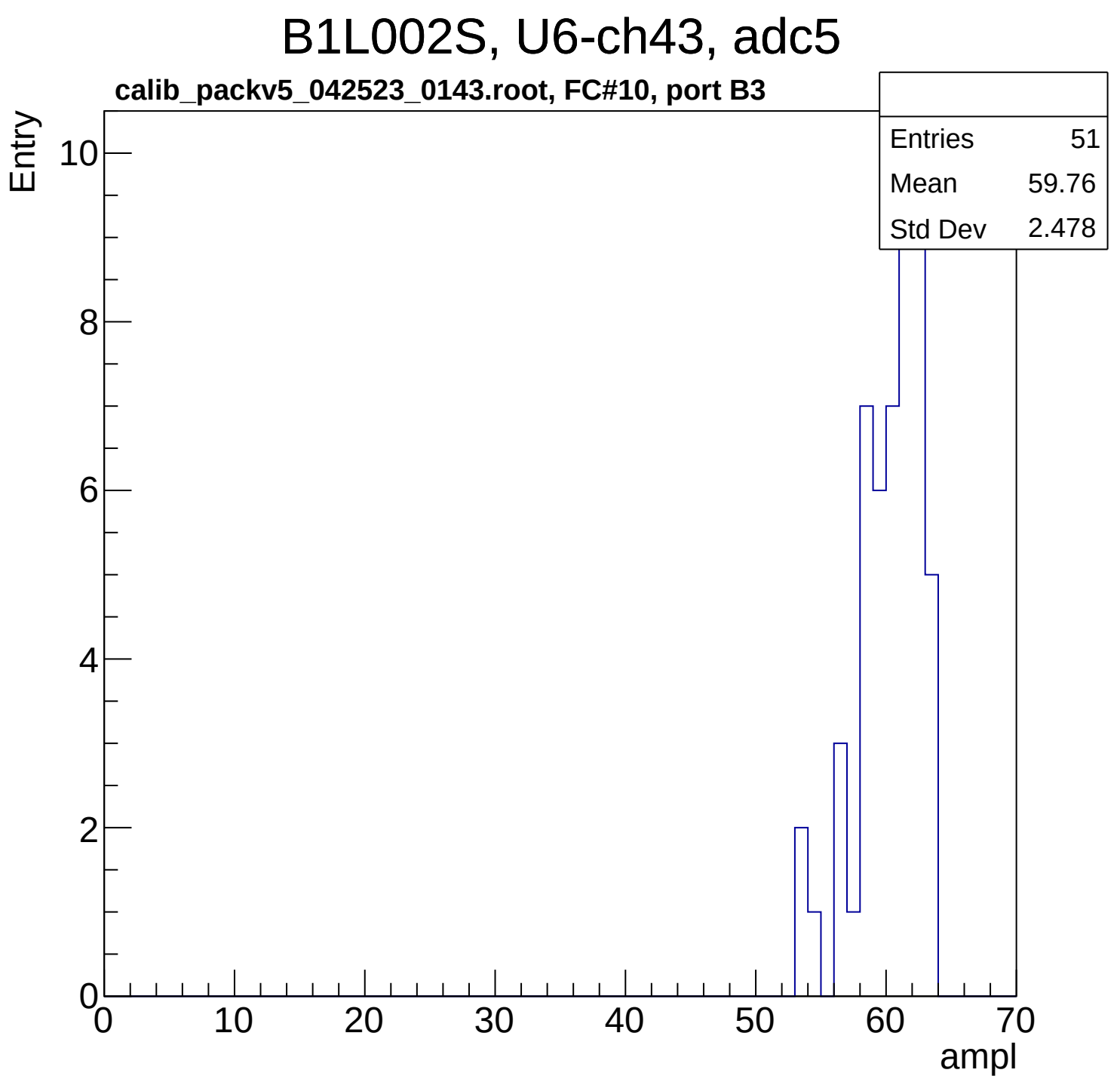

B1L002S, U6-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 29.32
Std Dev 4.68
Gaus mean : 29.8085
Gaus Width: 3.7952
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

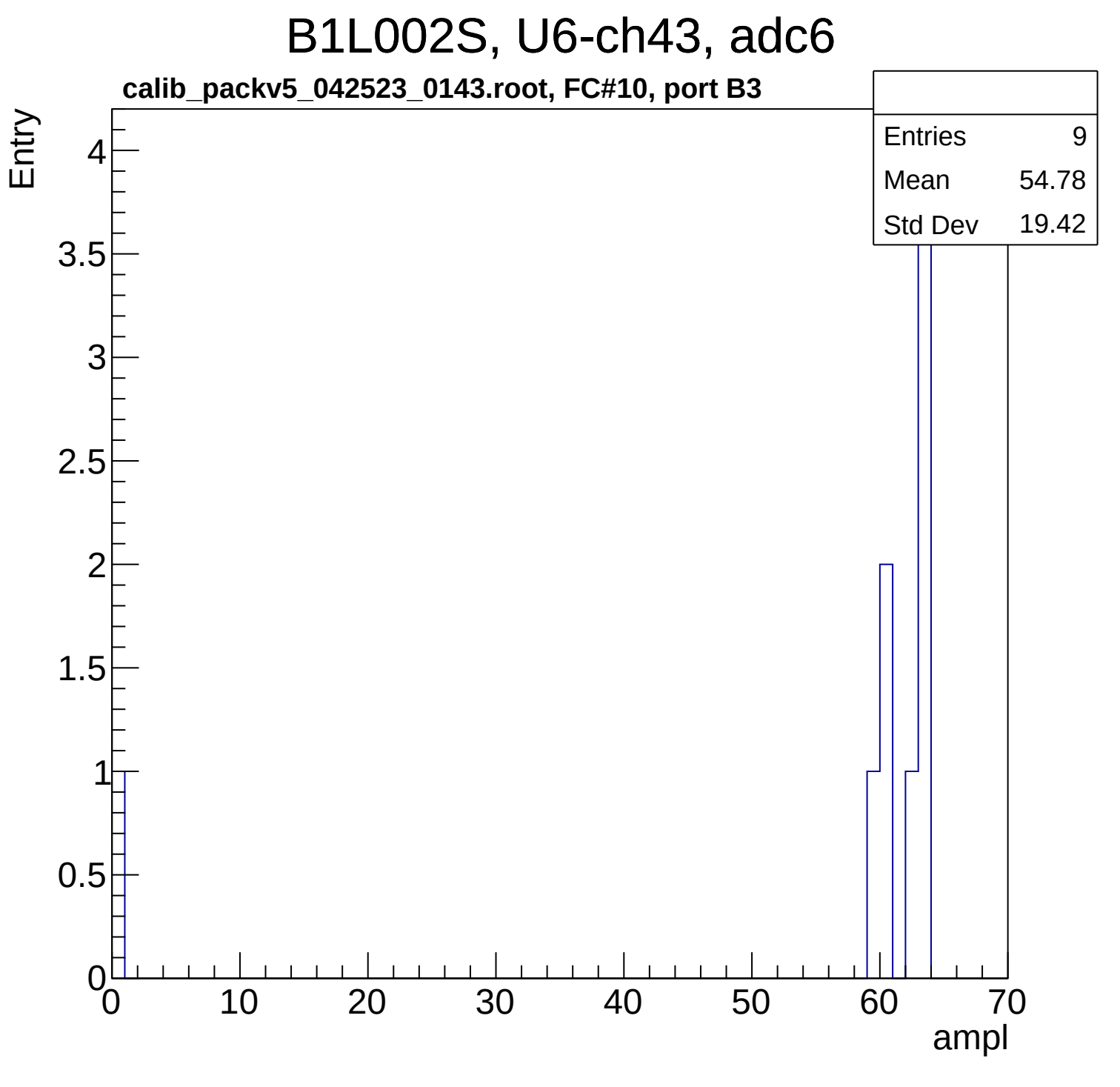

B1L002S, U6-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 36.23
Std Dev 3.322
Gaus mean : 36.8233
Gaus Width: 4.1999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

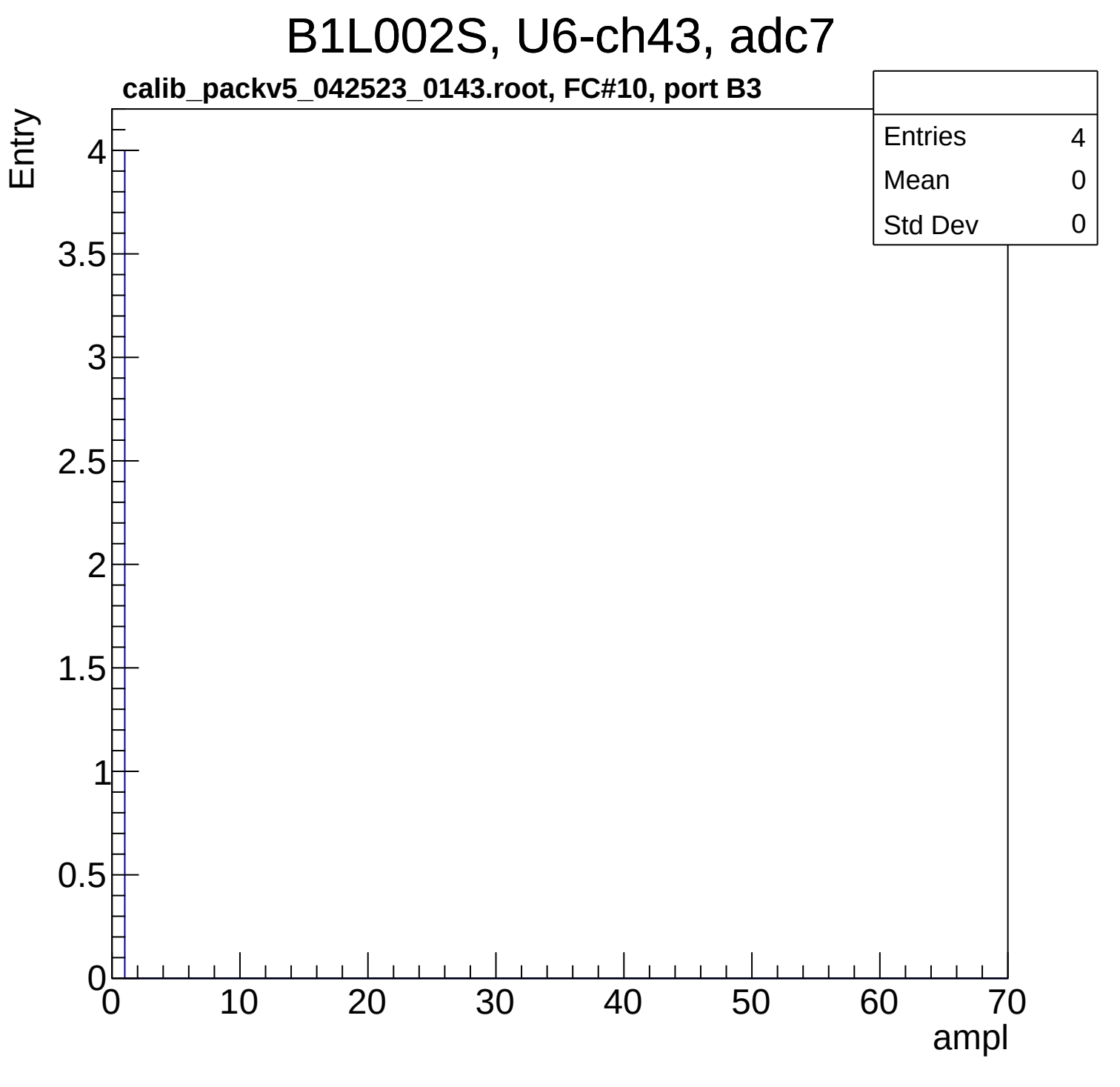

B1L002S, U6-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 43.32
Std Dev 2.996
Gaus mean : 43.6254
Gaus Width: 3.0101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

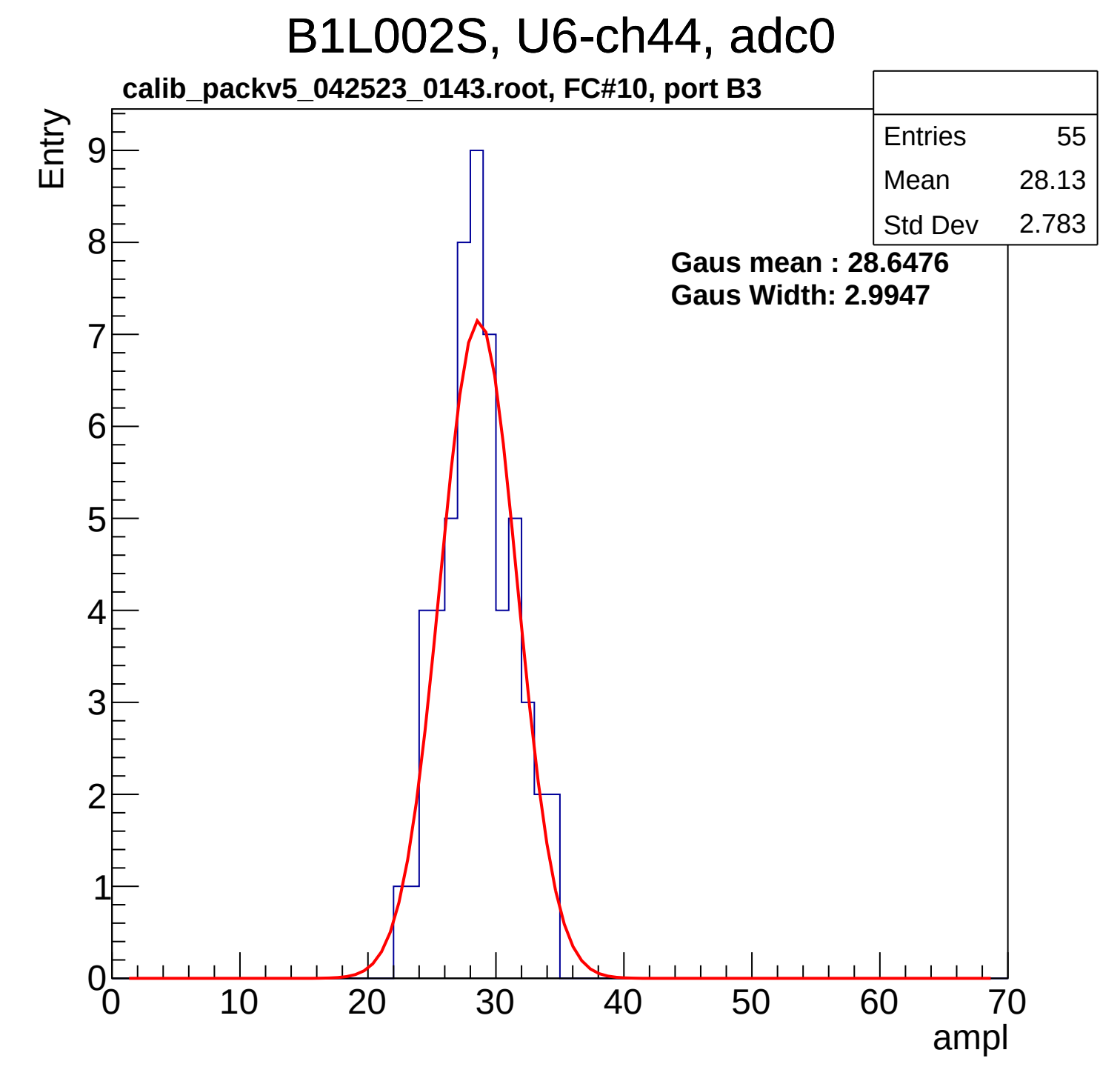

B1L002S, U6-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 49.56
Std Dev 3.004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

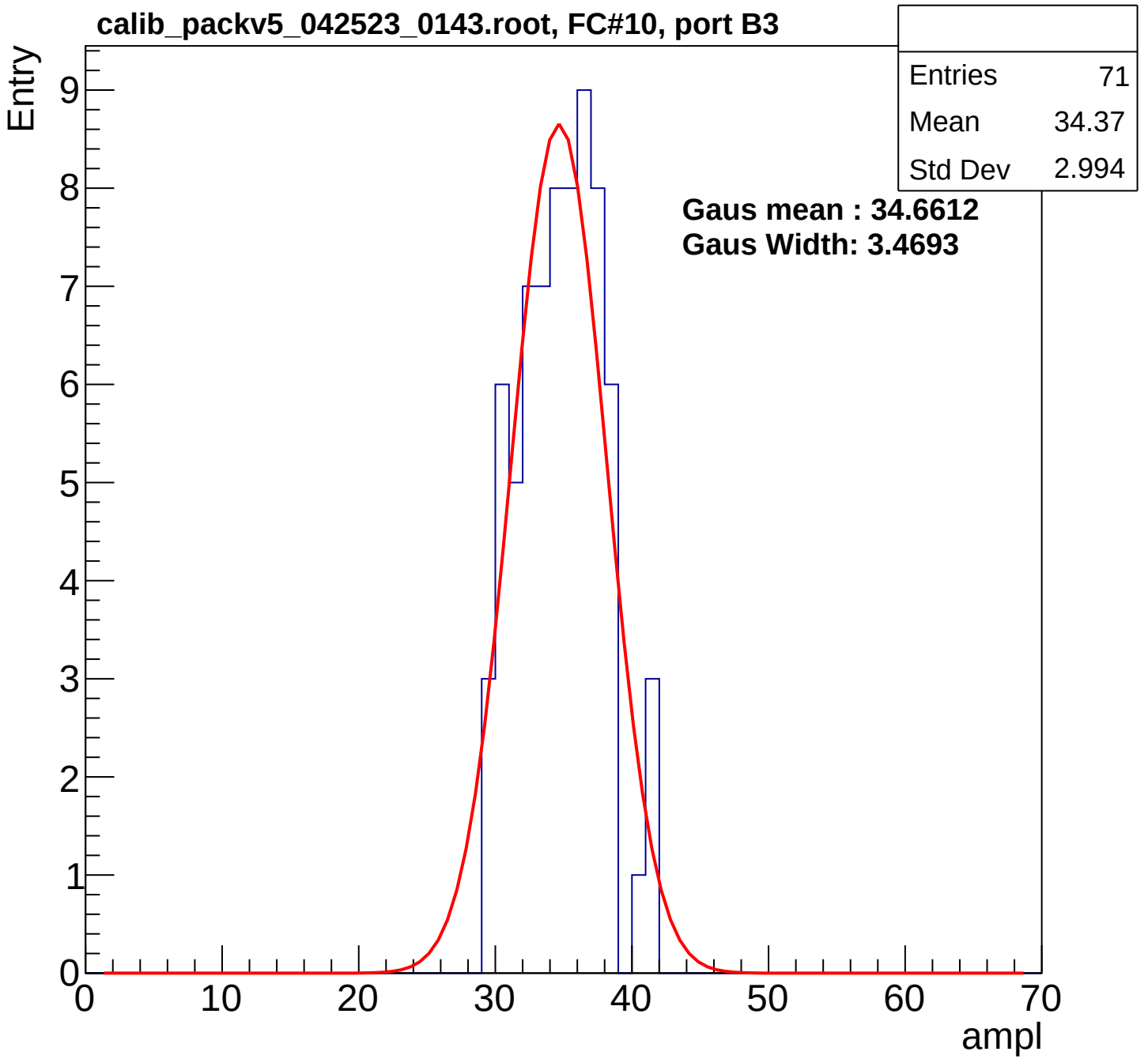

B1L002S, U6-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.81
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

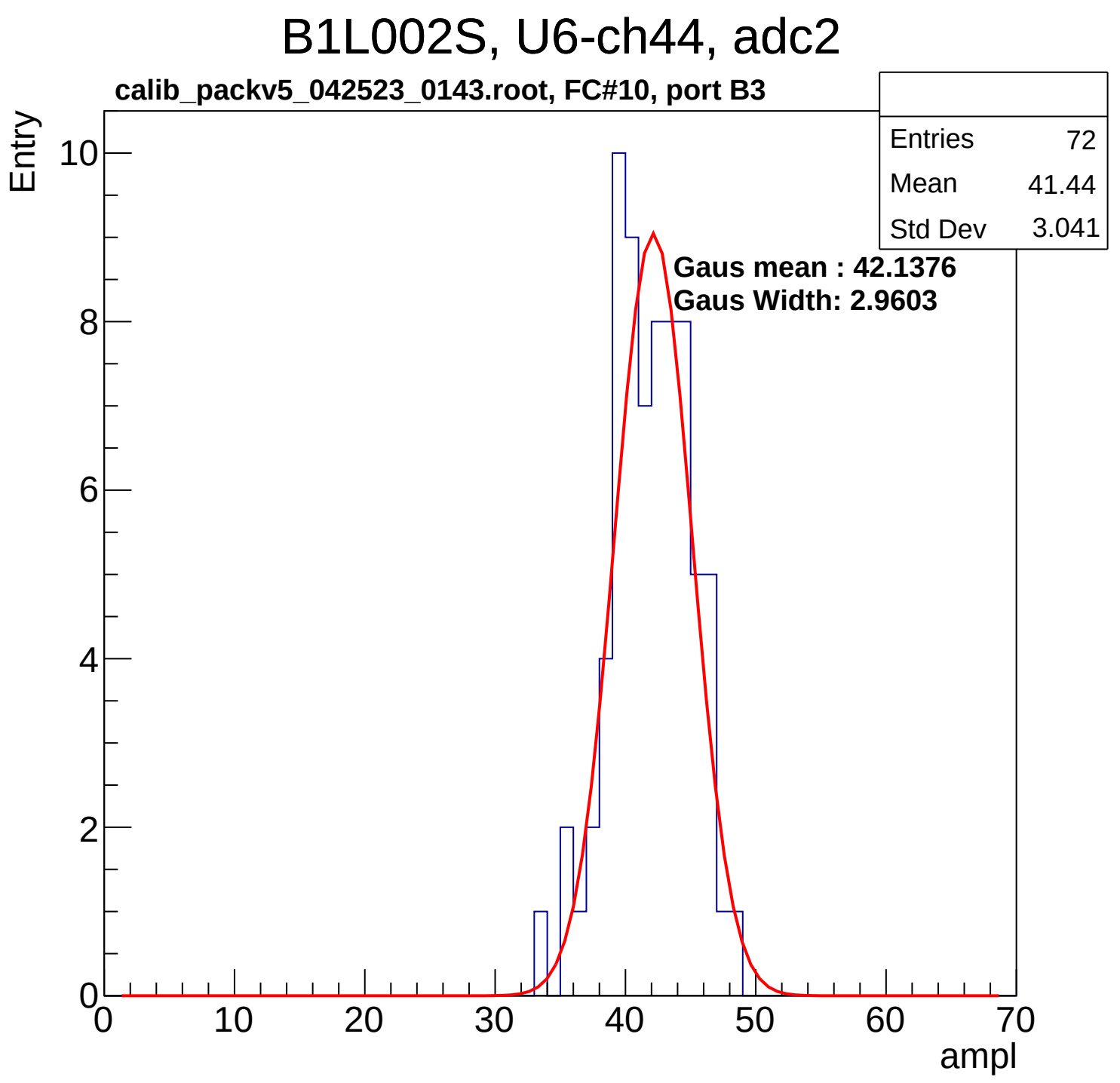

B1L002S, U6-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.76
Std Dev 2.478
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

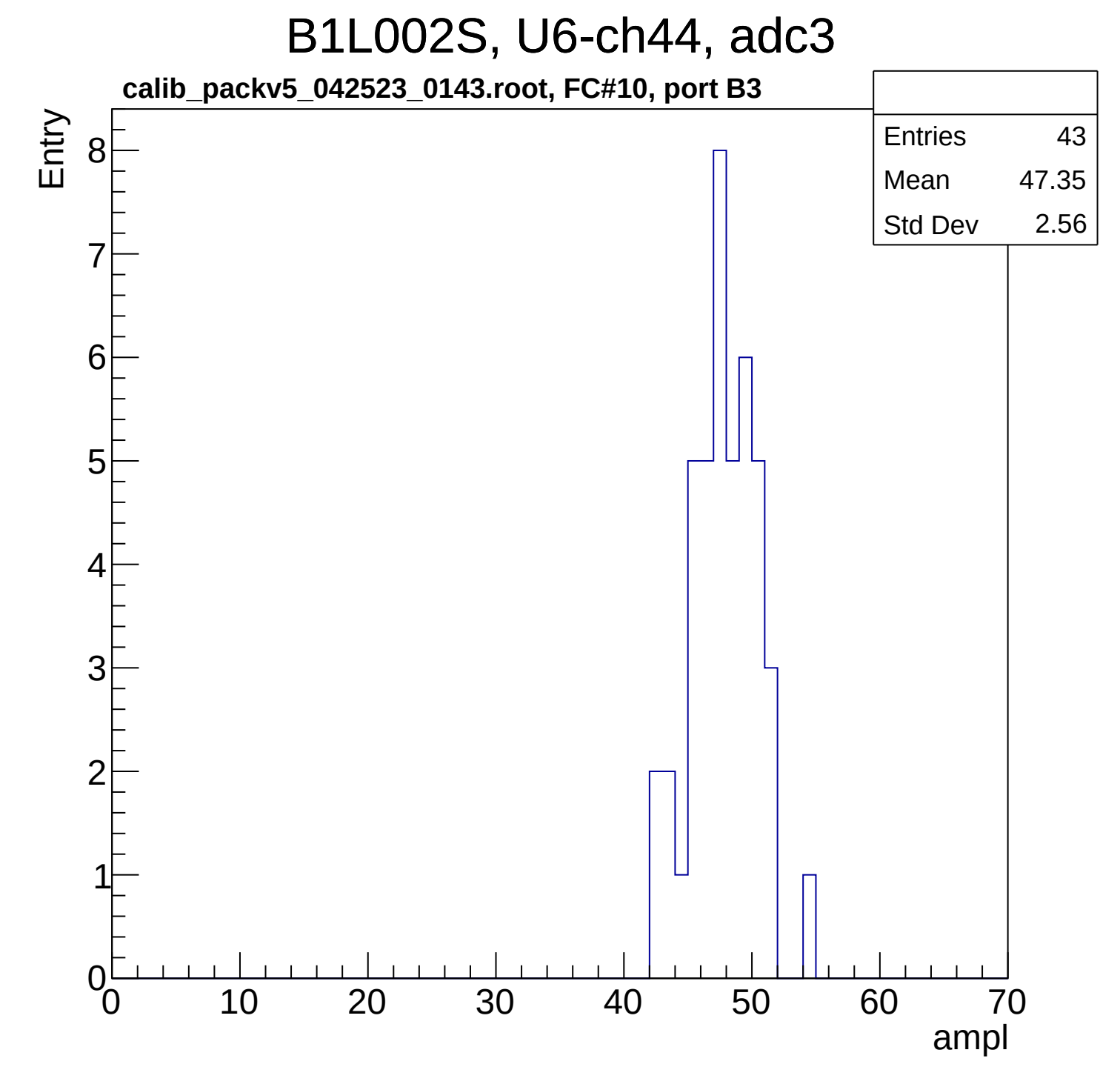

B1L002S, U6-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 54.78
Std Dev 19.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

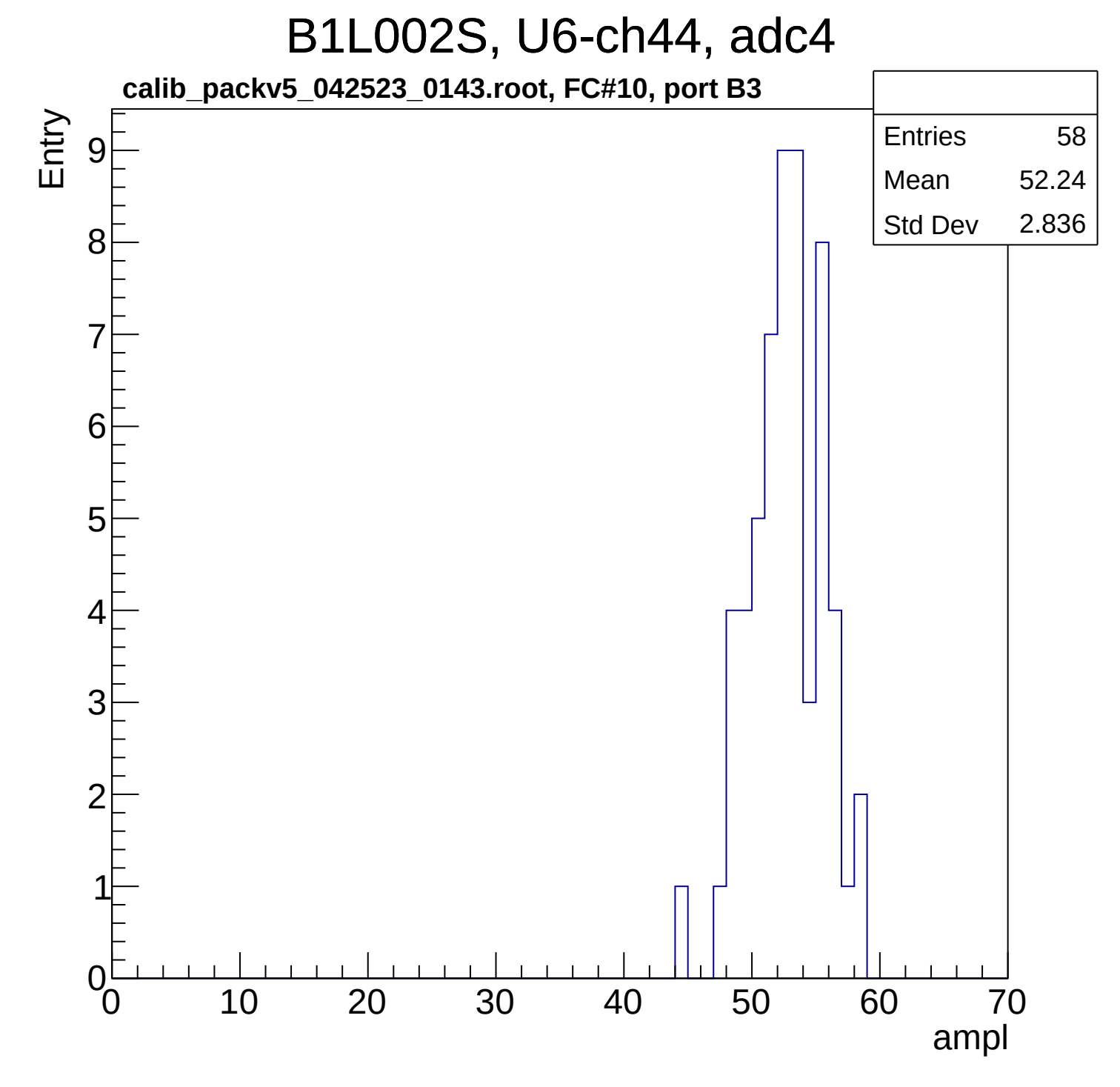

B1L002S, U6-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 0
Std Dev 0
Entry
4
3.5
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

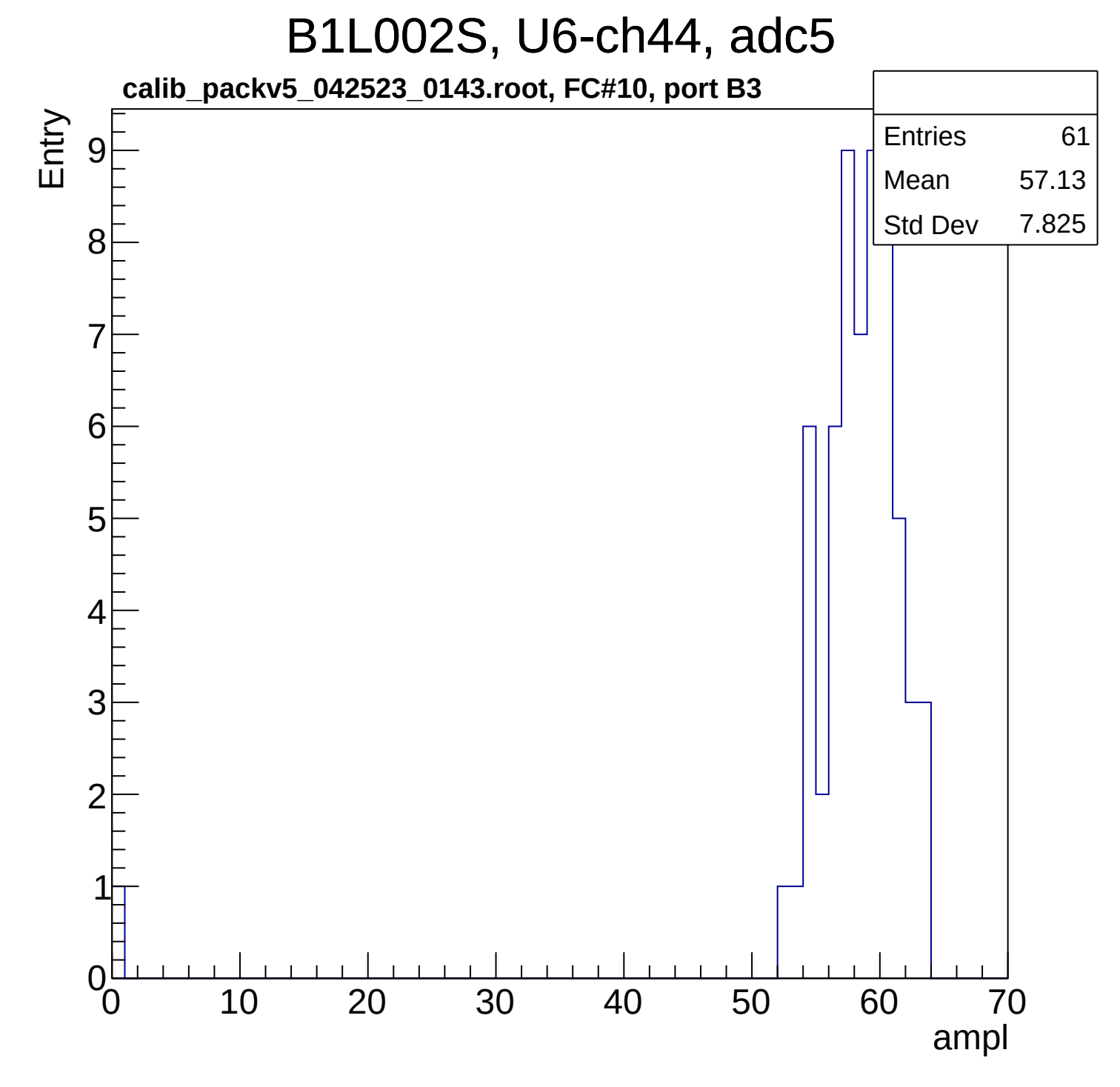

B1L002S, U6-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.13
Std Dev 2.783
Gaus mean : 28.6476
Gaus Width: 2.9947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

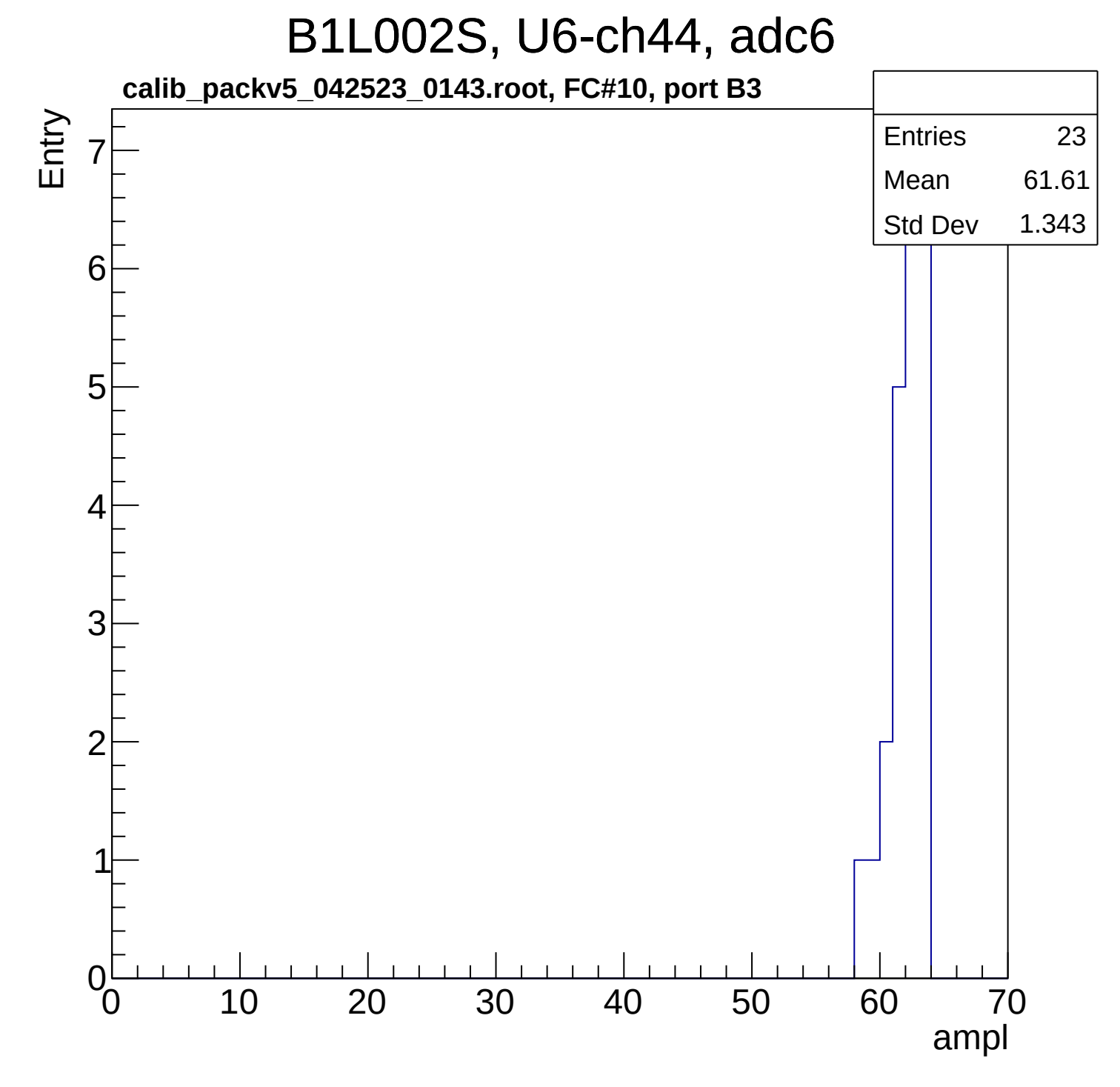

B1L002S, U6-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.37
Std Dev 2.994
Gaus mean : 34.6612
Gaus Width: 3.4693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

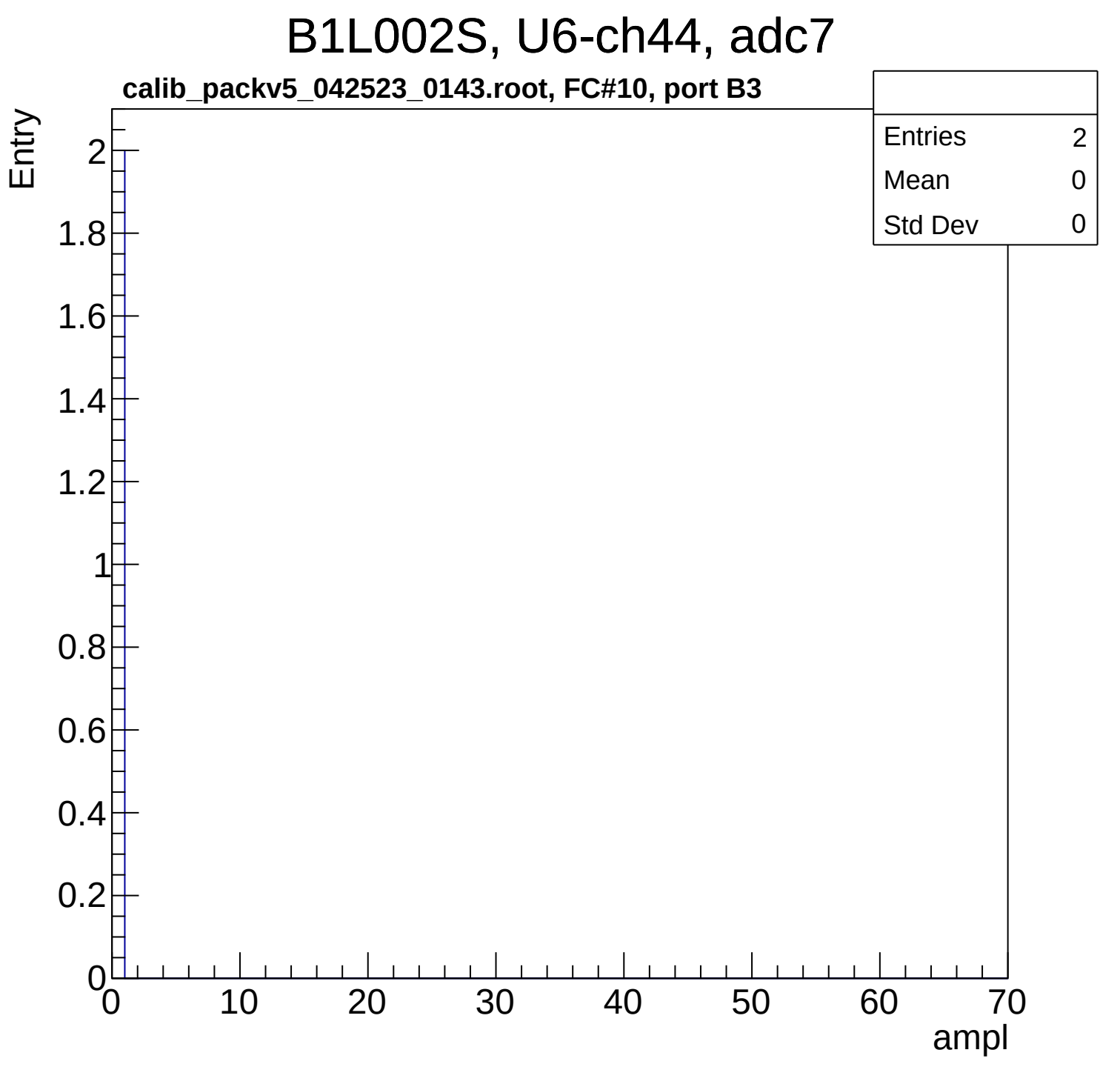

B1L002S, U6-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.44
Std Dev 3.041
Gaus mean : 42.1376
Gaus Width: 2.9603
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

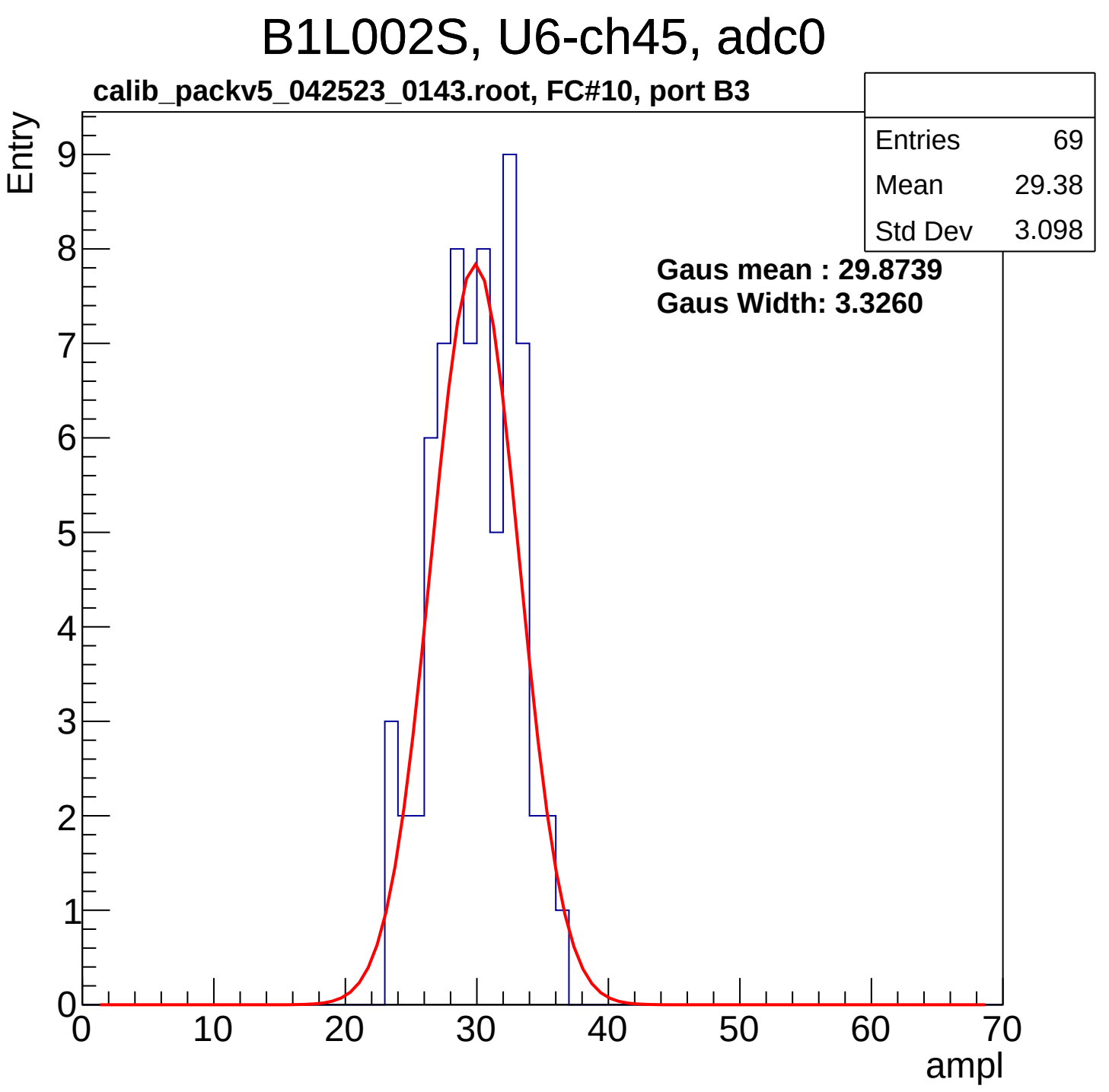

B1L002S, U6-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 47.35
Std Dev 2.56
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

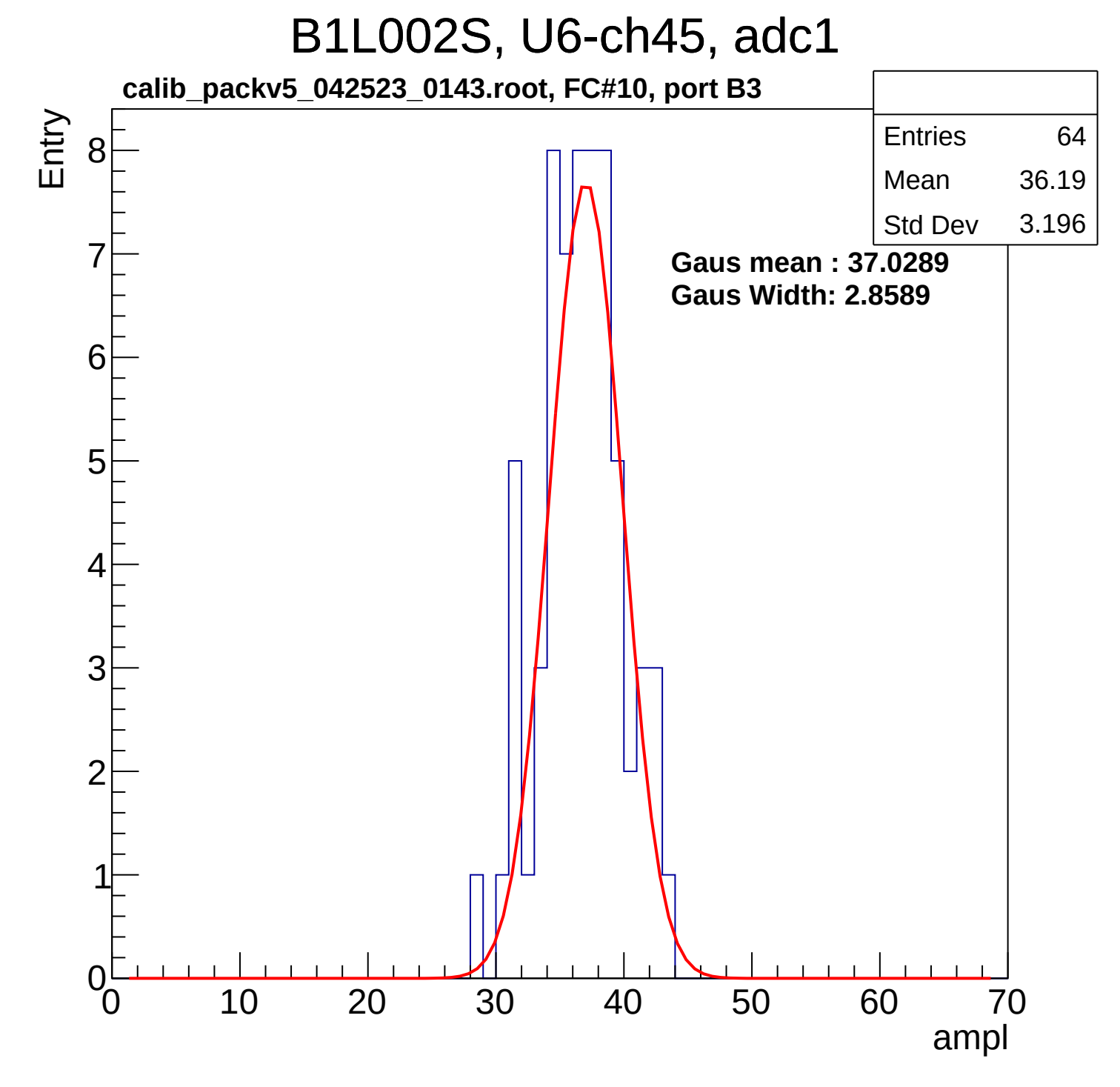

B1L002S, U6-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.24
Std Dev 2.836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

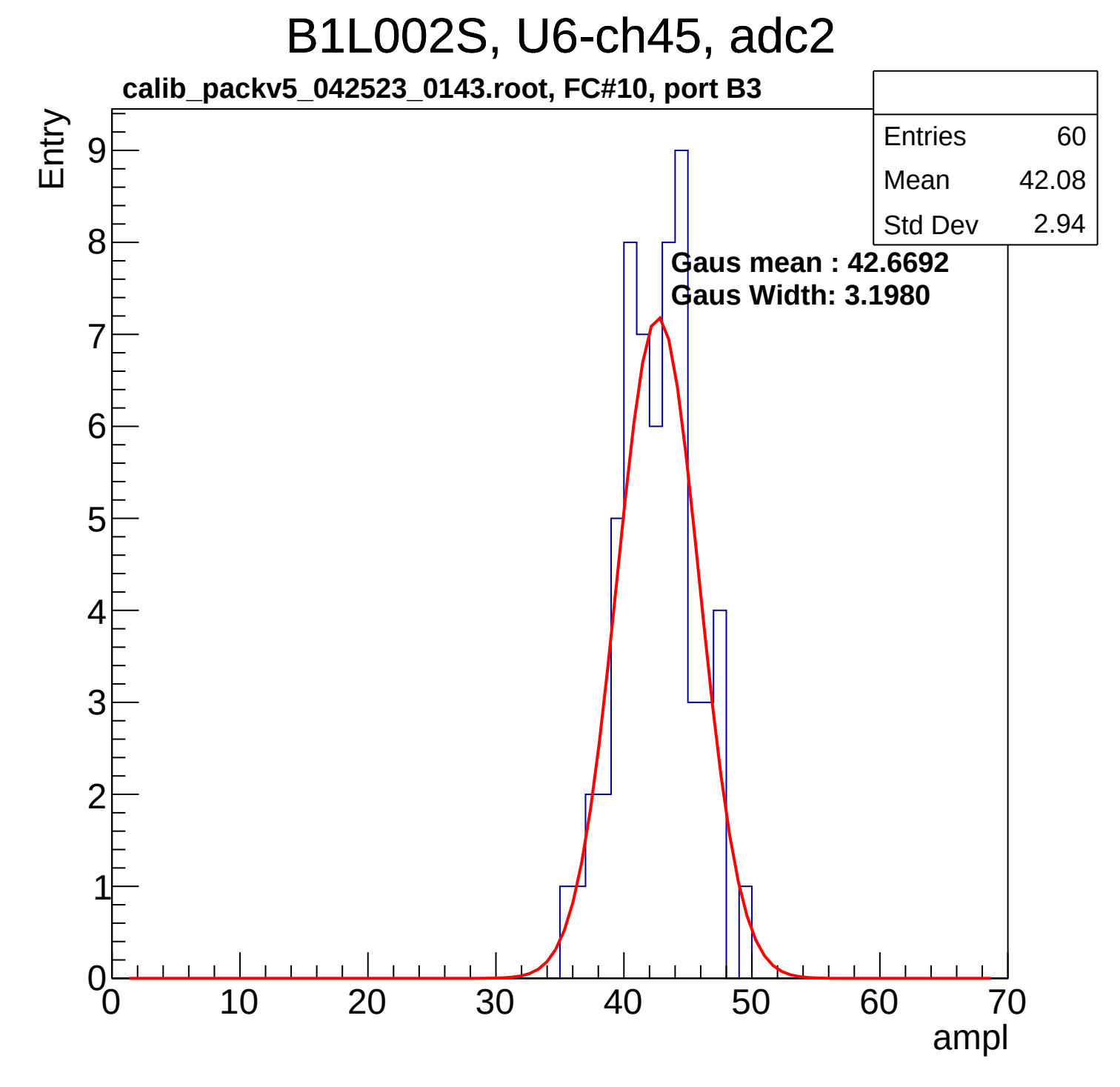

B1L002S, U6-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.13
Std Dev 7.825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

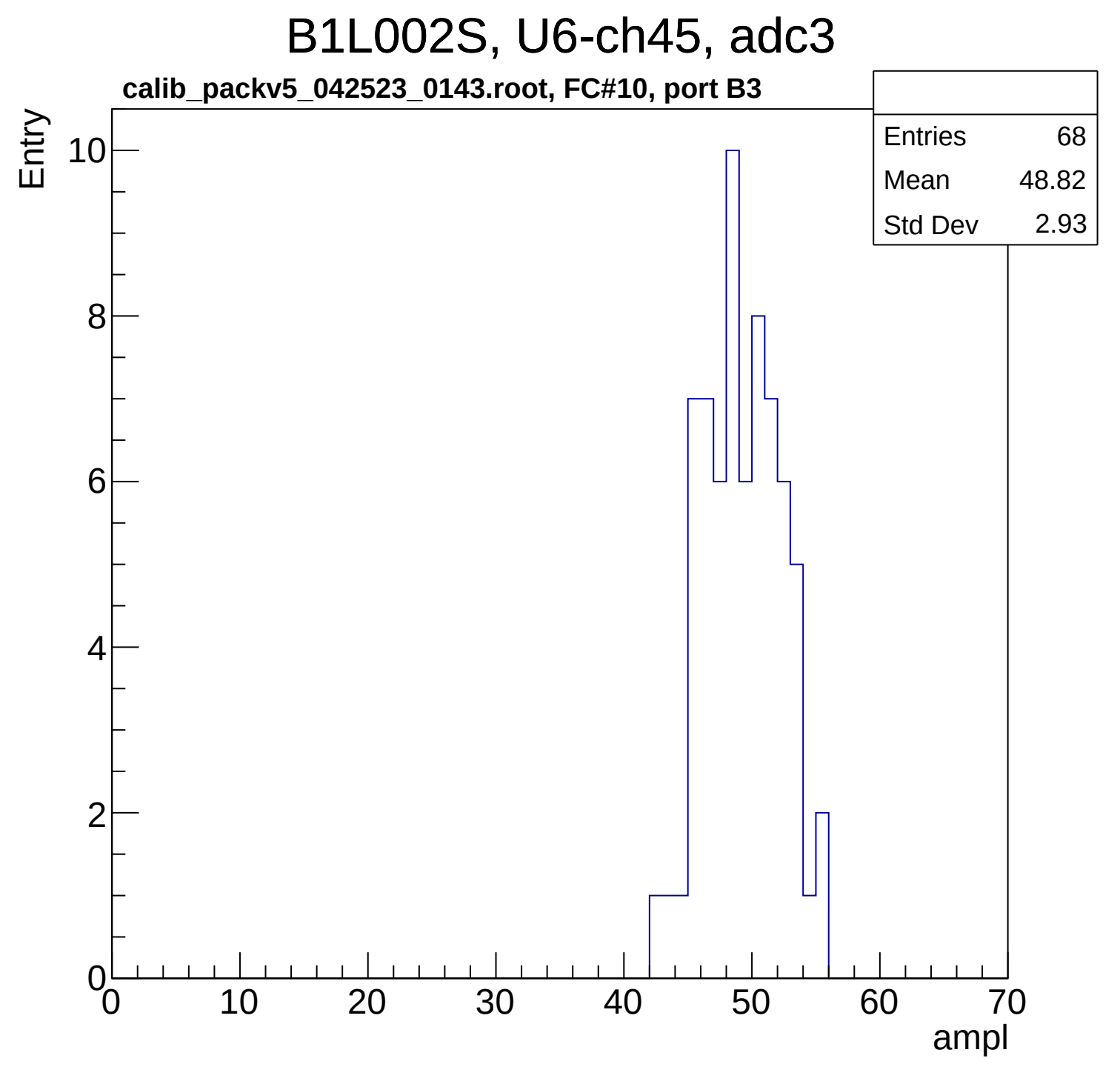

B1L002S, U6-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.61
Std Dev 1.343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

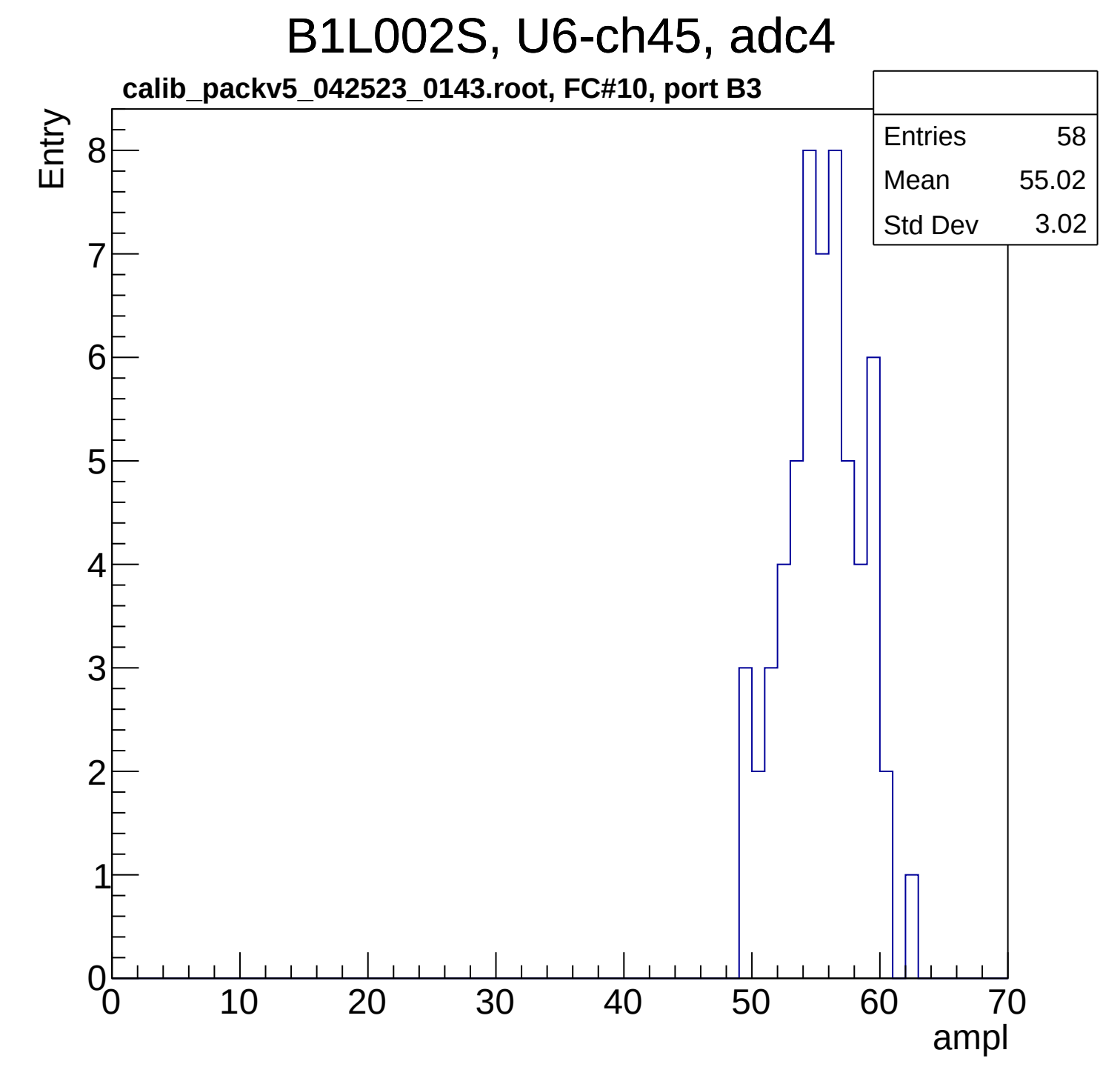

B1L002S, U6-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

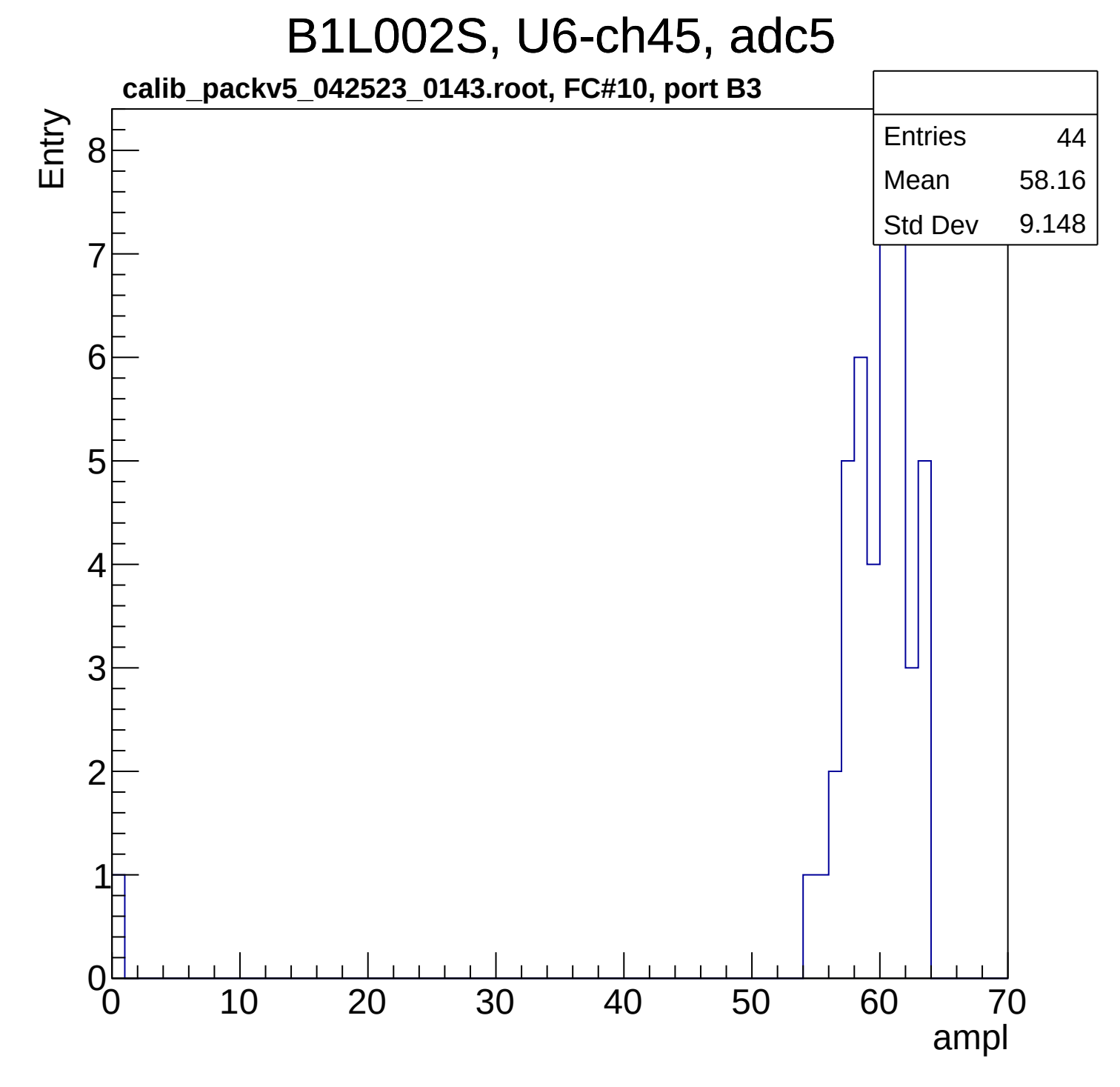

B1L002S, U6-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 29.38
Std Dev 3.098
Gaus mean : 29.8739
Gaus Width: 3.3260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

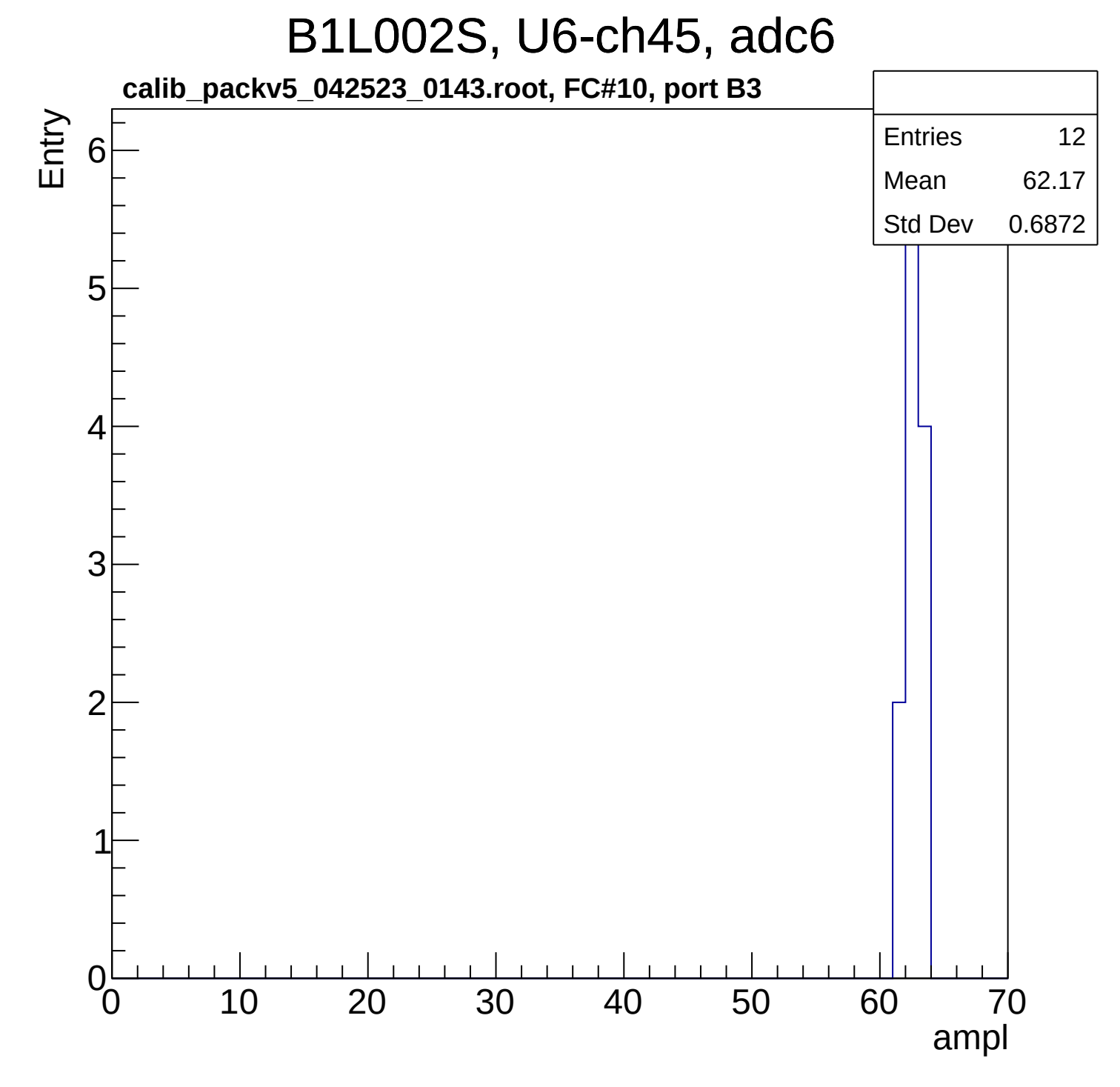

B1L002S, U6-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 36.19
Std Dev 3.196
Gaus mean : 37.0289
Gaus Width: 2.8589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

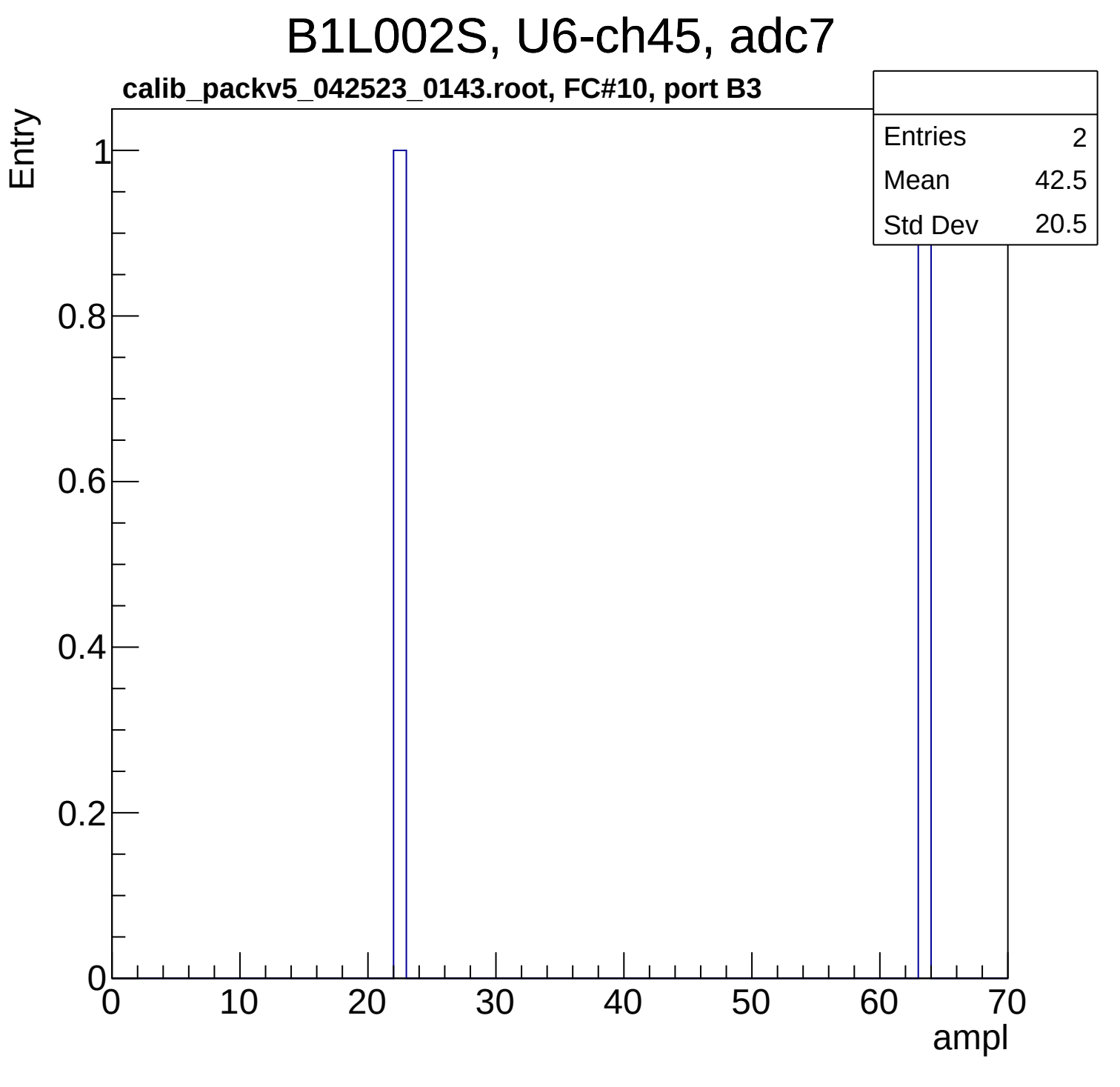

B1L002S, U6-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.08
Std Dev 2.94
Gaus mean : 42.6692
Gaus Width: 3.1980
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

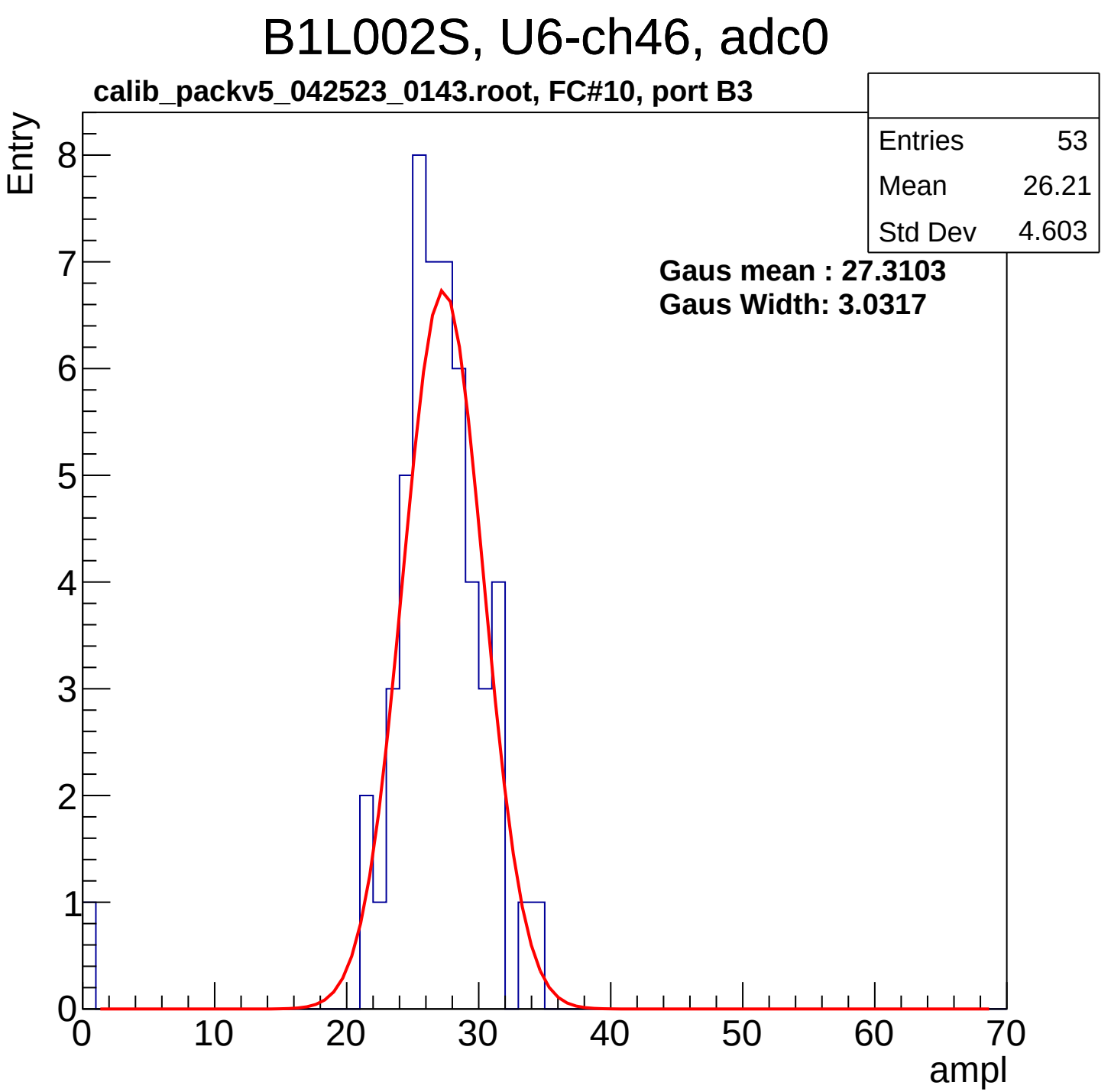

B1L002S, U6-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.82
Std Dev 2.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

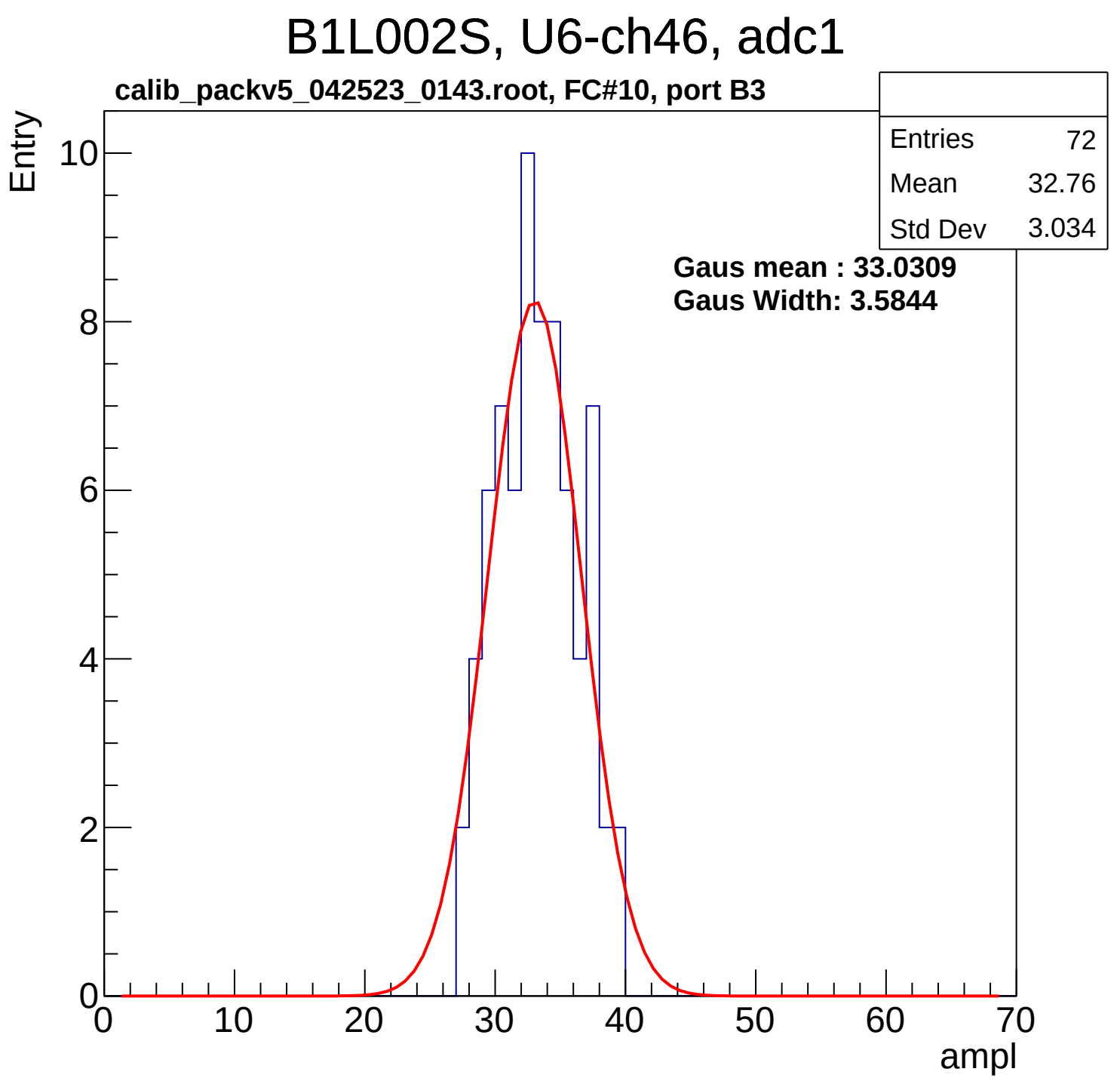

B1L002S, U6-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 55.02
Std Dev 3.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

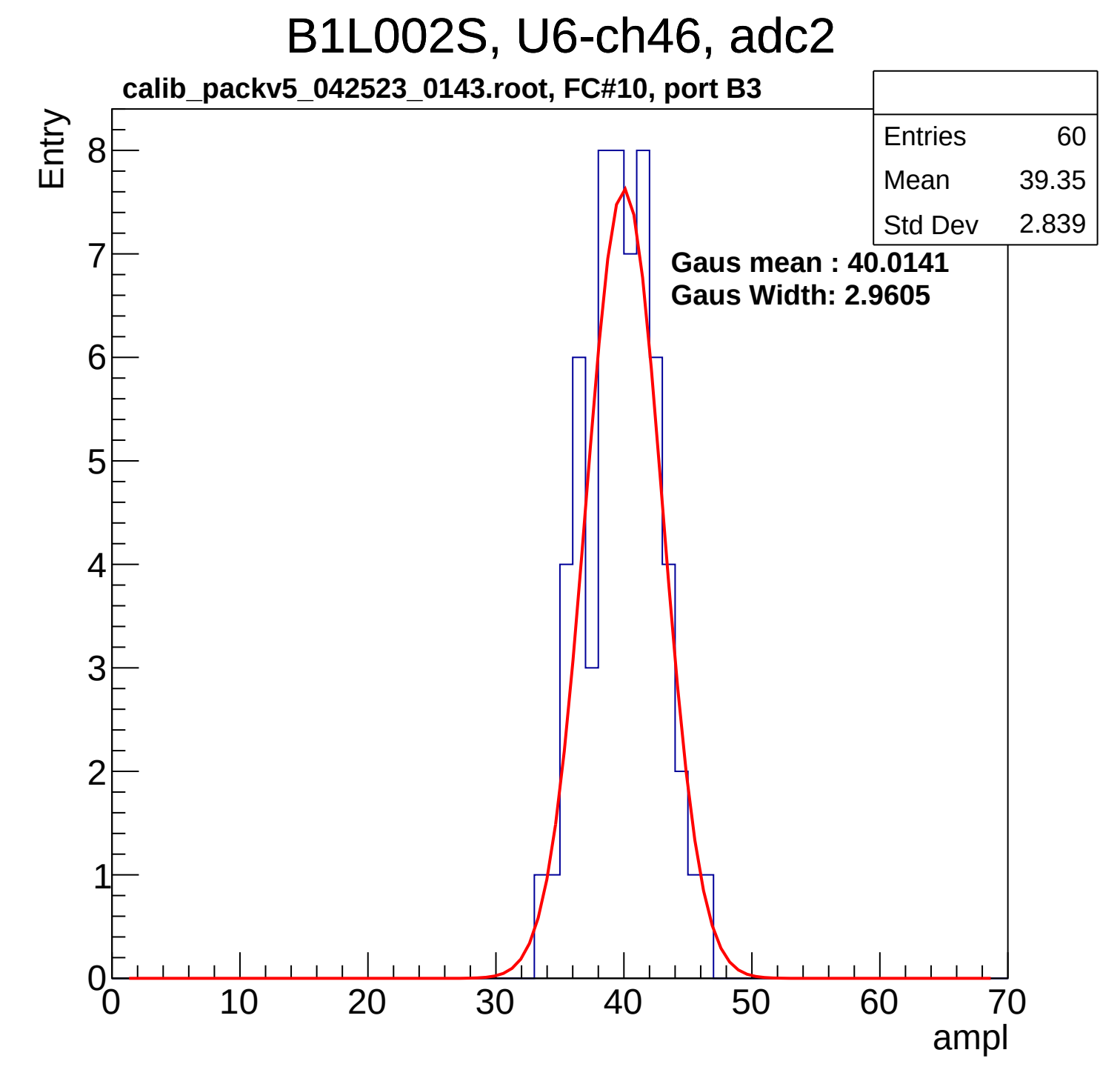

B1L002S, U6-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.16
Std Dev 9.148
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

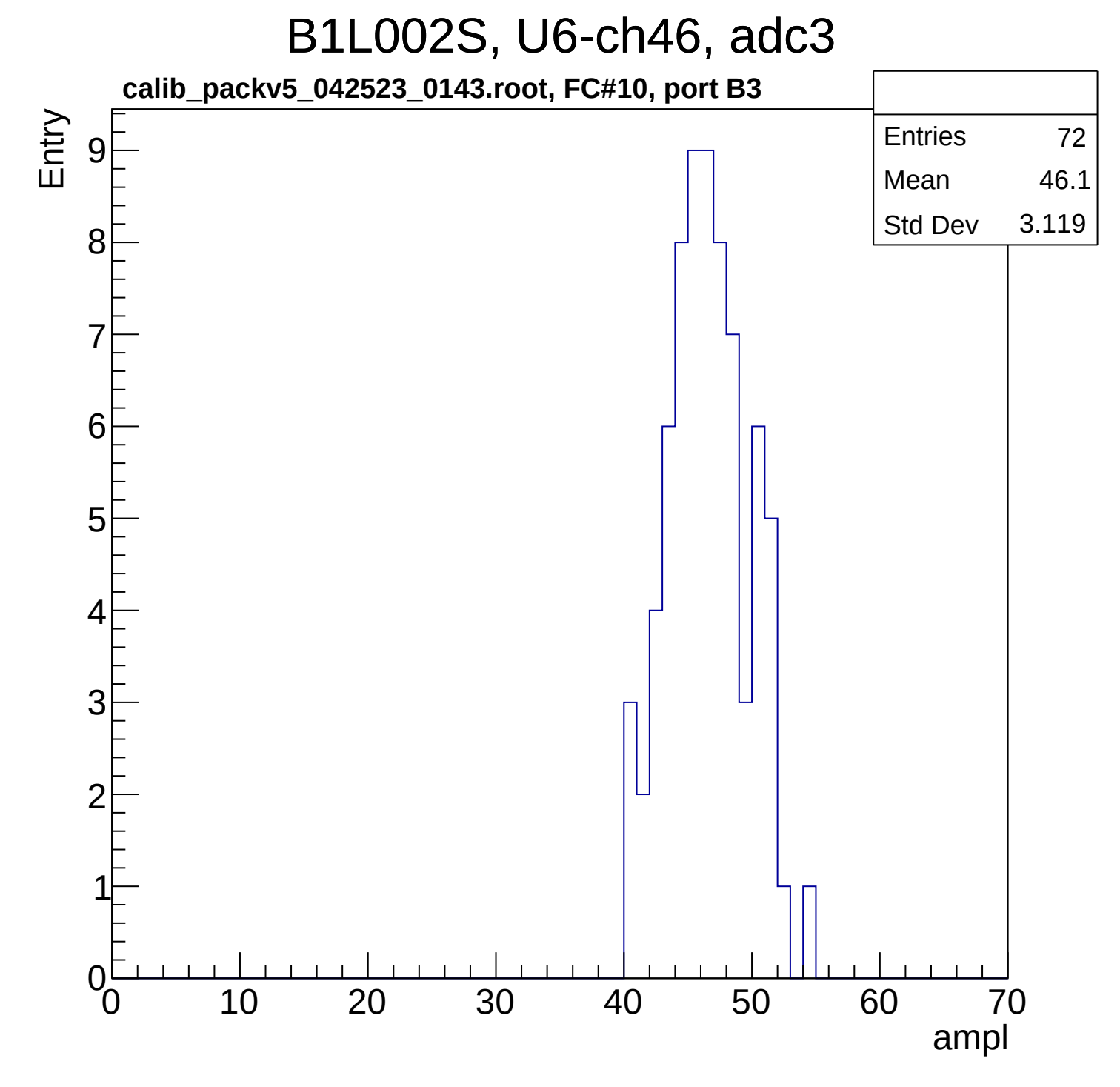

B1L002S, U6-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62.17
Std Dev 0.6872
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

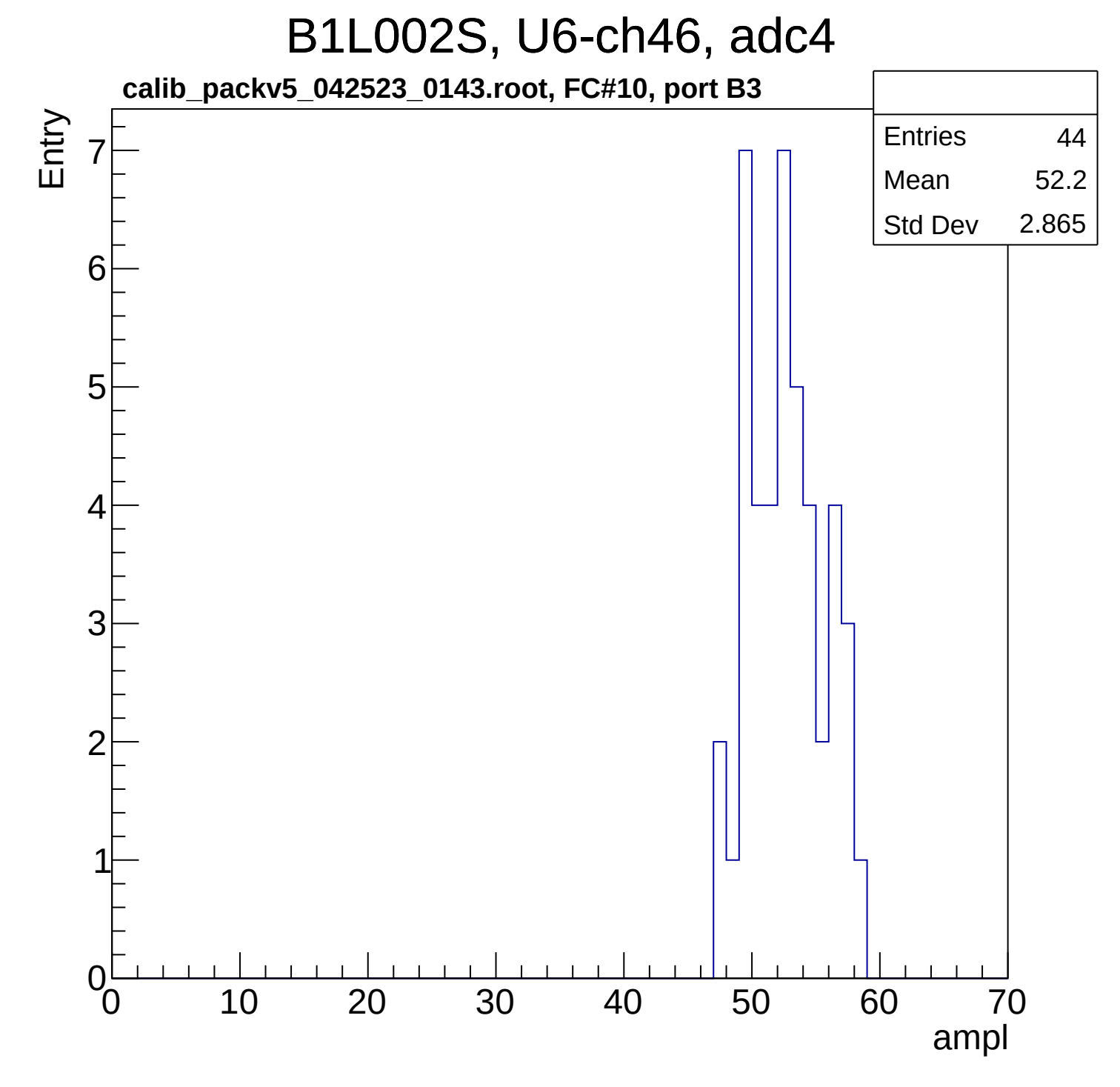

B1L002S, U6-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

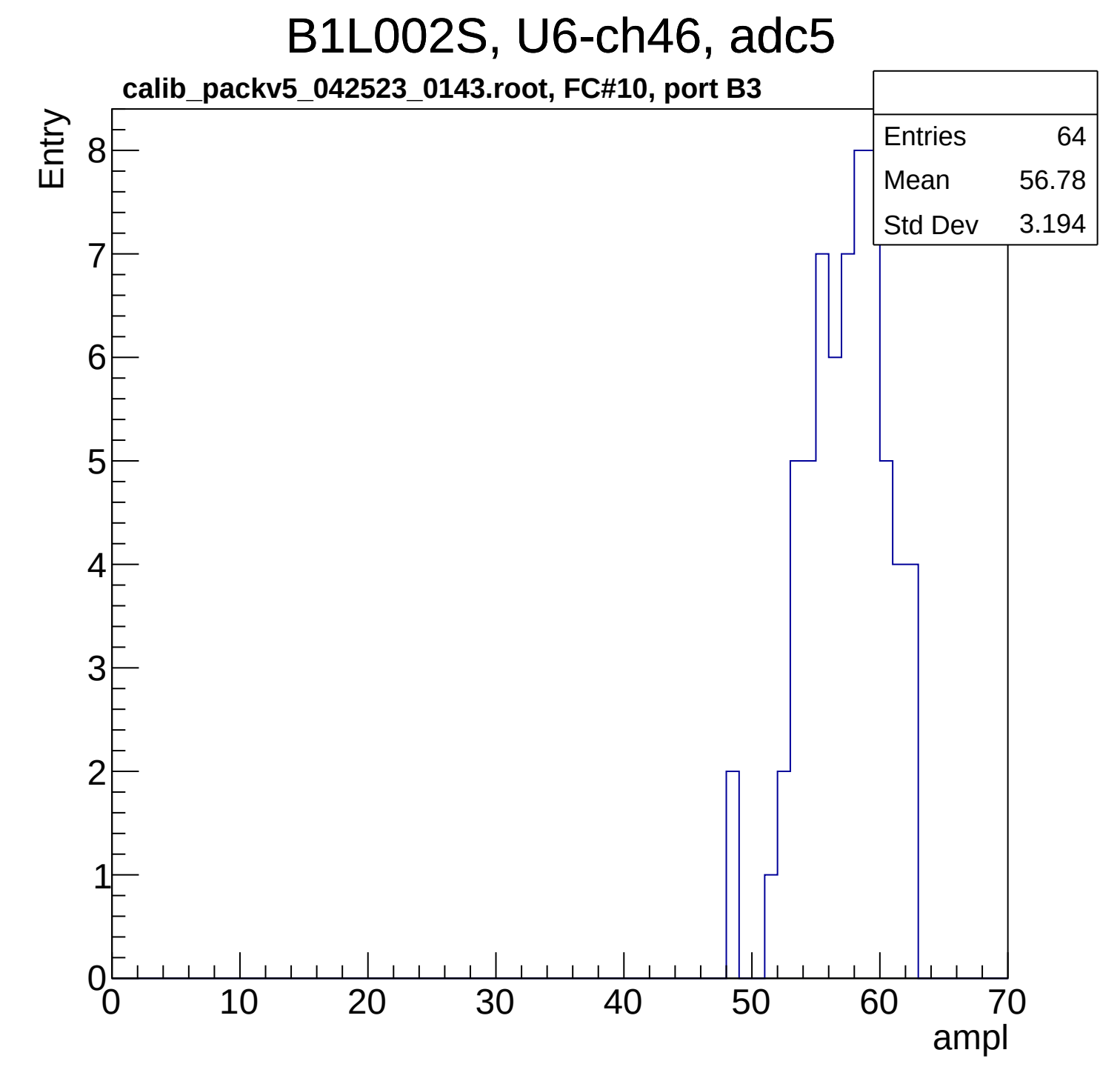

B1L002S, U6-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 26.21
Std Dev 4.603
Gaus mean : 27.3103
Gaus Width: 3.0317
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

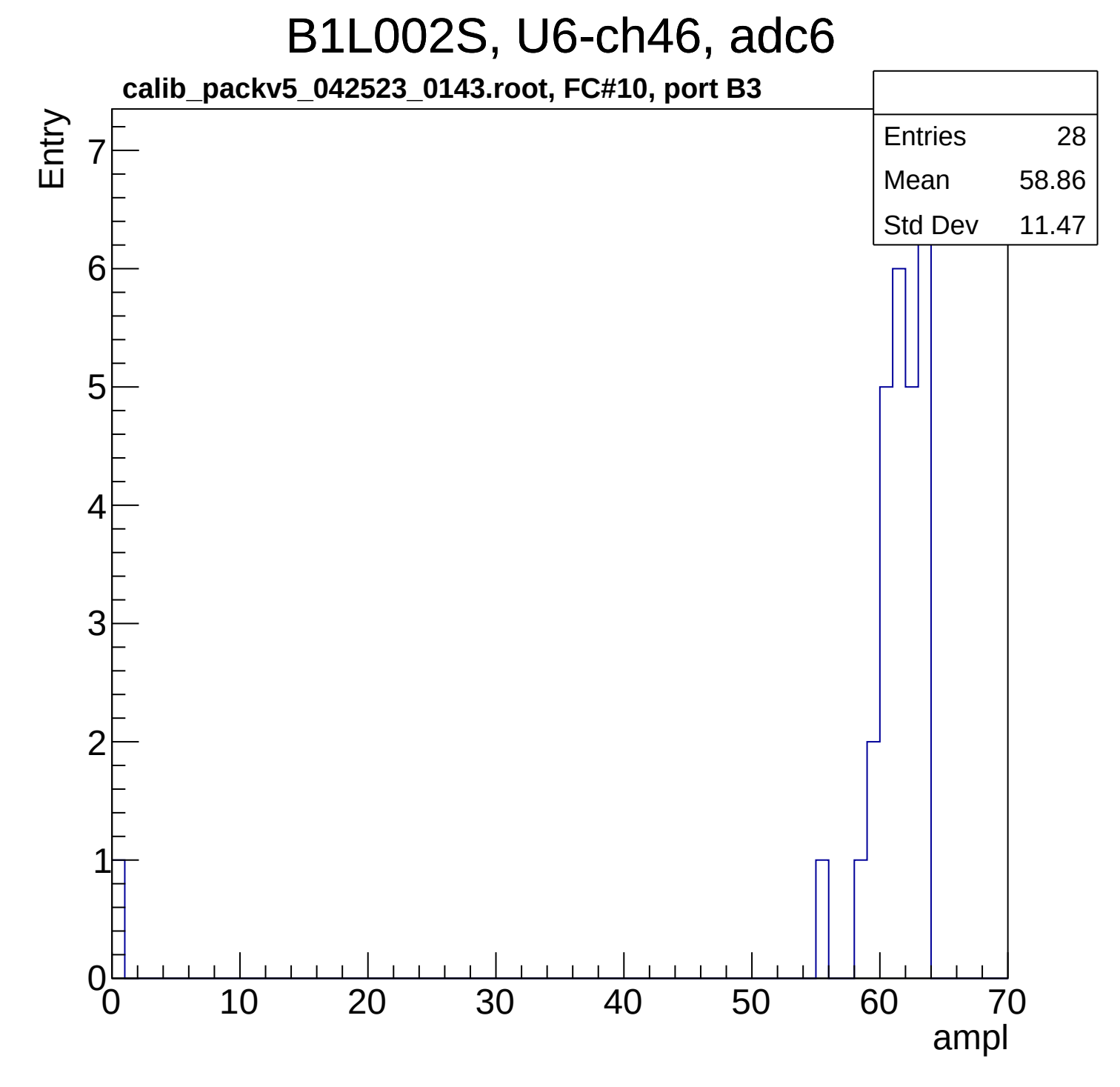

B1L002S, U6-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 32.76
Std Dev 3.034
Gaus mean : 33.0309
Gaus Width: 3.5844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

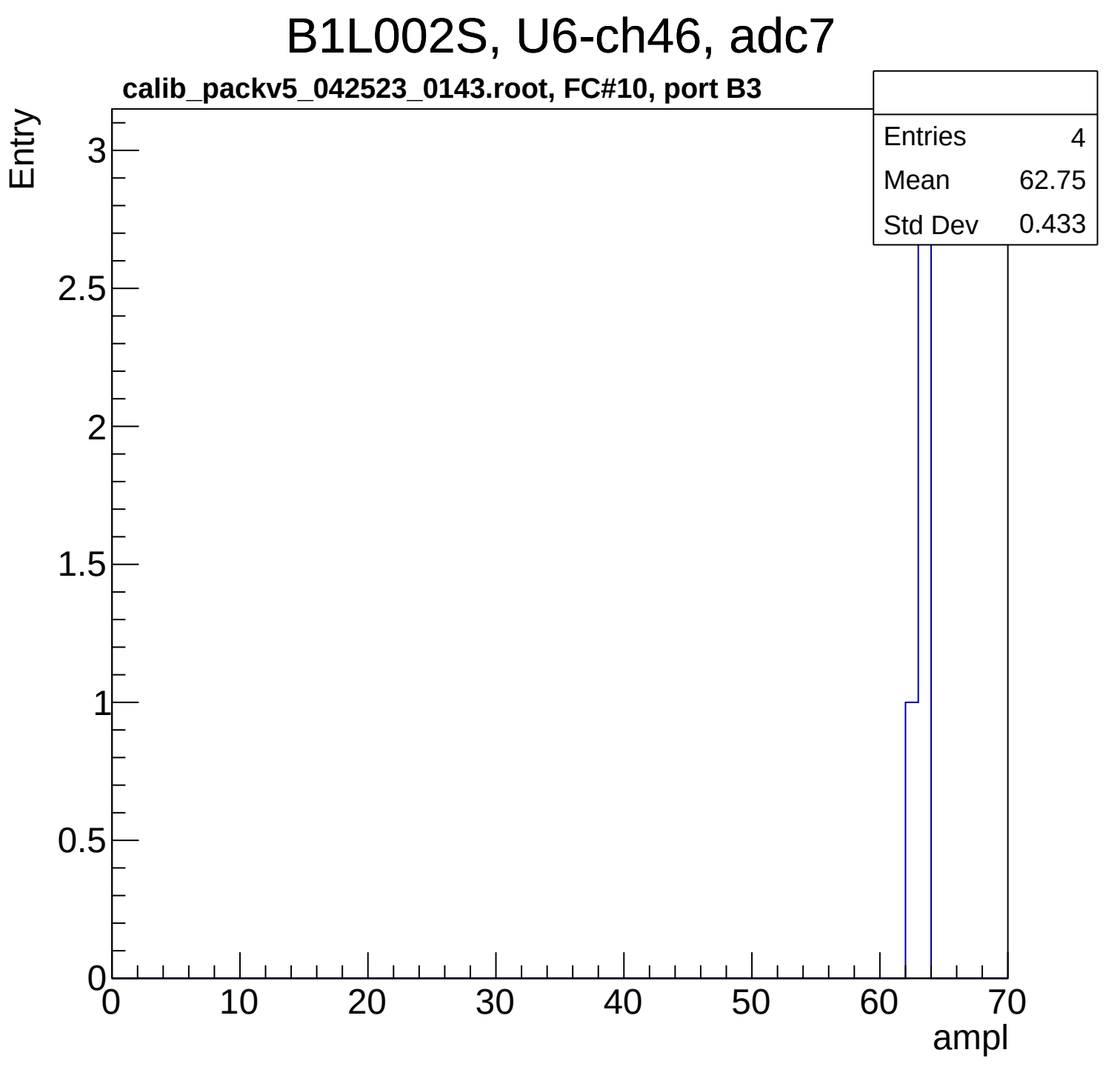

B1L002S, U6-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 39.35
Std Dev 2.839
Gaus mean : 40.0141
Gaus Width: 2.9605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

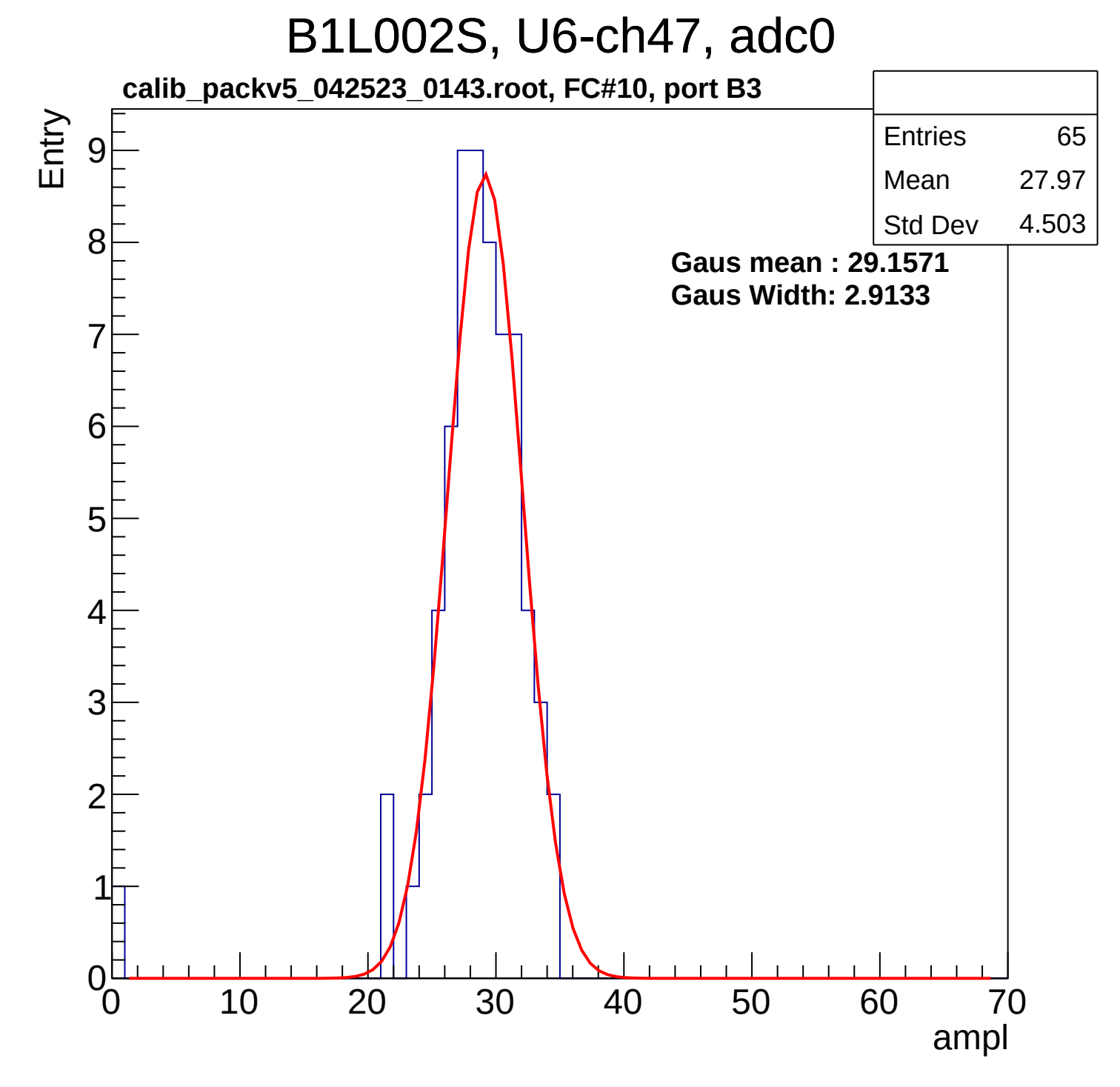

B1L002S, U6-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.1
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

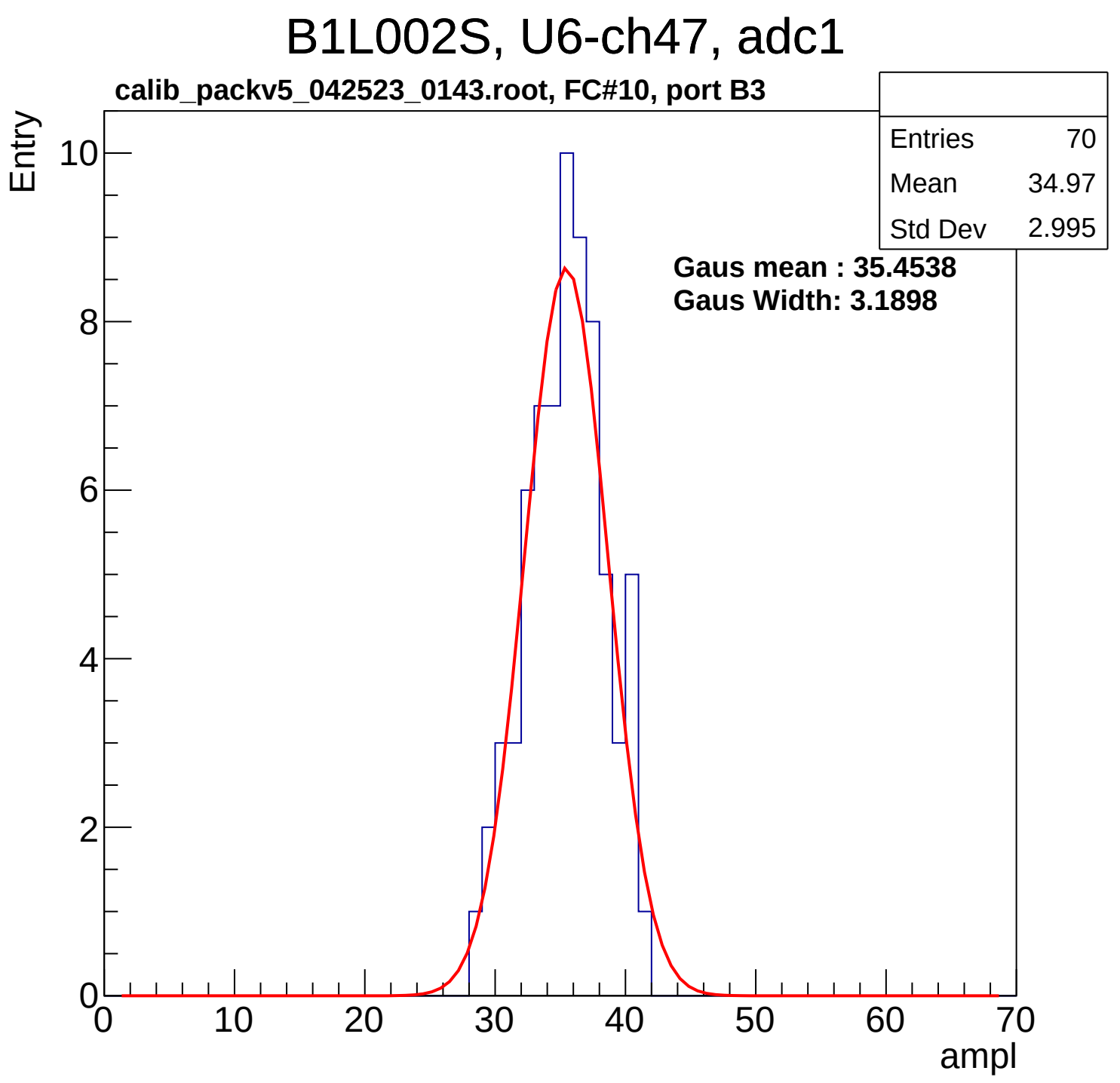

B1L002S, U6-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.2
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

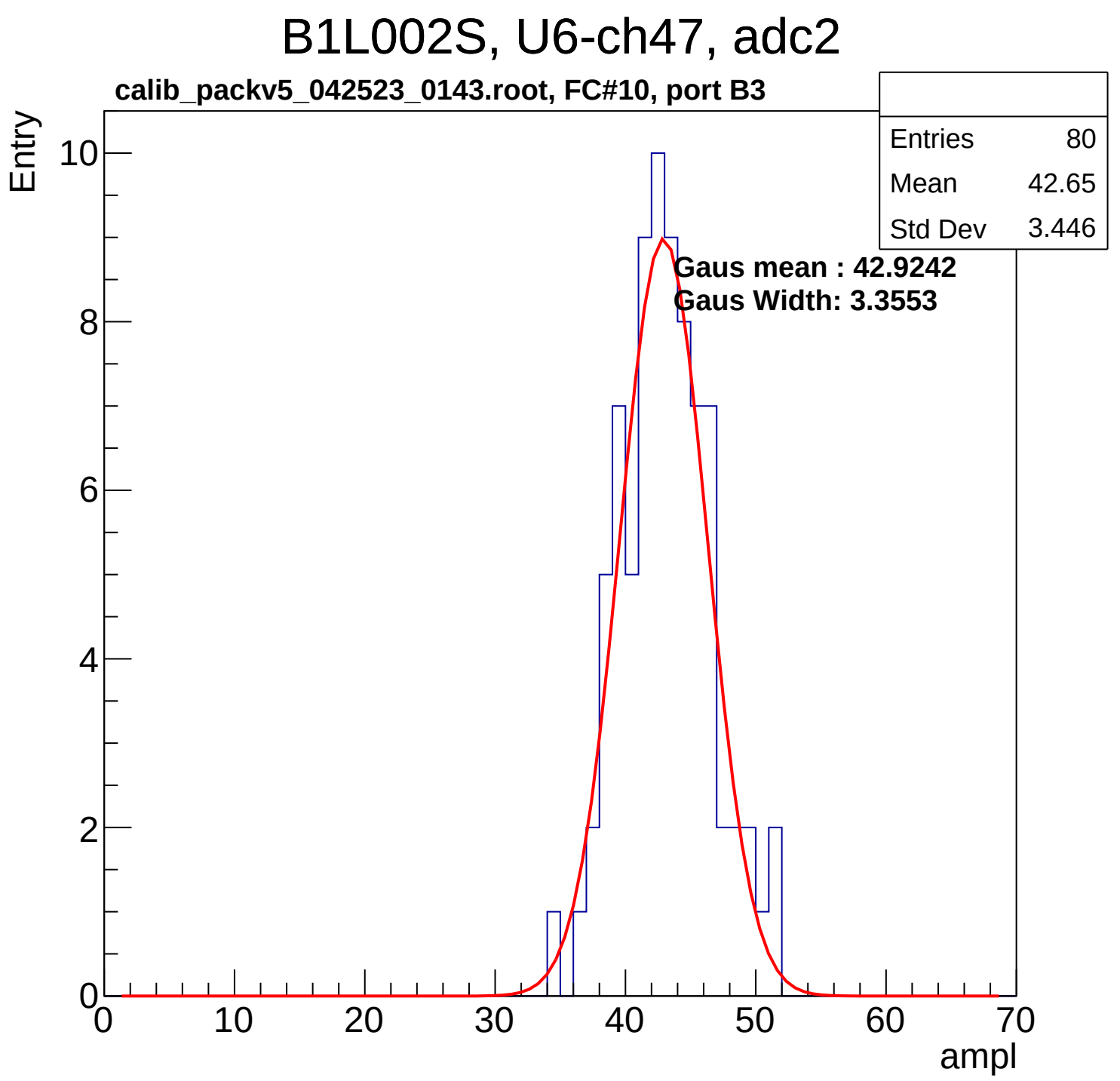

B1L002S, U6-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 56.78
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

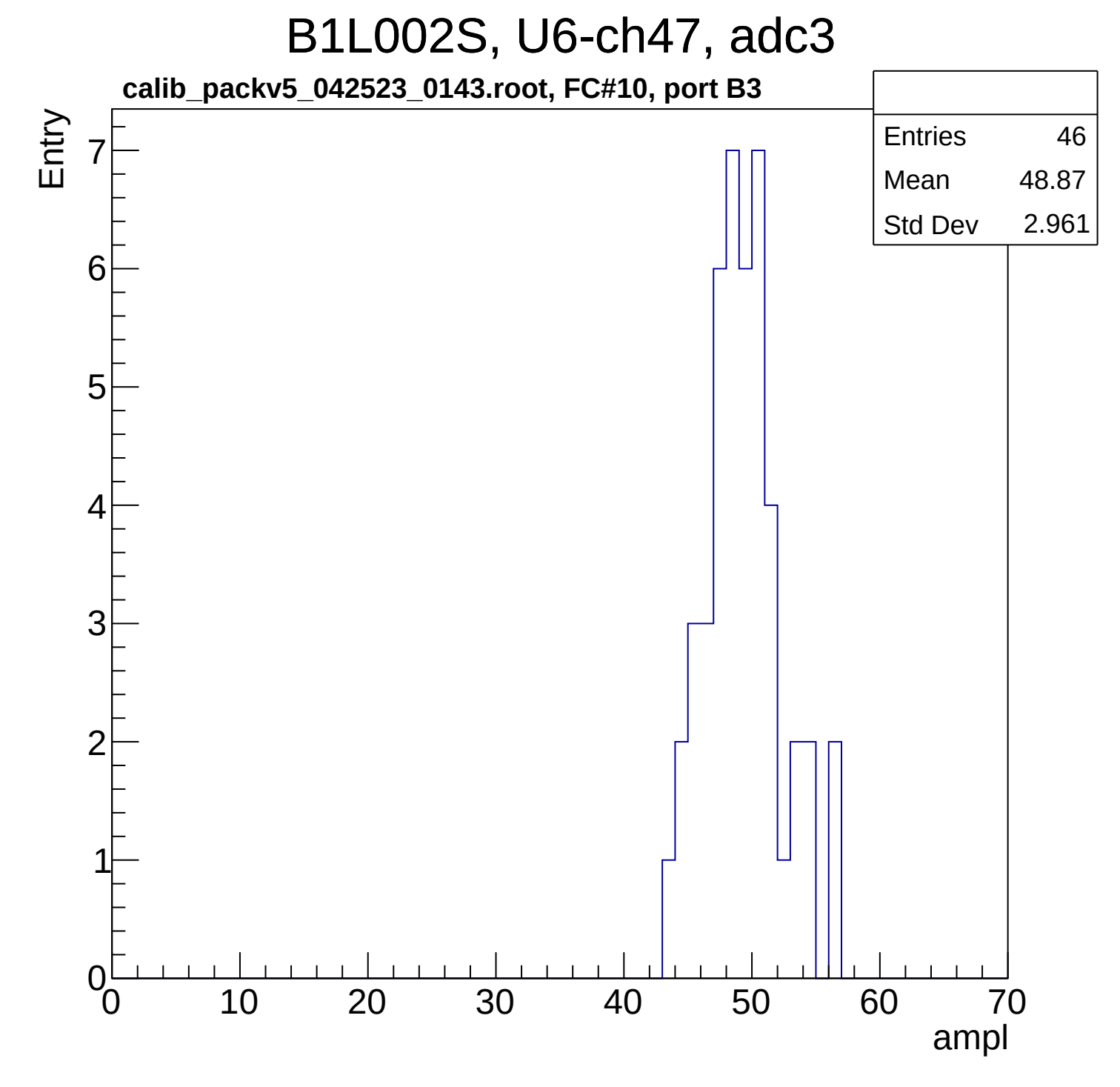

B1L002S, U6-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.86
Std Dev 11.47
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

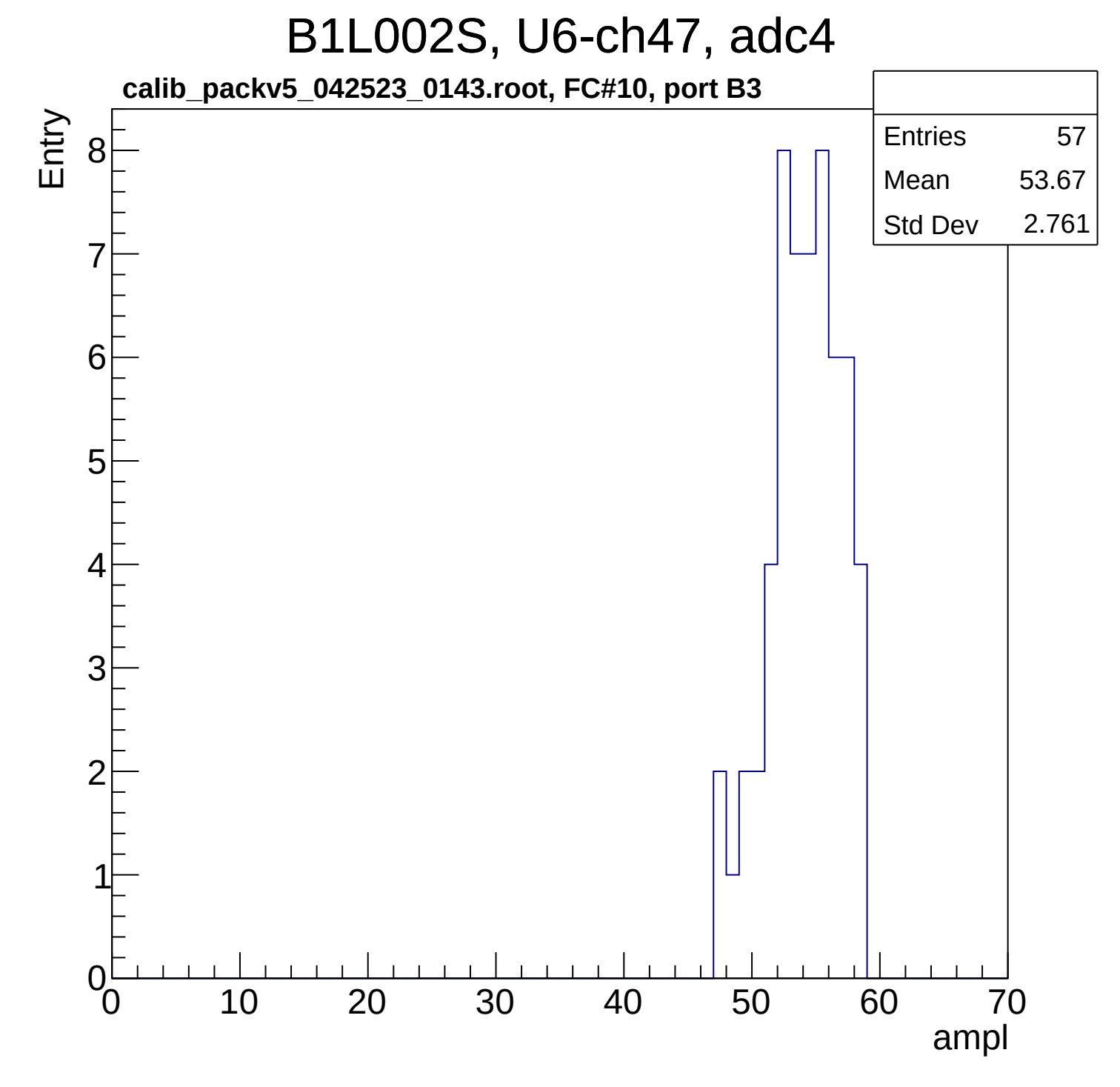

B1L002S, U6-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

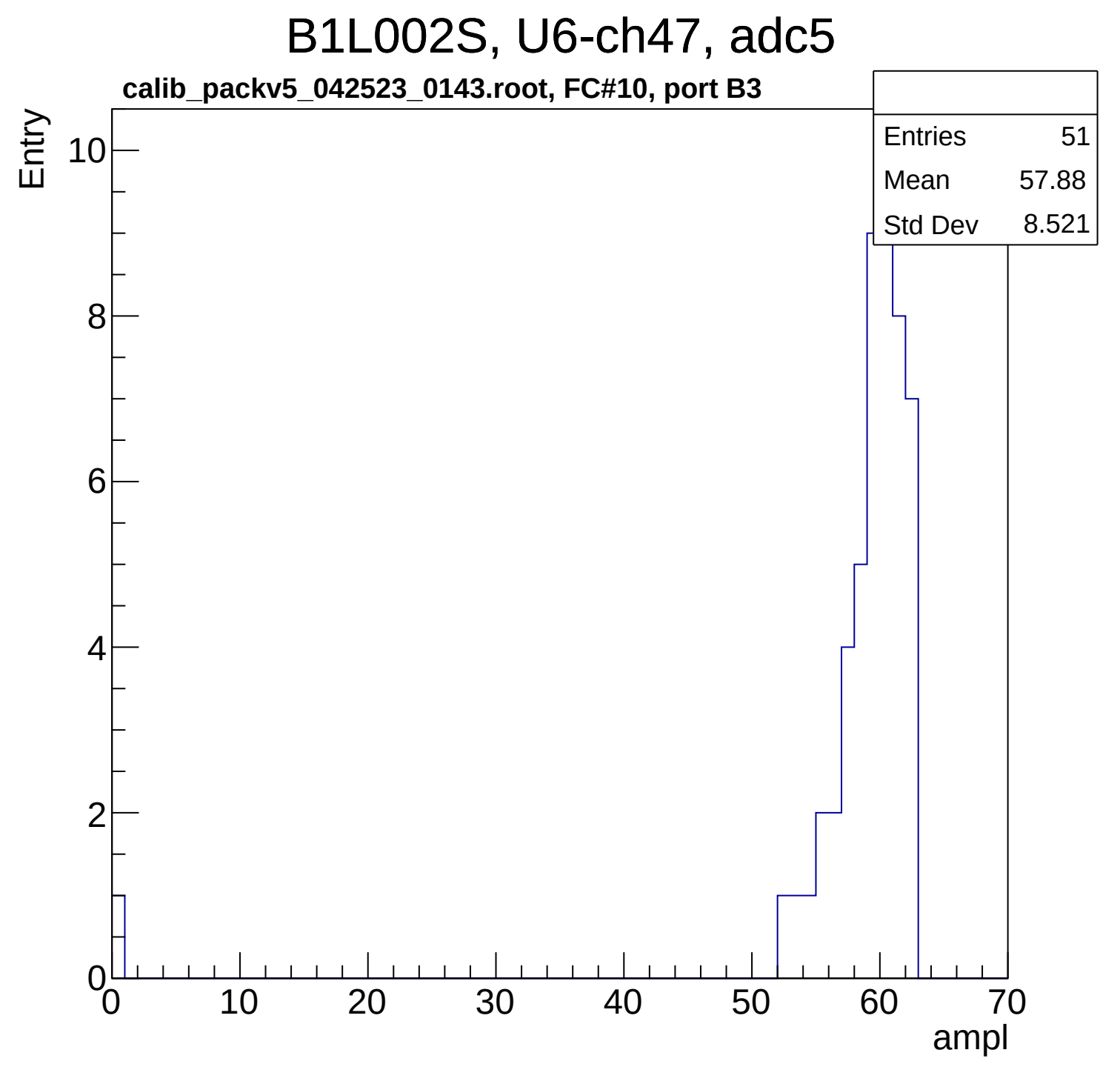

B1L002S, U6-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.97
Std Dev 4.503
Gaus mean : 29.1571
Gaus Width: 2.9133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

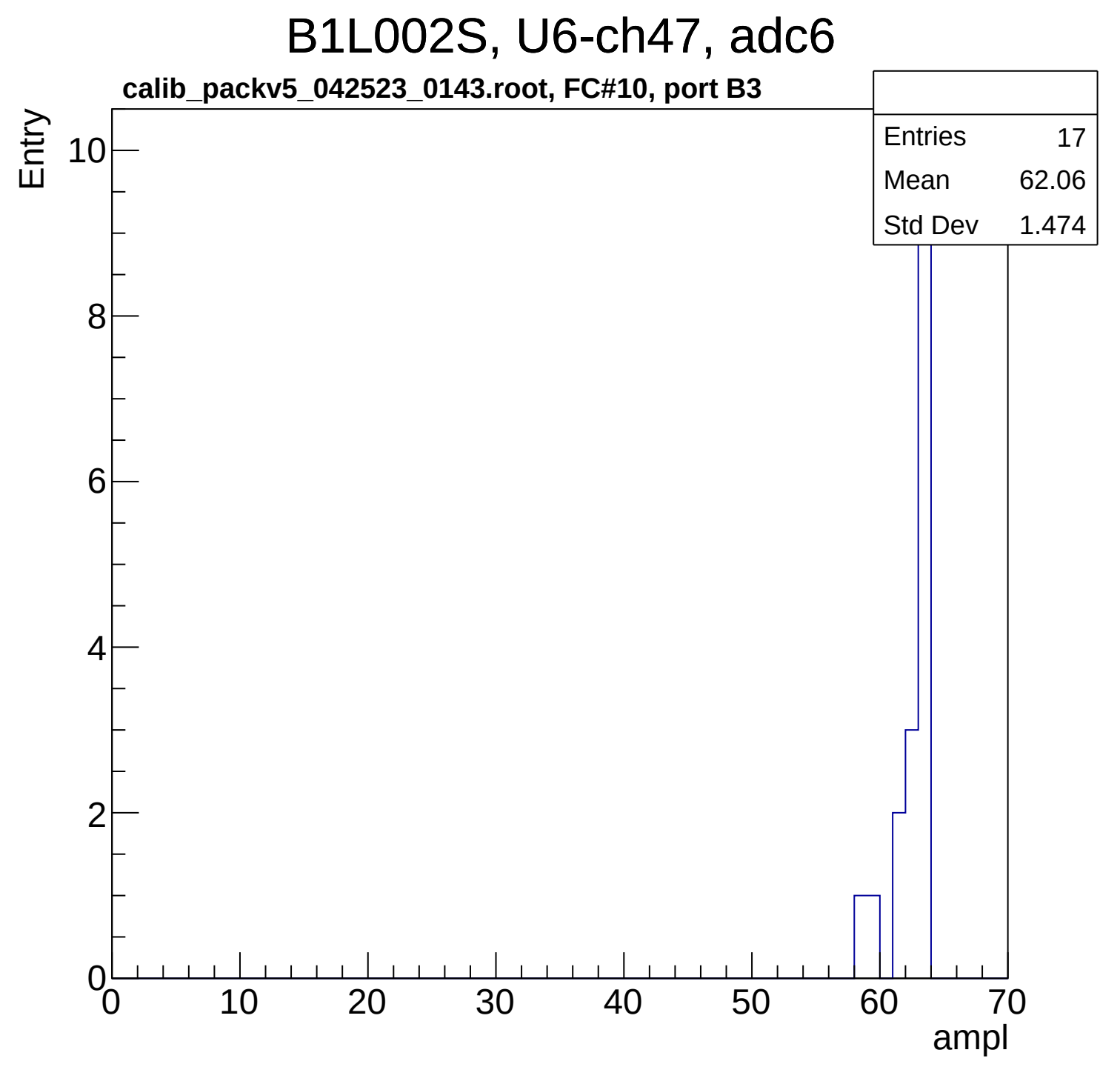

B1L002S, U6-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.97
Std Dev 2.995
Gaus mean : 35.4538
Gaus Width: 3.1898
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

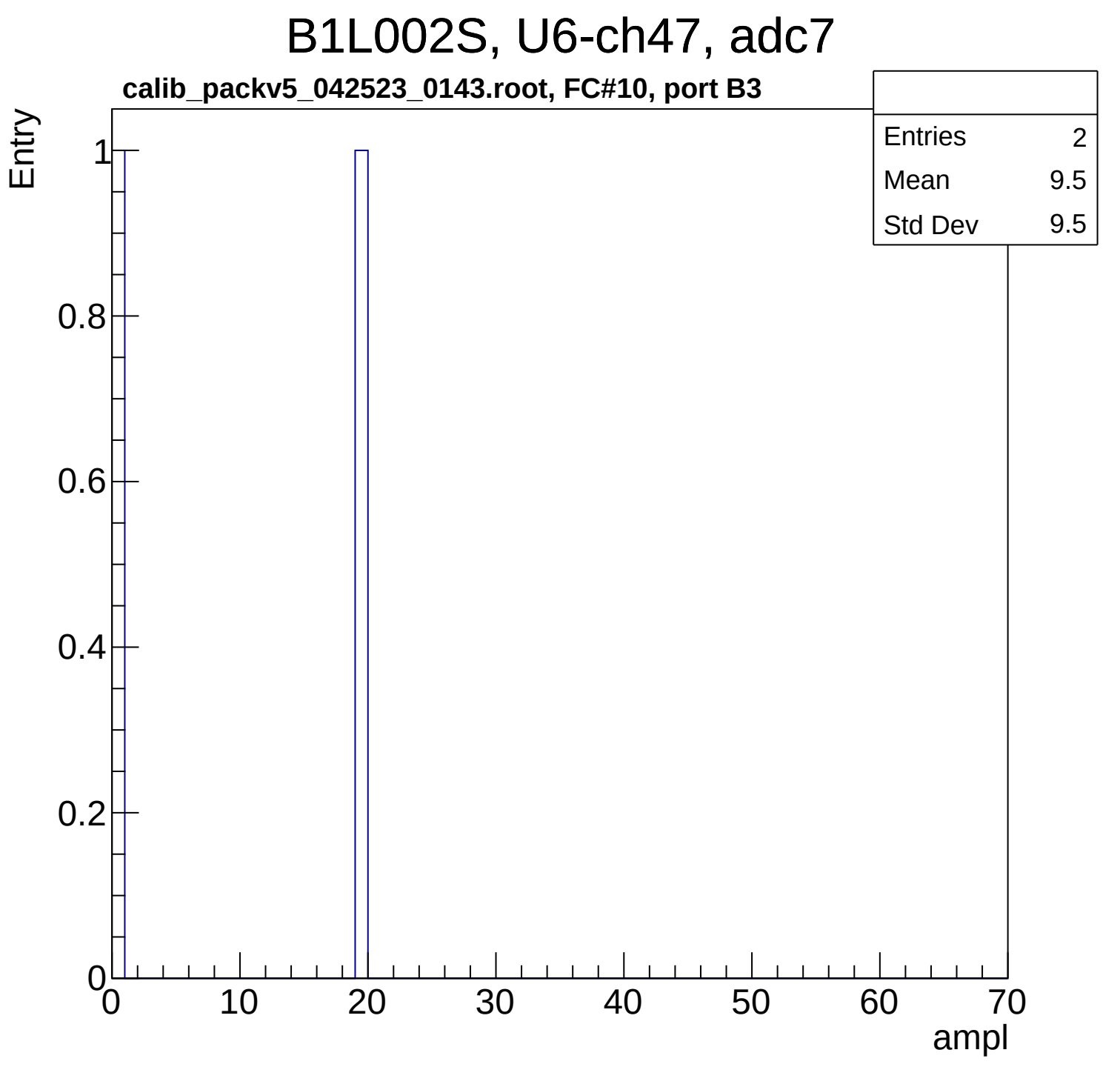

B1L002S, U6-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 42.65
Std Dev 3.446
Gaus mean : 42.9242
Gaus Width: 3.3553
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

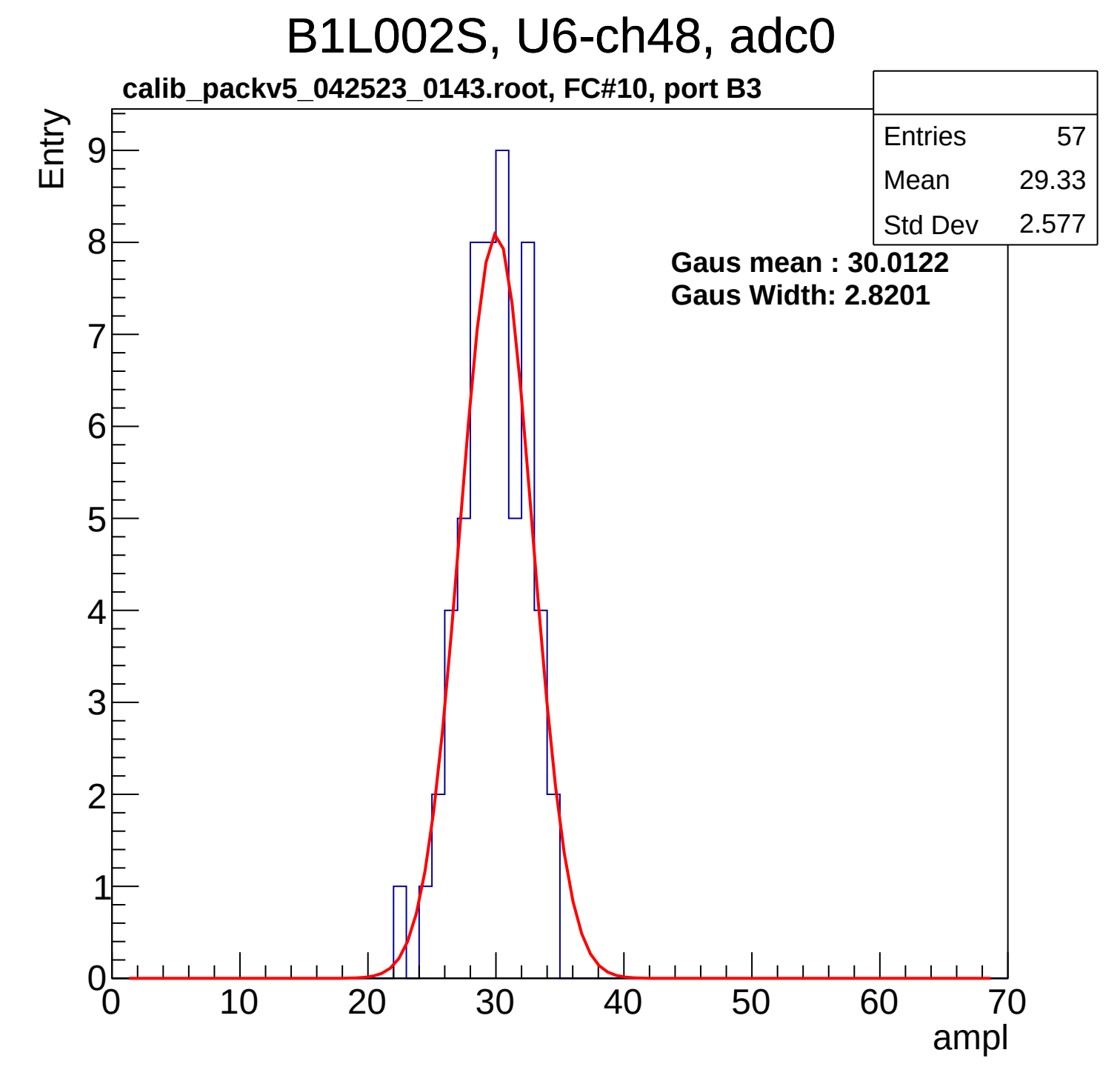

B1L002S, U6-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 48.87
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

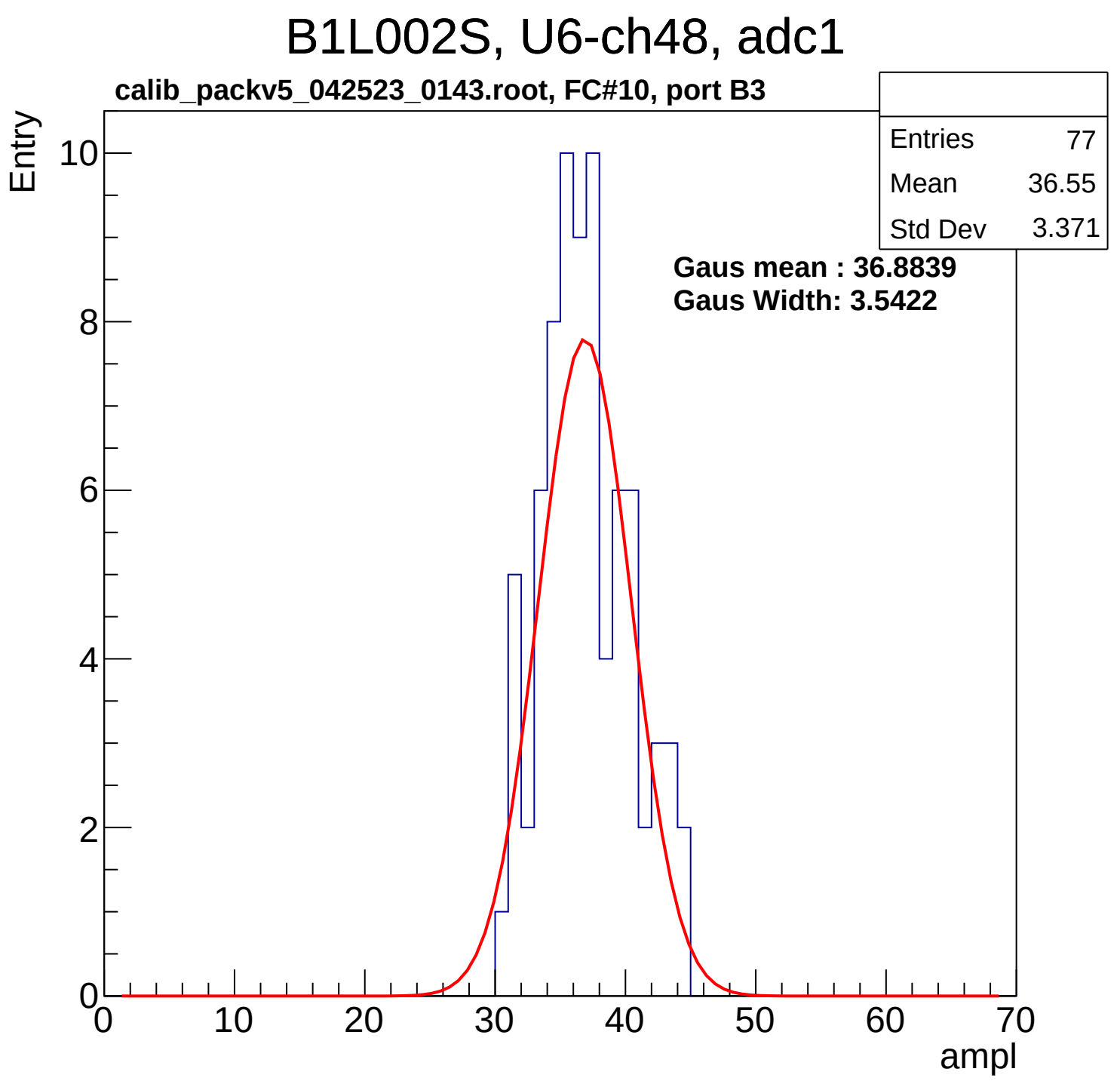

B1L002S, U6-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.67
Std Dev 2.761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

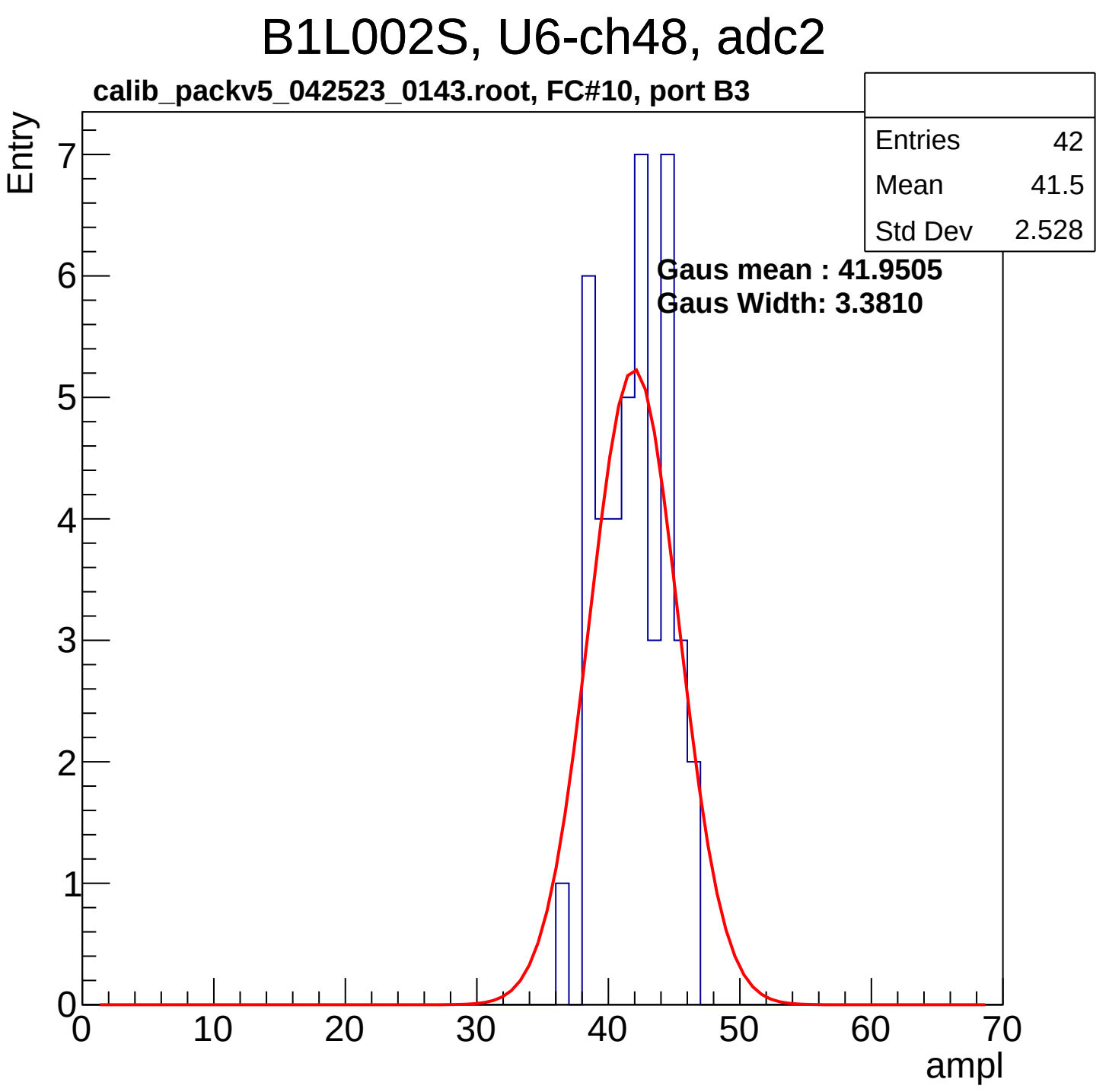

B1L002S, U6-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.88
Std Dev 8.521
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

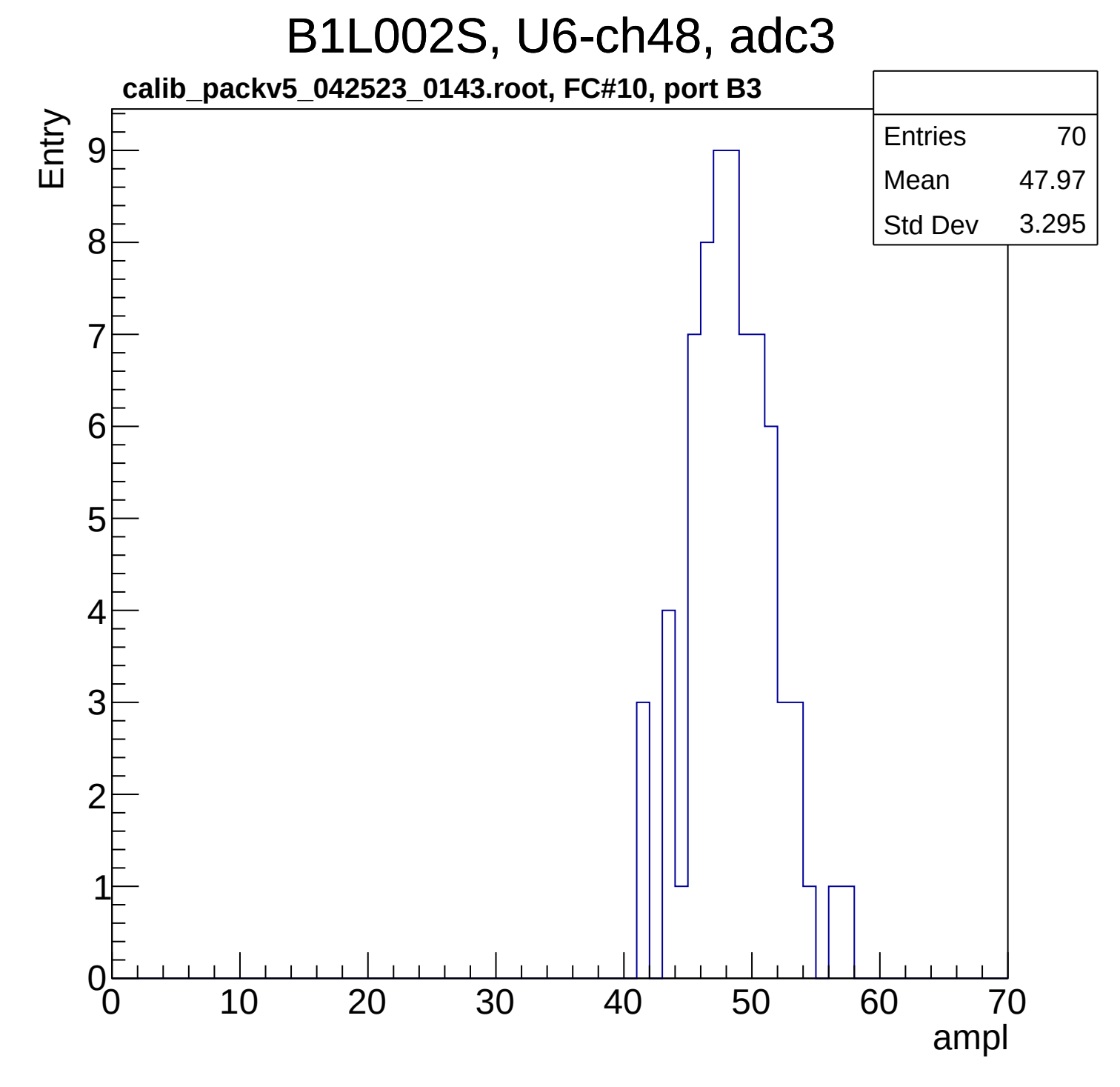

B1L002S, U6-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 62.06
Std Dev 1.474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

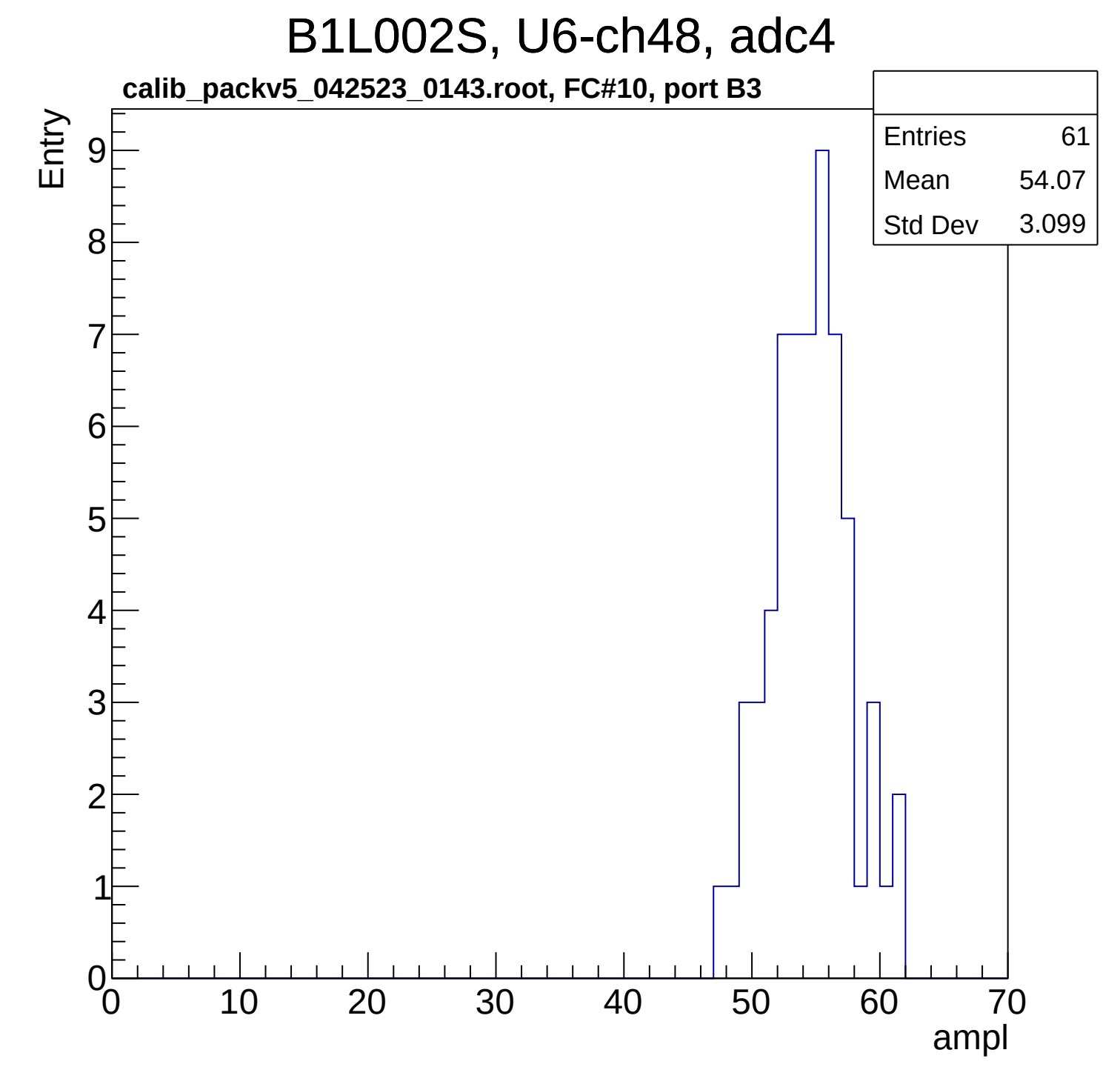

B1L002S, U6-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

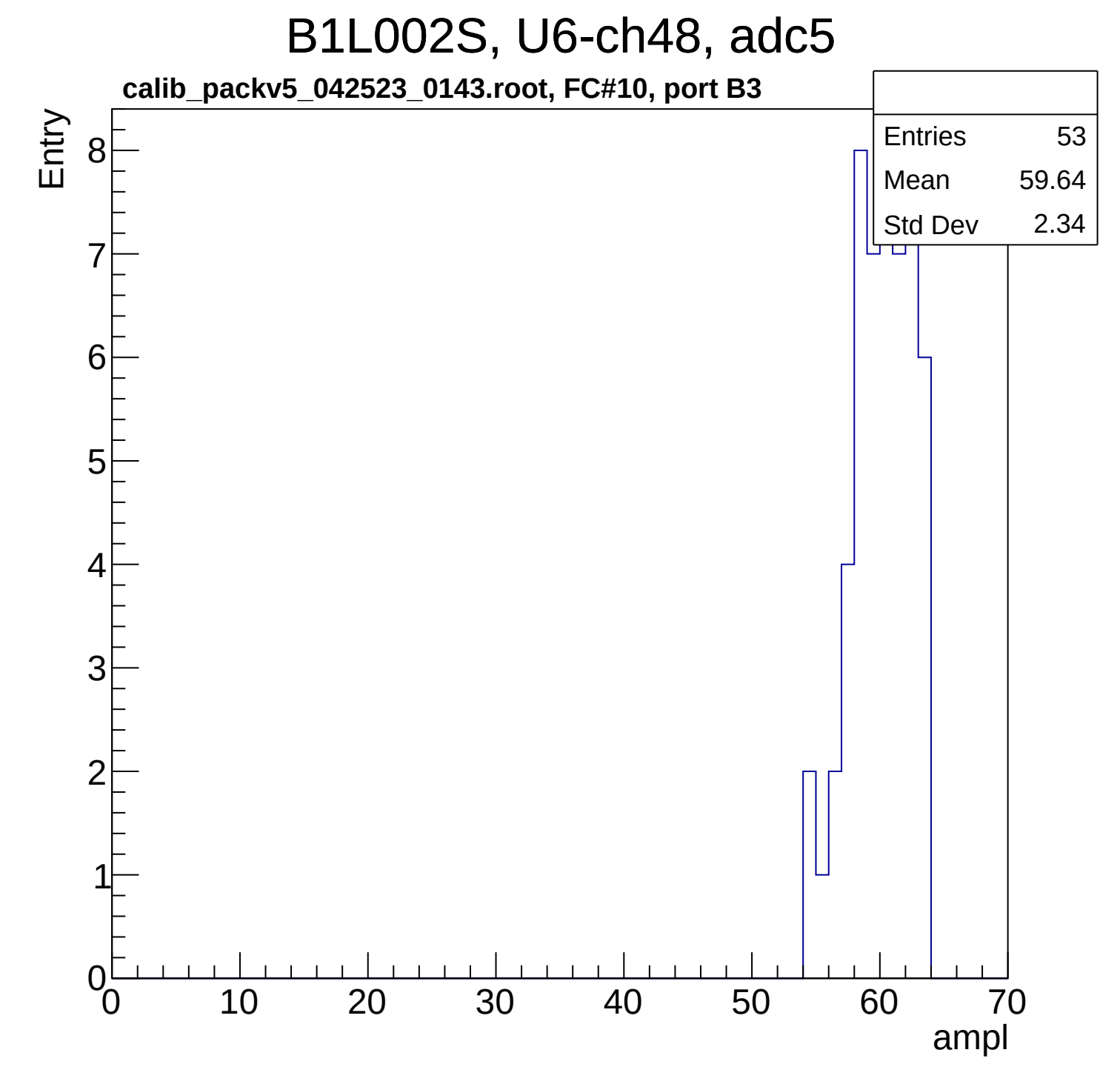

B1L002S, U6-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.33
Std Dev 2.577
Gaus mean : 30.0122
Gaus Width: 2.8201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

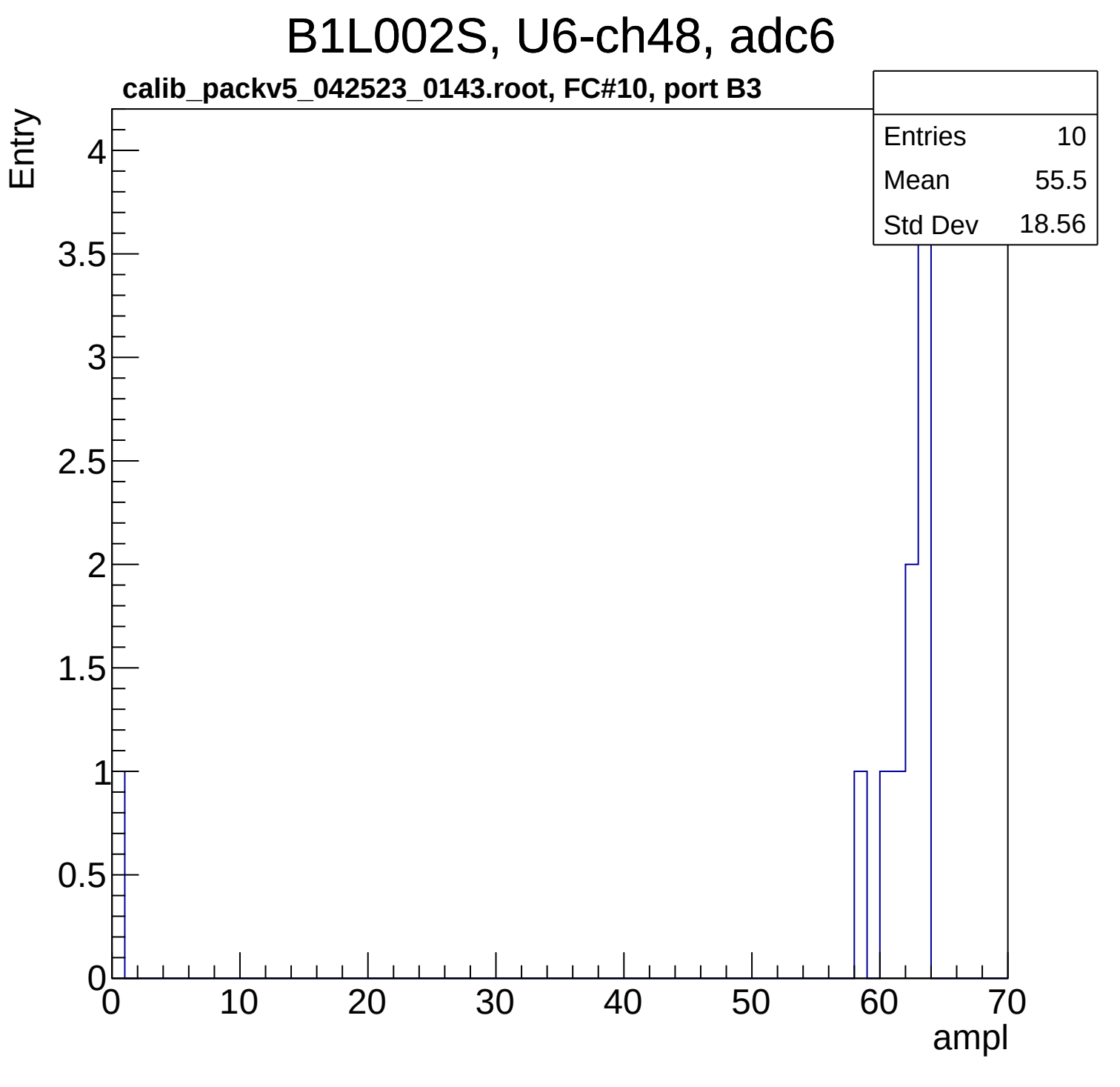

B1L002S, U6-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 36.55
Std Dev 3.371
Gaus mean : 36.8839
Gaus Width: 3.5422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

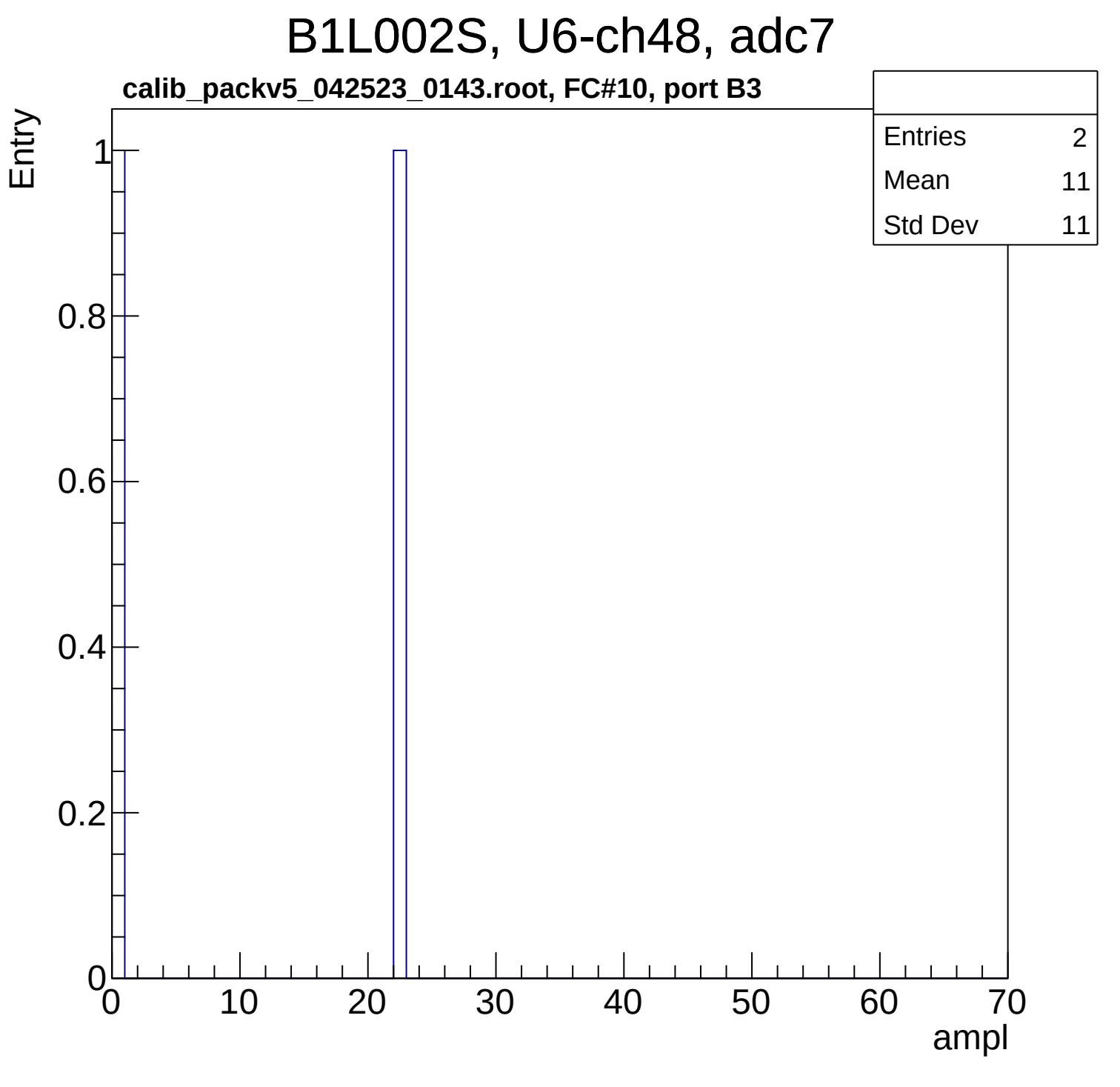

B1L002S, U6-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 41.5
Std Dev 2.528
Gaus mean : 41.9505
Gaus Width: 3.3810
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

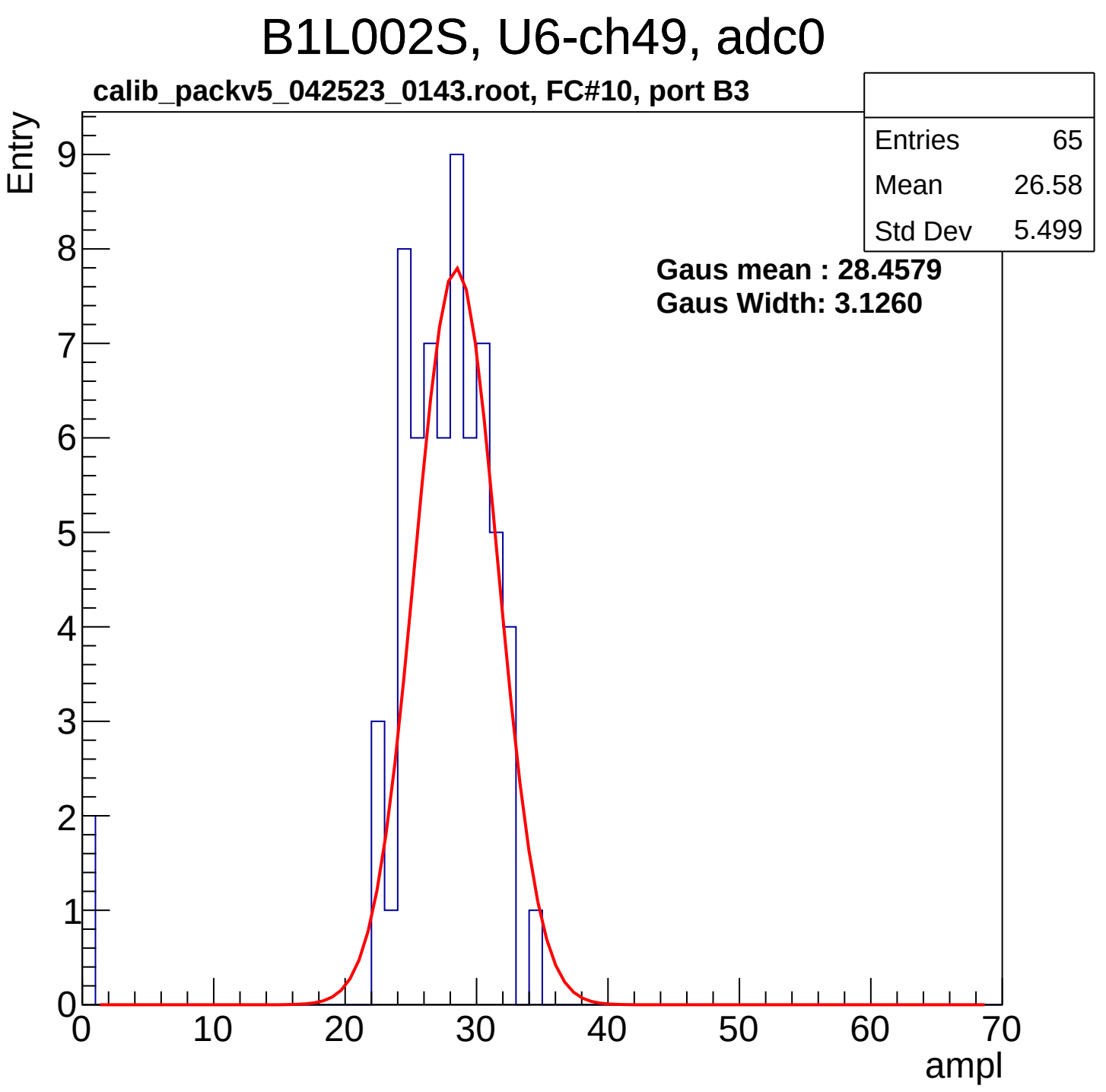

B1L002S, U6-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.97
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

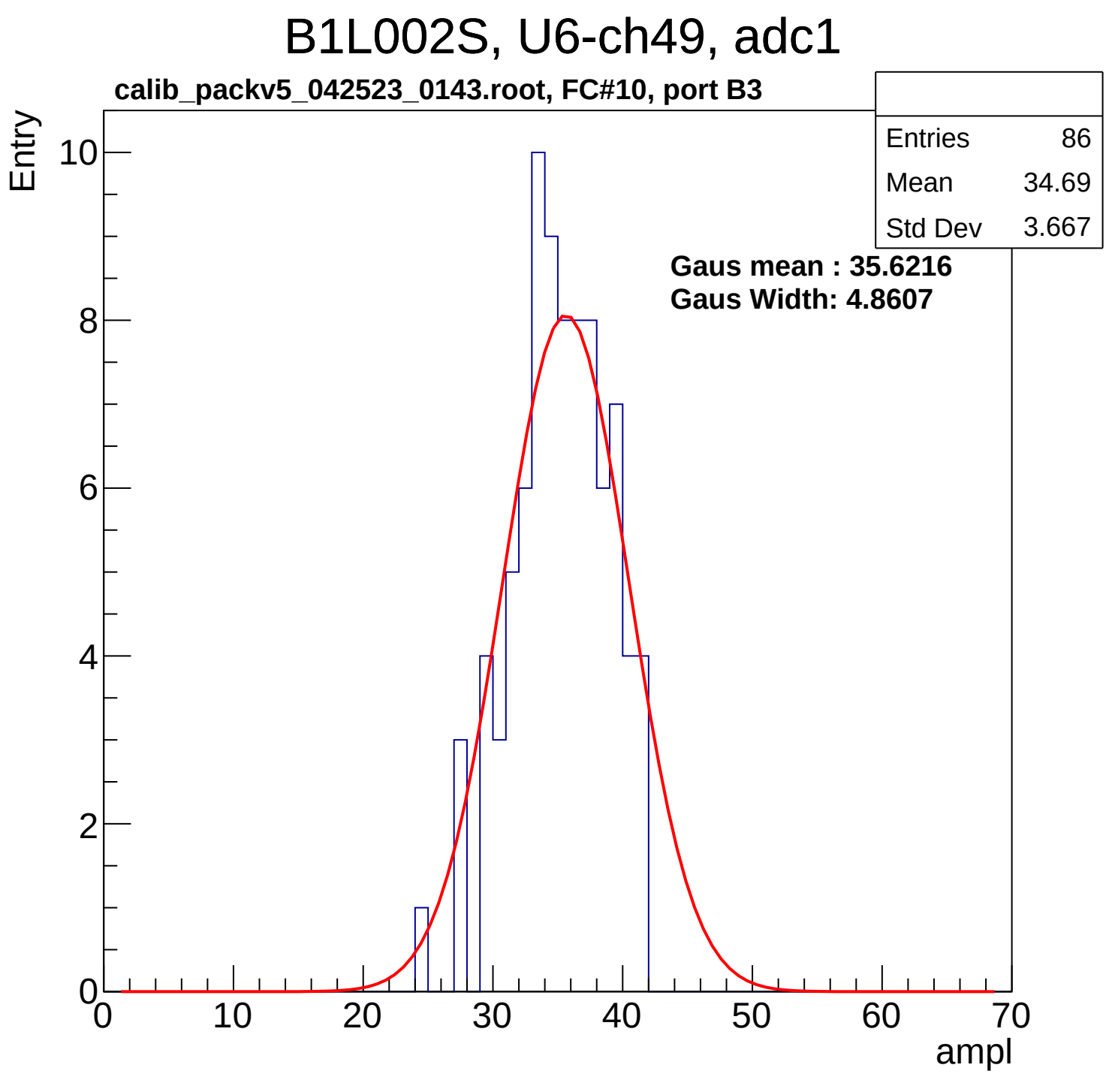

B1L002S, U6-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.07
Std Dev 3.099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

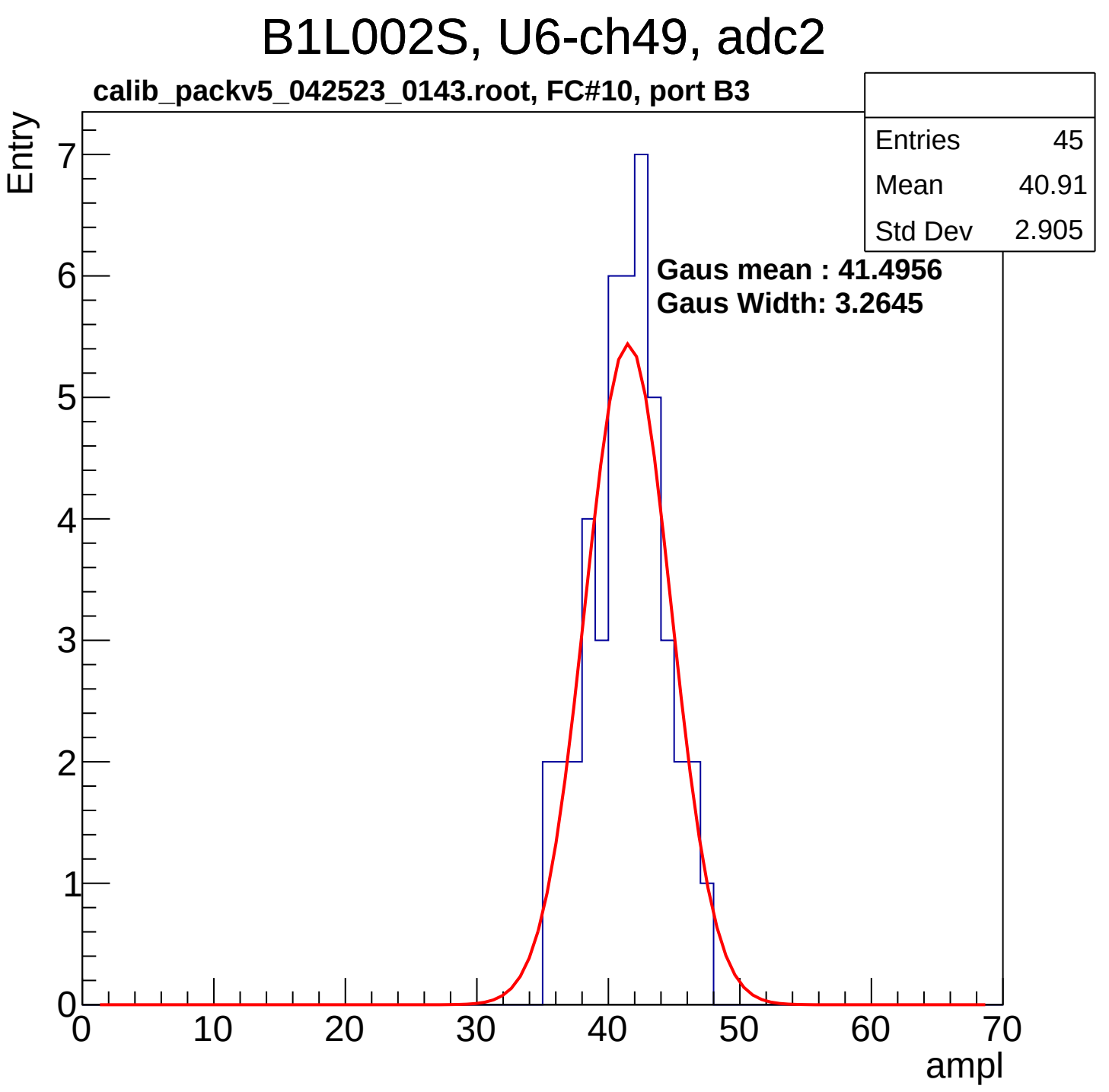

B1L002S, U6-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.64
Std Dev 2.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

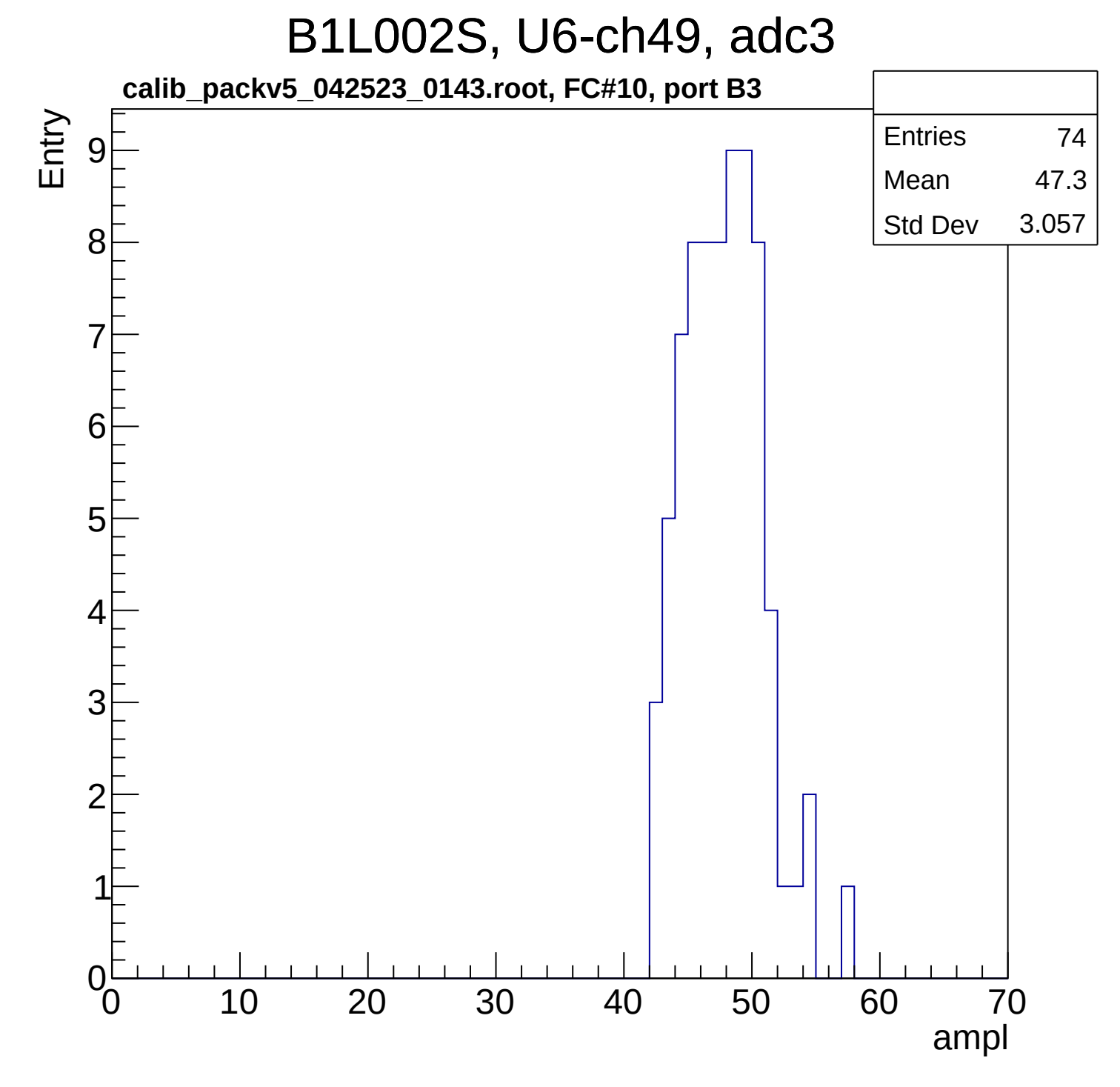

B1L002S, U6-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.5
Std Dev 18.56
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

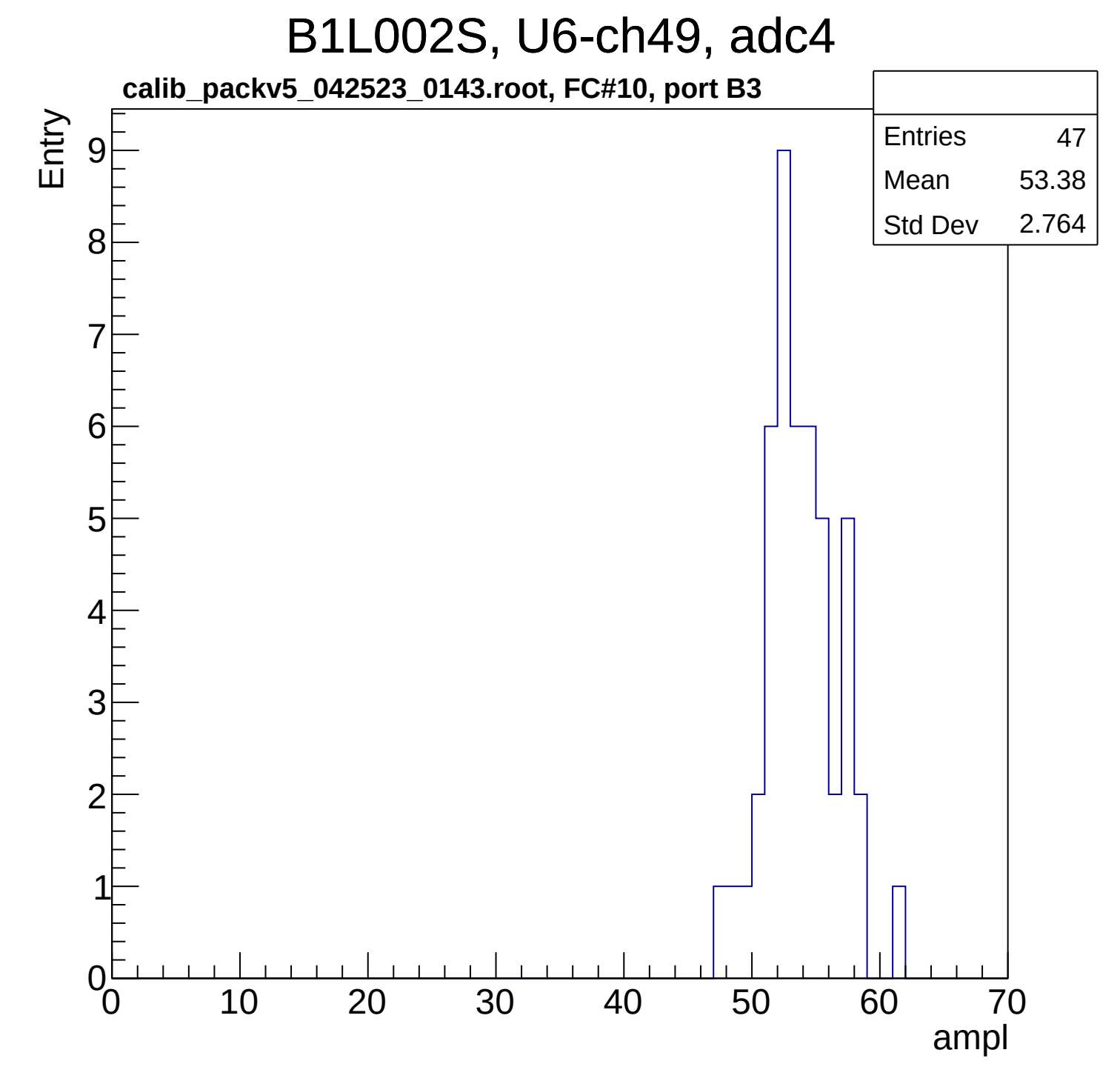

B1L002S, U6-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

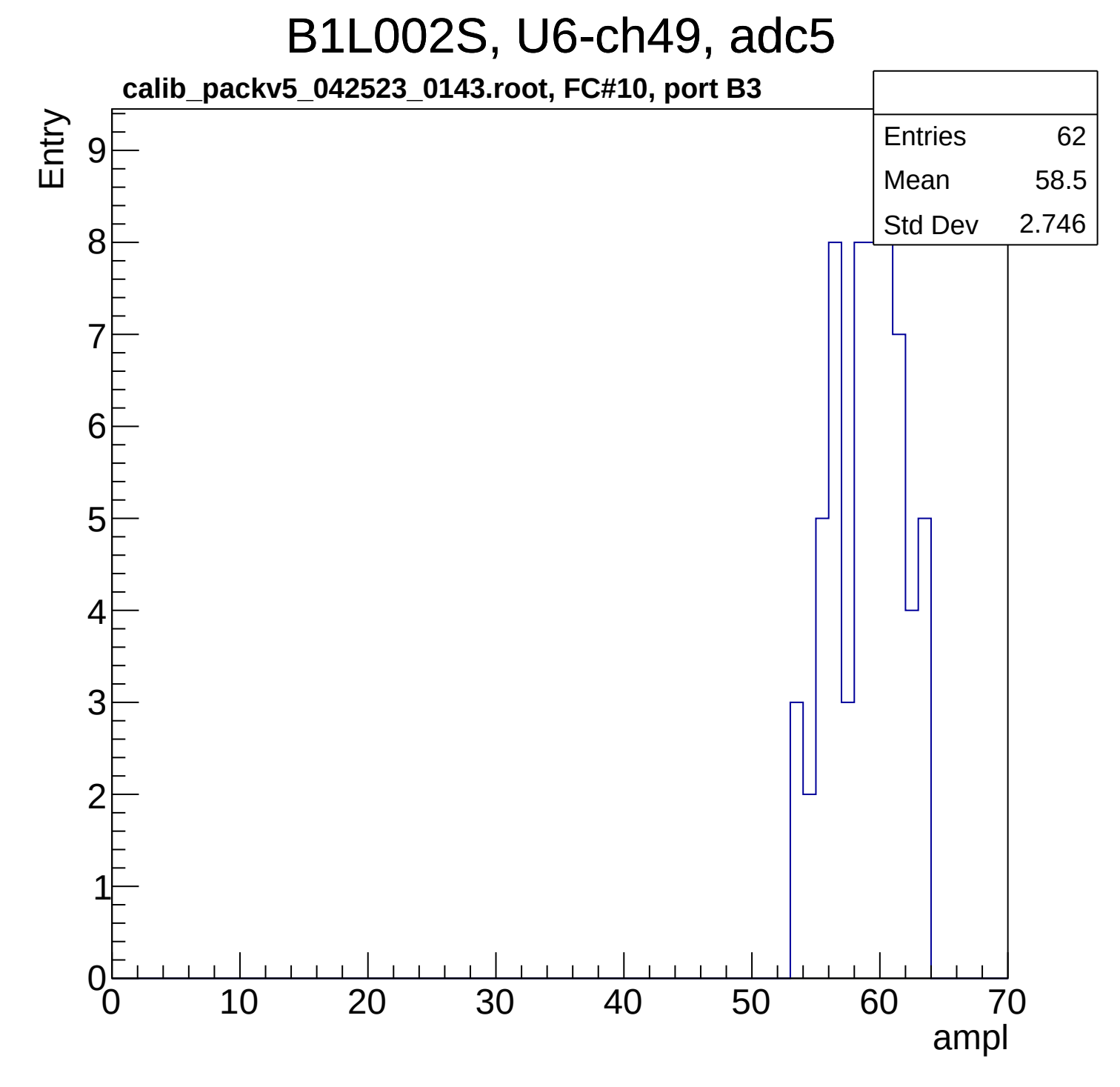

B1L002S, U6-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.58
Std Dev 5.499
Gaus mean : 28.4579
Gaus Width: 3.1260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

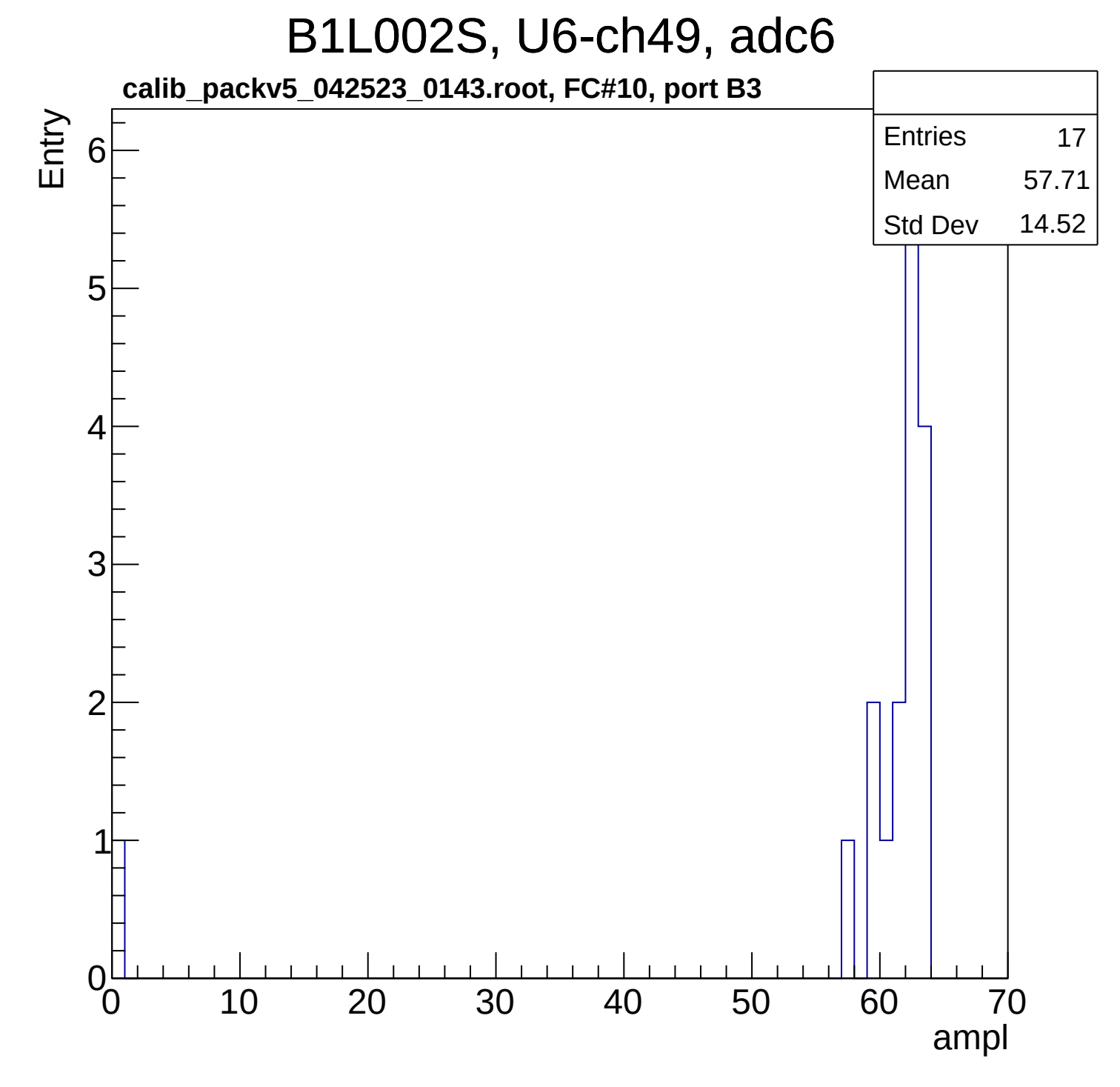

B1L002S, U6-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 34.69
Std Dev 3.667
Gaus mean : 35.6216
Gaus Width: 4.8607
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

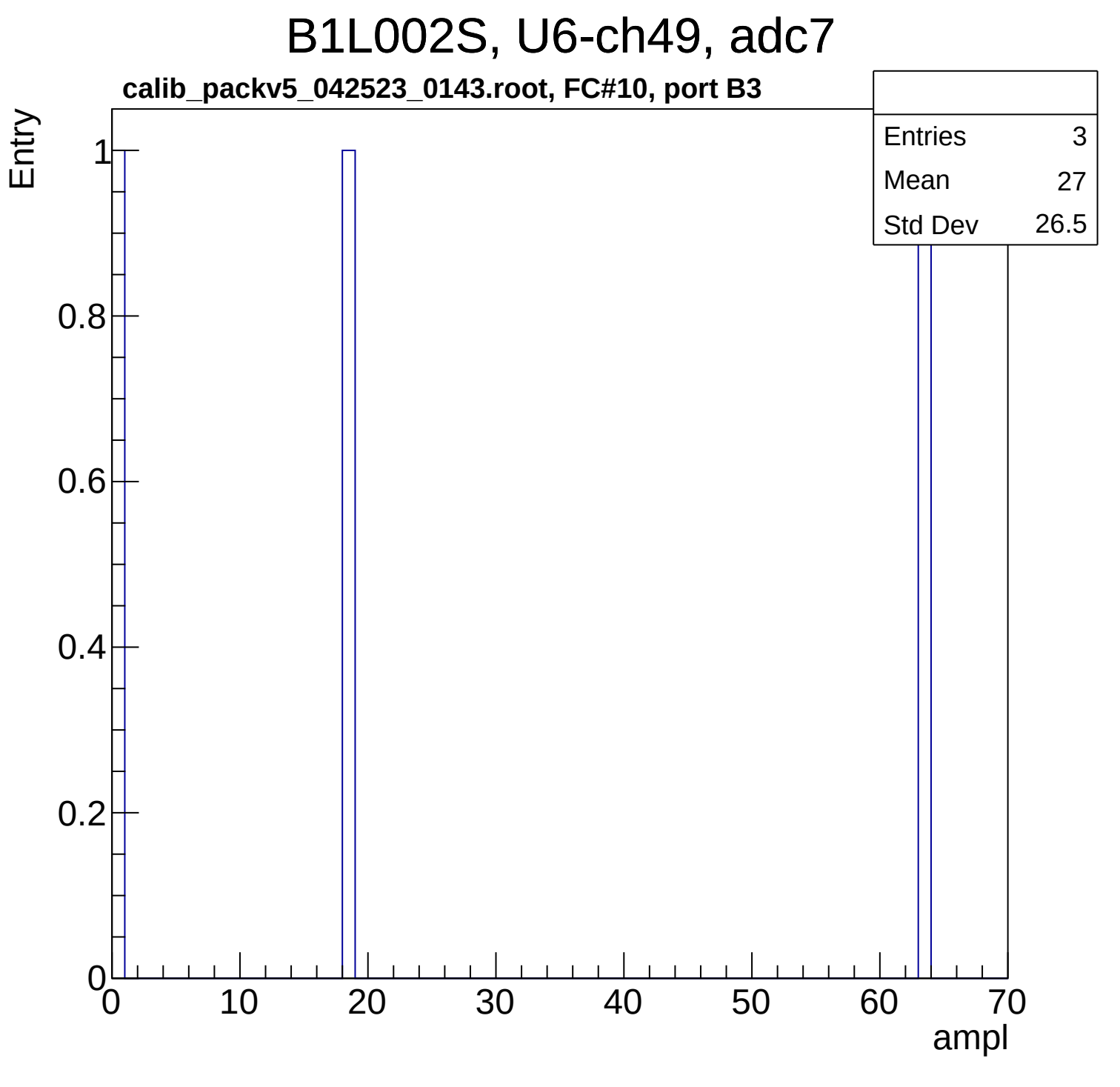

B1L002S, U6-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 40.91
Std Dev 2.905
Gaus mean : 41.4956
Gaus Width: 3.2645
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

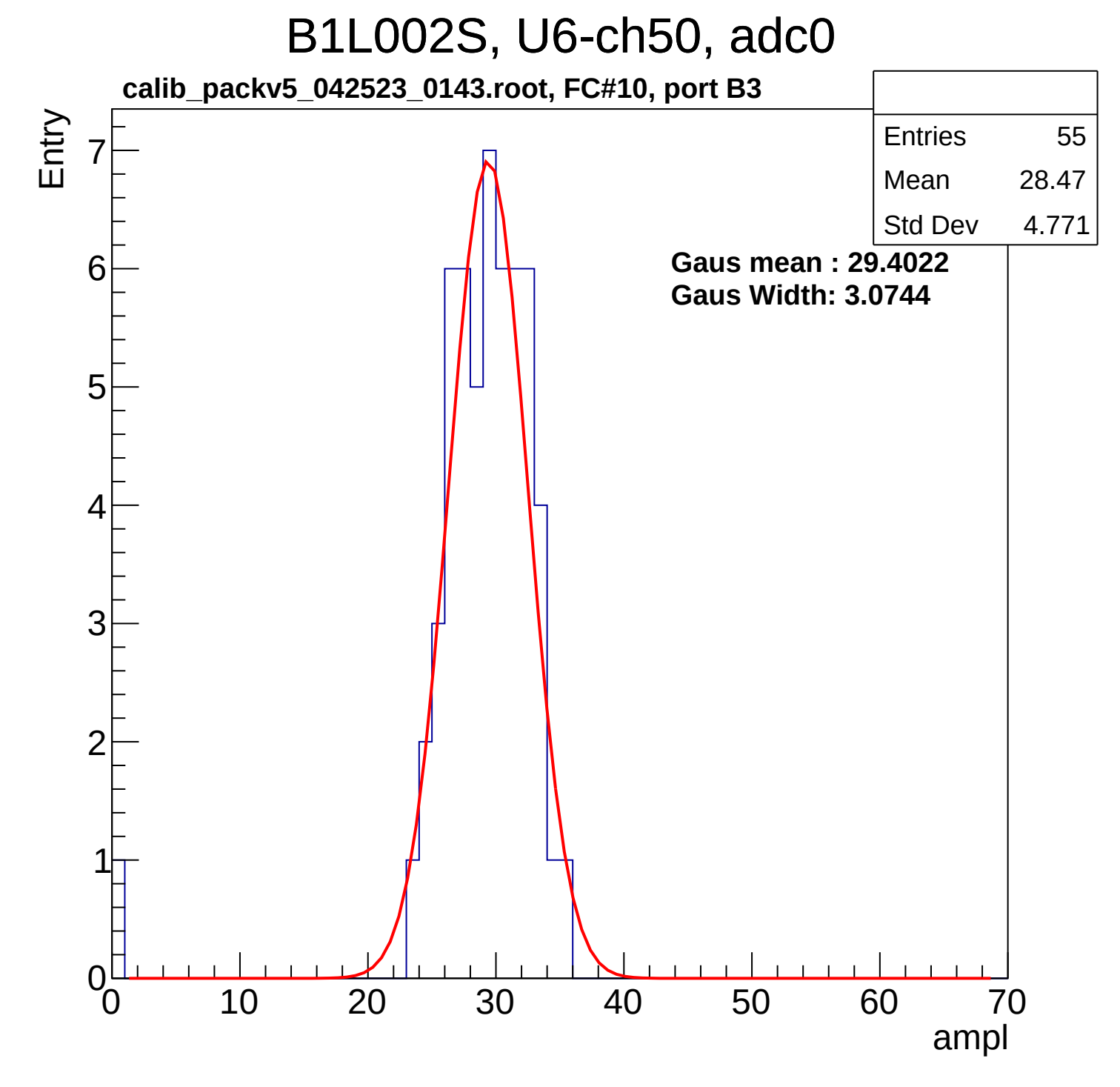

B1L002S, U6-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.3
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

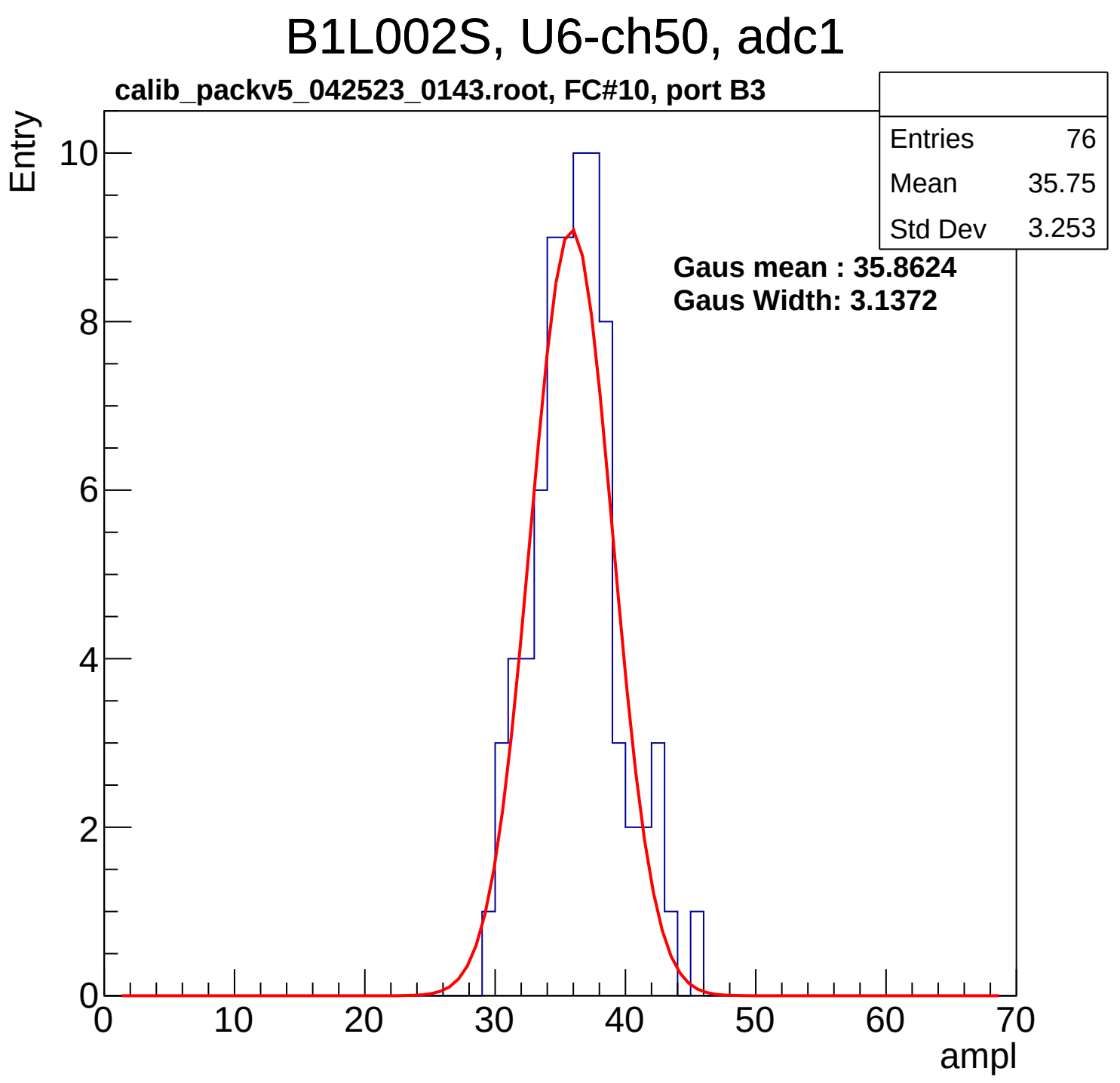

B1L002S, U6-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.38
Std Dev 2.764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

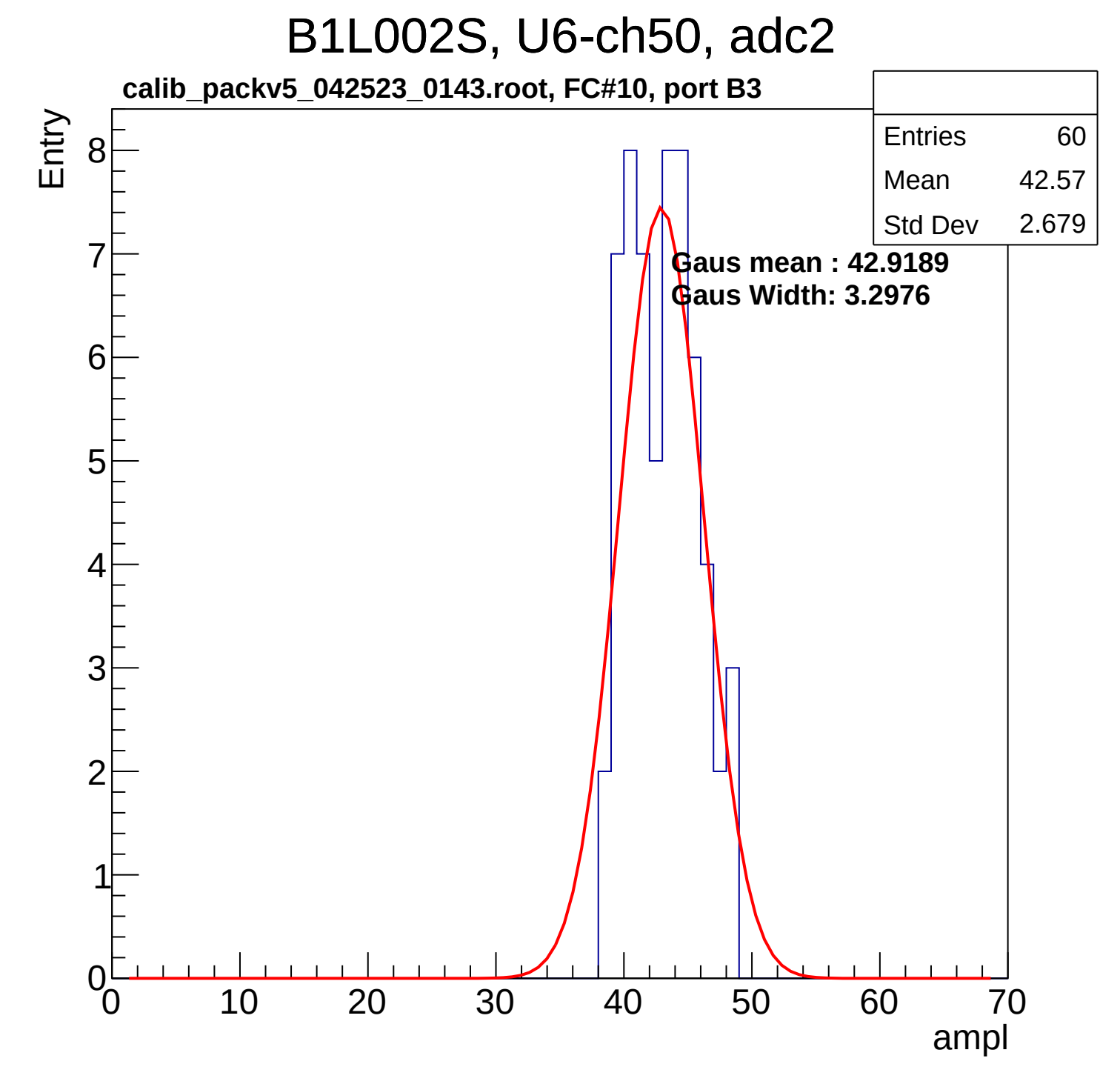

B1L002S, U6-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.5
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

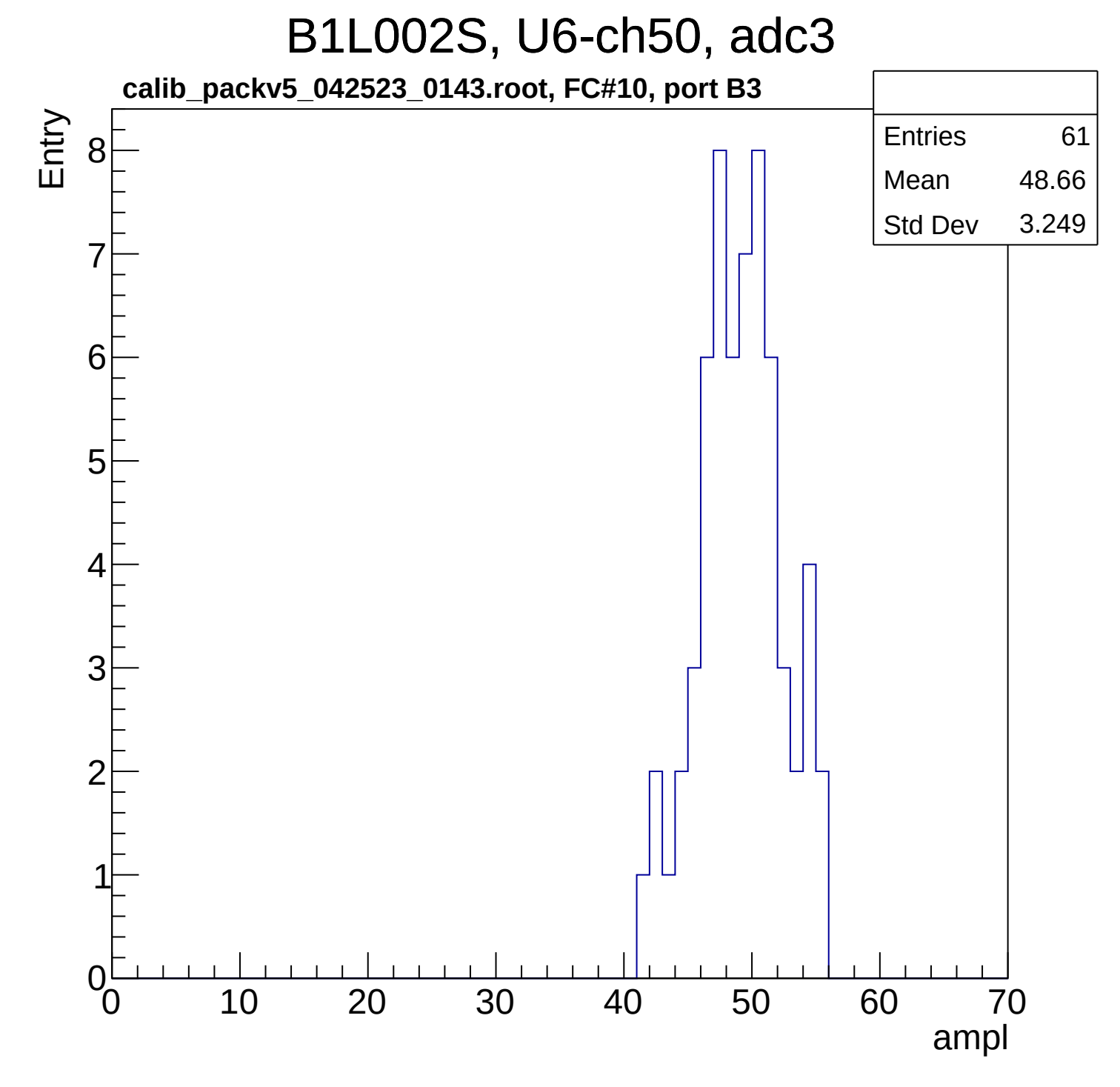

B1L002S, U6-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.71
Std Dev 14.52
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

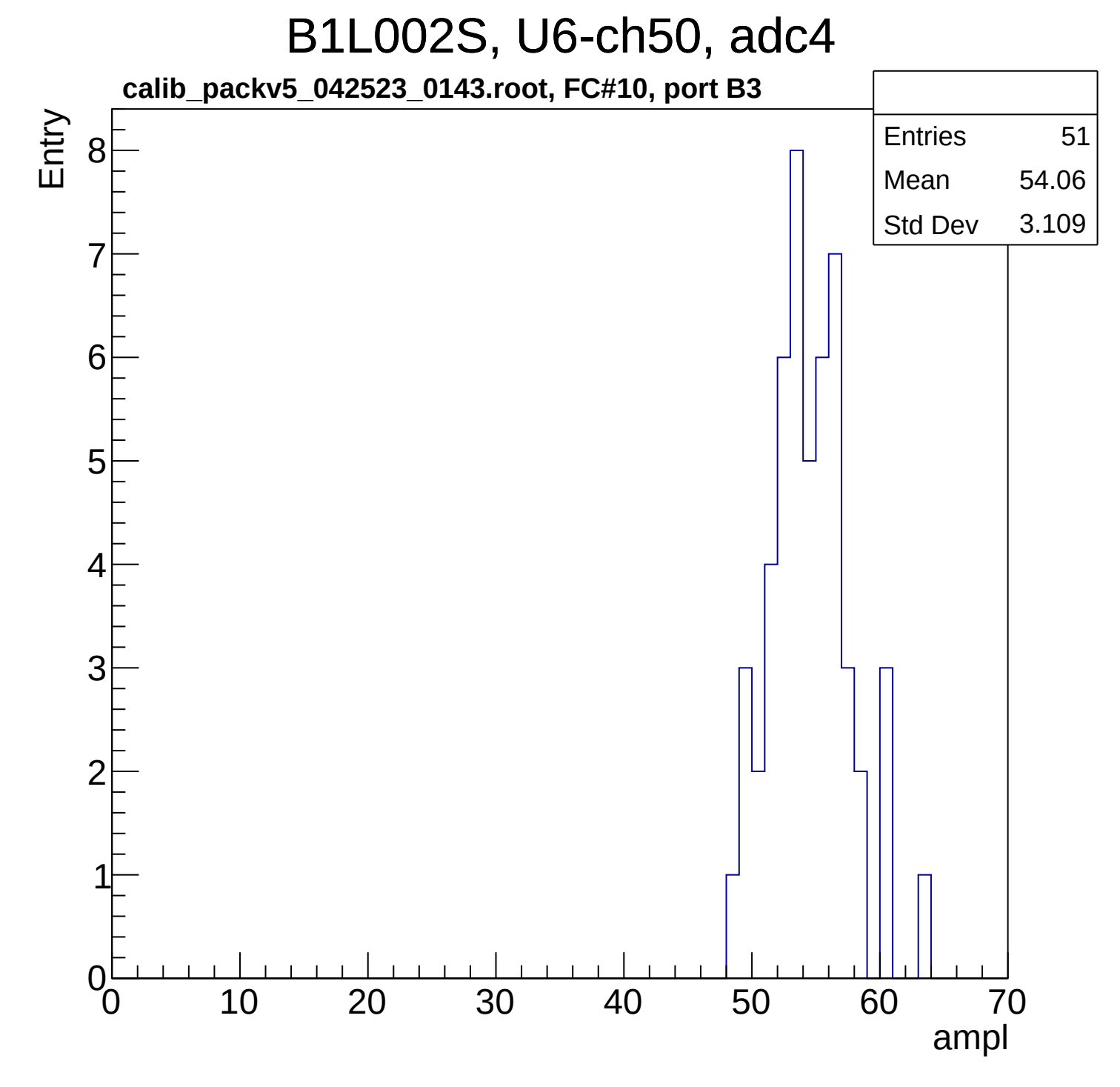

B1L002S, U6-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27
Std Dev 26.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

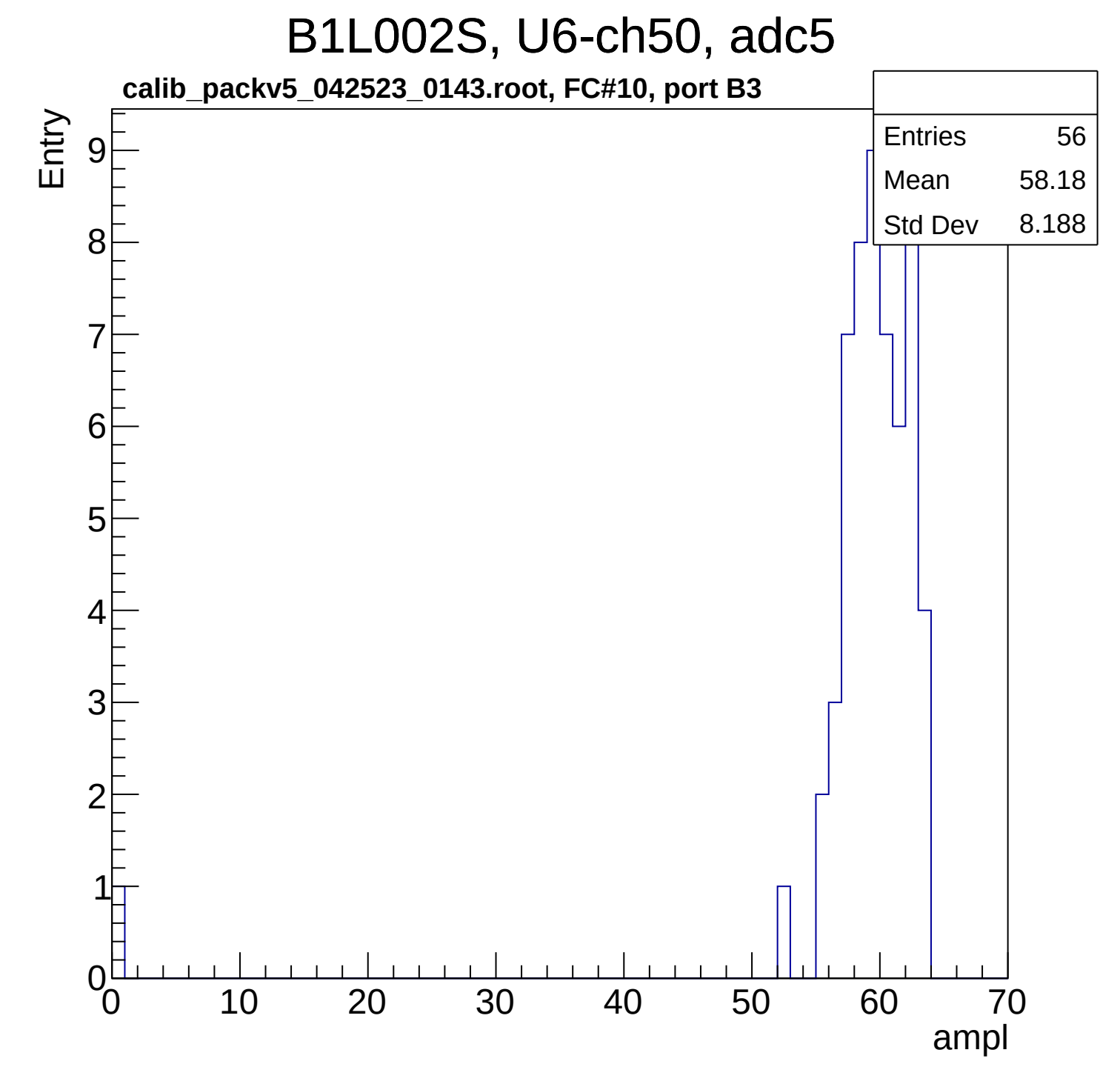

B1L002S, U6-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.47
Std Dev 4.771
Gaus mean : 29.4022
Gaus Width: 3.0744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

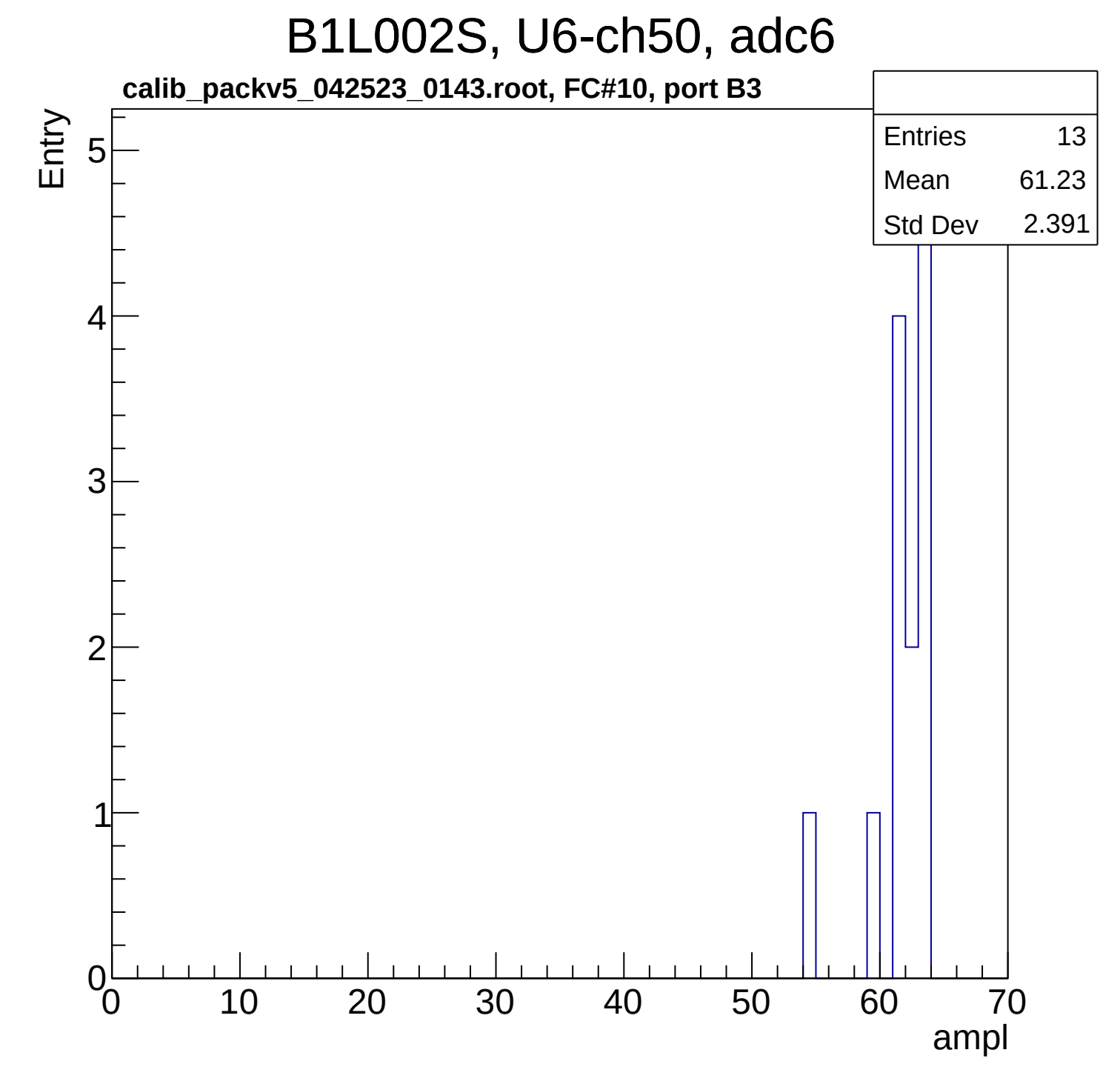

B1L002S, U6-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35.75
Std Dev 3.253
Gaus mean : 35.8624
Gaus Width: 3.1372
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

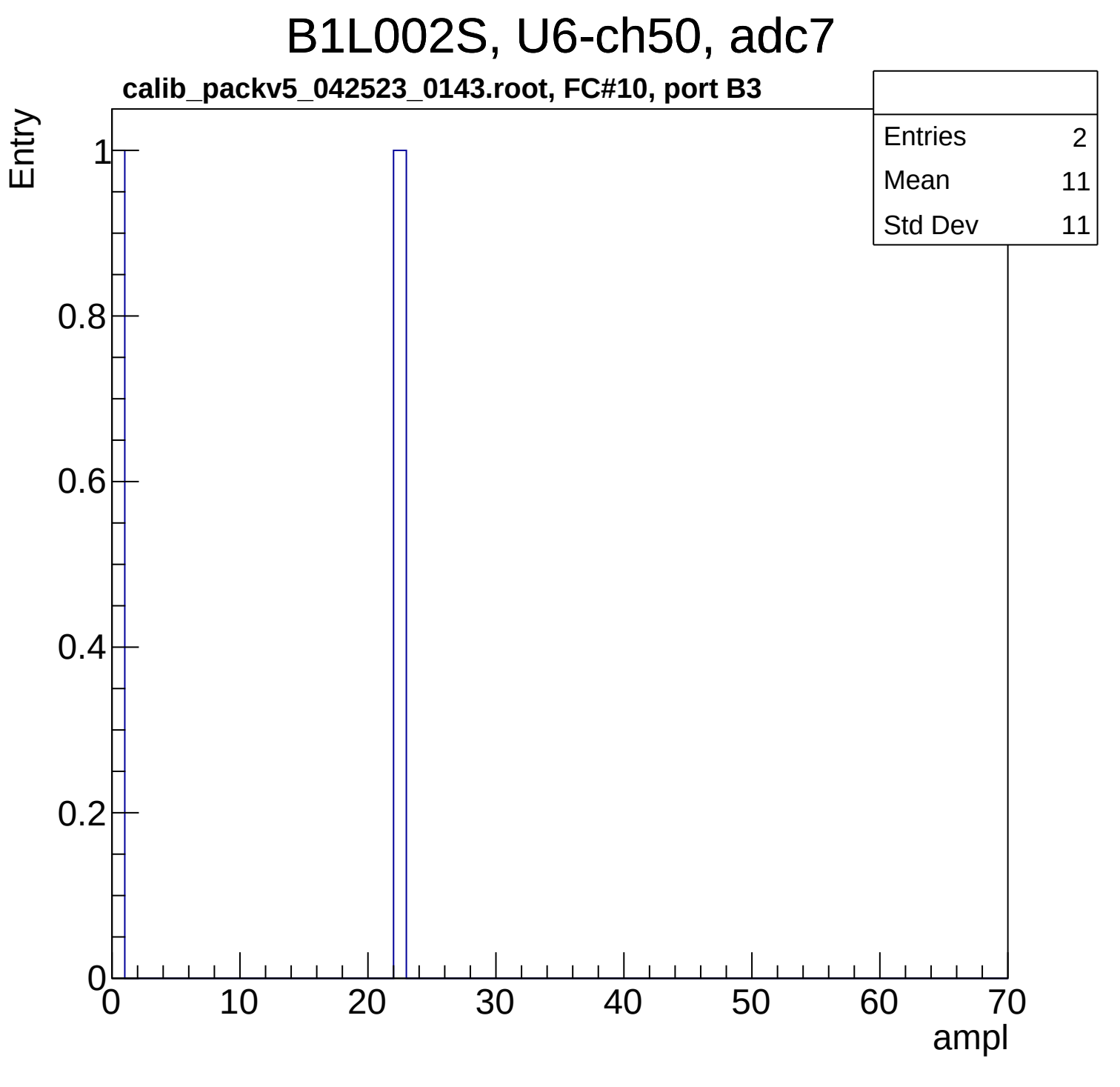

B1L002S, U6-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.57
Std Dev 2.679
Gaus mean : 42.9189
Gaus Width: 3.2976
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

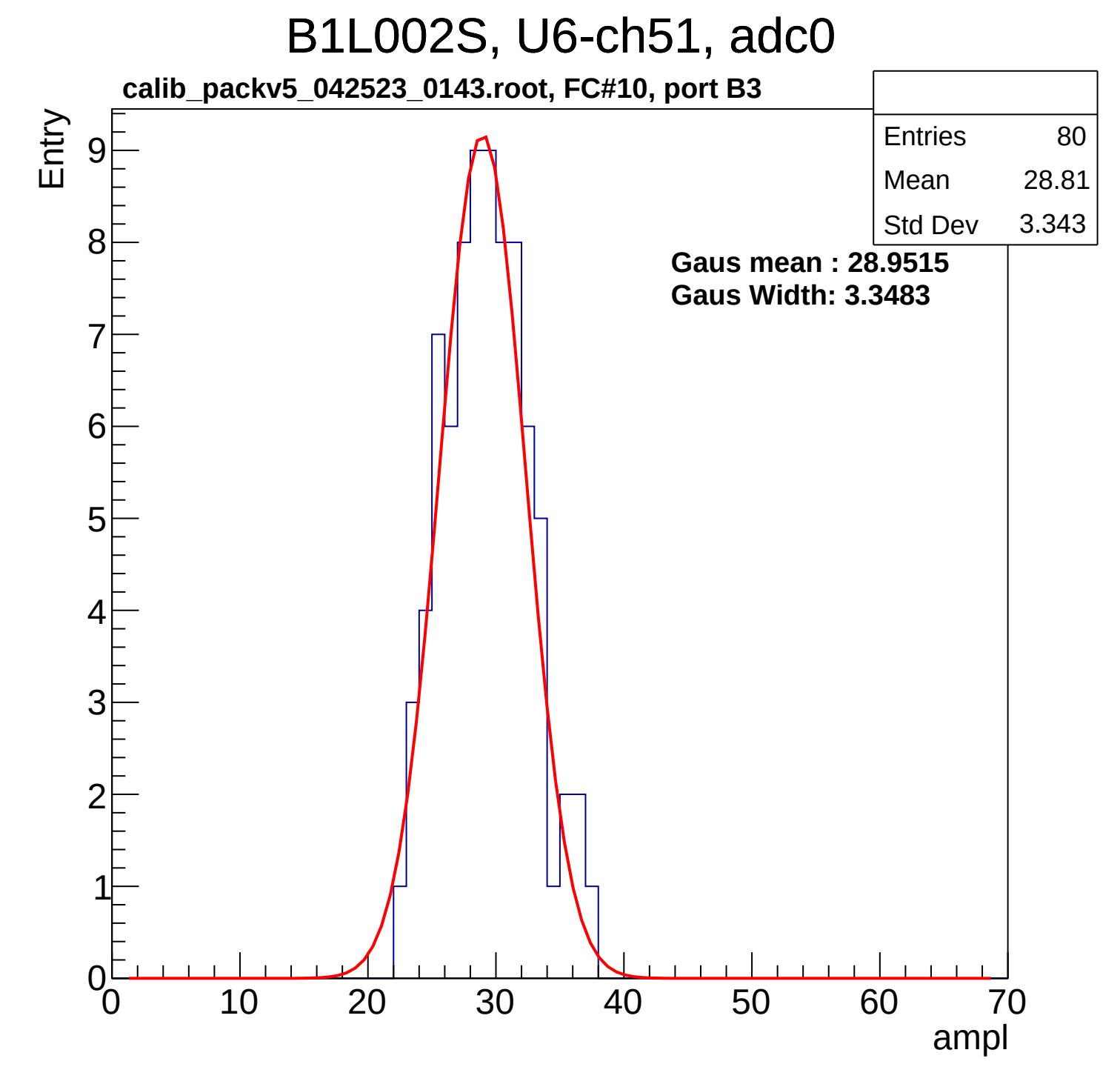

B1L002S, U6-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.66
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

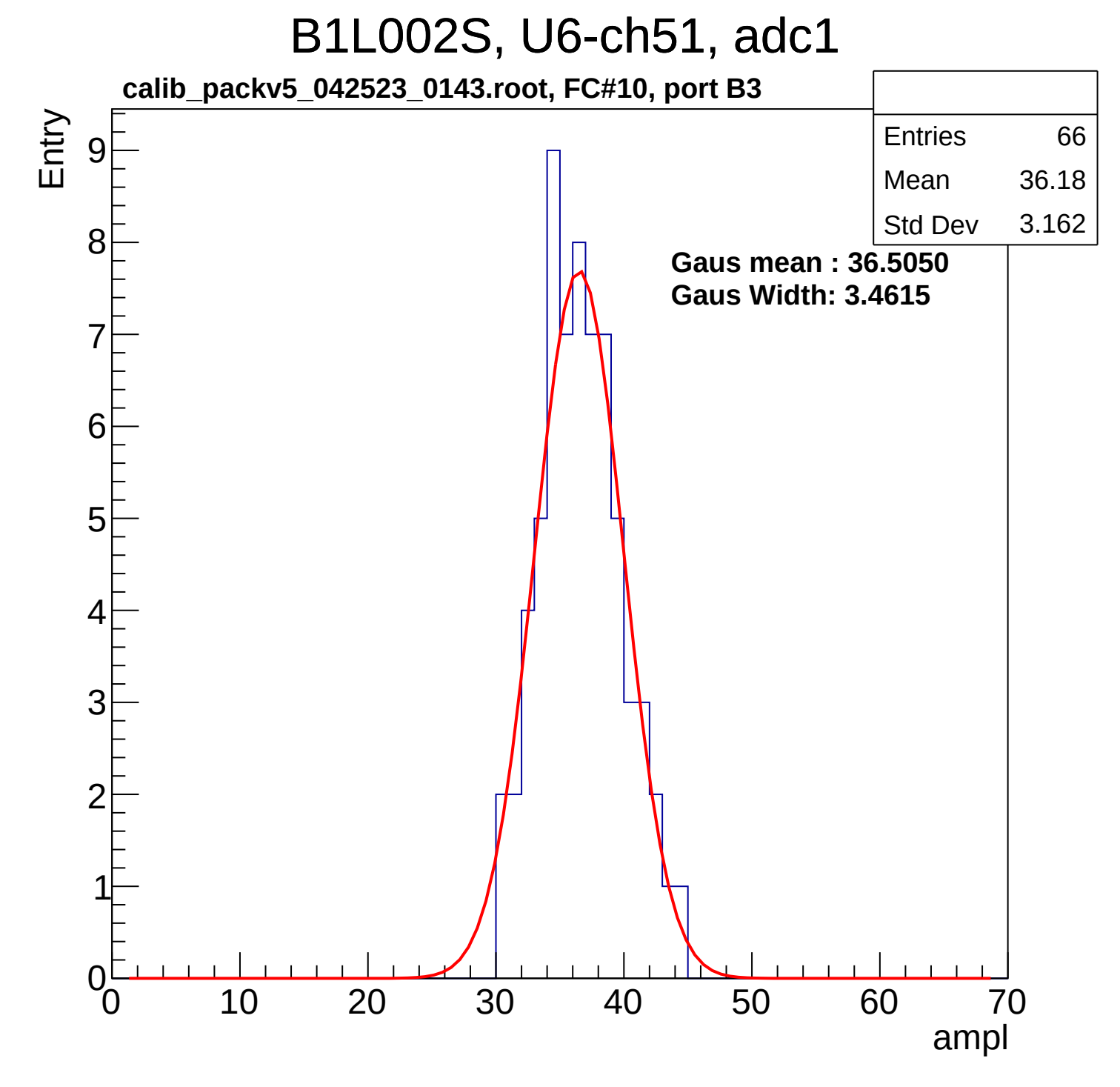

B1L002S, U6-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.06
Std Dev 3.109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

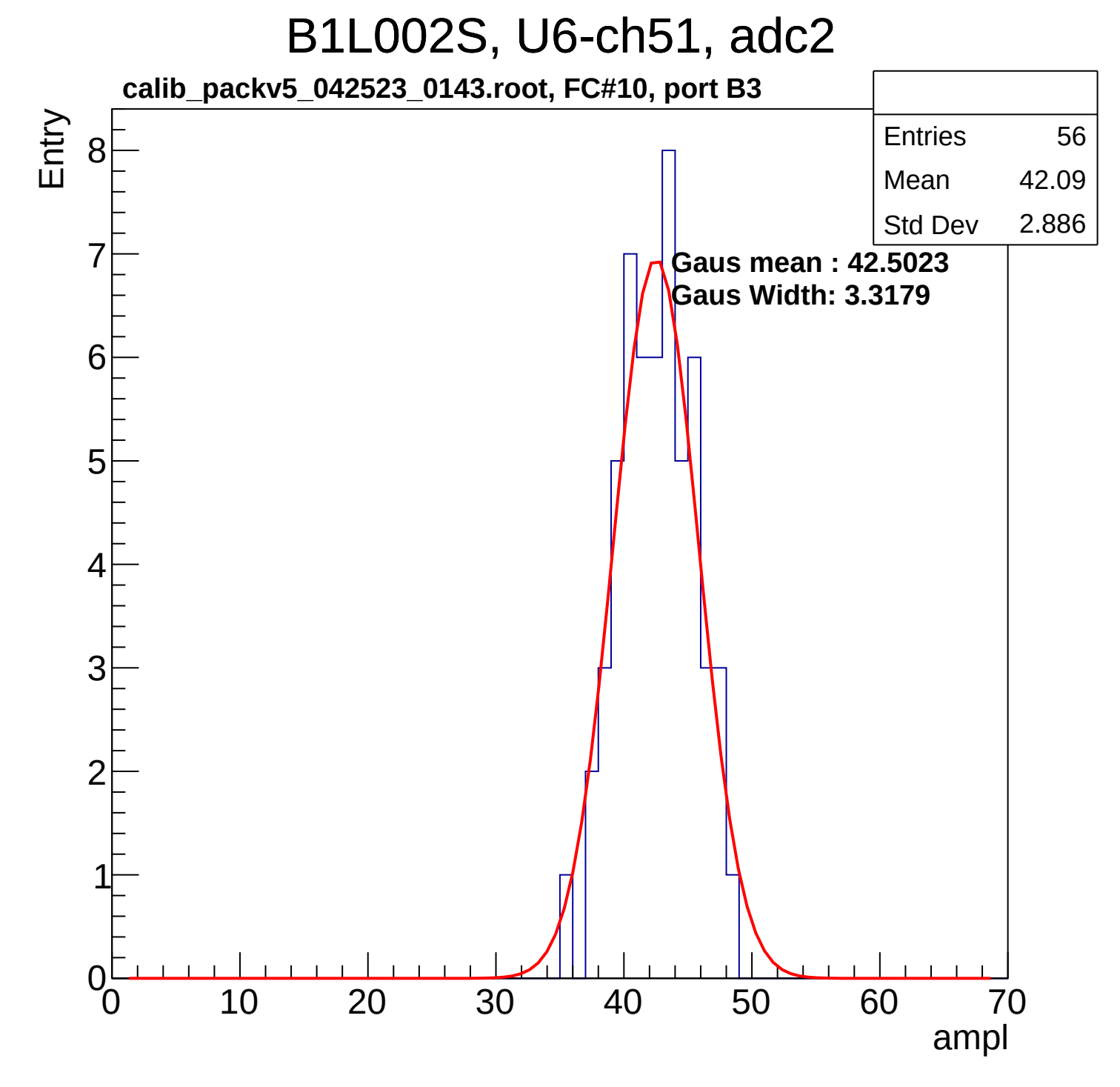

B1L002S, U6-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.18
Std Dev 8.188
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

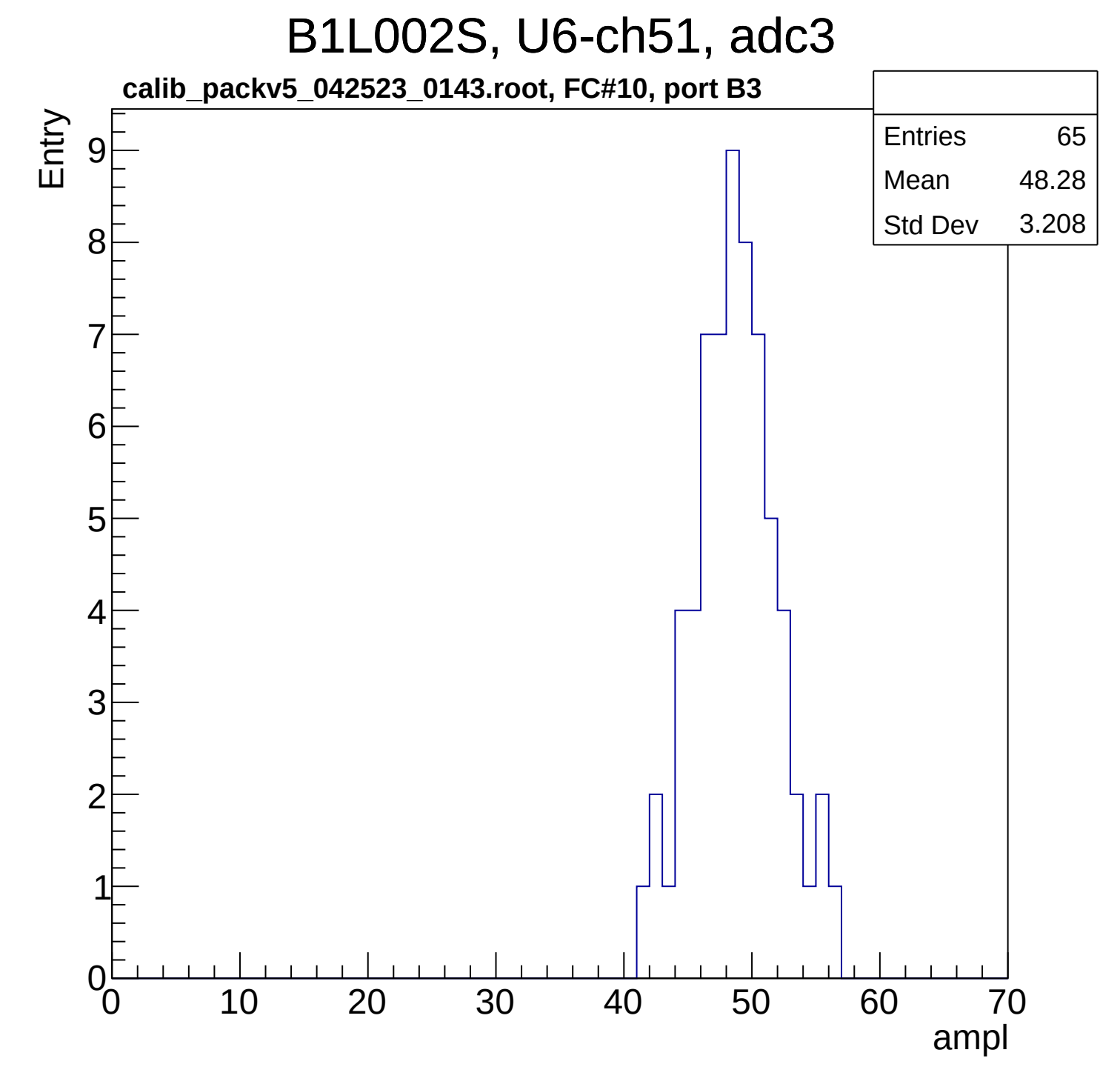

B1L002S, U6-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.23
Std Dev 2.391
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

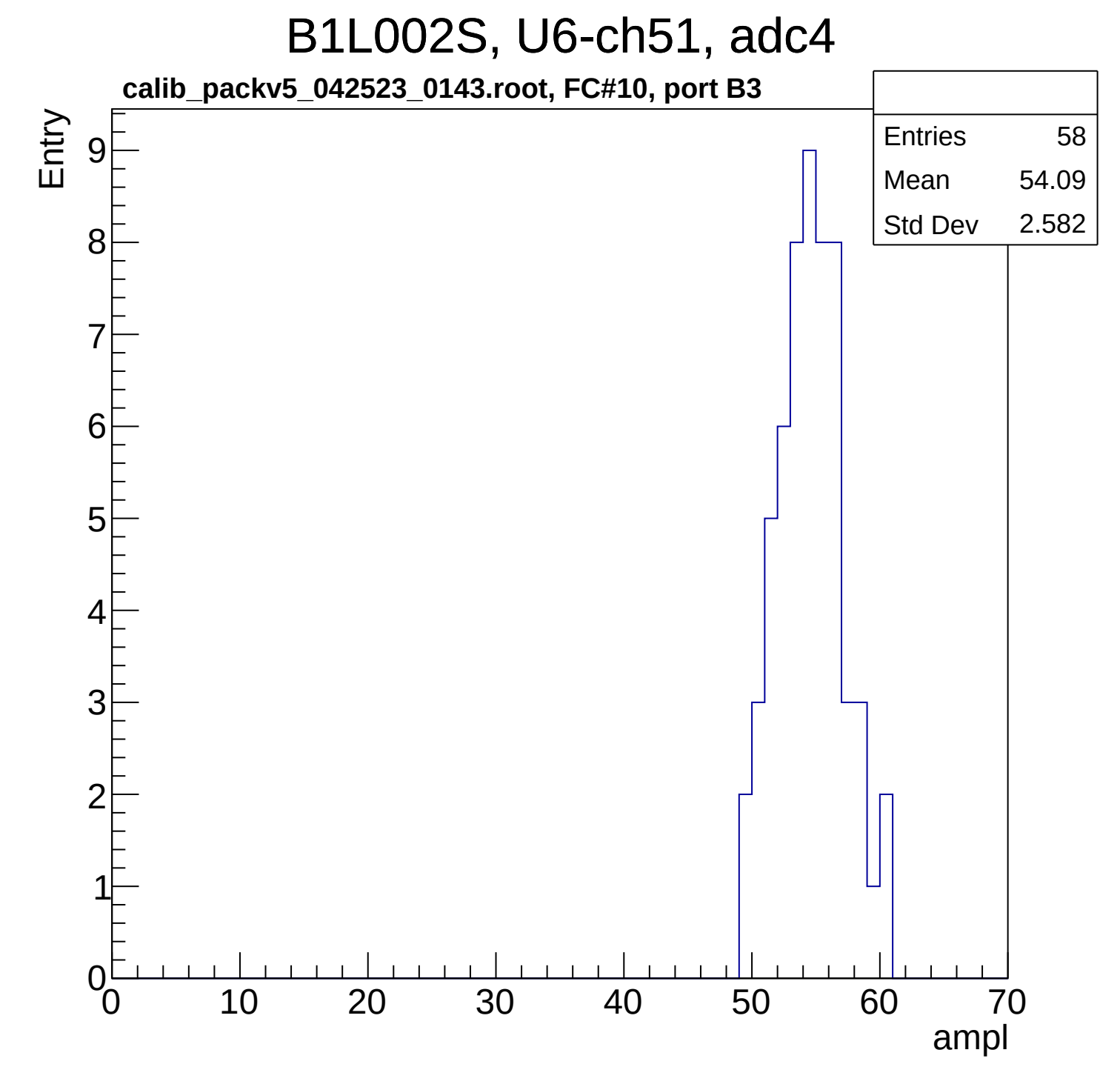

B1L002S, U6-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

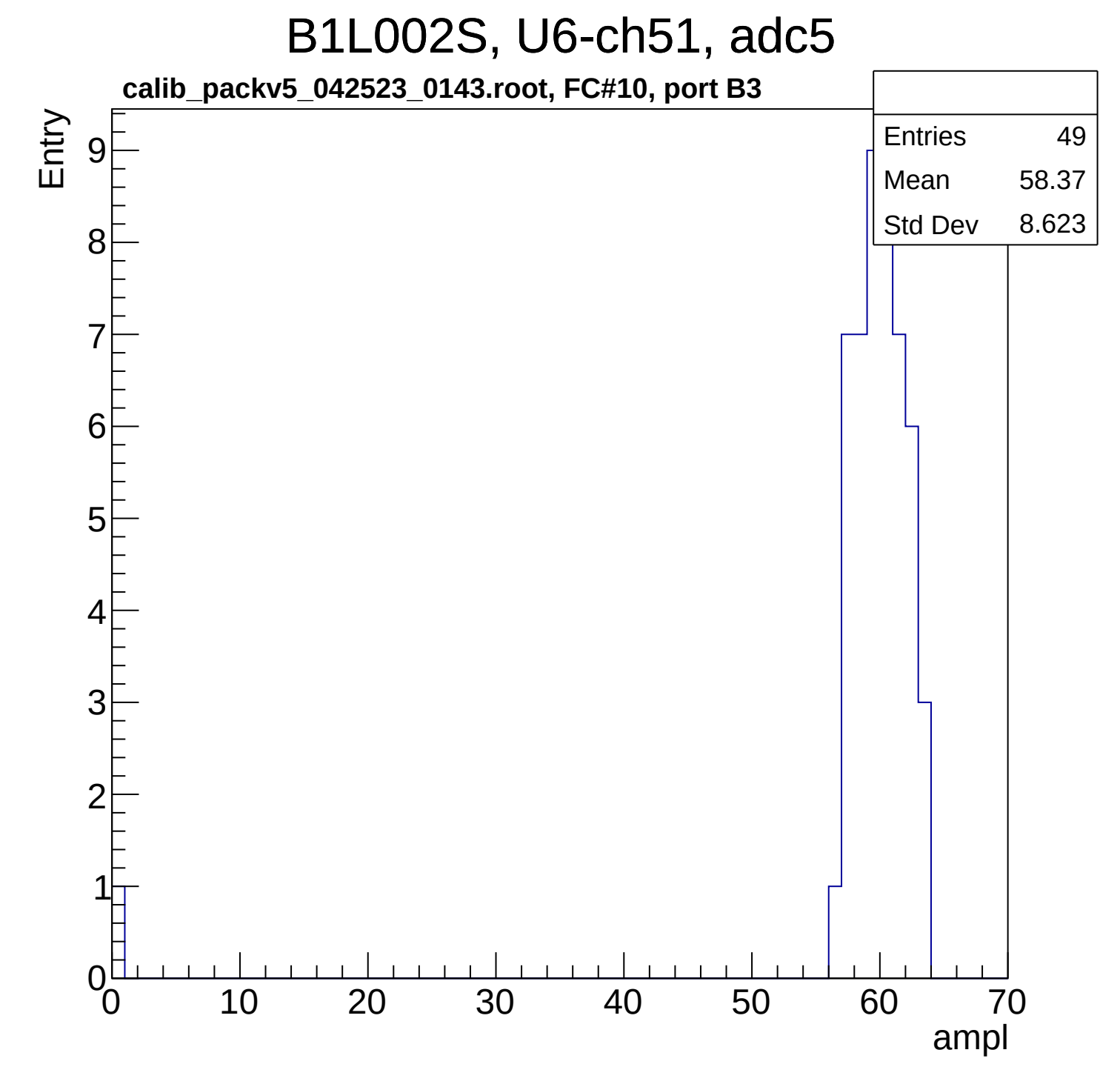

B1L002S, U6-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 28.81
Std Dev 3.343
Gaus mean : 28.9515
Gaus Width: 3.3483
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

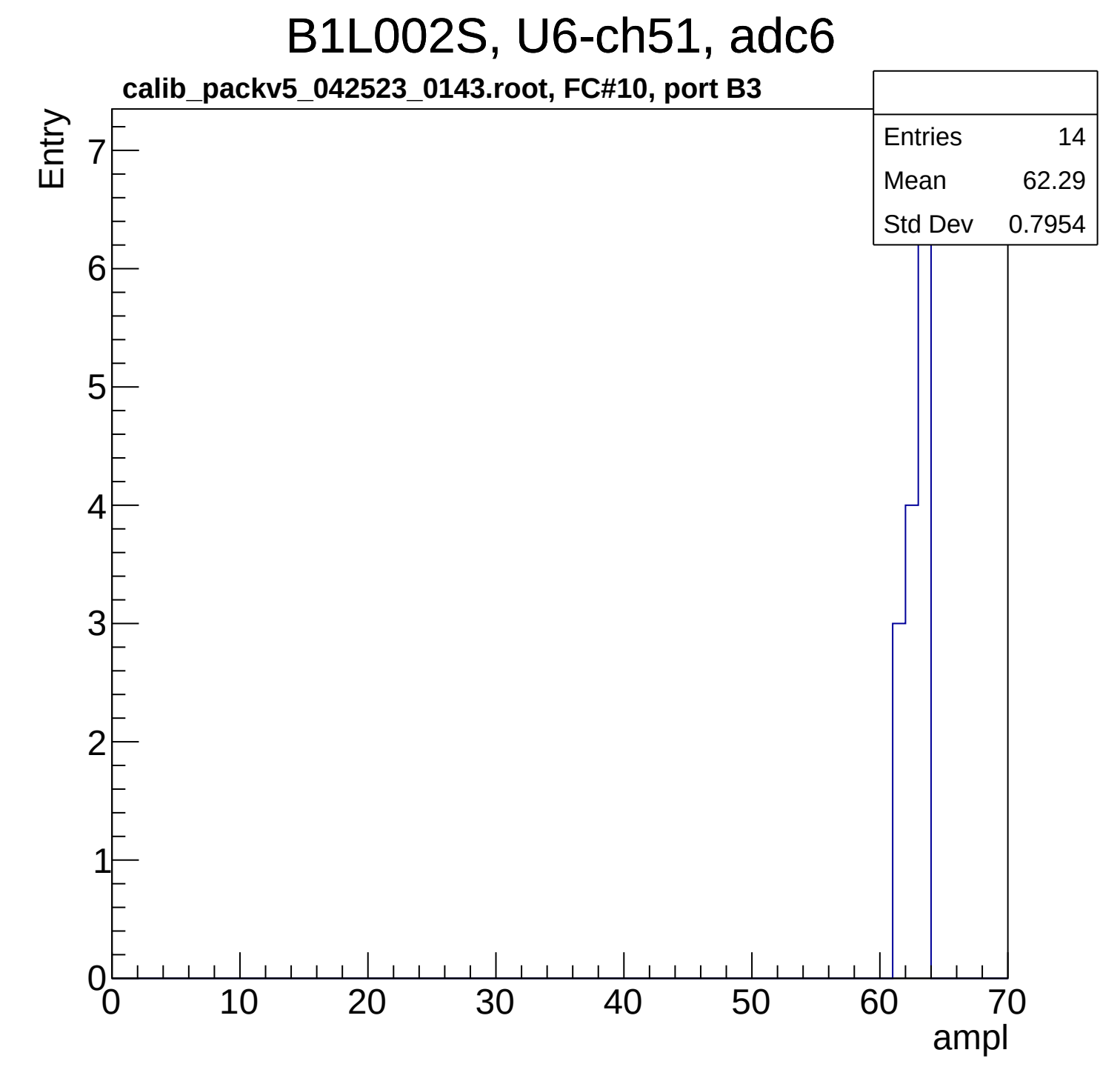

B1L002S, U6-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 36.18
Std Dev 3.162
Gaus mean : 36.5050
Gaus Width: 3.4615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

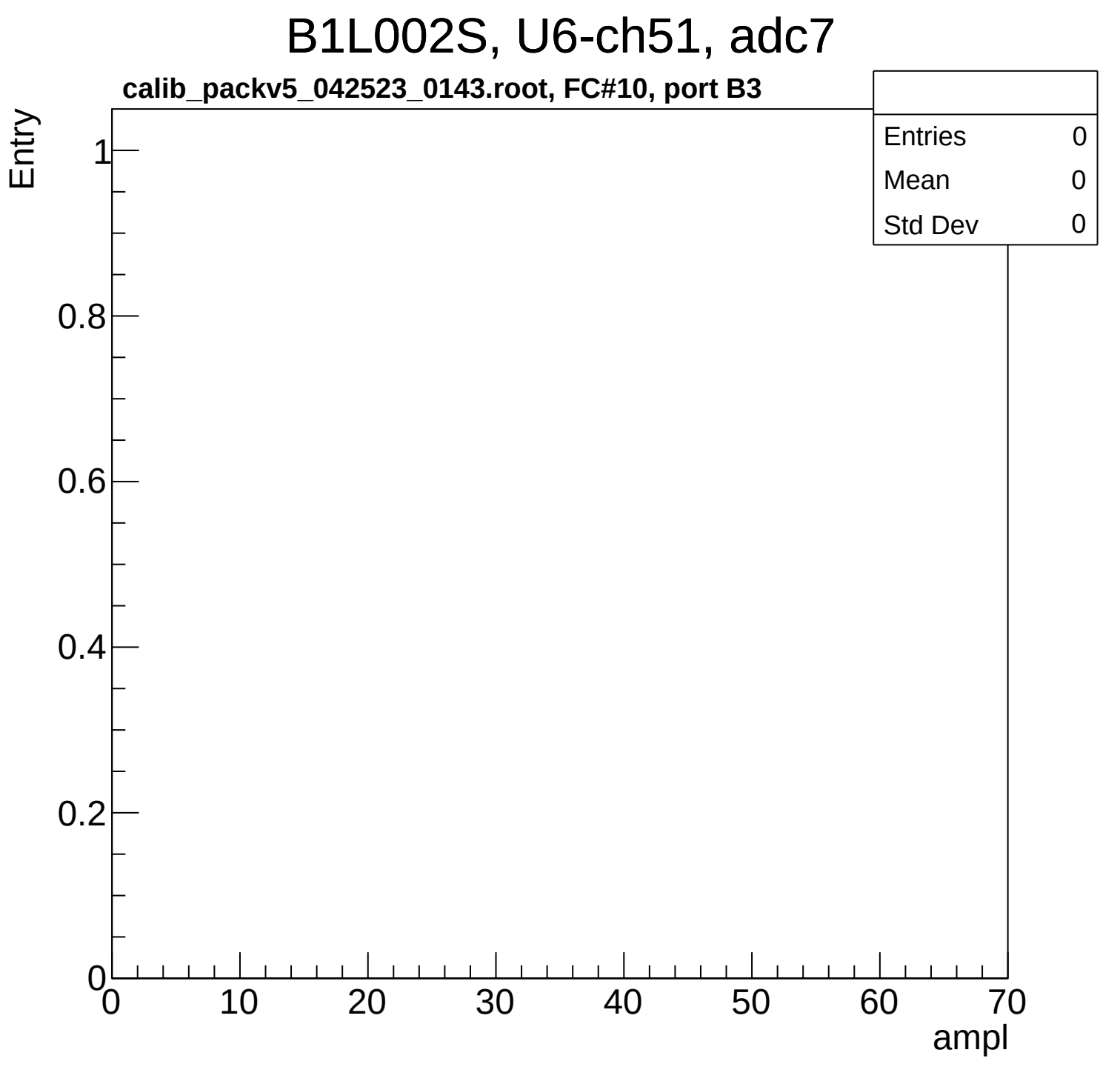

B1L002S, U6-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.09
Std Dev 2.886
Gaus mean : 42.5023
Gaus Width: 3.3179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

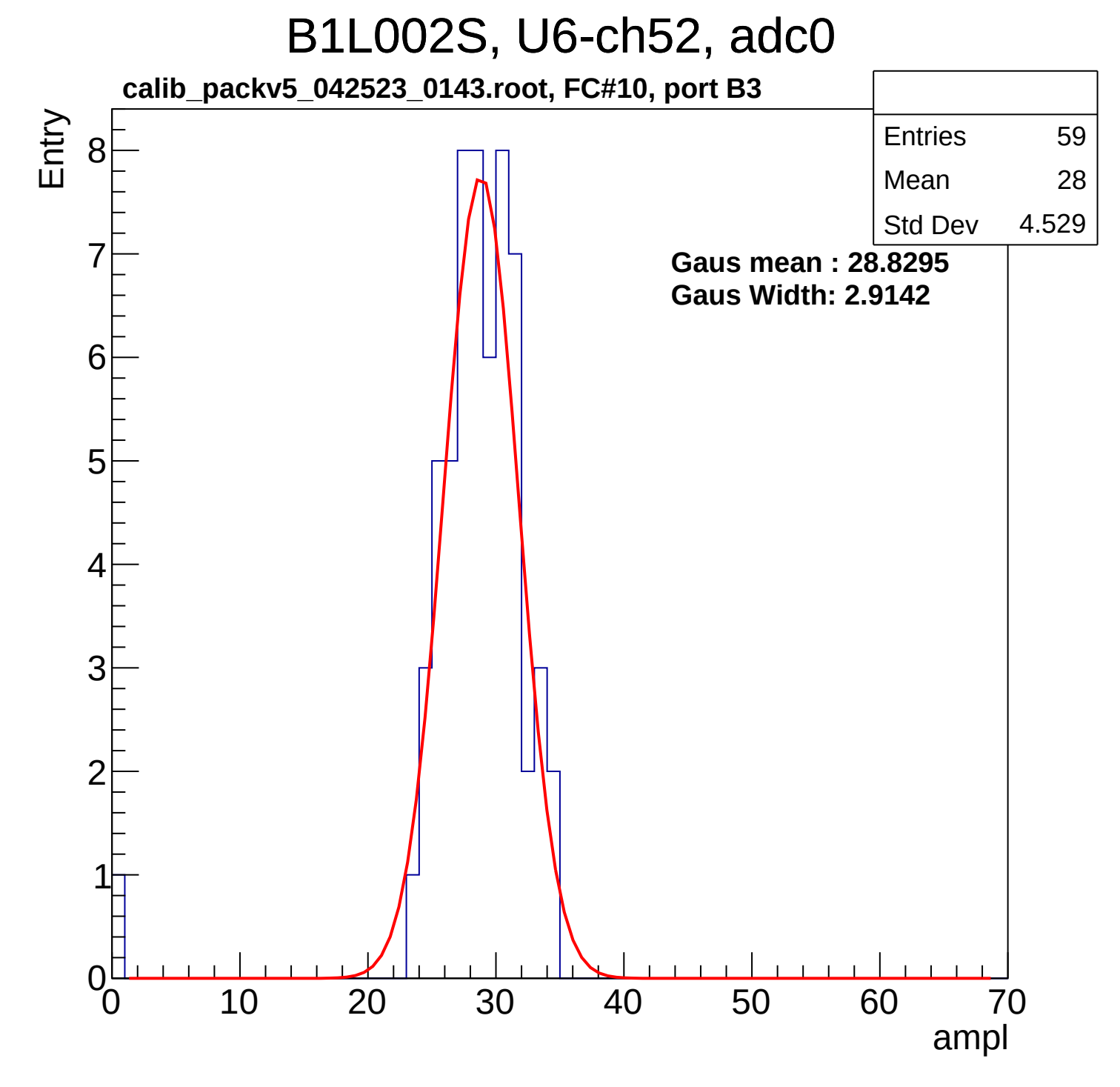

B1L002S, U6-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.28
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

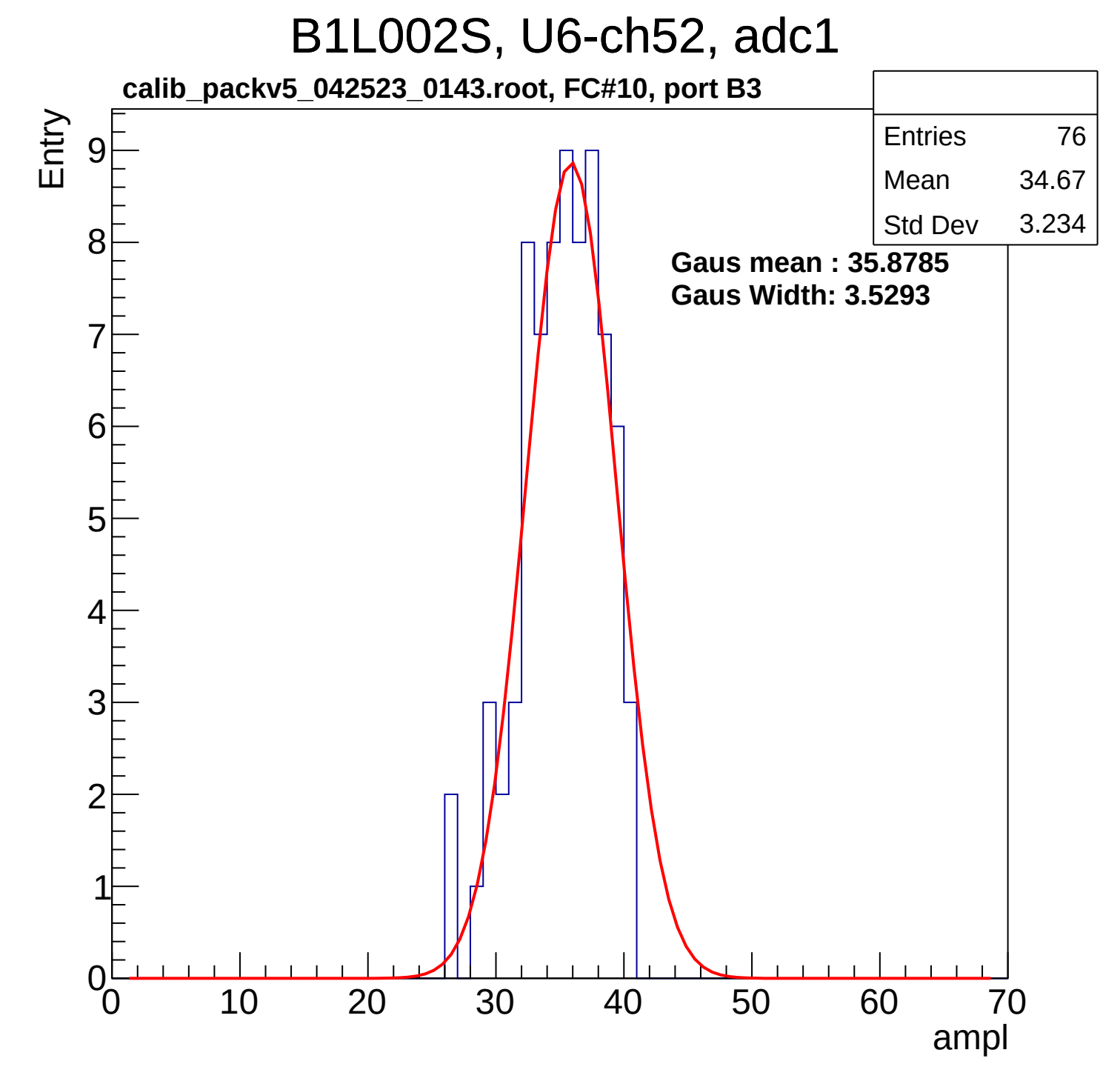

B1L002S, U6-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.09
Std Dev 2.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

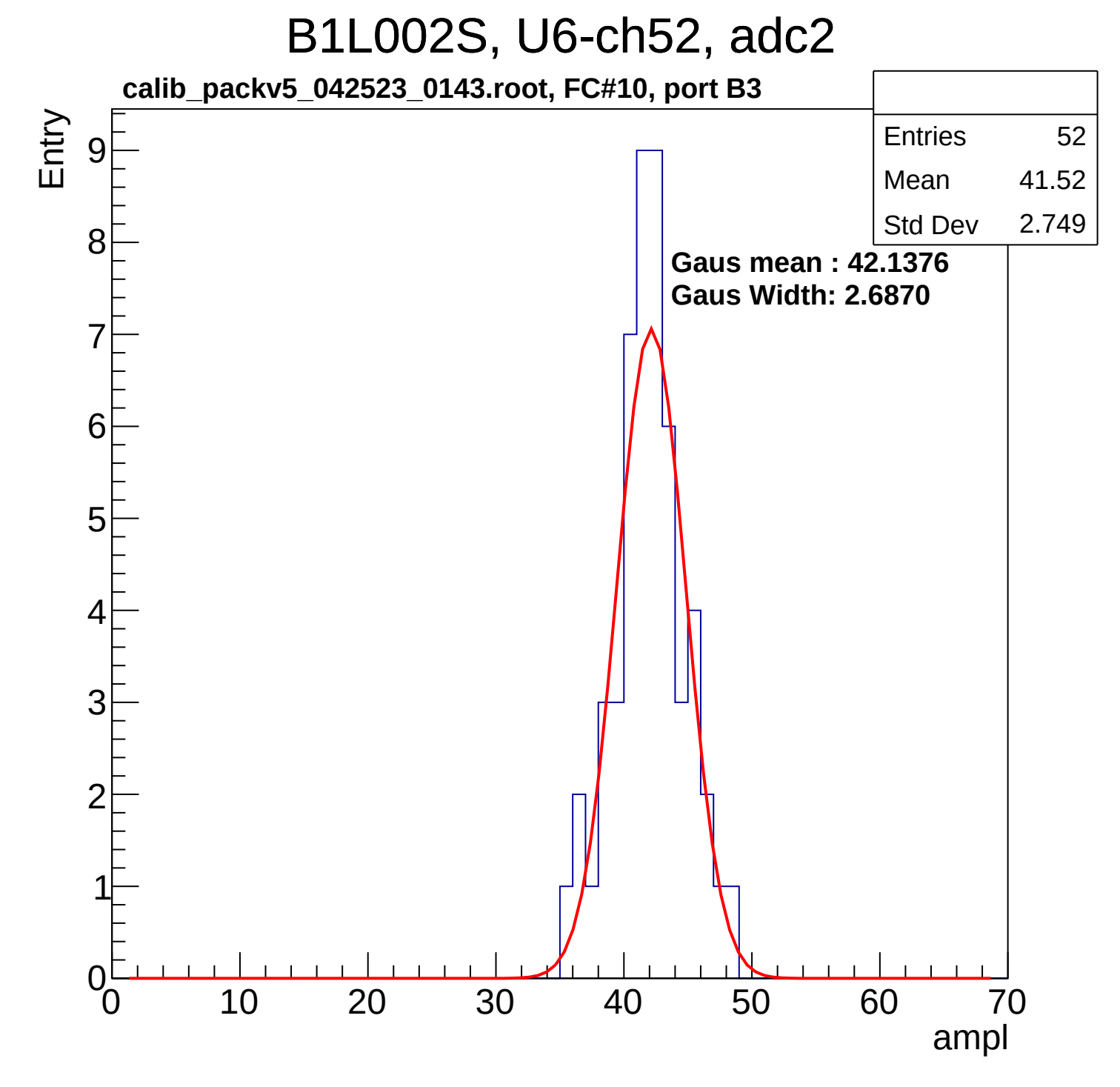

B1L002S, U6-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.37
Std Dev 8.623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

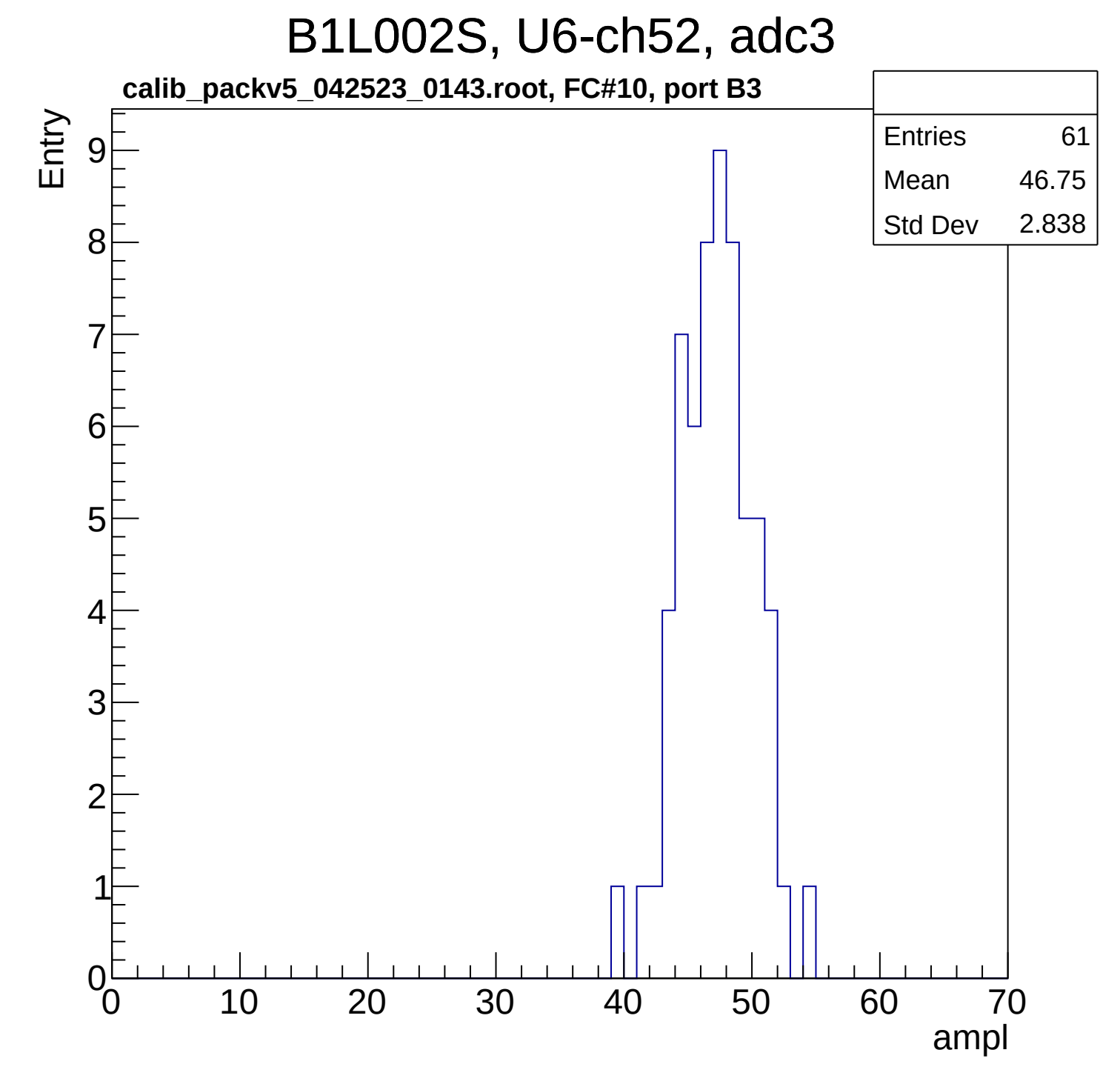

B1L002S, U6-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62.29
Std Dev 0.7954
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

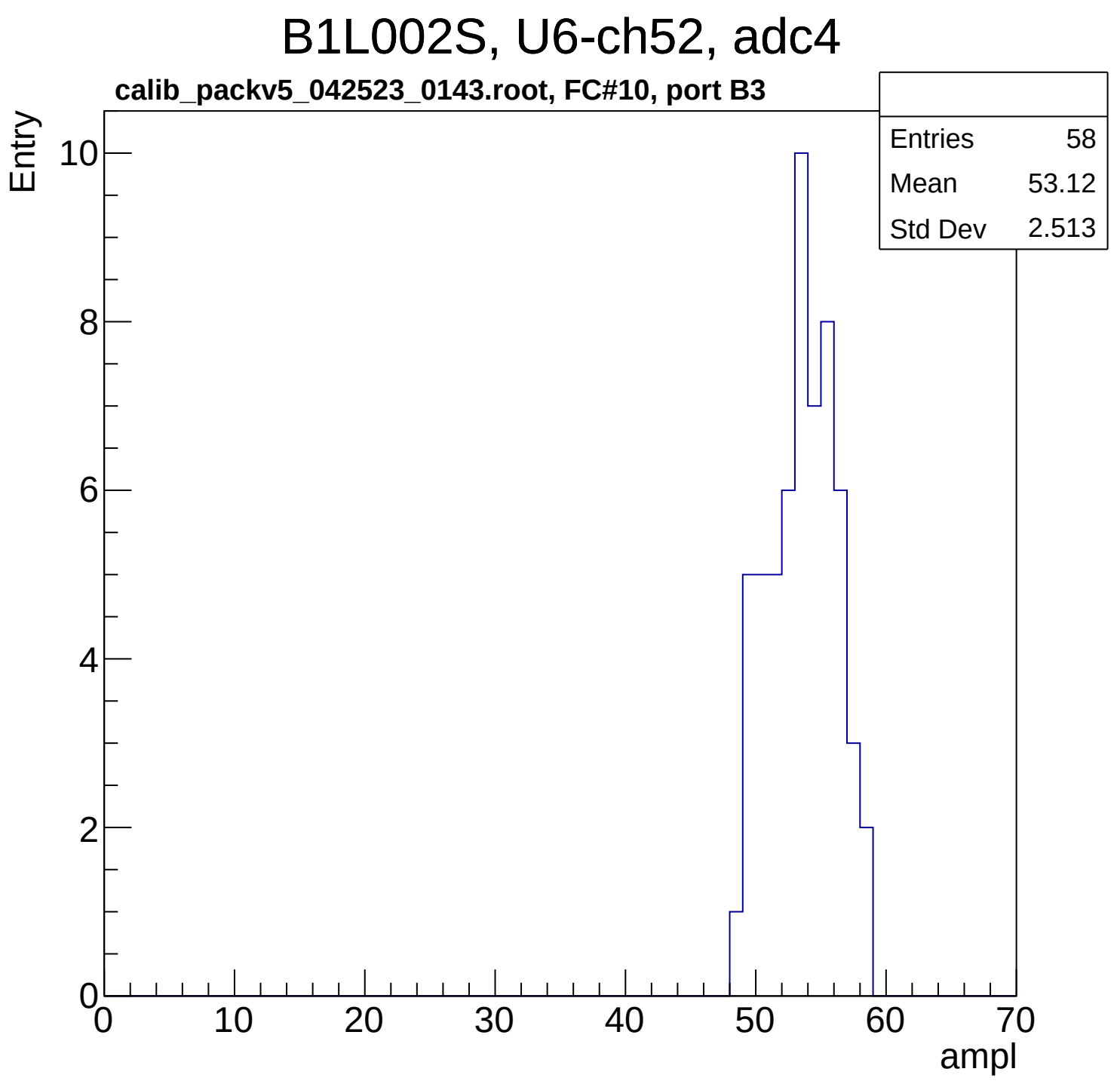

B1L002S, U6-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

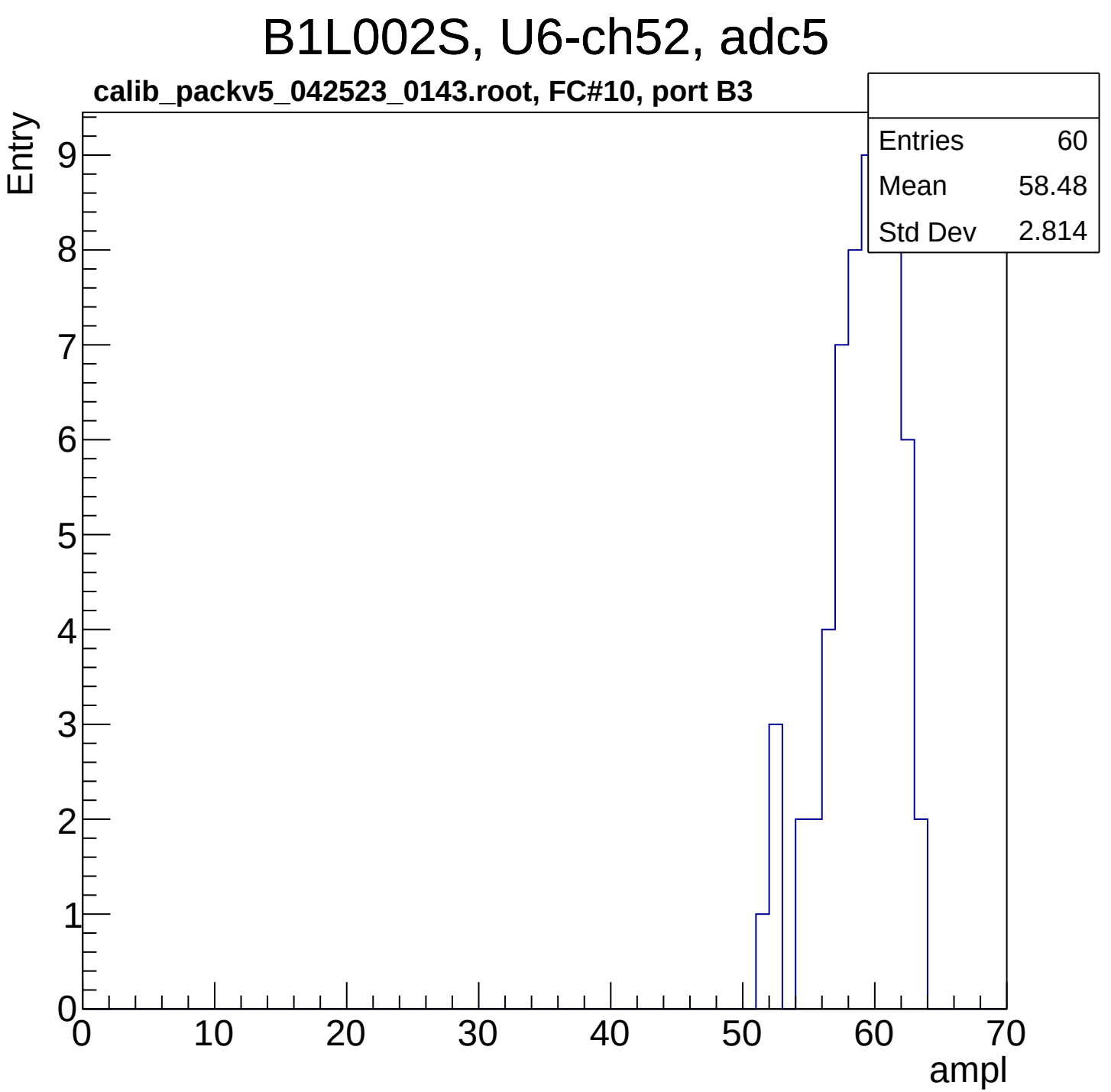

B1L002S, U6-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28
Std Dev 4.529
Gaus mean : 28.8295
Gaus Width: 2.9142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

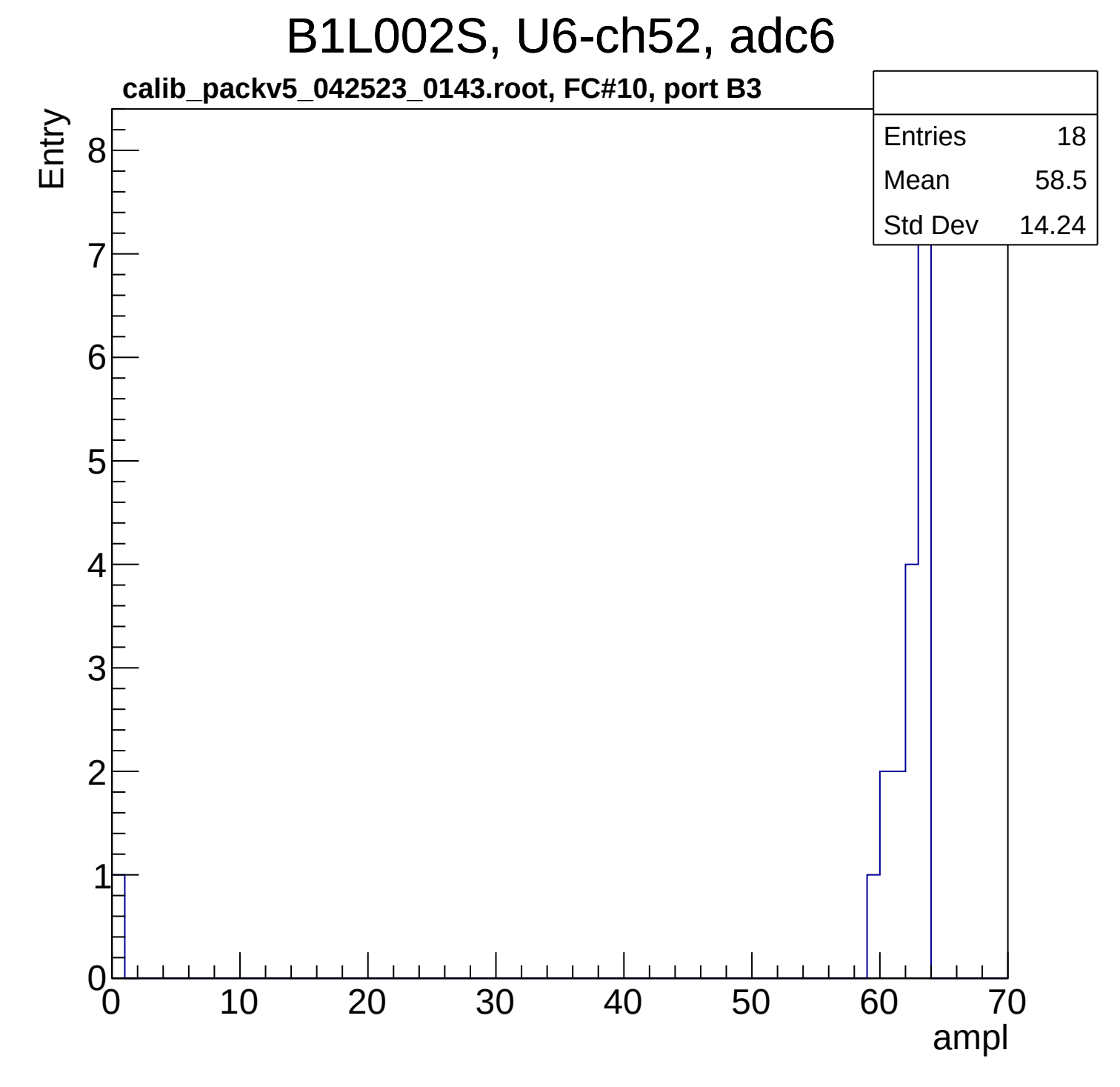

B1L002S, U6-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.67
Std Dev 3.234
Gaus mean : 35.8785
Gaus Width: 3.5293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

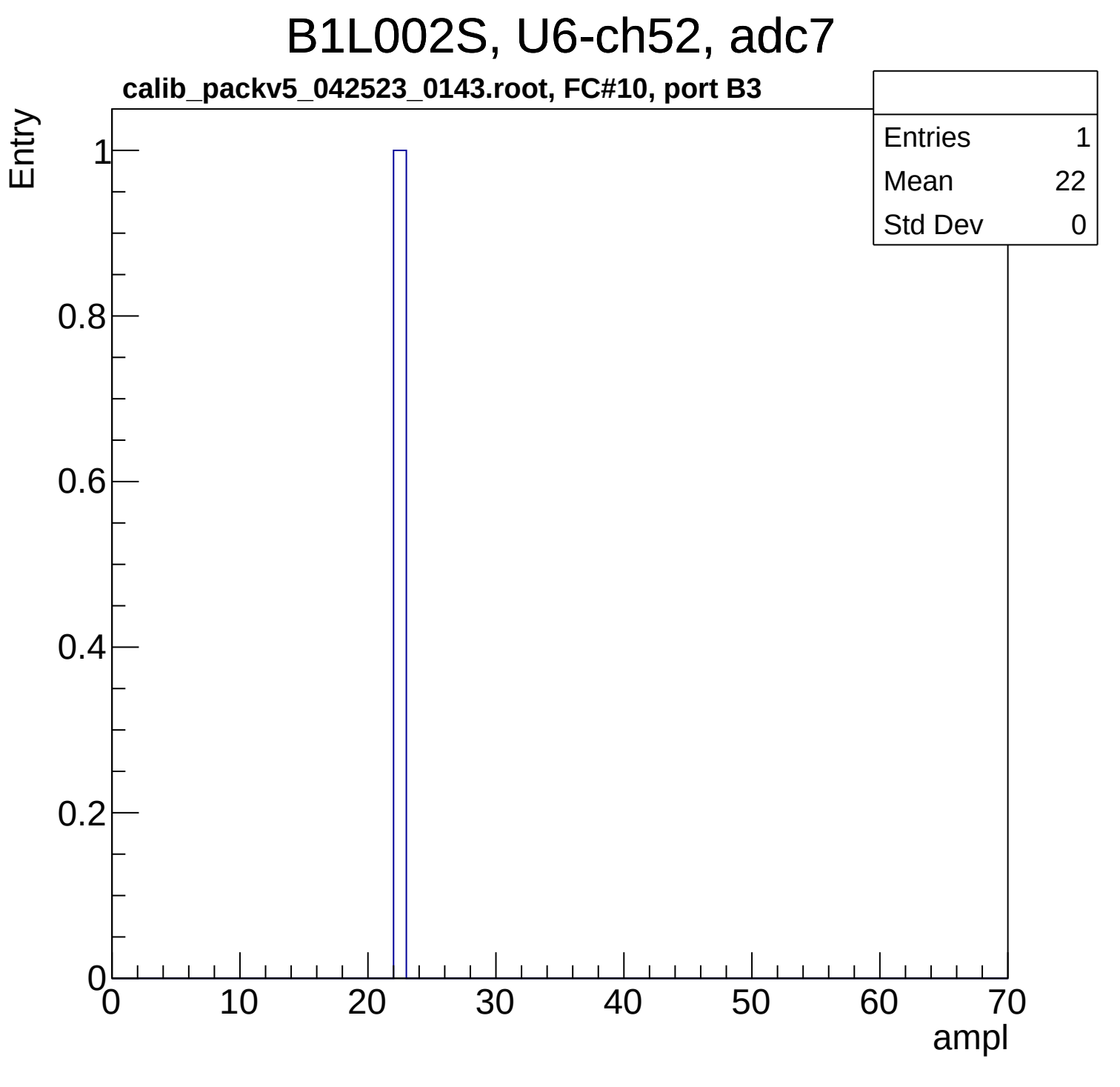

B1L002S, U6-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.52
Std Dev 2.749
Gaus mean : 42.1376
Gaus Width: 2.6870
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

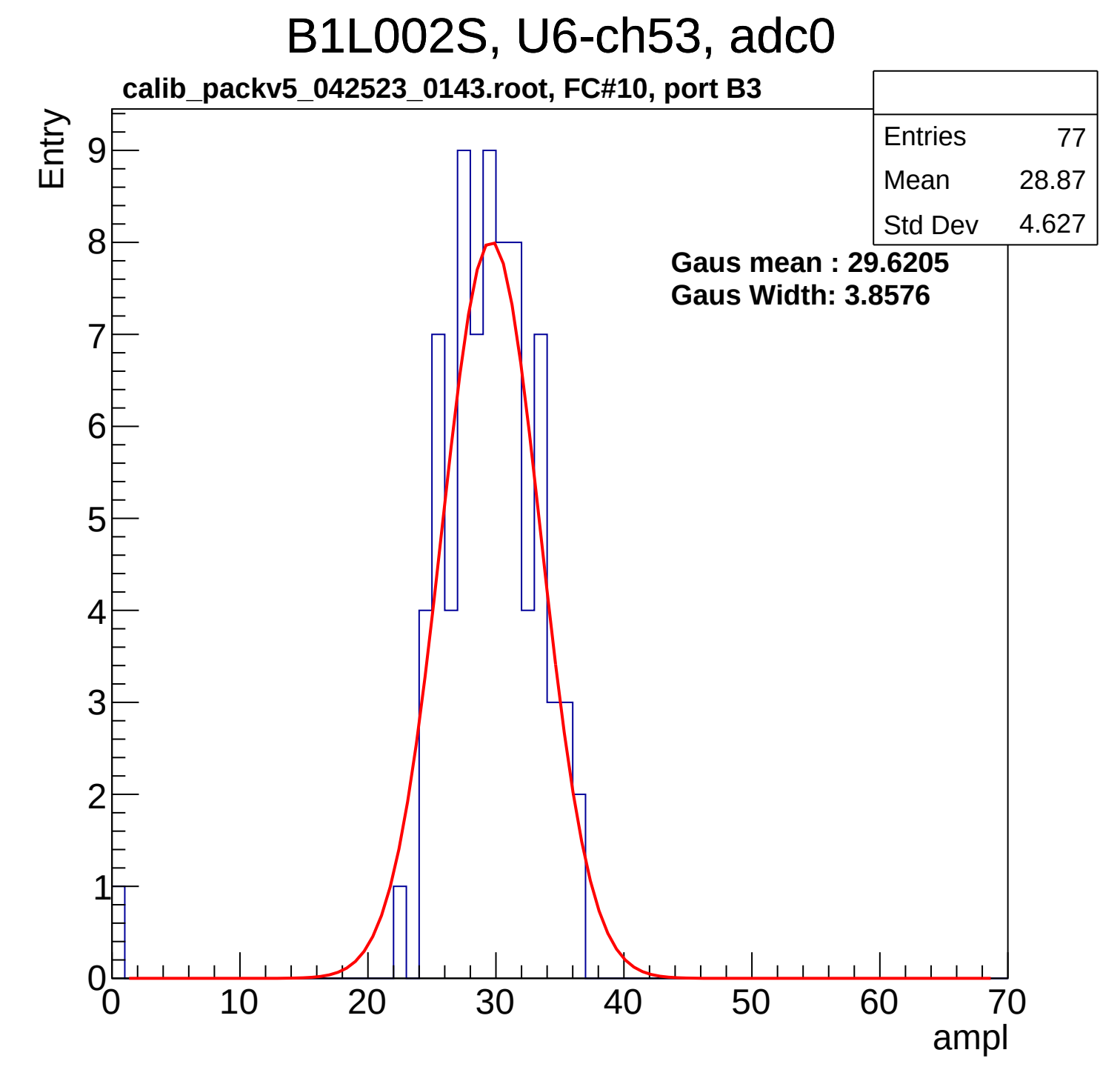

B1L002S, U6-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.75
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

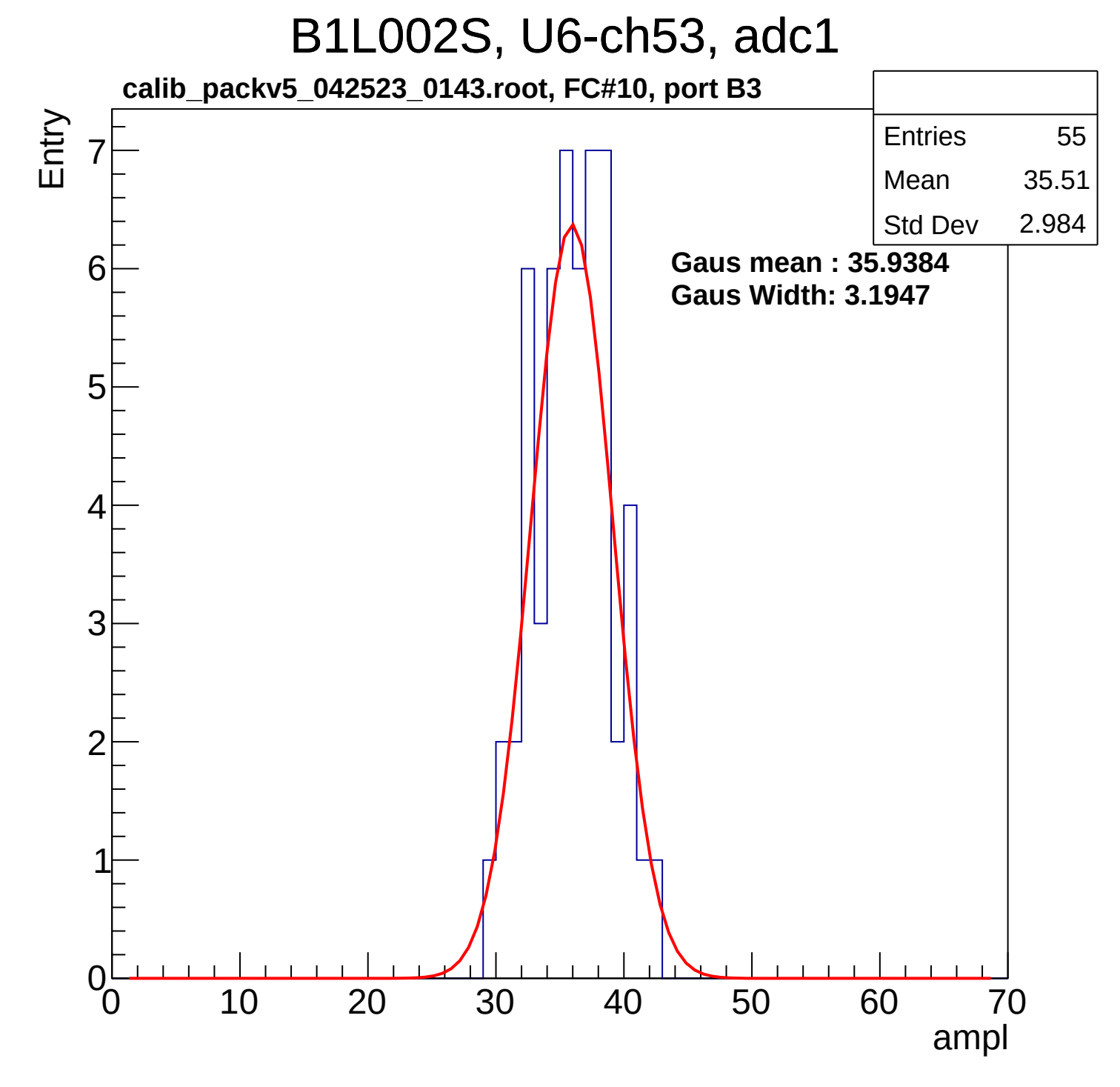

B1L002S, U6-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.12
Std Dev 2.513
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

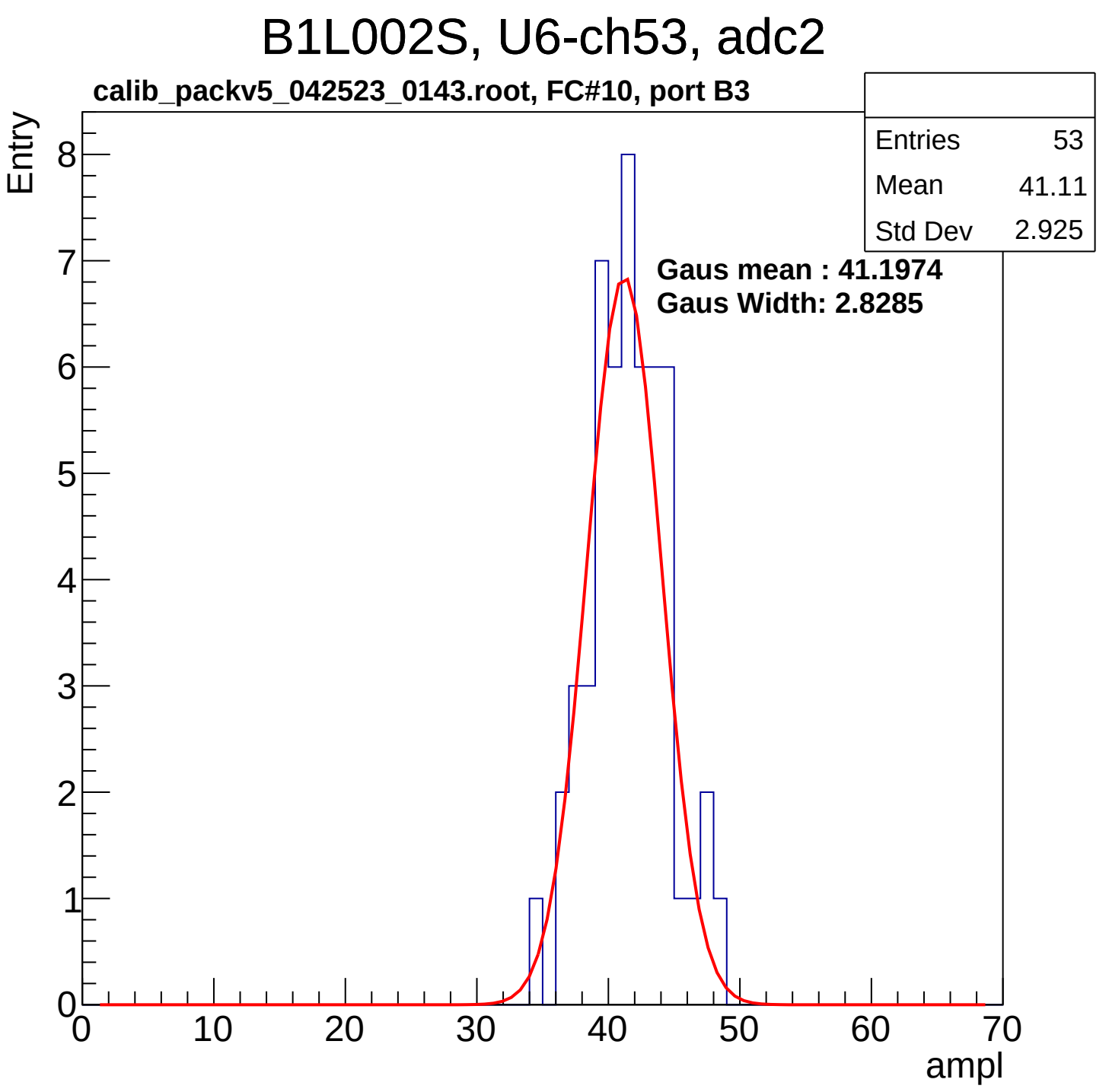

B1L002S, U6-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.48
Std Dev 2.814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

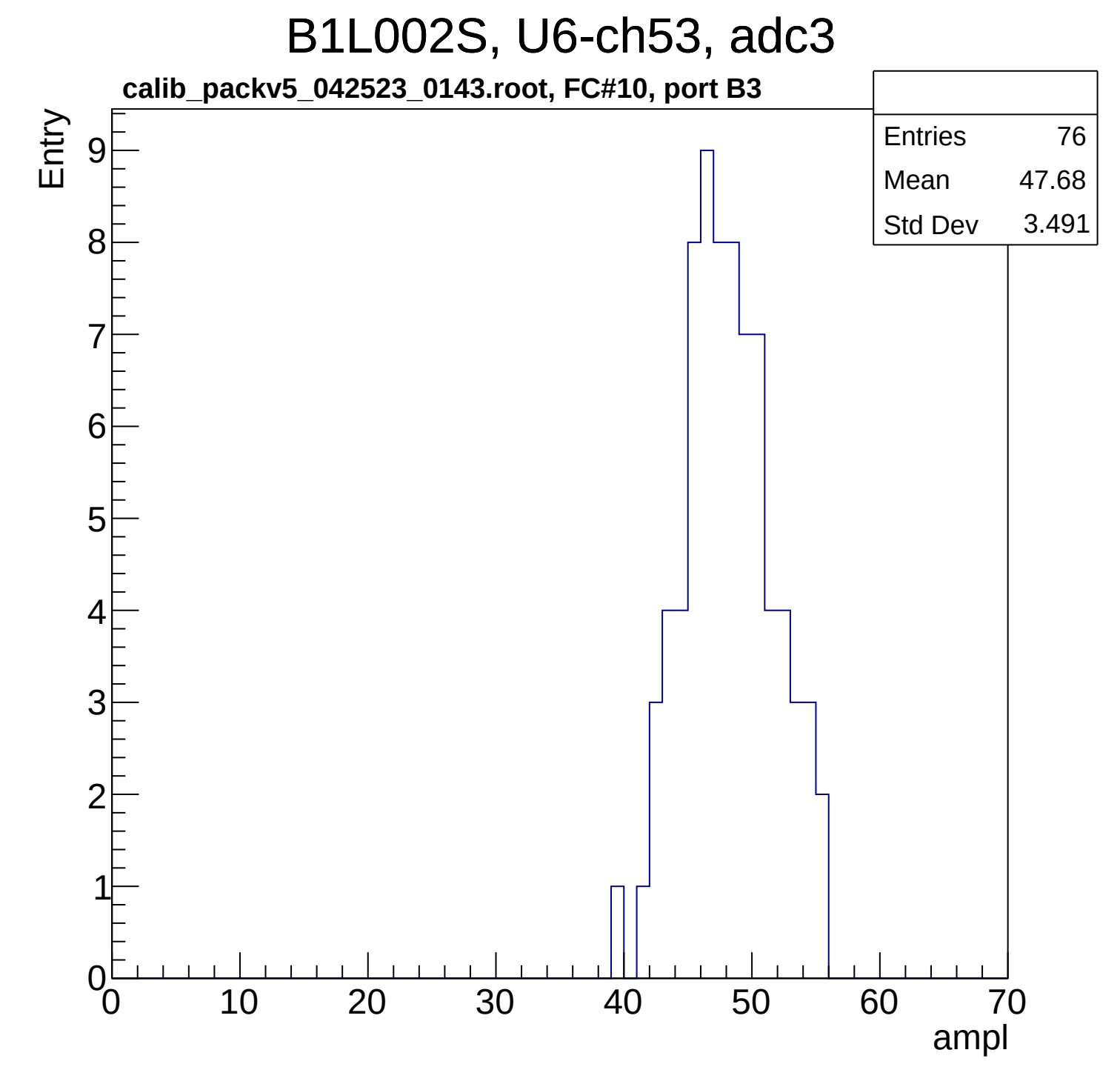

B1L002S, U6-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.5
Std Dev 14.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

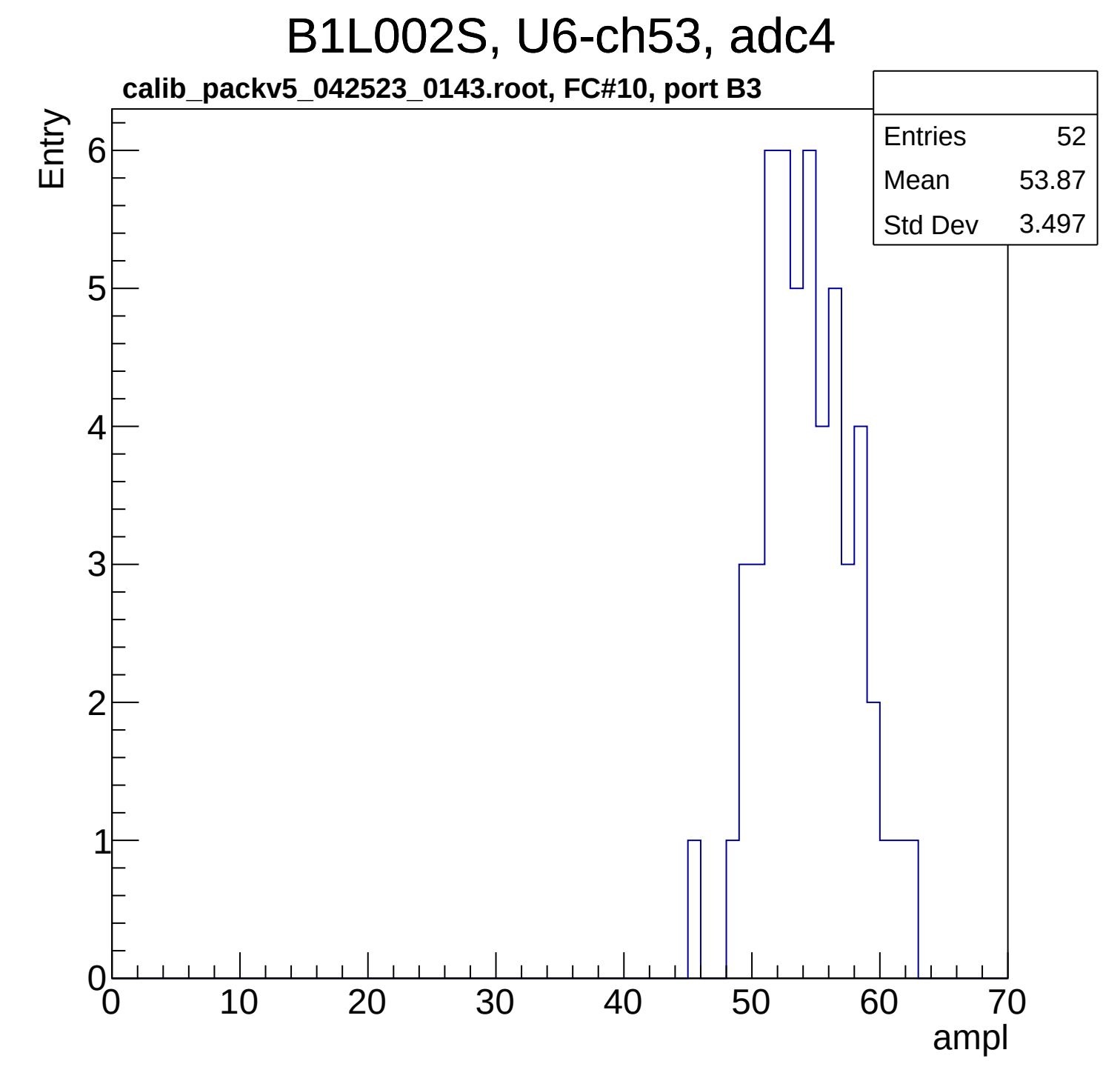

B1L002S, U6-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

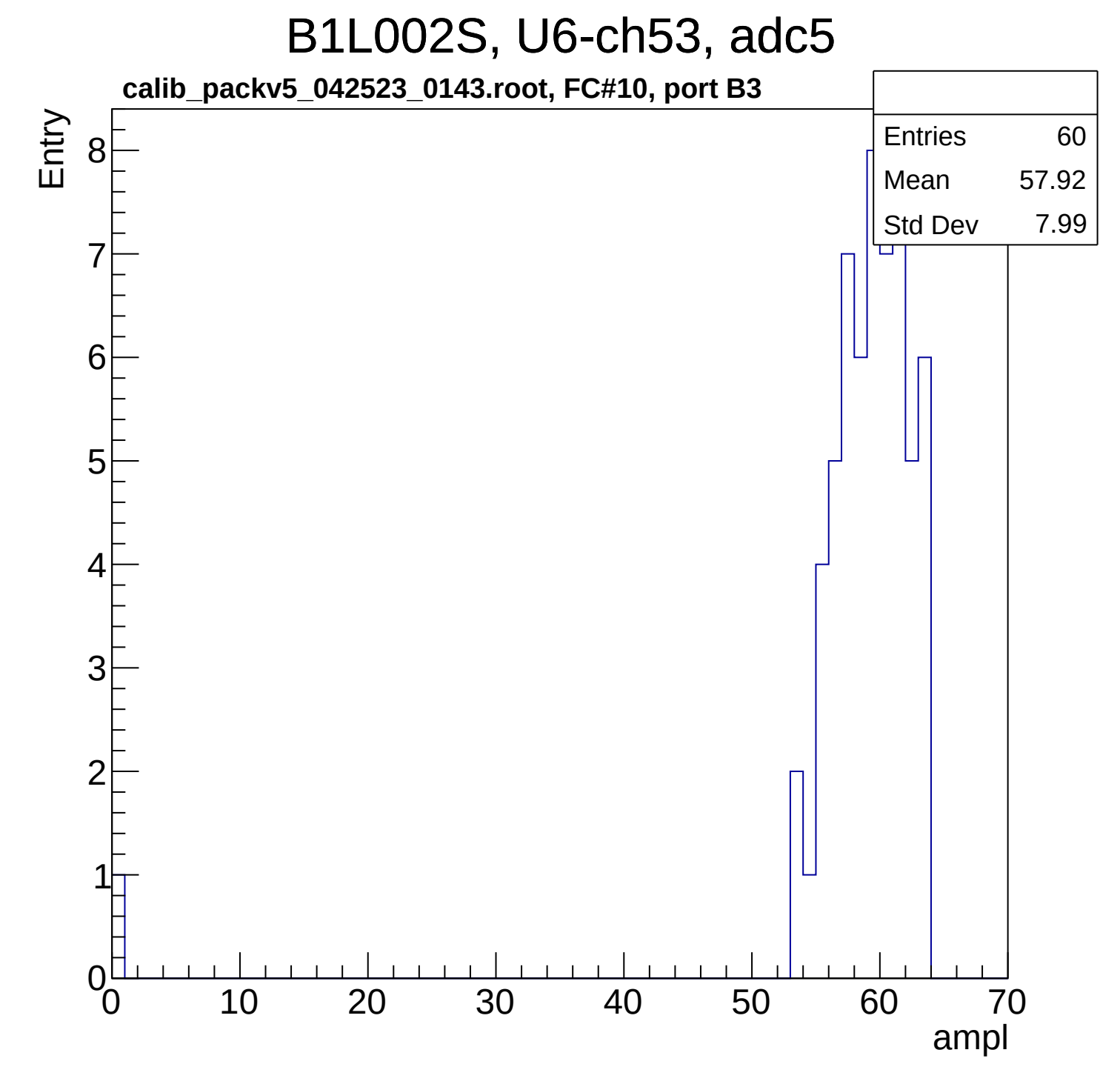

B1L002S, U6-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.87
Std Dev 4.627
Gaus mean : 29.6205
Gaus Width: 3.8576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

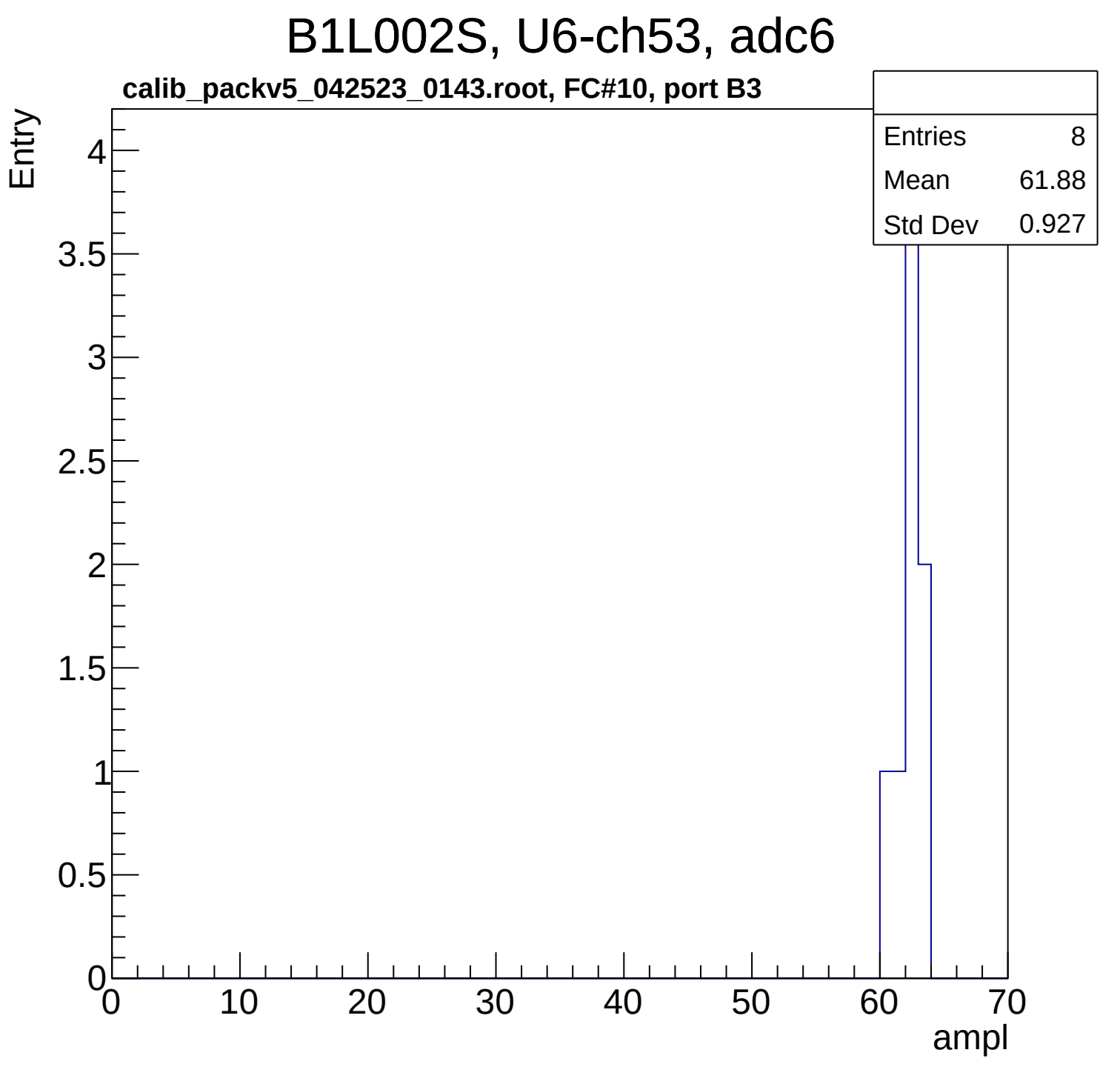

B1L002S, U6-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.51
Std Dev 2.984
Gaus mean : 35.9384
Gaus Width: 3.1947
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

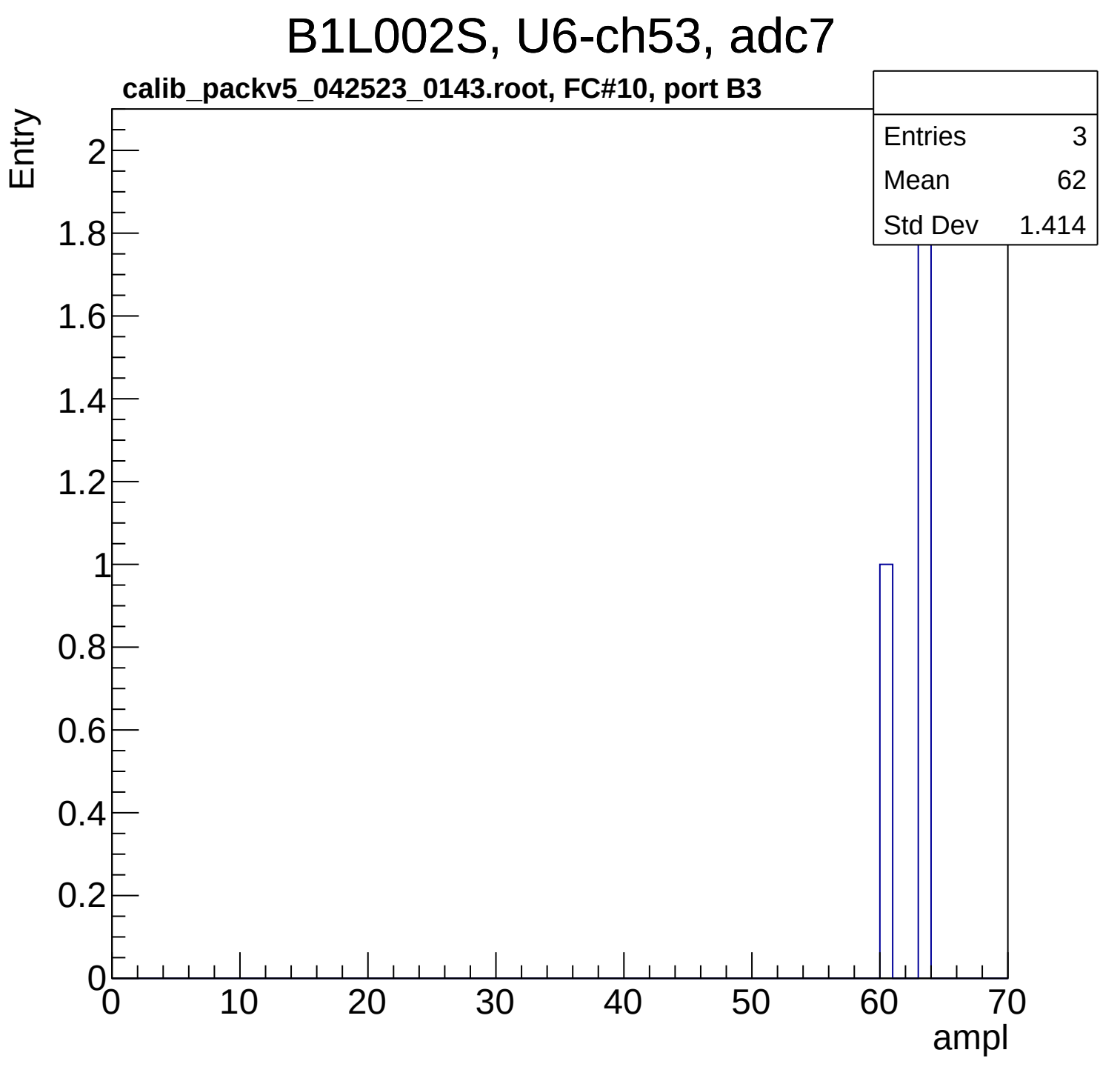

B1L002S, U6-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.11
Std Dev 2.925
Gaus mean : 41.1974
Gaus Width: 2.8285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

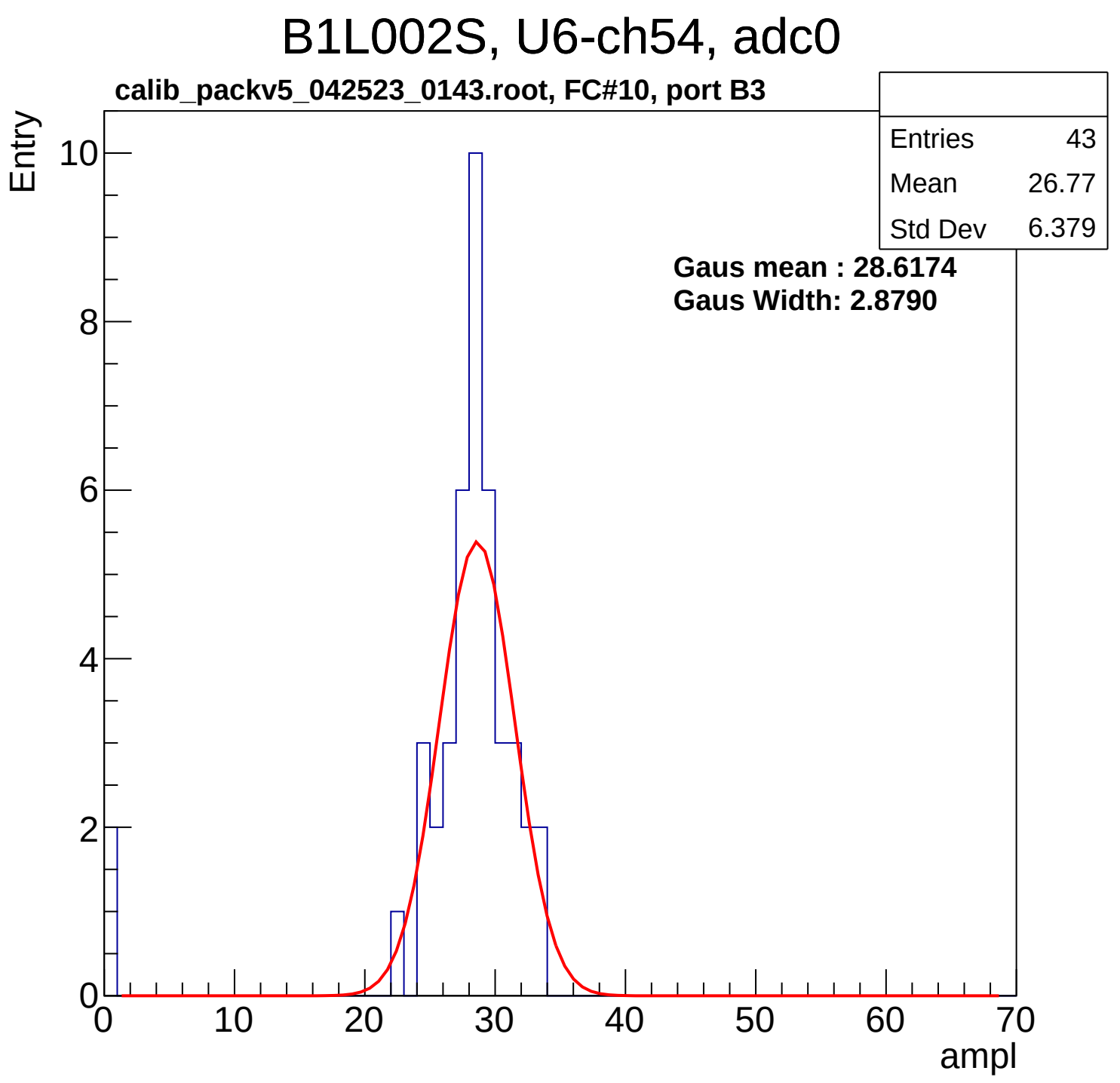

B1L002S, U6-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.68
Std Dev 3.491
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

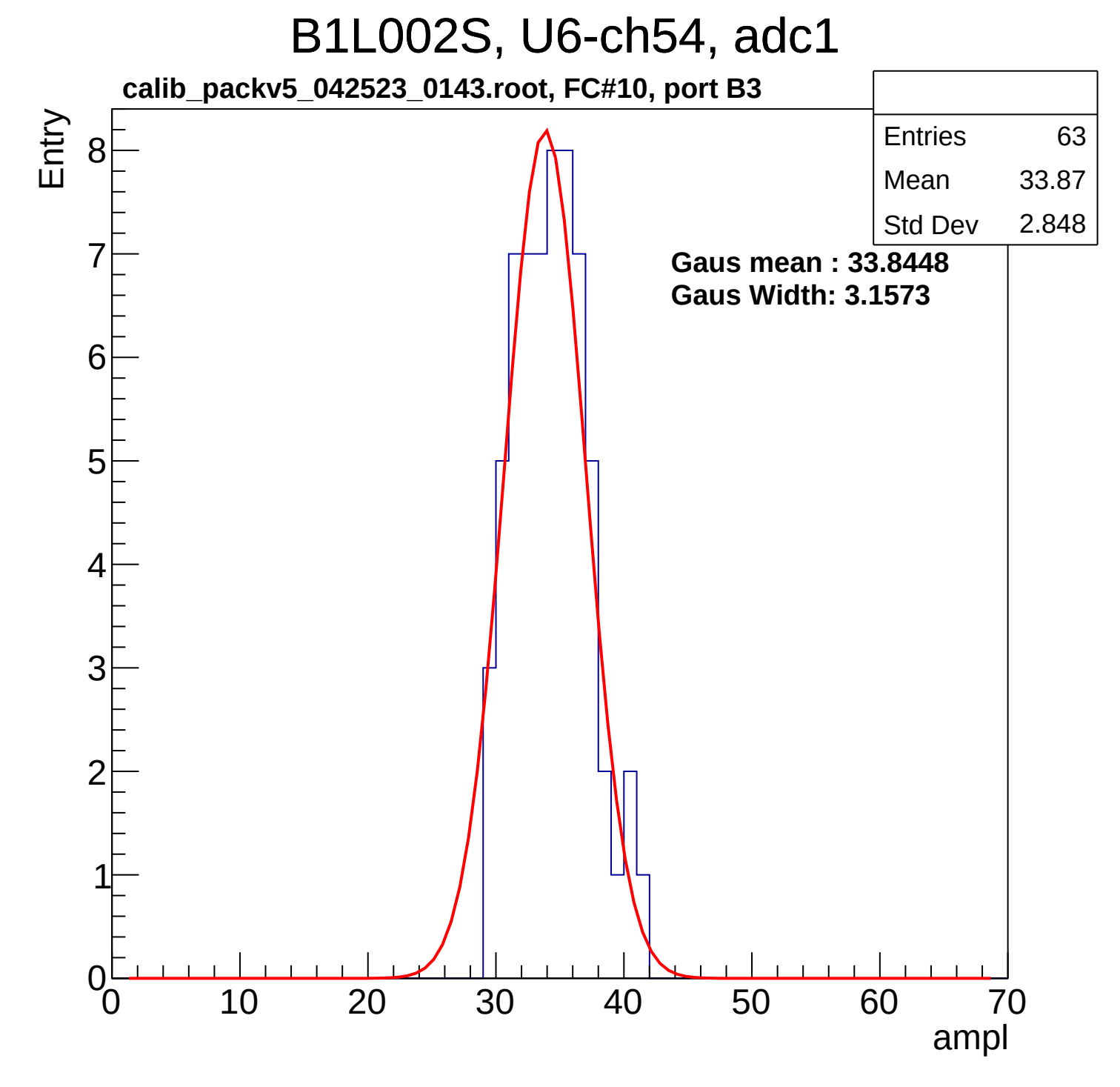

B1L002S, U6-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.87
Std Dev 3.497
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

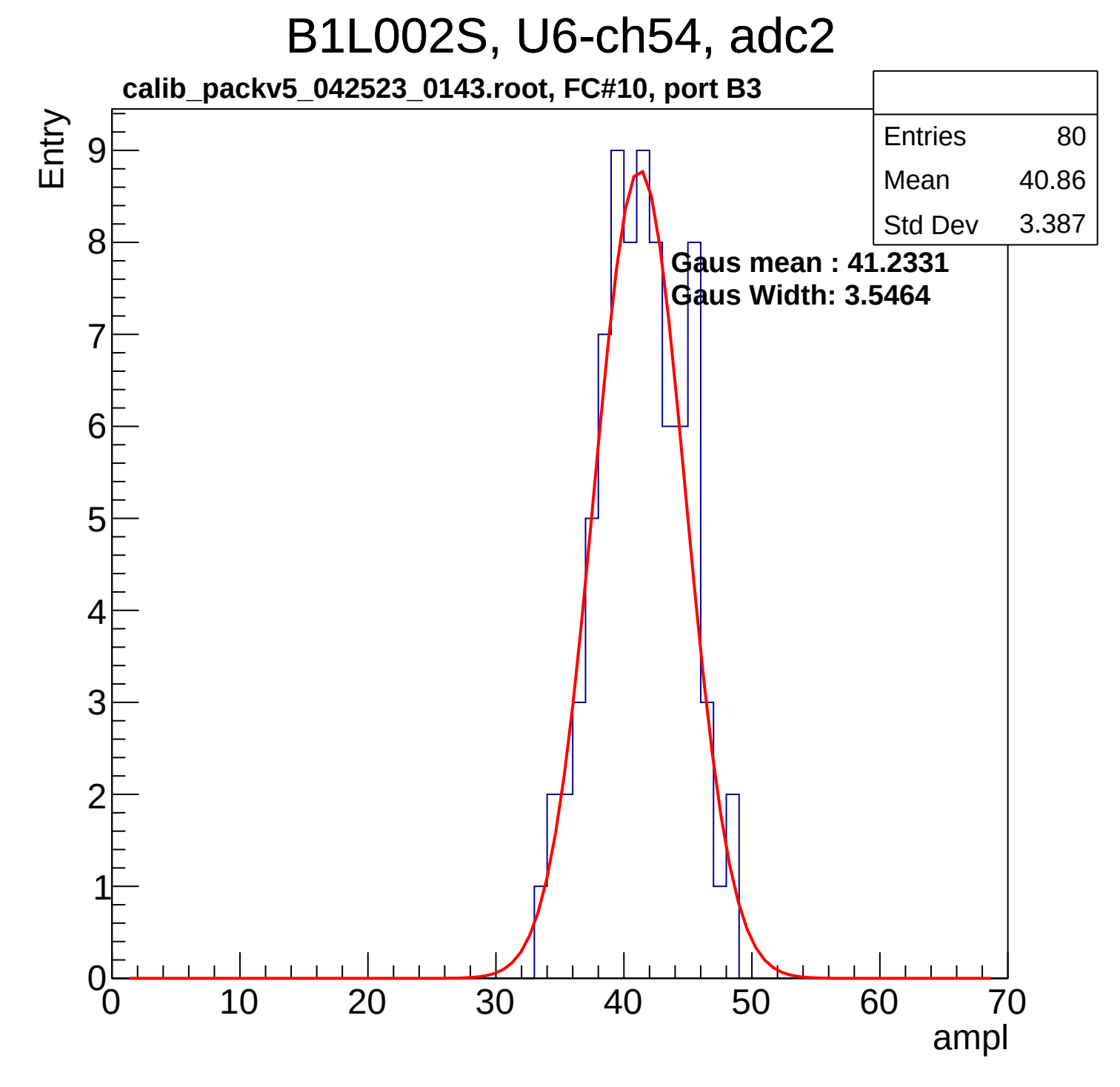

B1L002S, U6-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.92
Std Dev 7.99
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

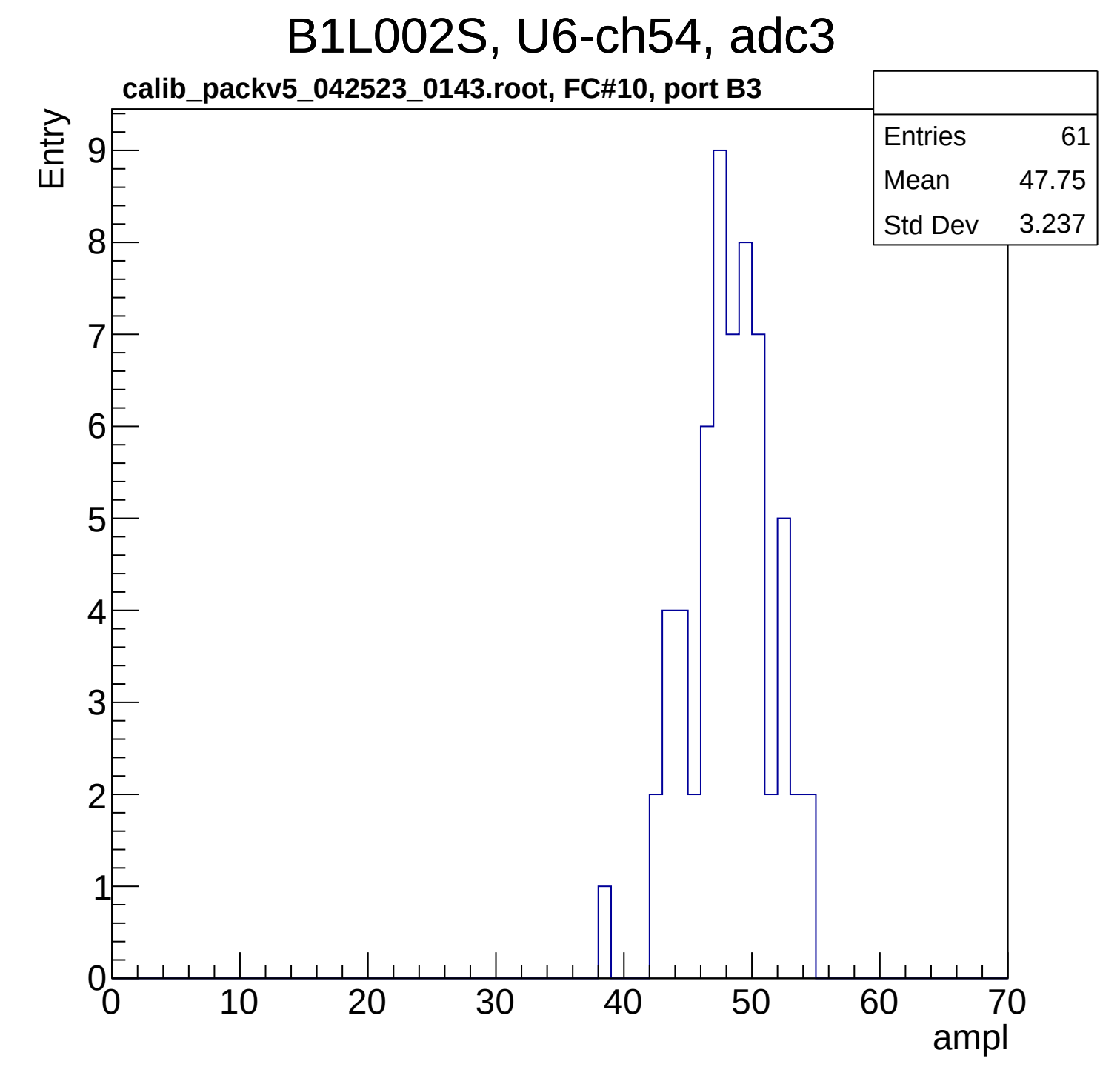

B1L002S, U6-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61.88
Std Dev 0.927
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

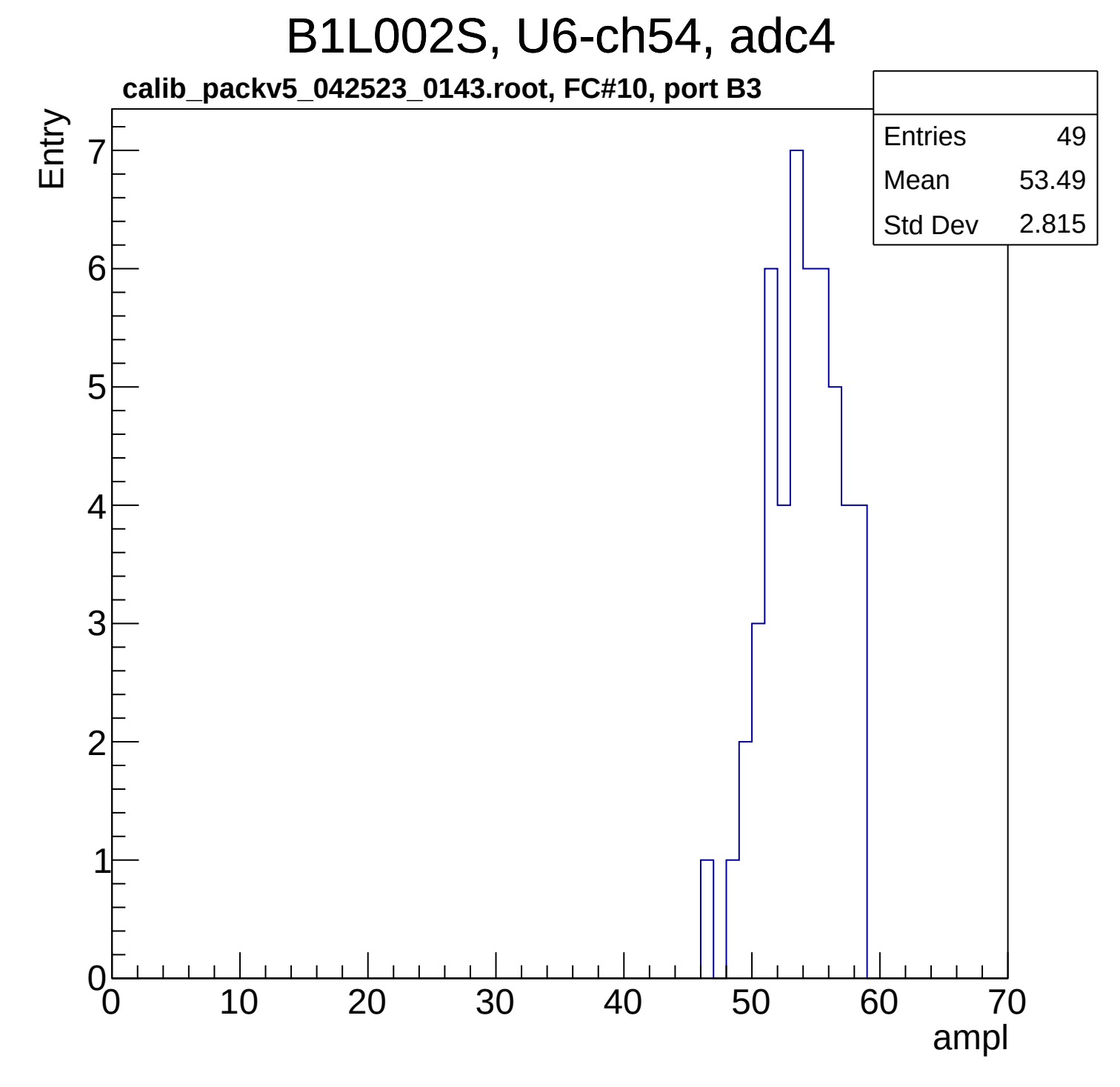

B1L002S, U6-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

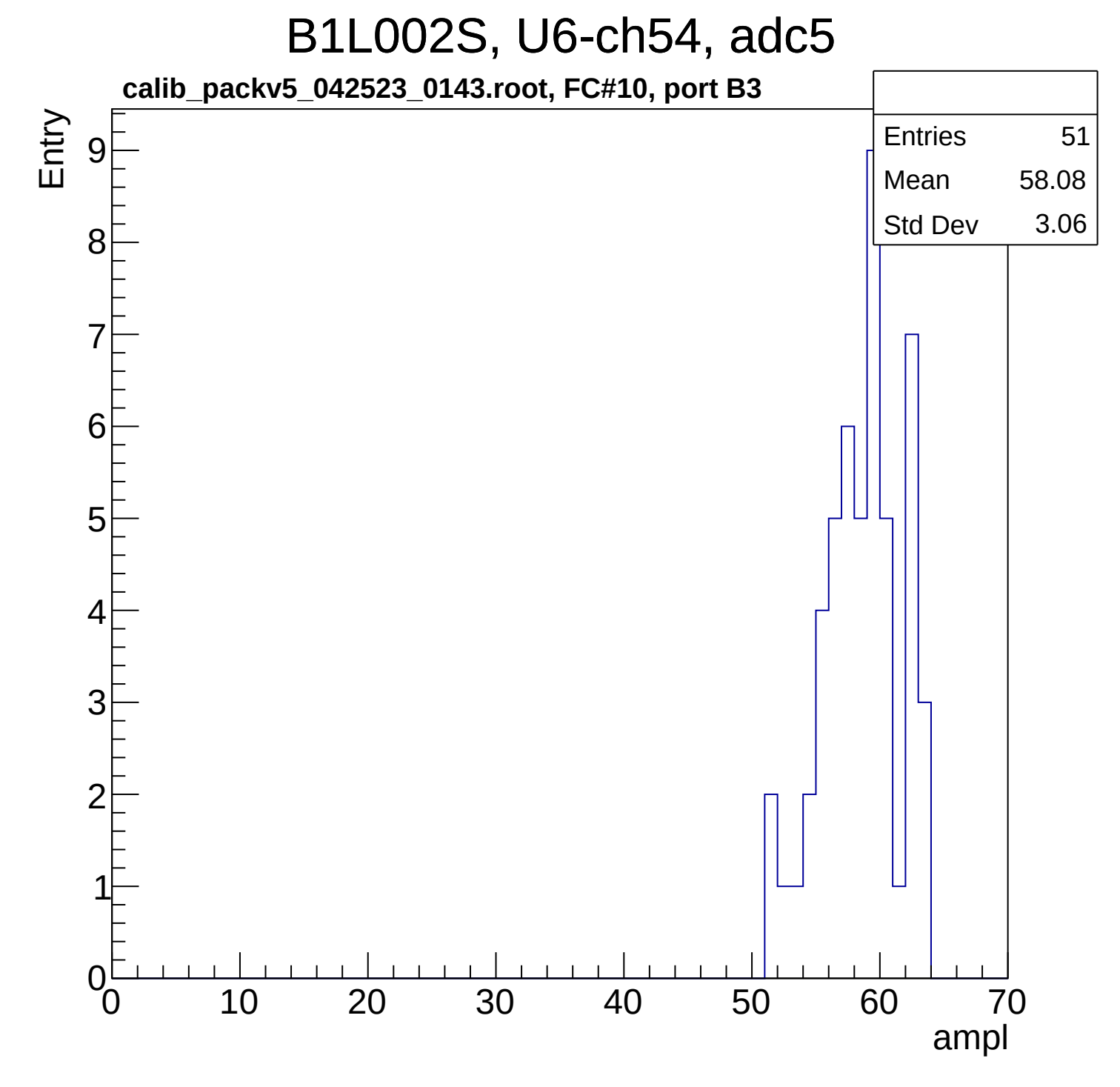

B1L002S, U6-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 26.77
Std Dev 6.379
Gaus mean : 28.6174
Gaus Width: 2.8790
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

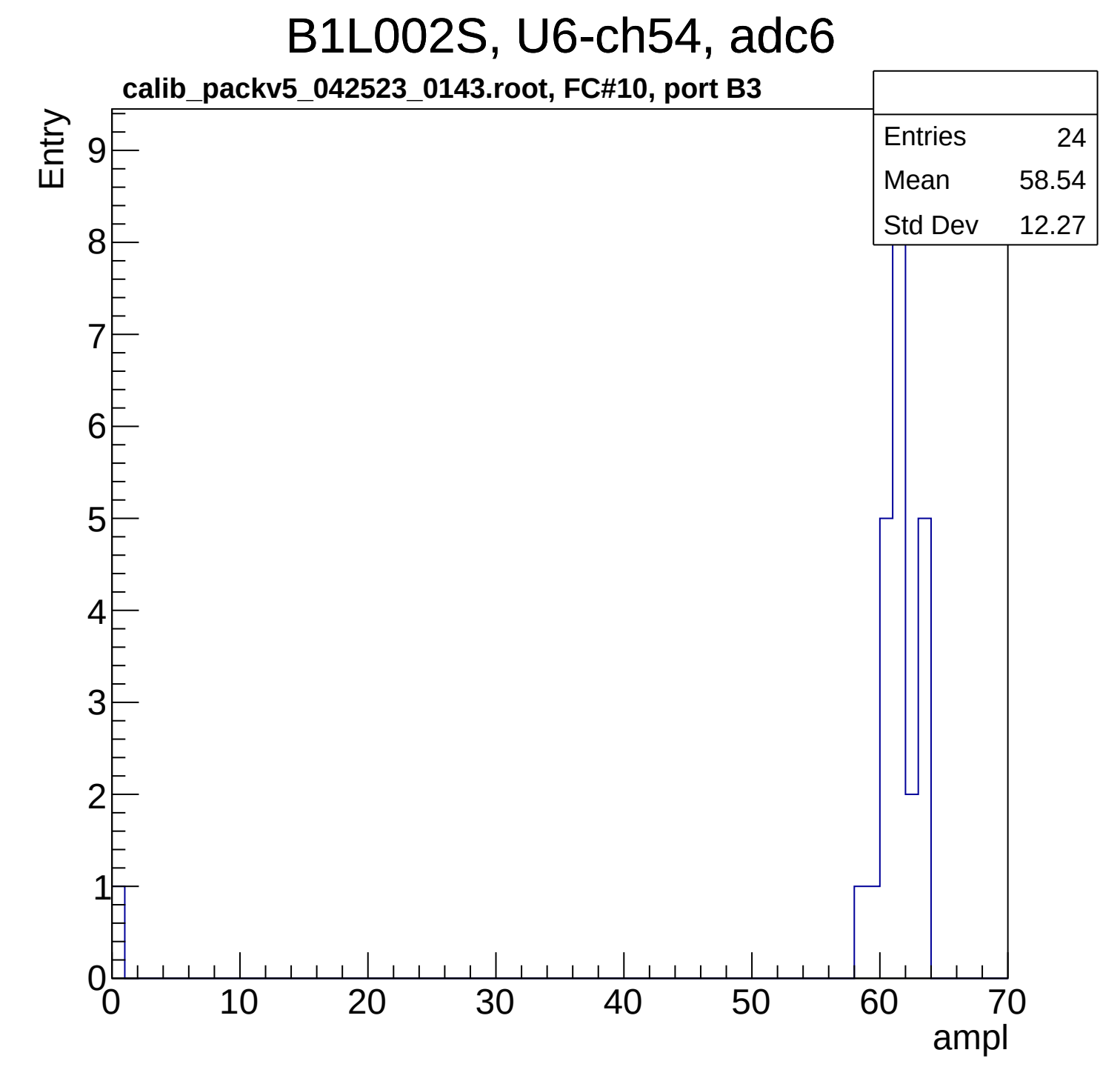

B1L002S, U6-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.87
Std Dev 2.848
Gaus mean : 33.8448
Gaus Width: 3.1573
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

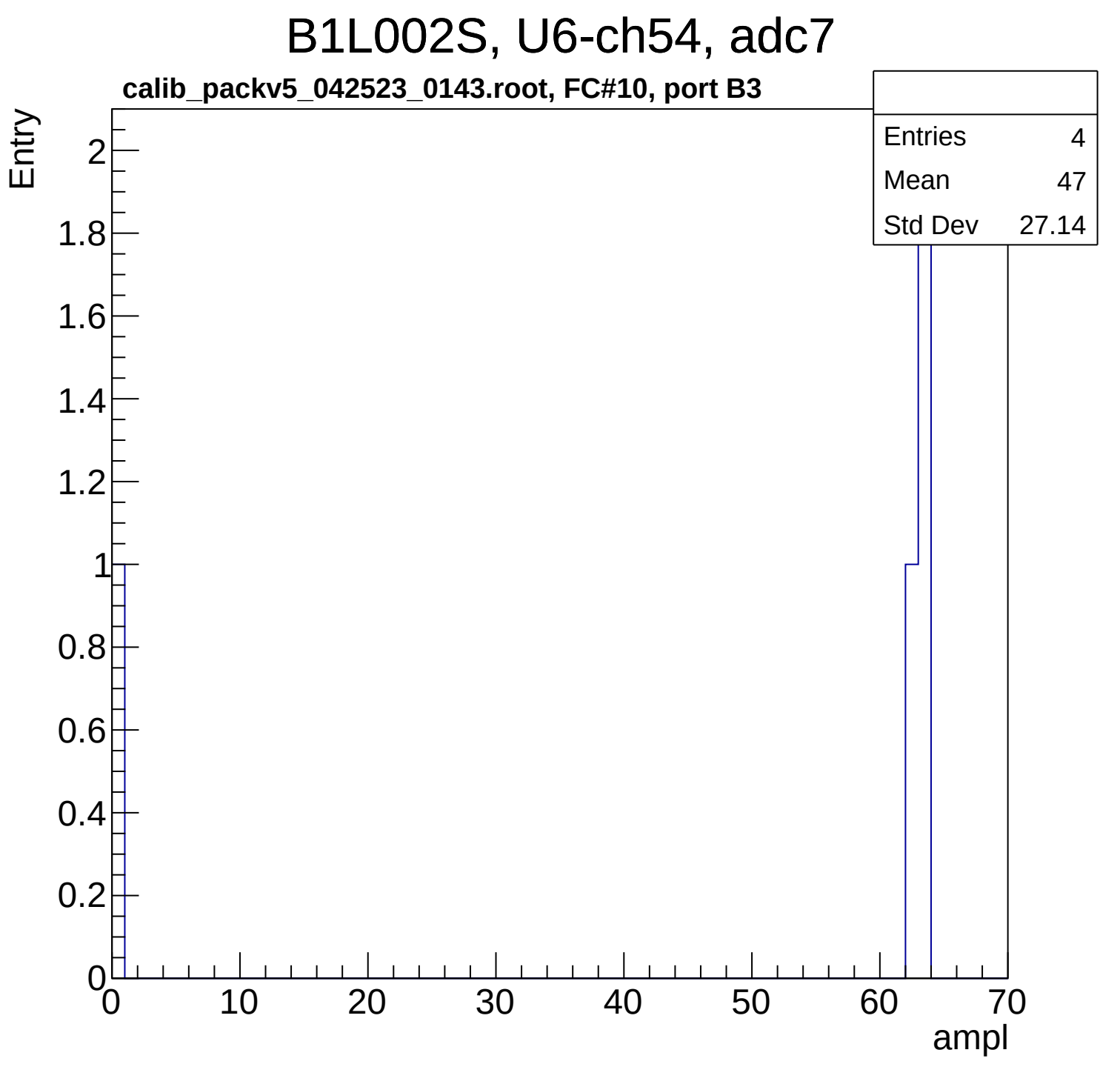

B1L002S, U6-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 40.86
Std Dev 3.387
Gaus mean : 41.2331
Gaus Width: 3.5464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

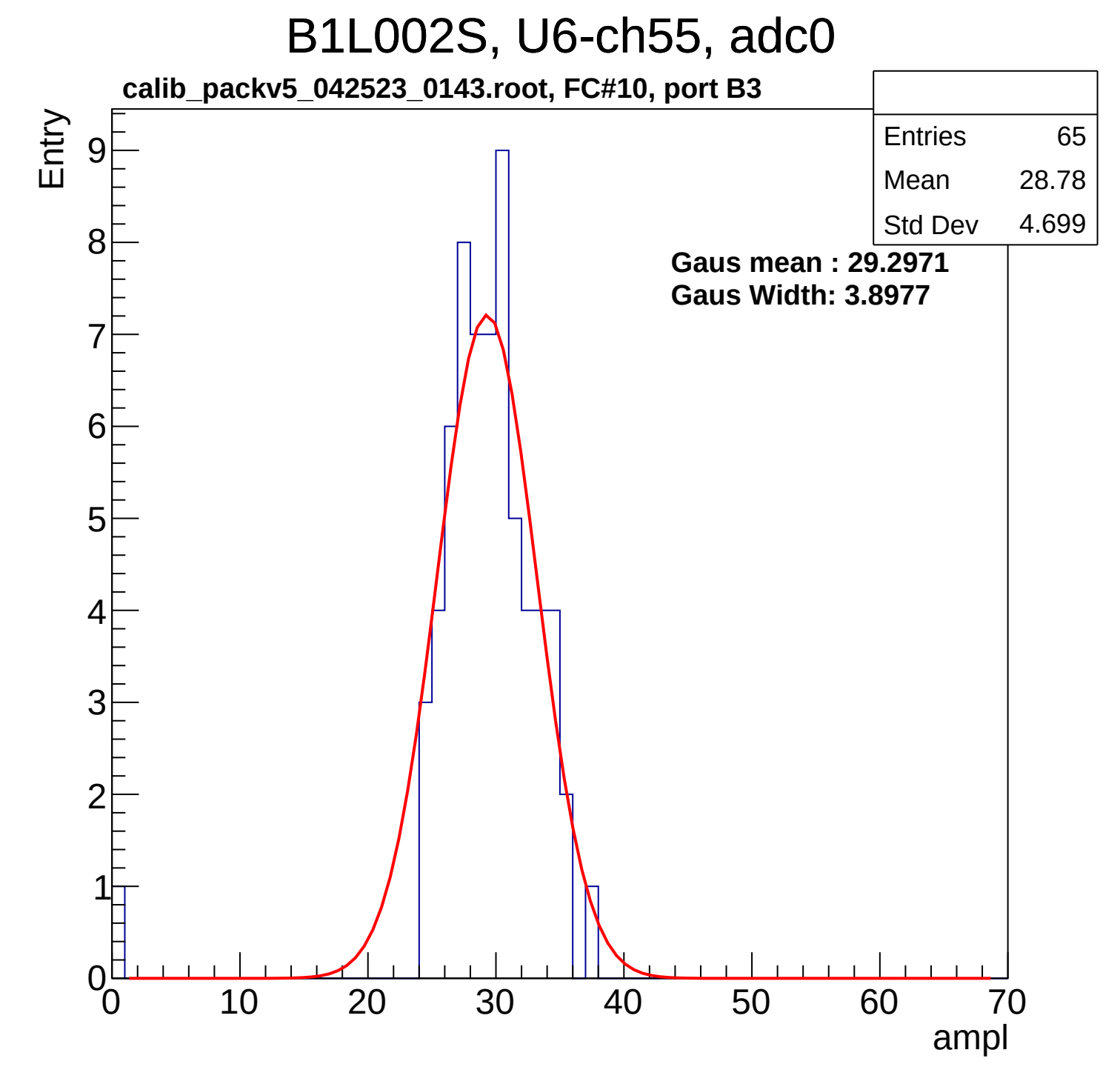

B1L002S, U6-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.75
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

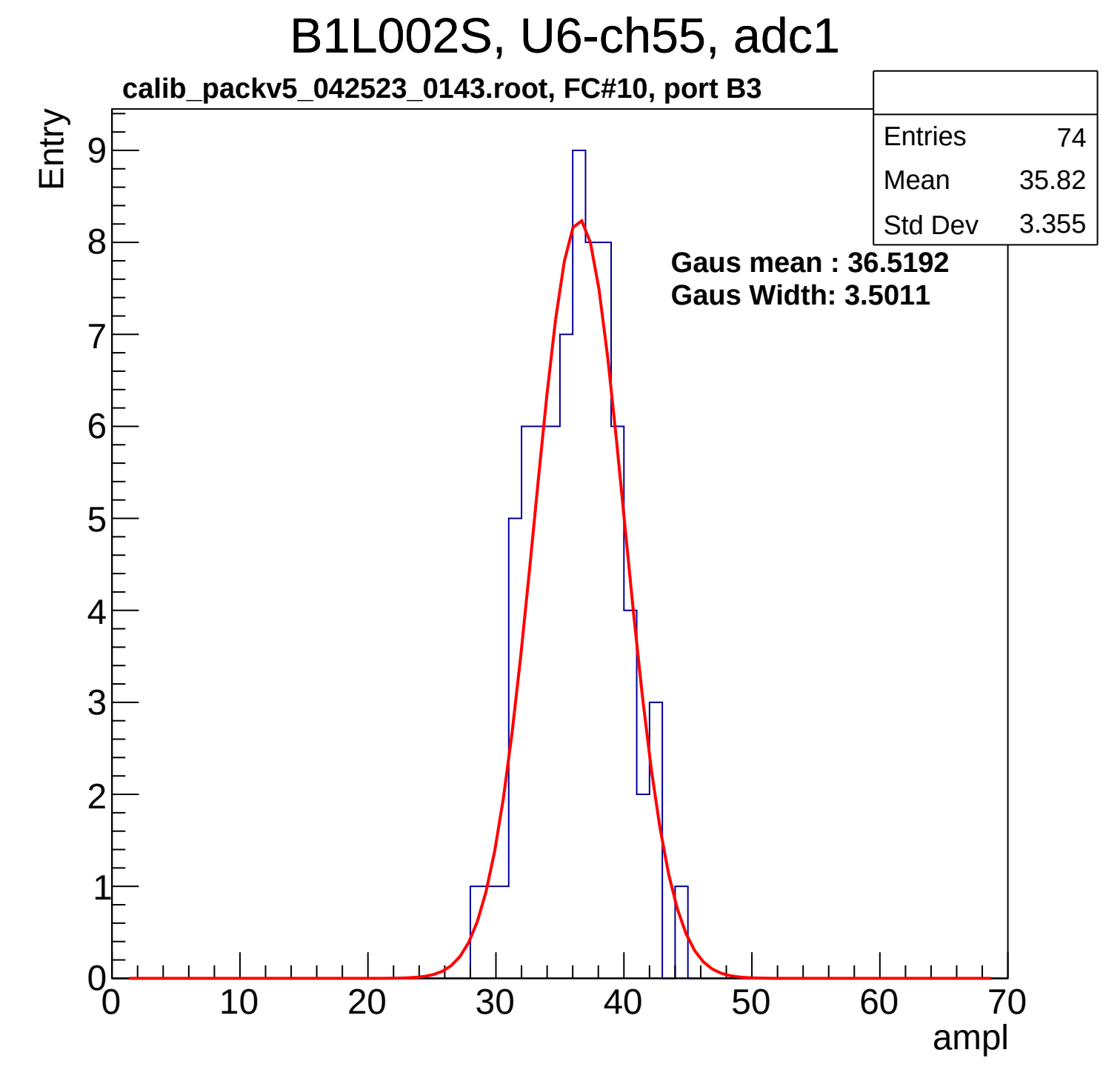

B1L002S, U6-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.49
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

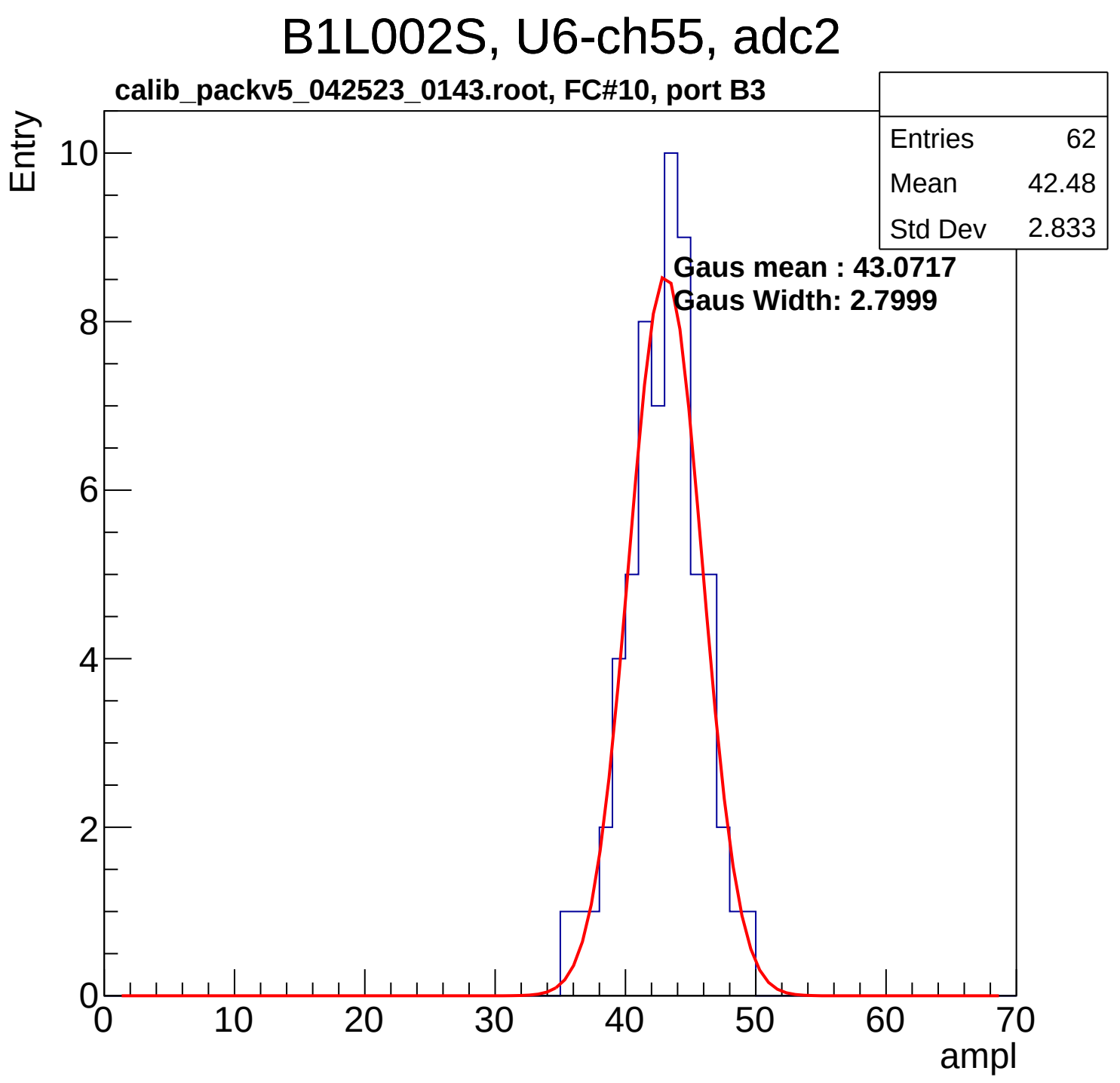

B1L002S, U6-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.08
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

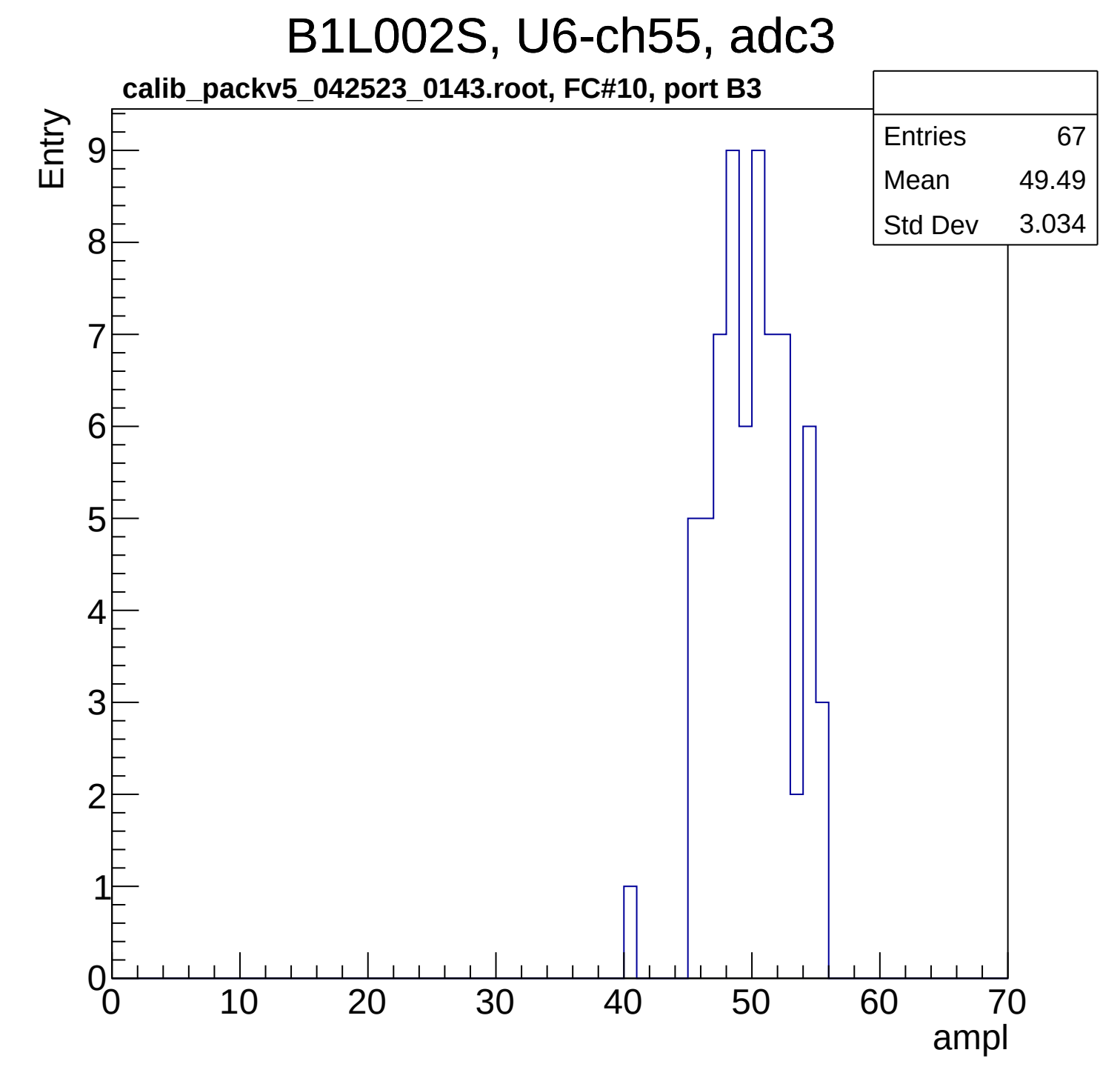

B1L002S, U6-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.54
Std Dev 12.27
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

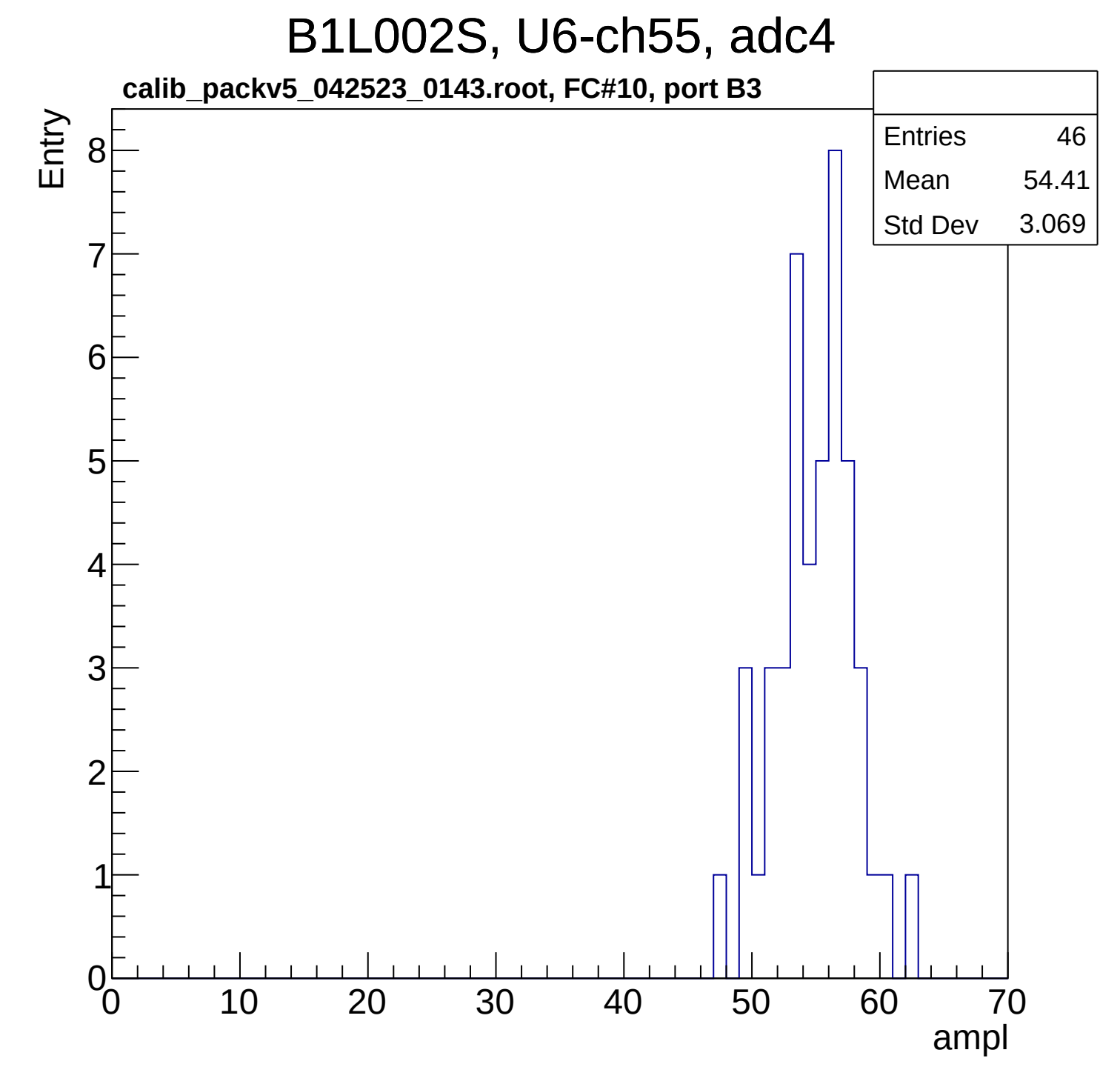

B1L002S, U6-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

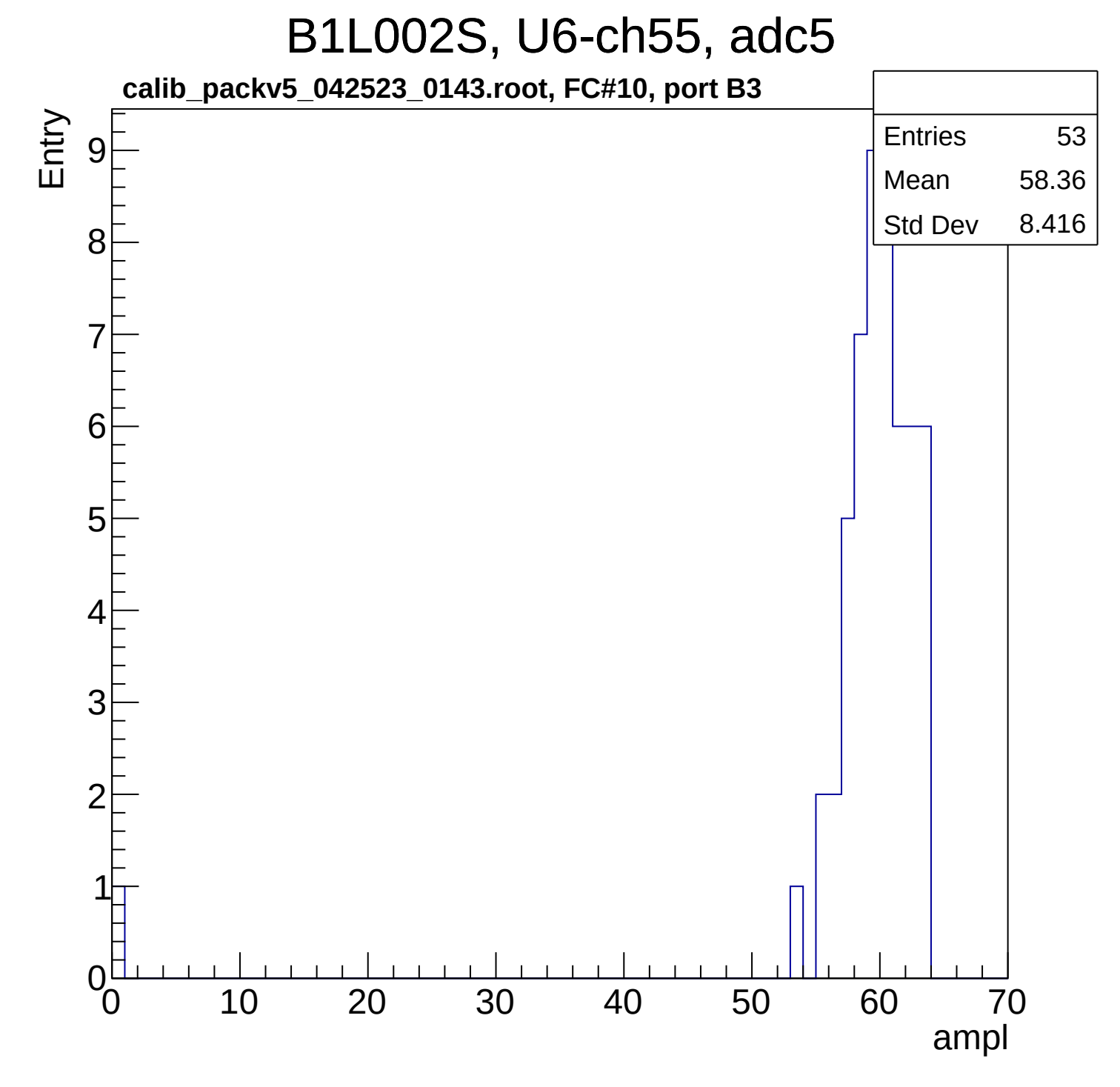

B1L002S, U6-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.78
Std Dev 4.699
Gaus mean : 29.2971
Gaus Width: 3.8977
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

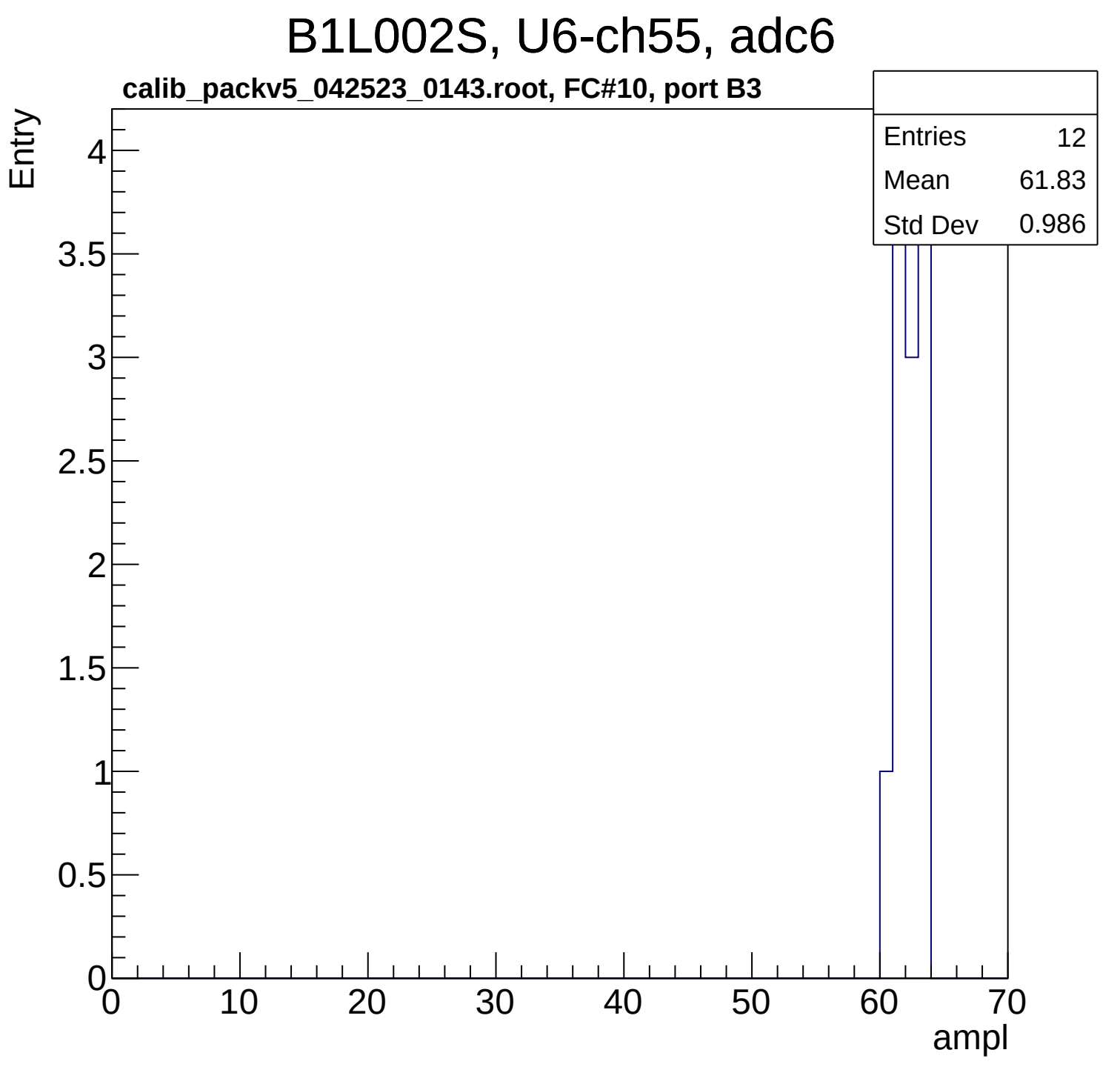

B1L002S, U6-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.82
Std Dev 3.355
Gaus mean : 36.5192
Gaus Width: 3.5011
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

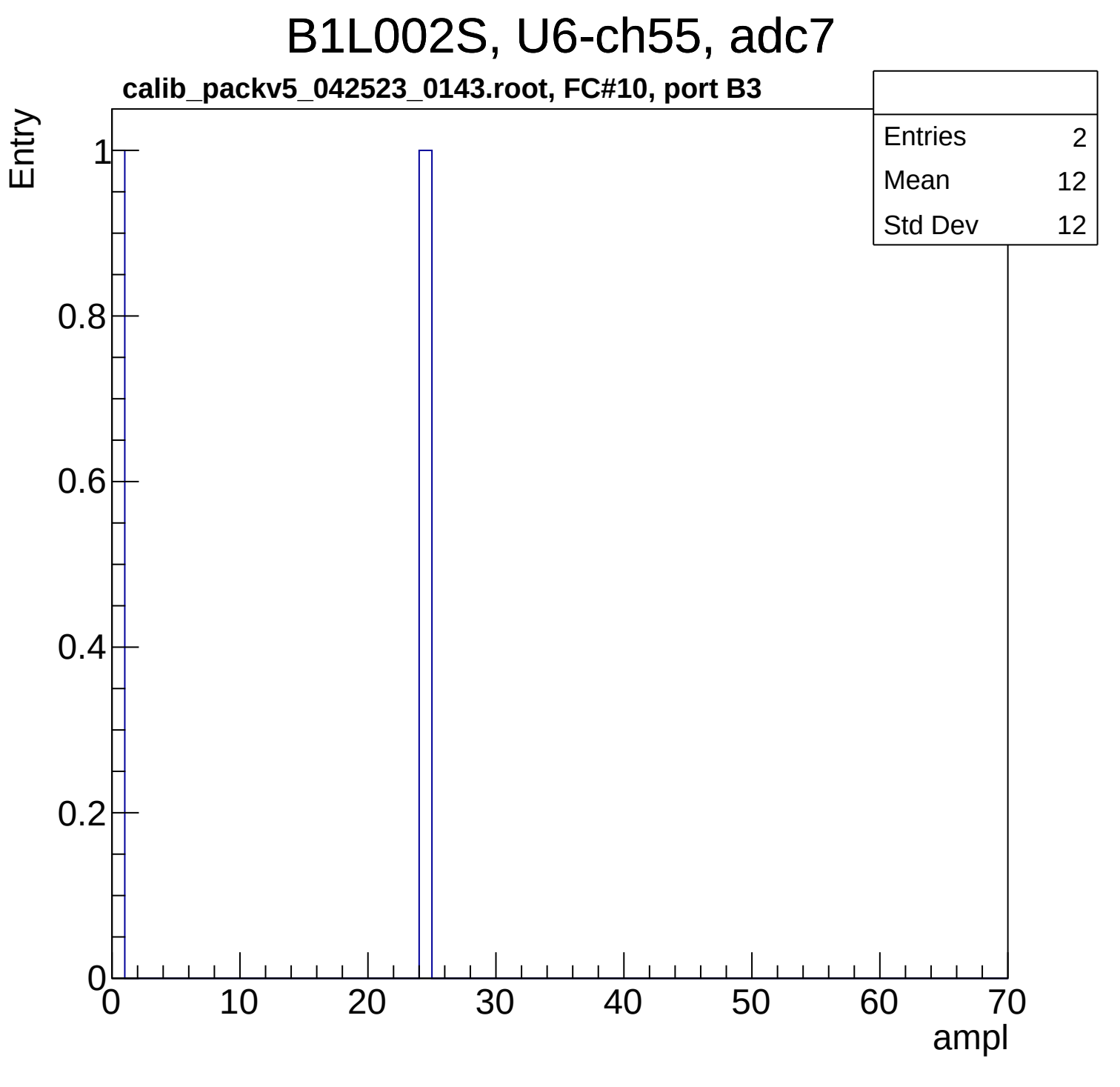

B1L002S, U6-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.48
Std Dev 2.833
Gaus mean : 43.0717
Gaus Width: 2.7999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

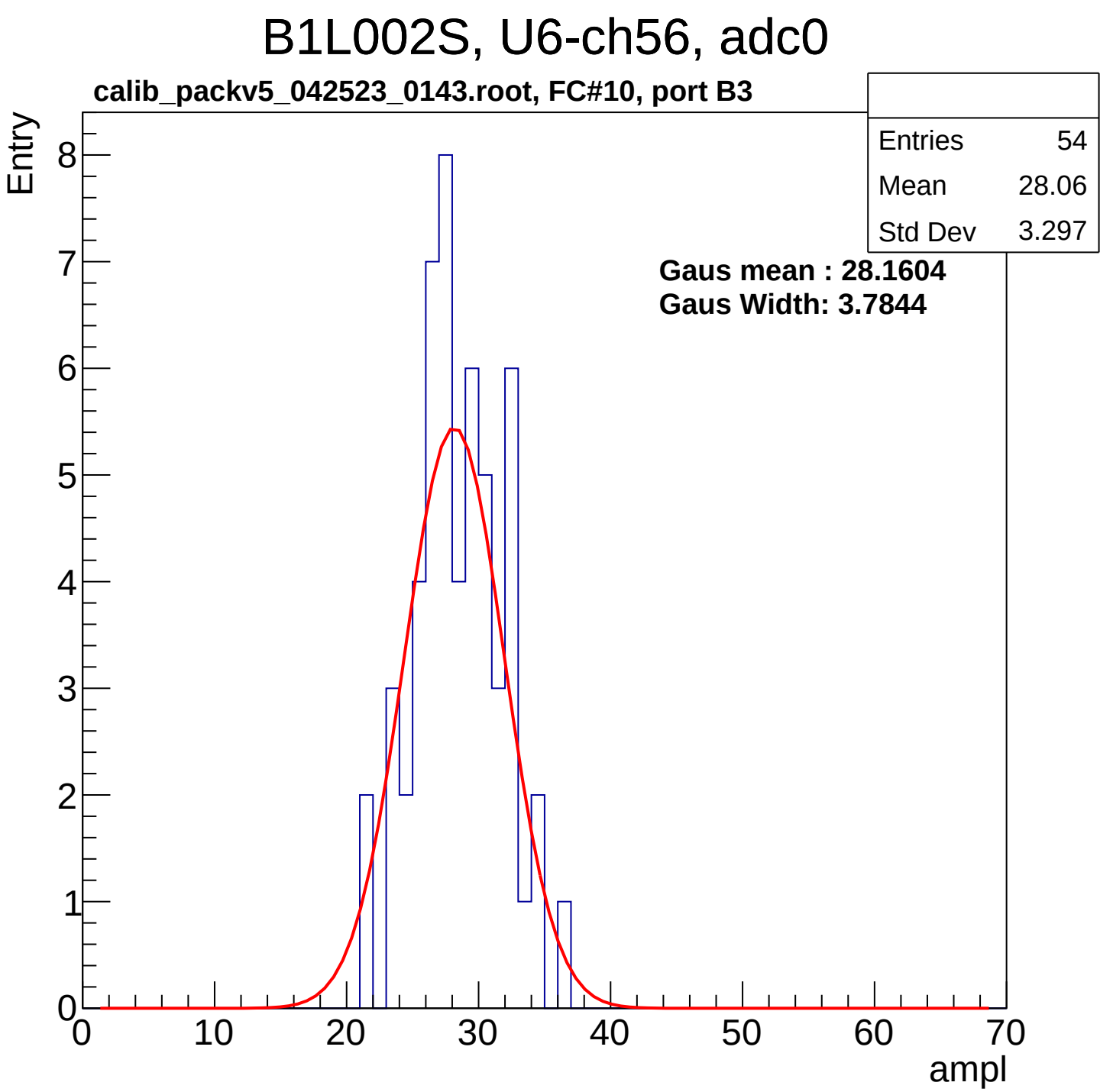

B1L002S, U6-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 49.49
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

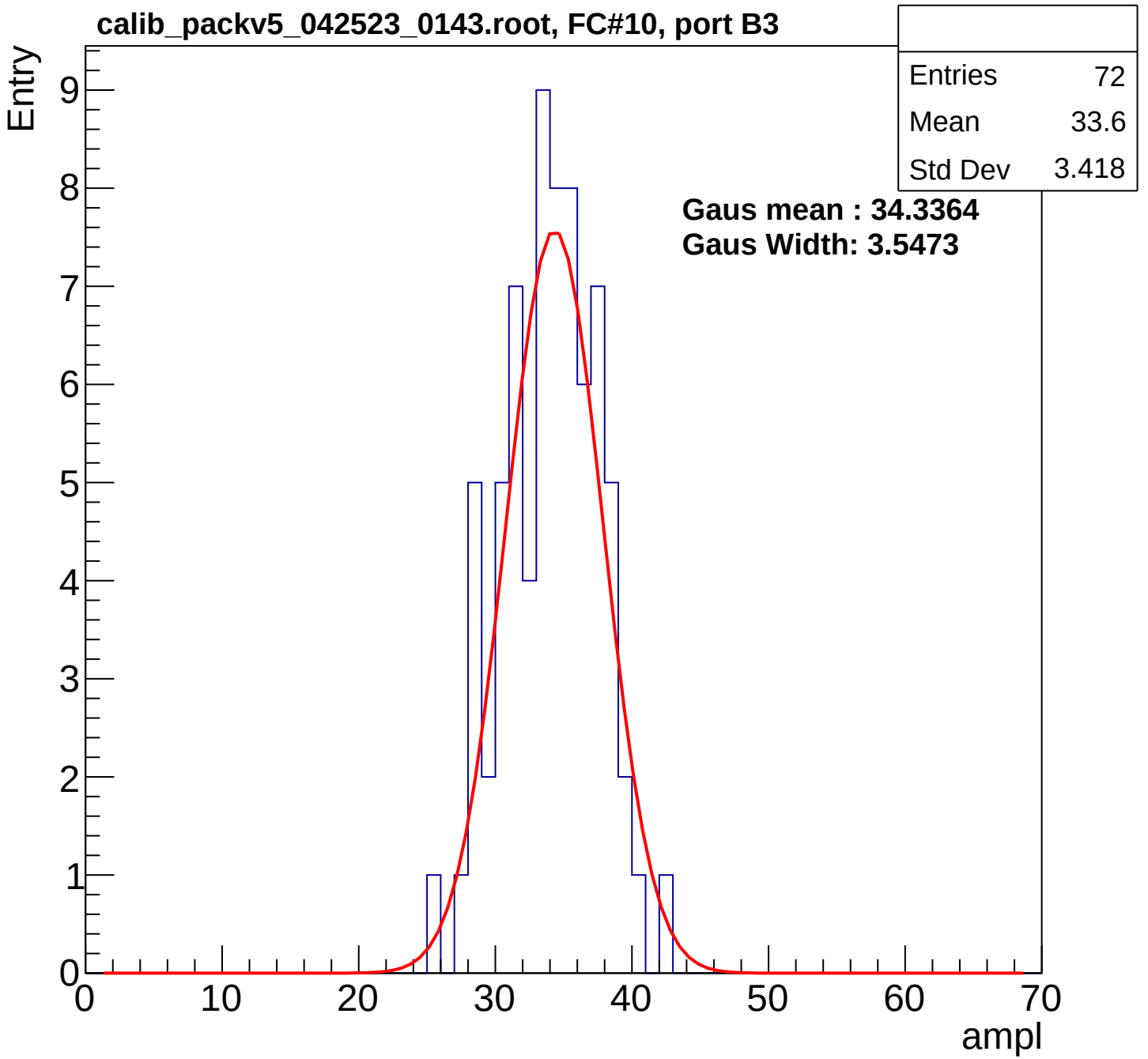

B1L002S, U6-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 54.41
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

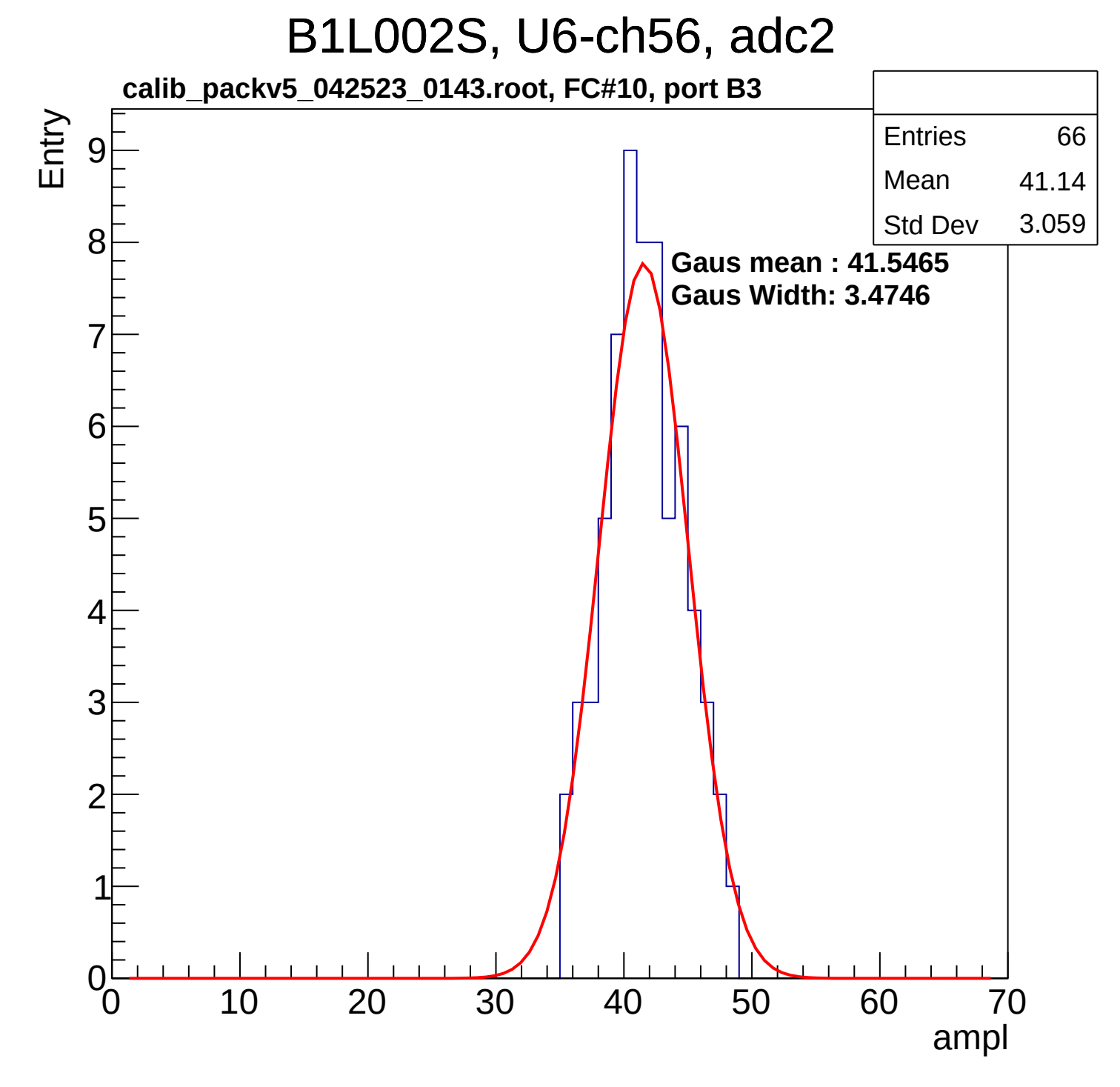

B1L002S, U6-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.36
Std Dev 8.416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

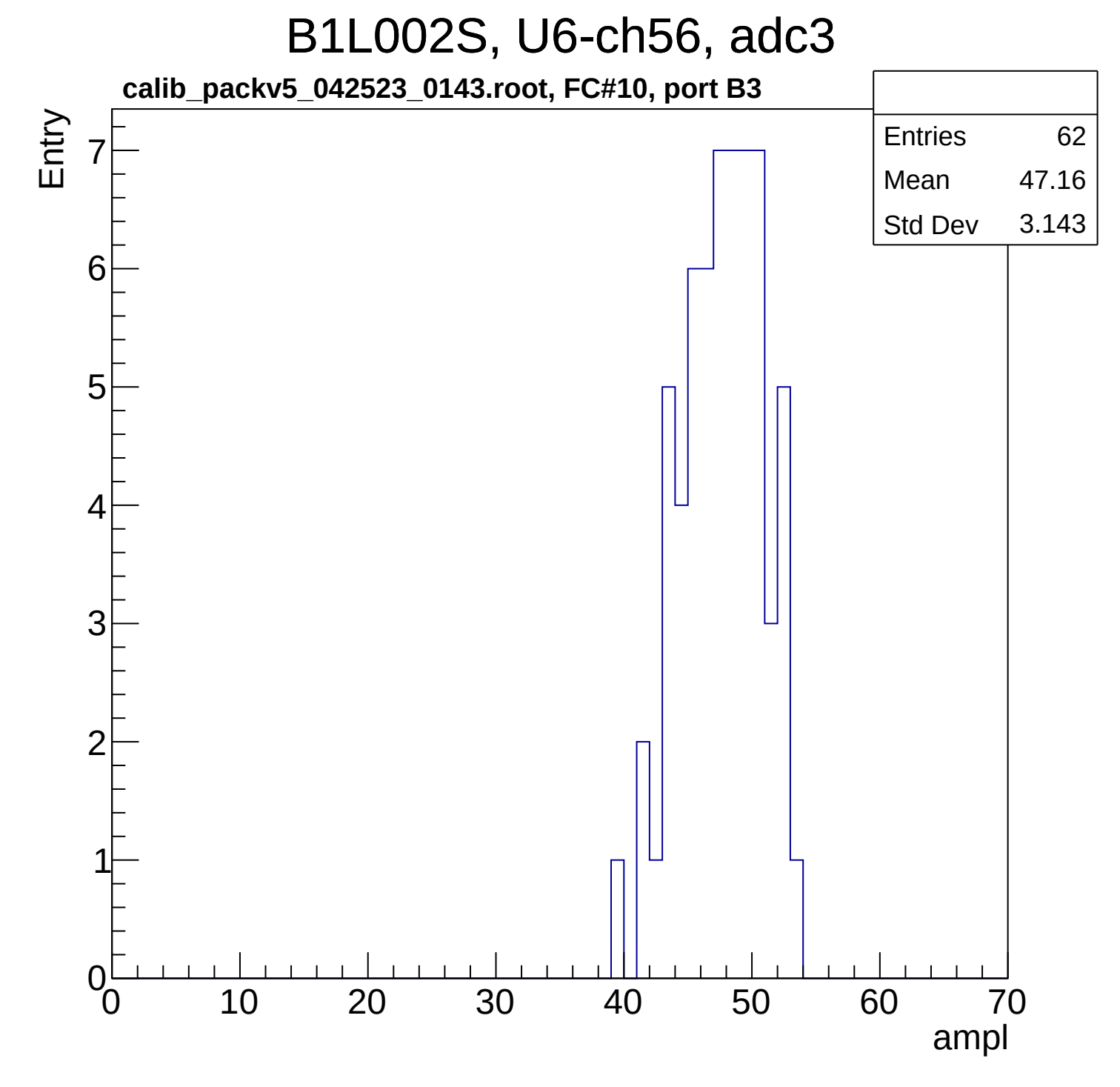

B1L002S, U6-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.83
Std Dev 0.986
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

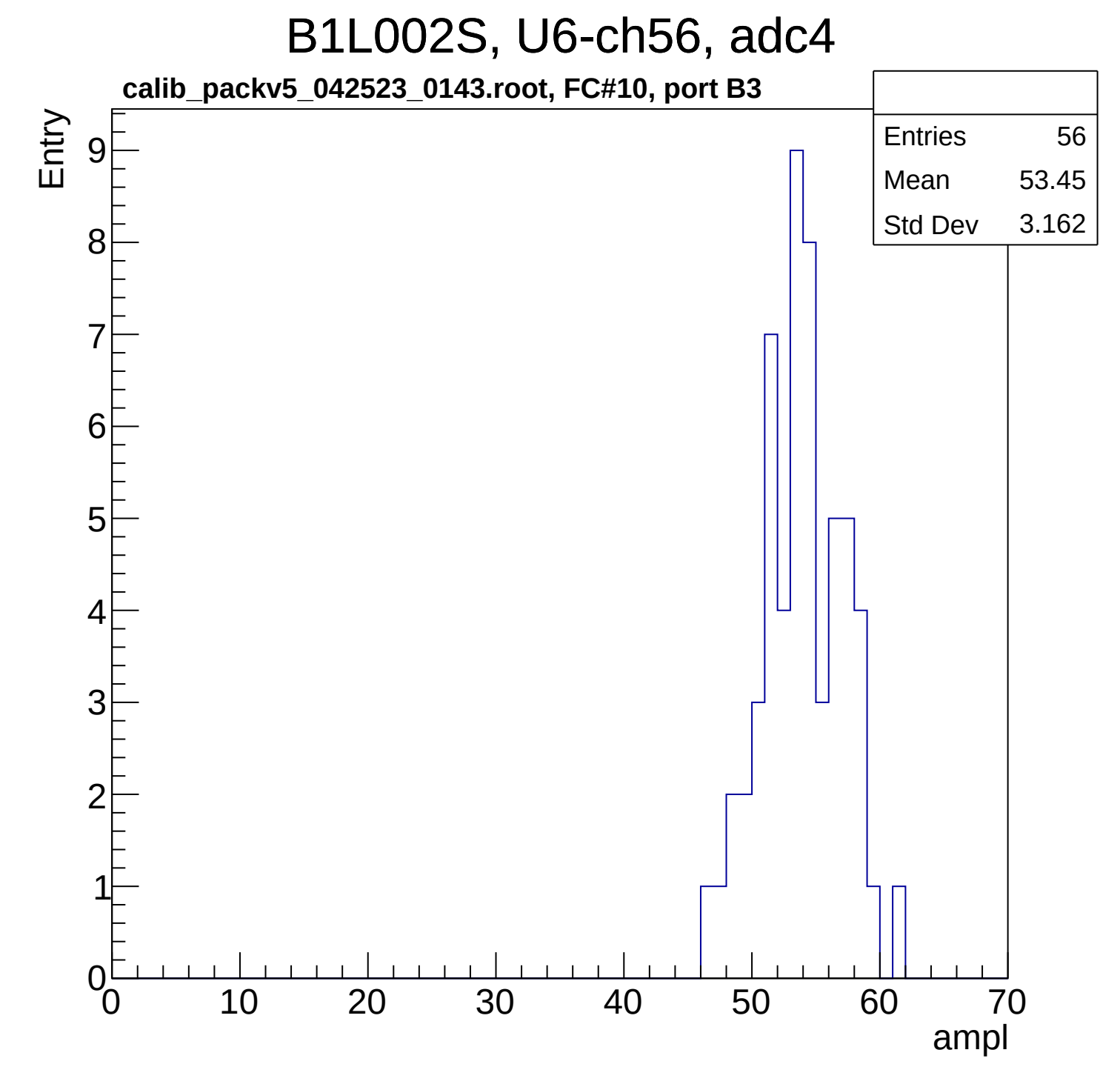

B1L002S, U6-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

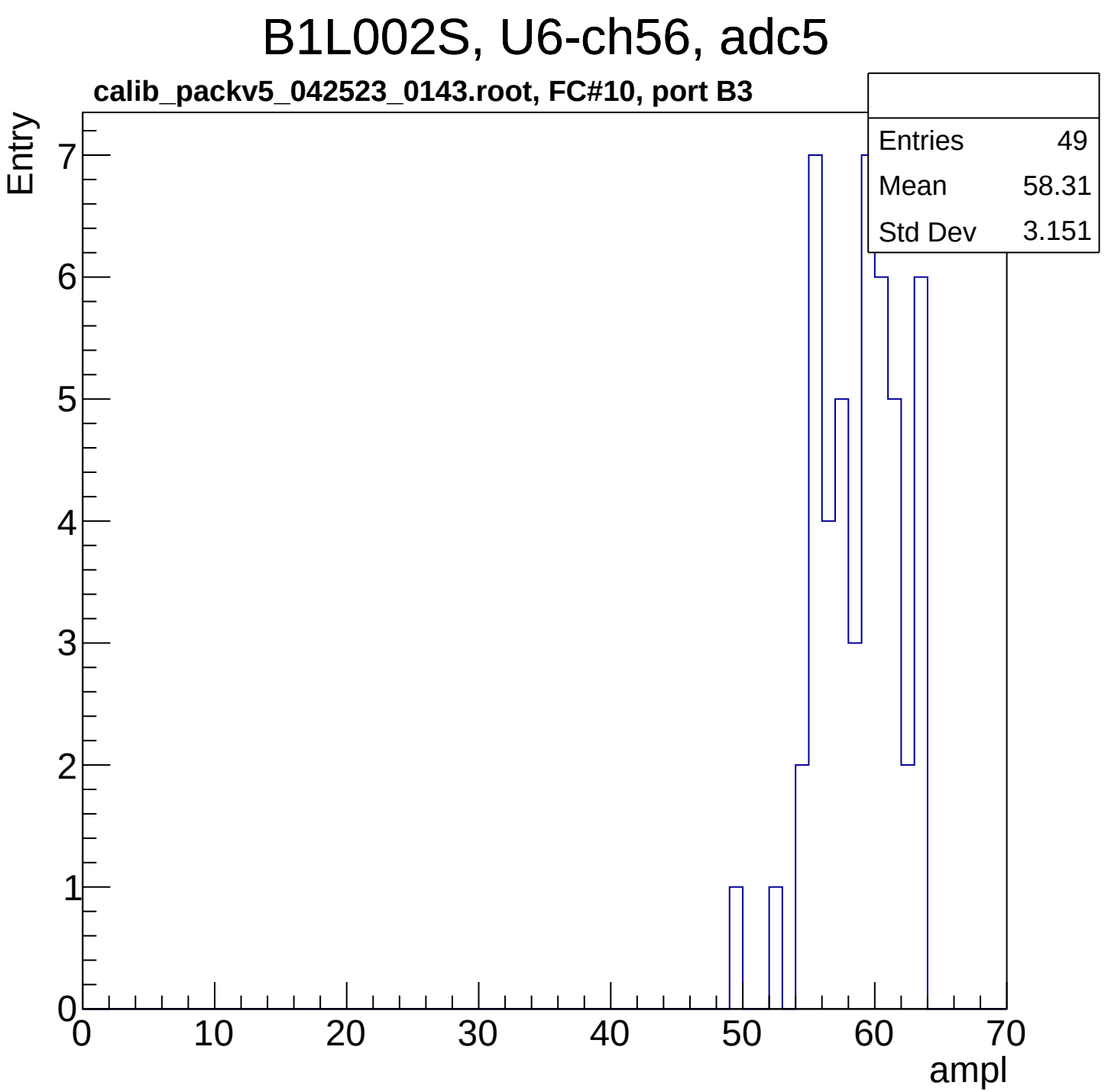

B1L002S, U6-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 28.06
Std Dev 3.297
Gaus mean : 28.1604
Gaus Width: 3.7844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

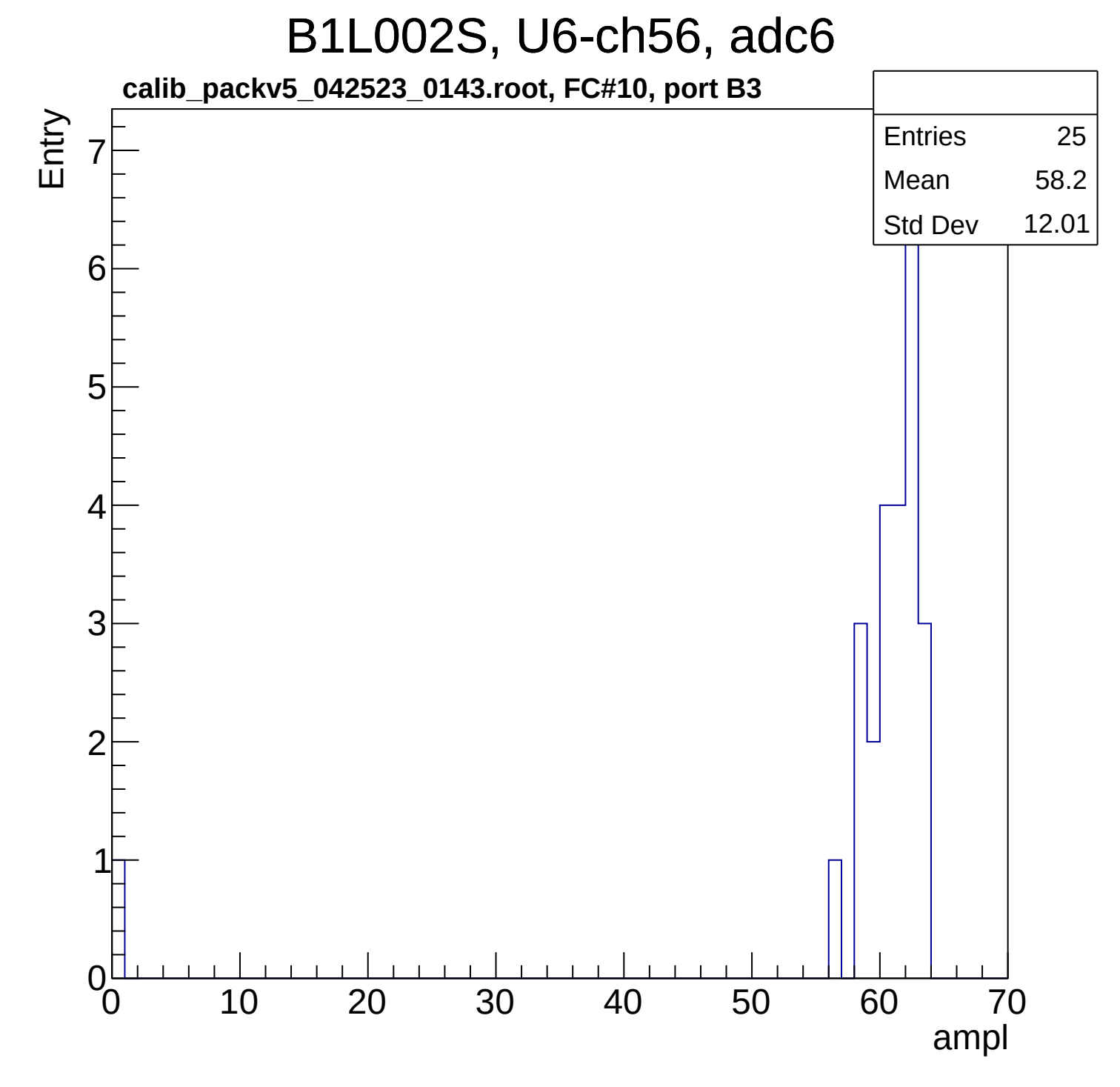

B1L002S, U6-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.6
Std Dev 3.418
Gaus mean : 34.3364
Gaus Width: 3.5473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

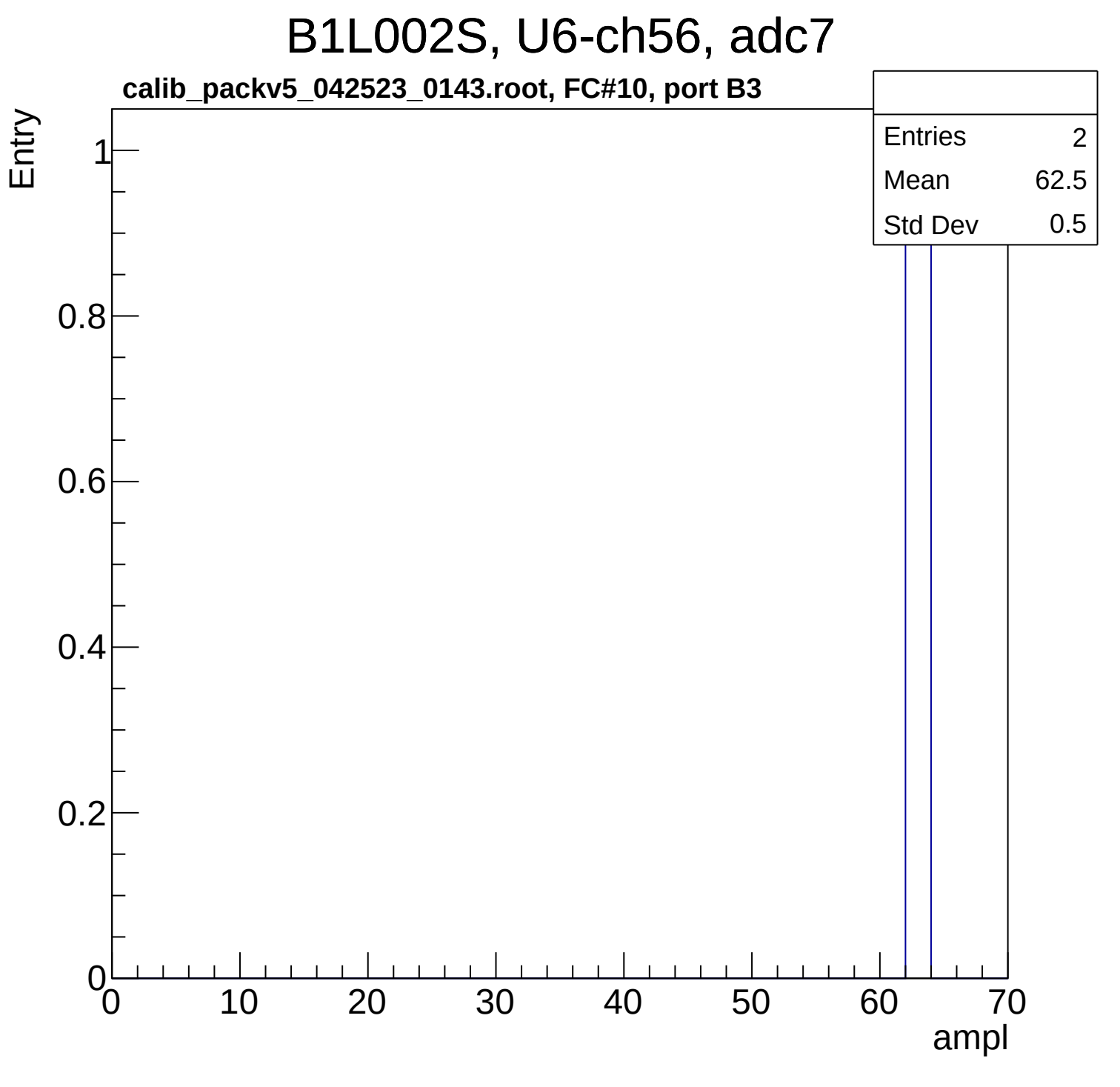

B1L002S, U6-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.14
Std Dev 3.059
Gaus mean : 41.5465
Gaus Width: 3.4746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

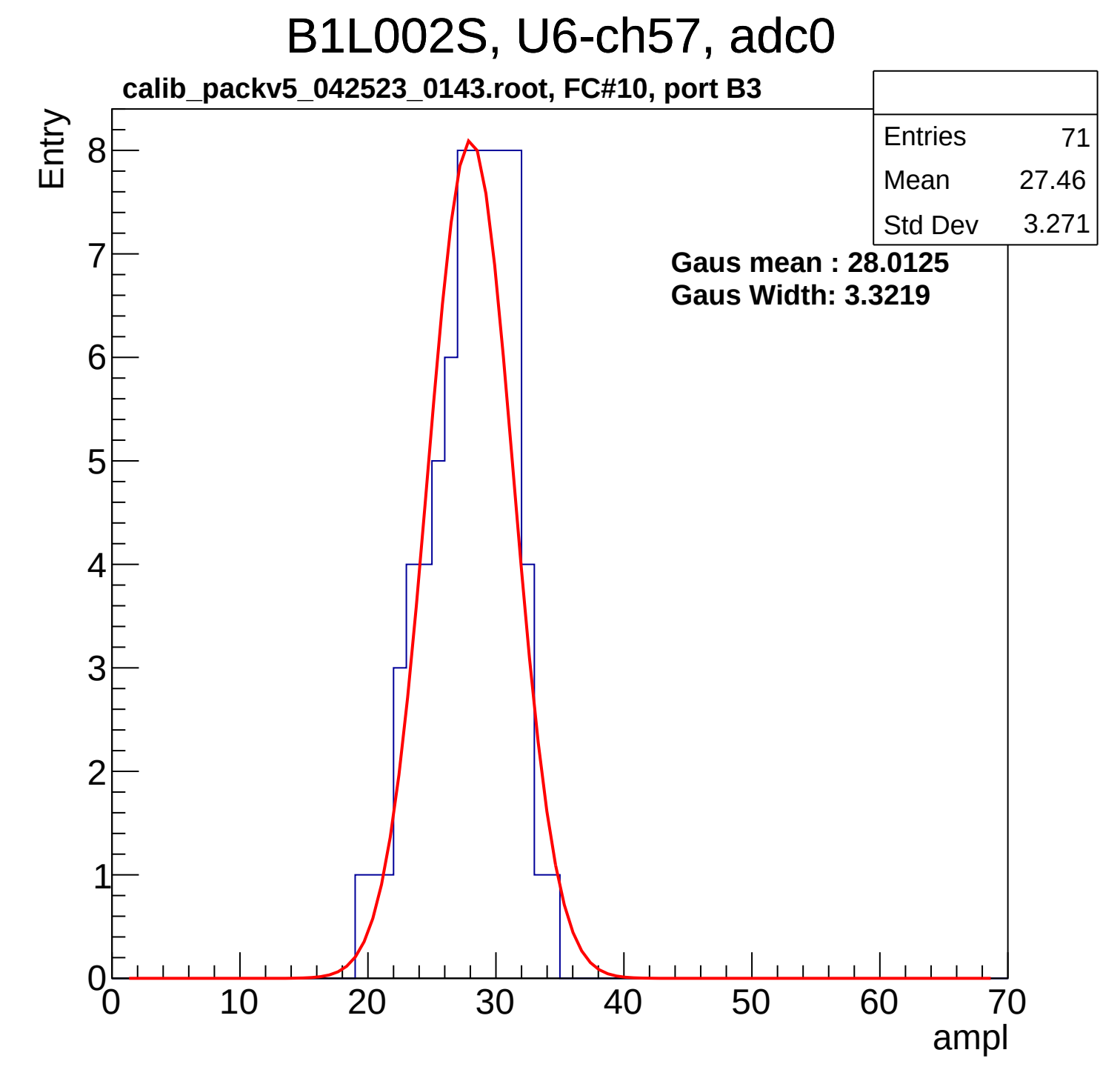

B1L002S, U6-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.16
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

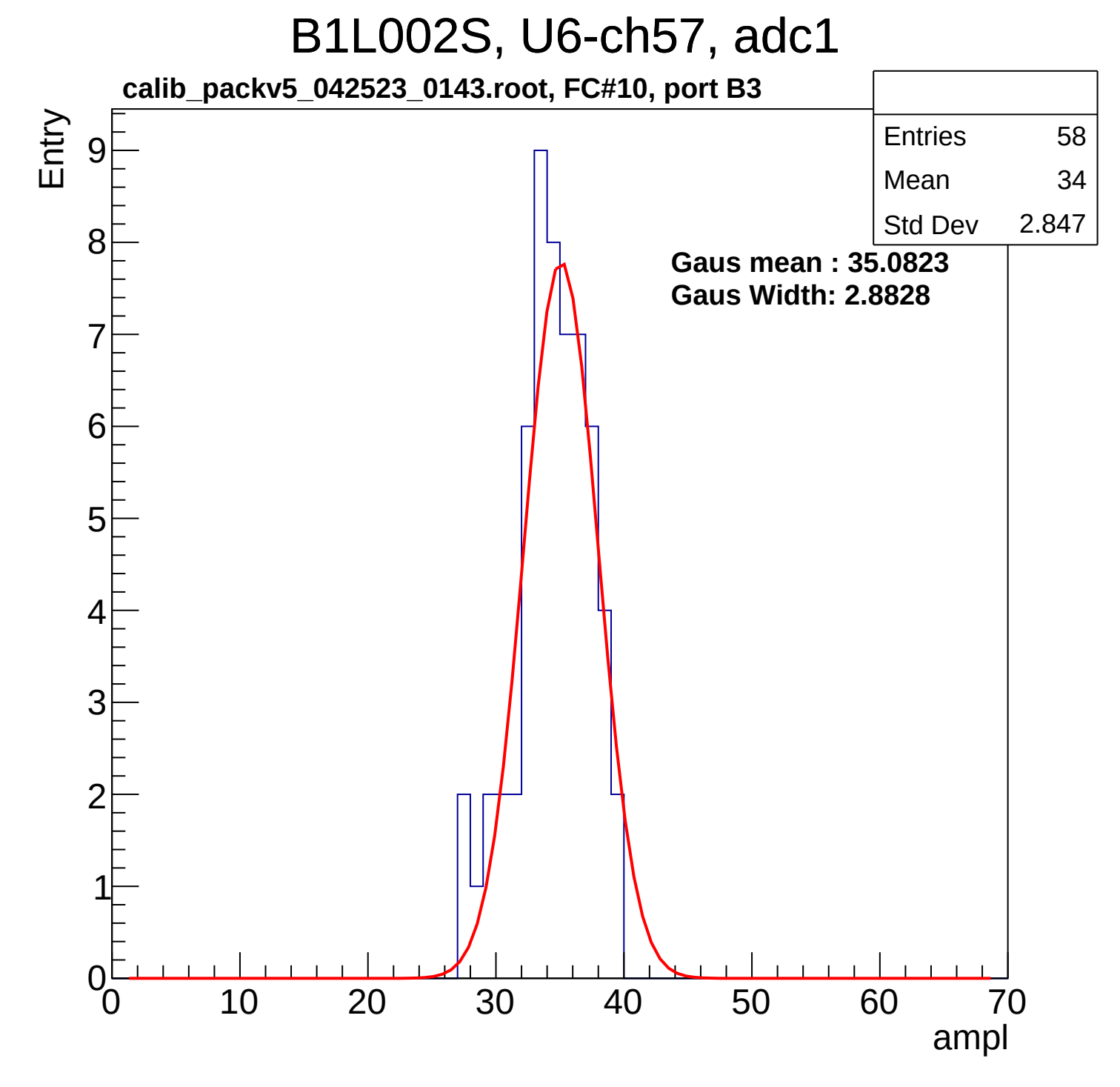

B1L002S, U6-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.45
Std Dev 3.162
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

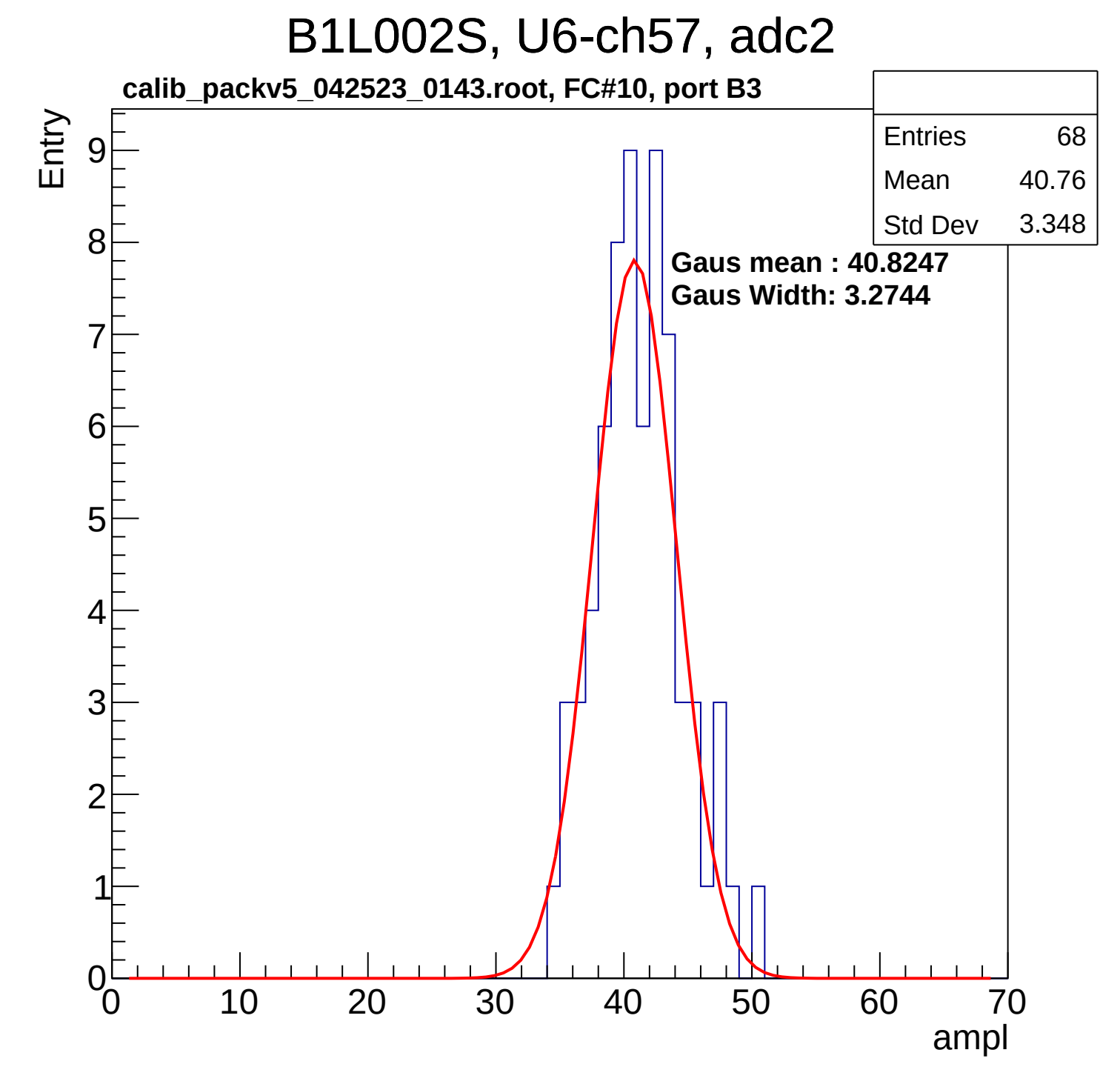

B1L002S, U6-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.31
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

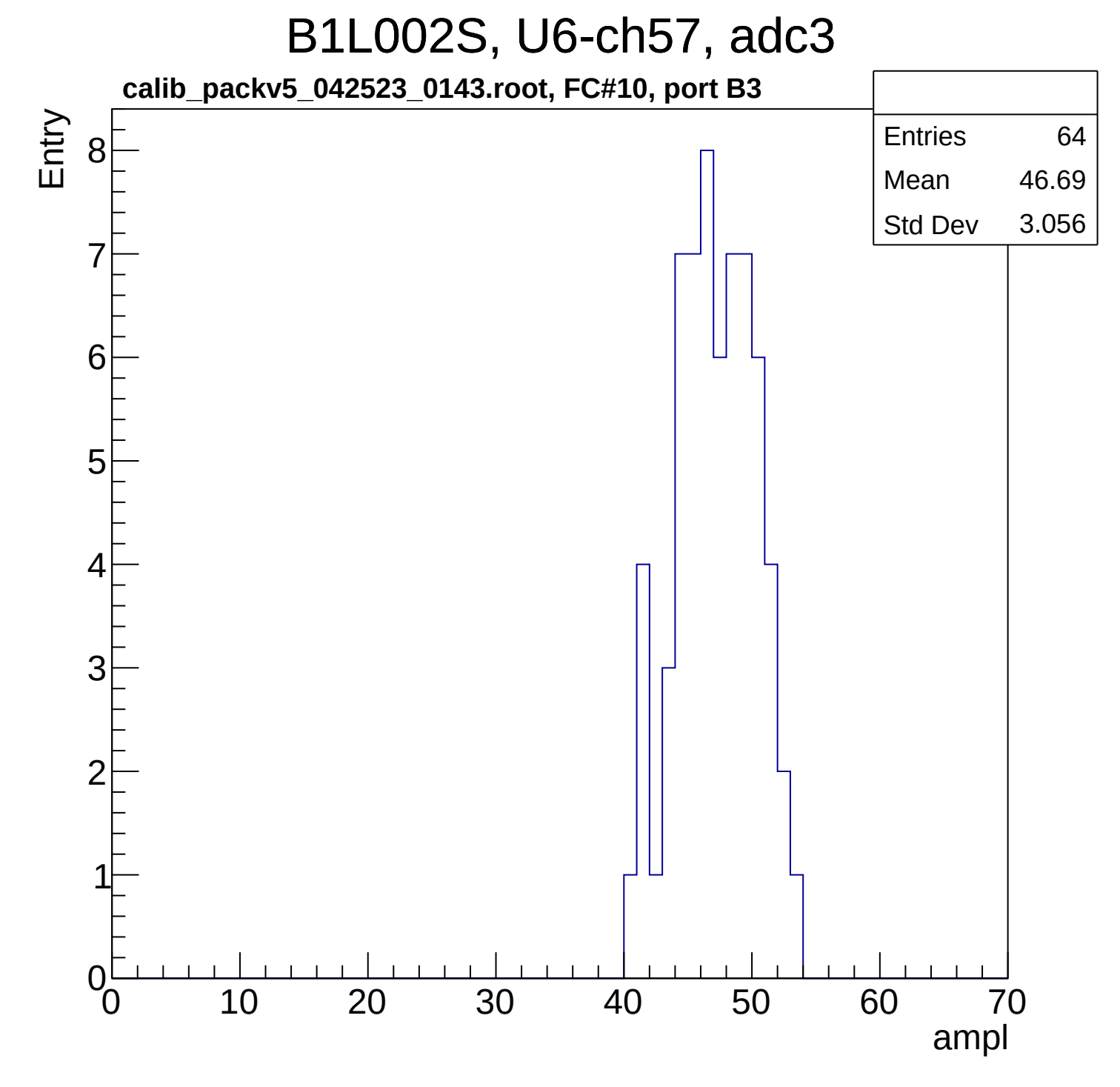

B1L002S, U6-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.2
Std Dev 12.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

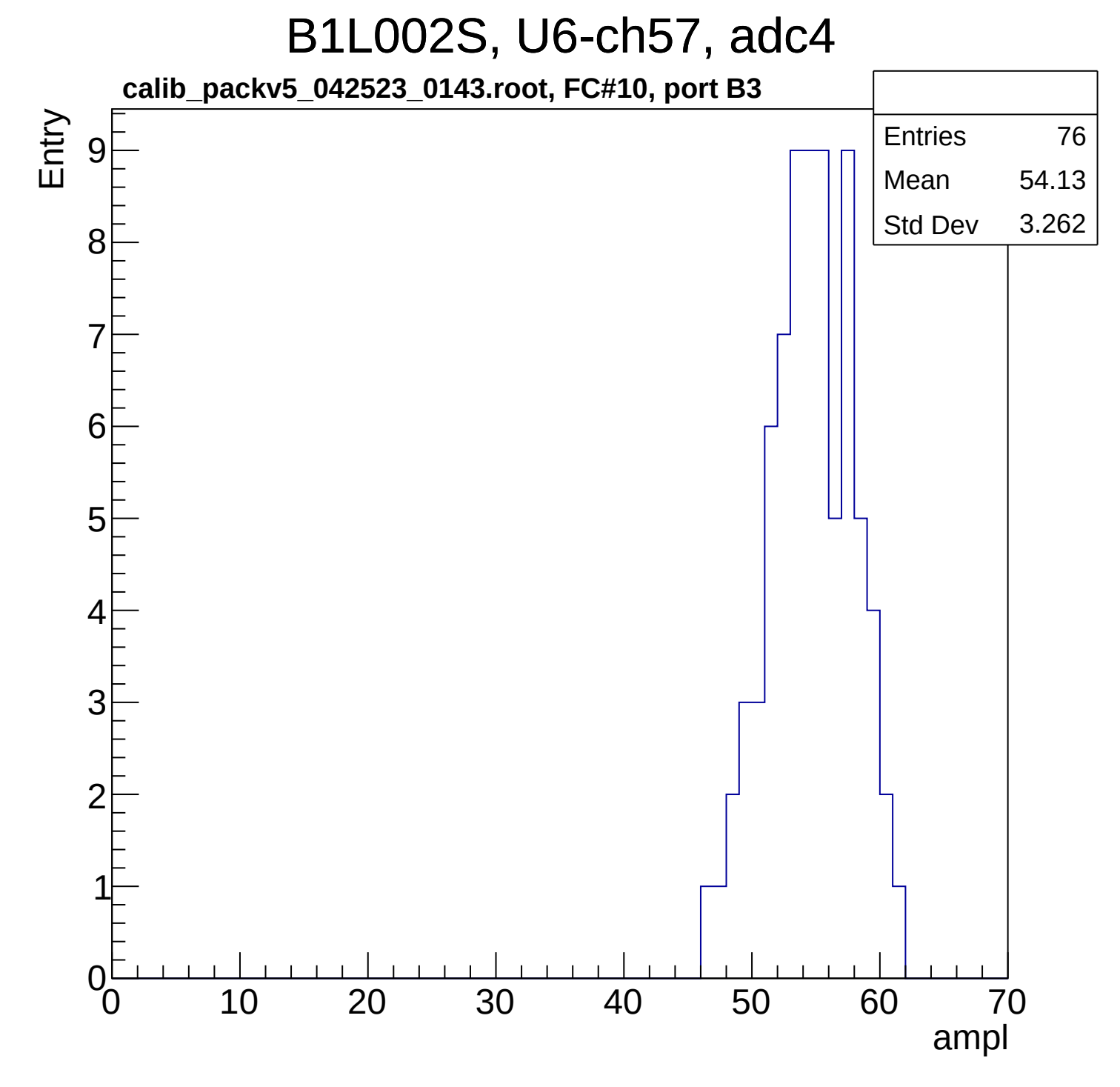

B1L002S, U6-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

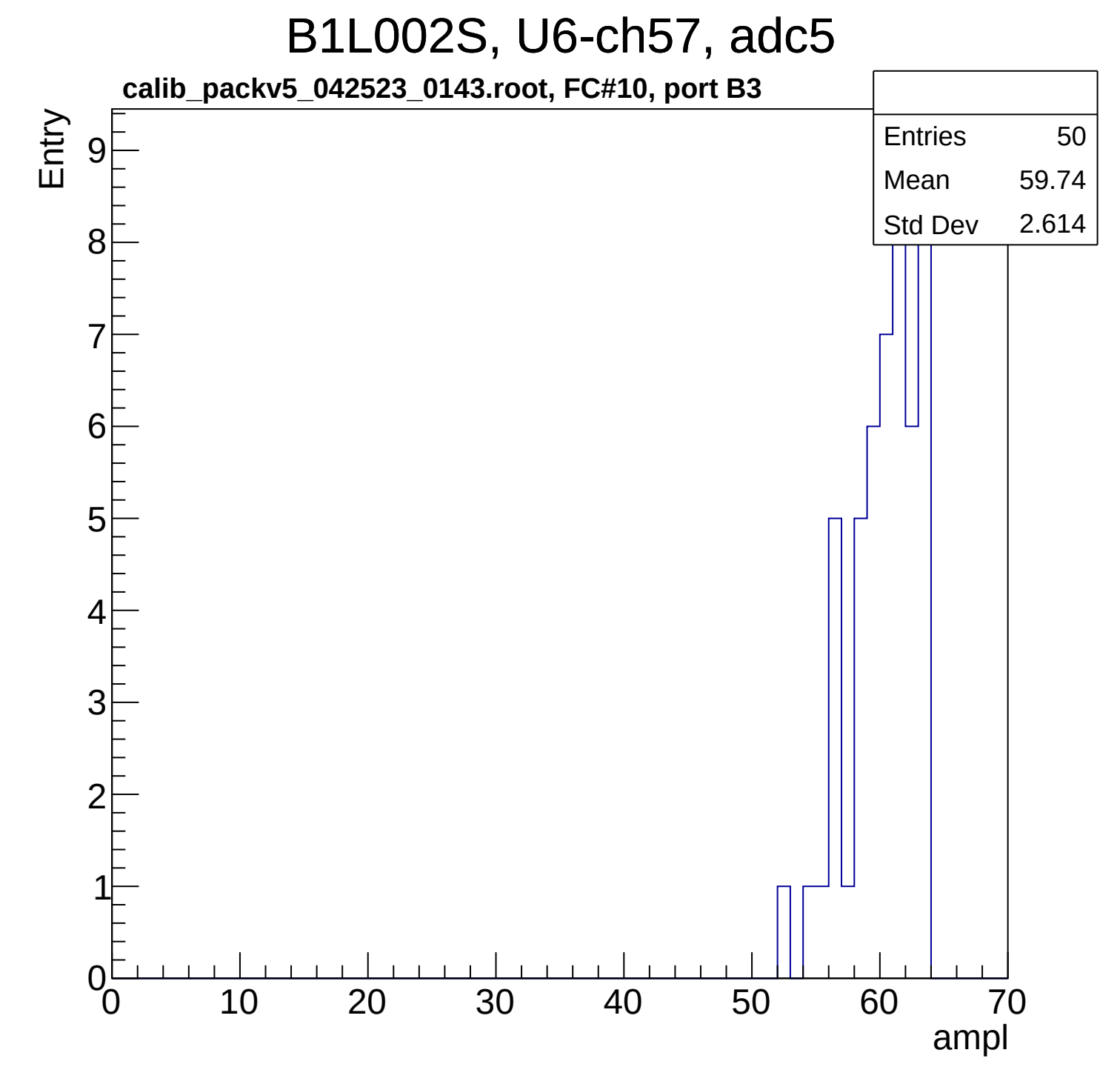

B1L002S, U6-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.46
Std Dev 3.271
Gaus mean : 28.0125
Gaus Width: 3.3219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

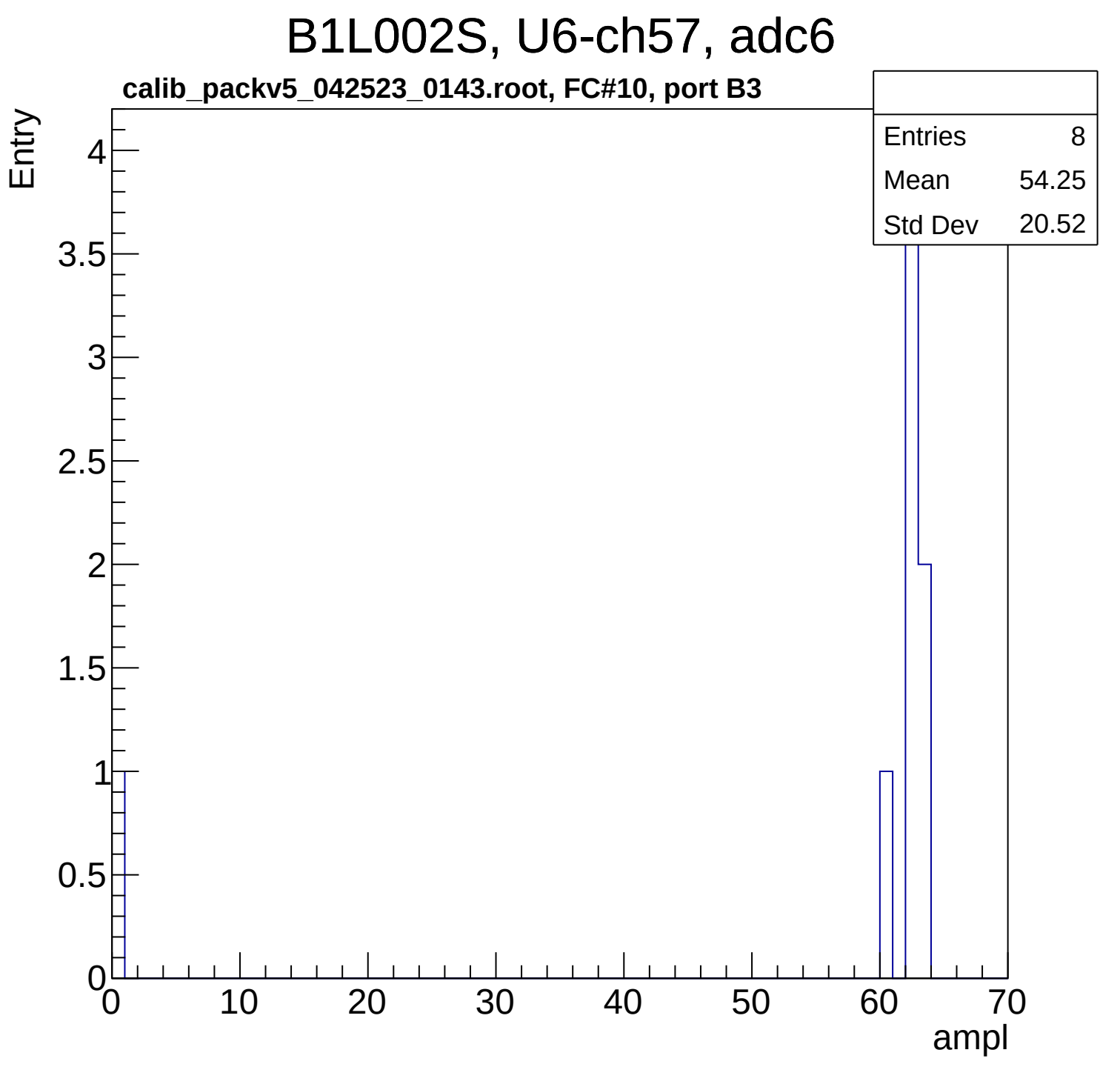

B1L002S, U6-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34
Std Dev 2.847
Gaus mean : 35.0823
Gaus Width: 2.8828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

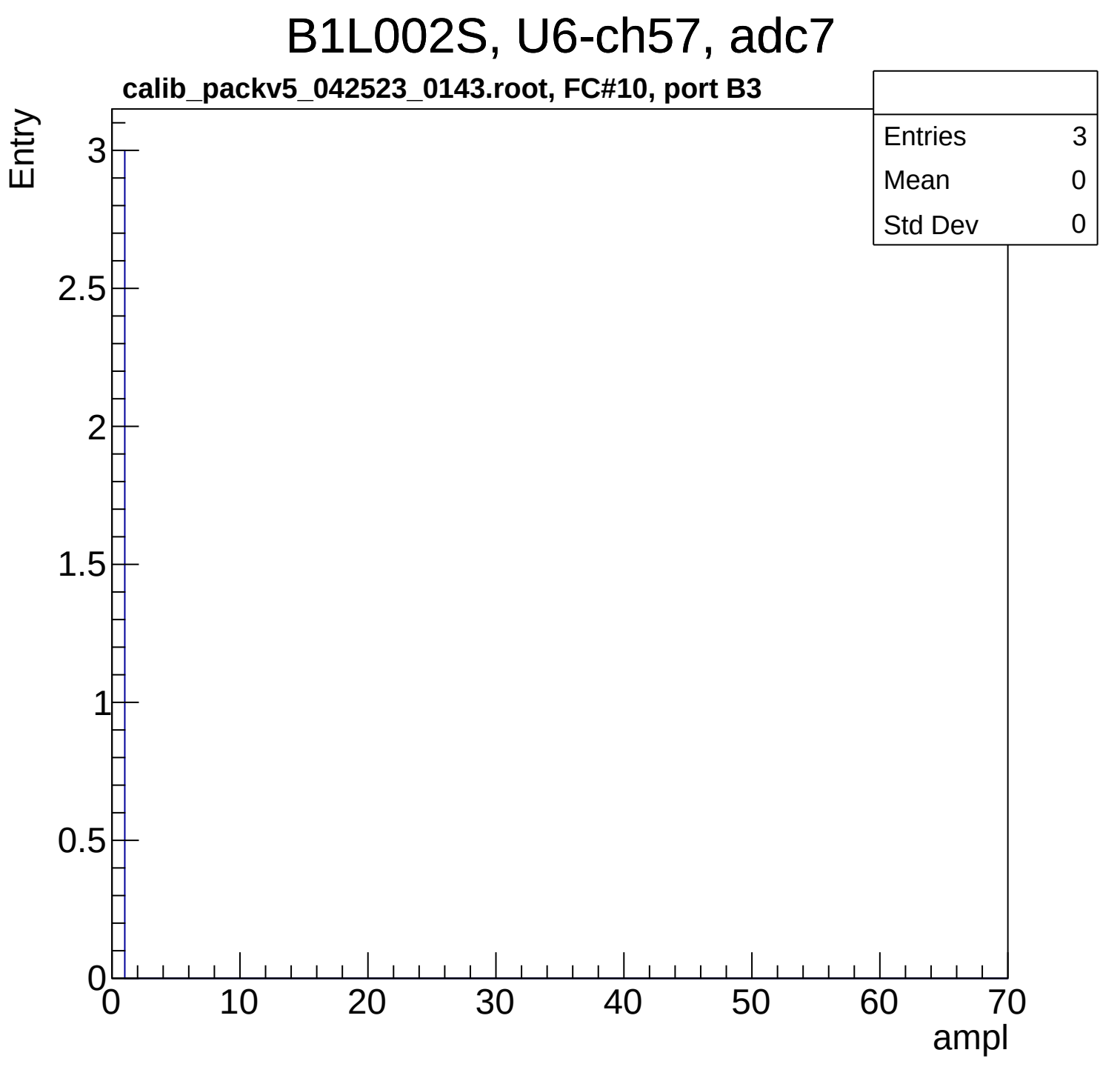

B1L002S, U6-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.76
Std Dev 3.348
Gaus mean : 40.8247
Gaus Width: 3.2744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

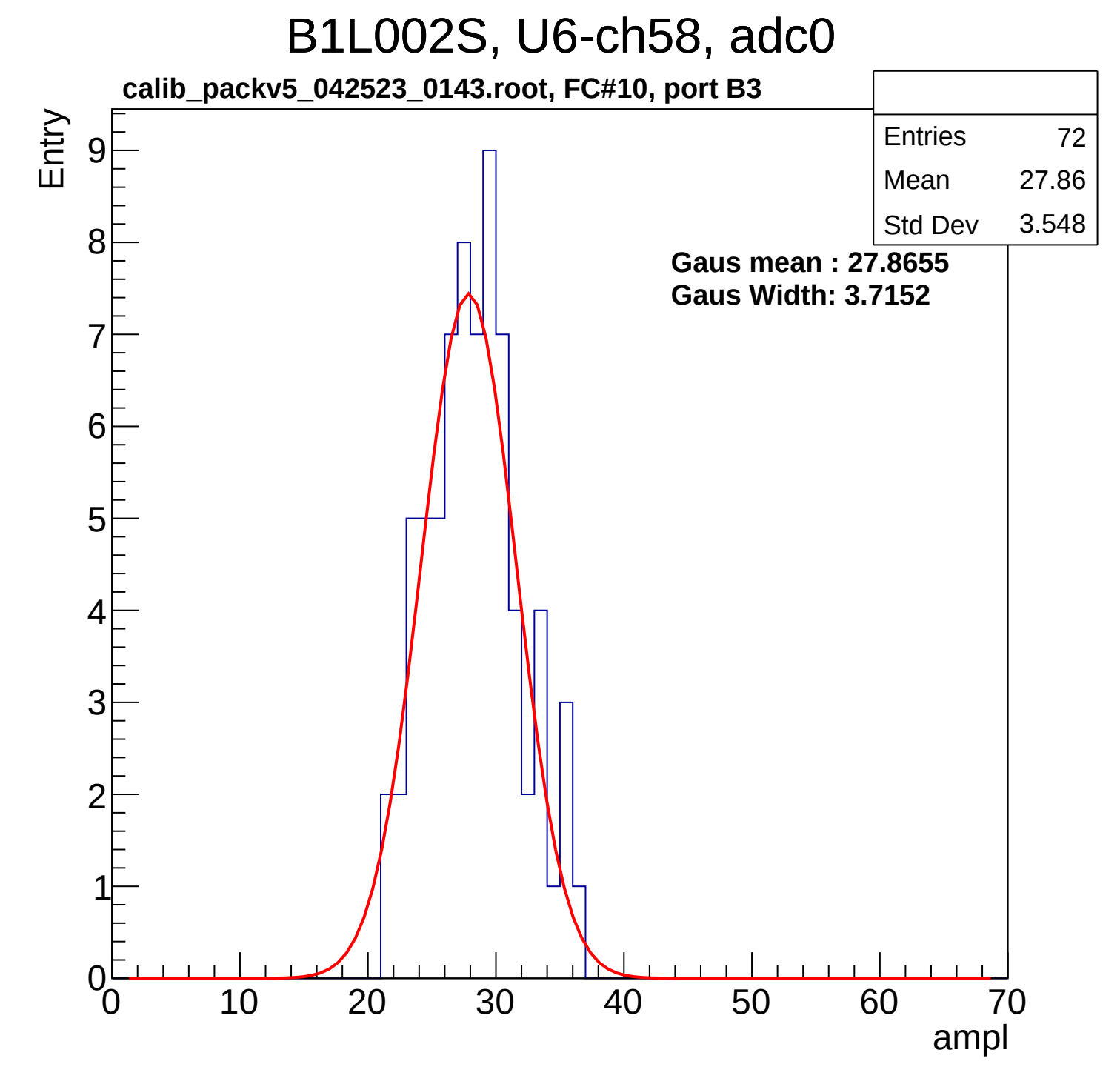

B1L002S, U6-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.69
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

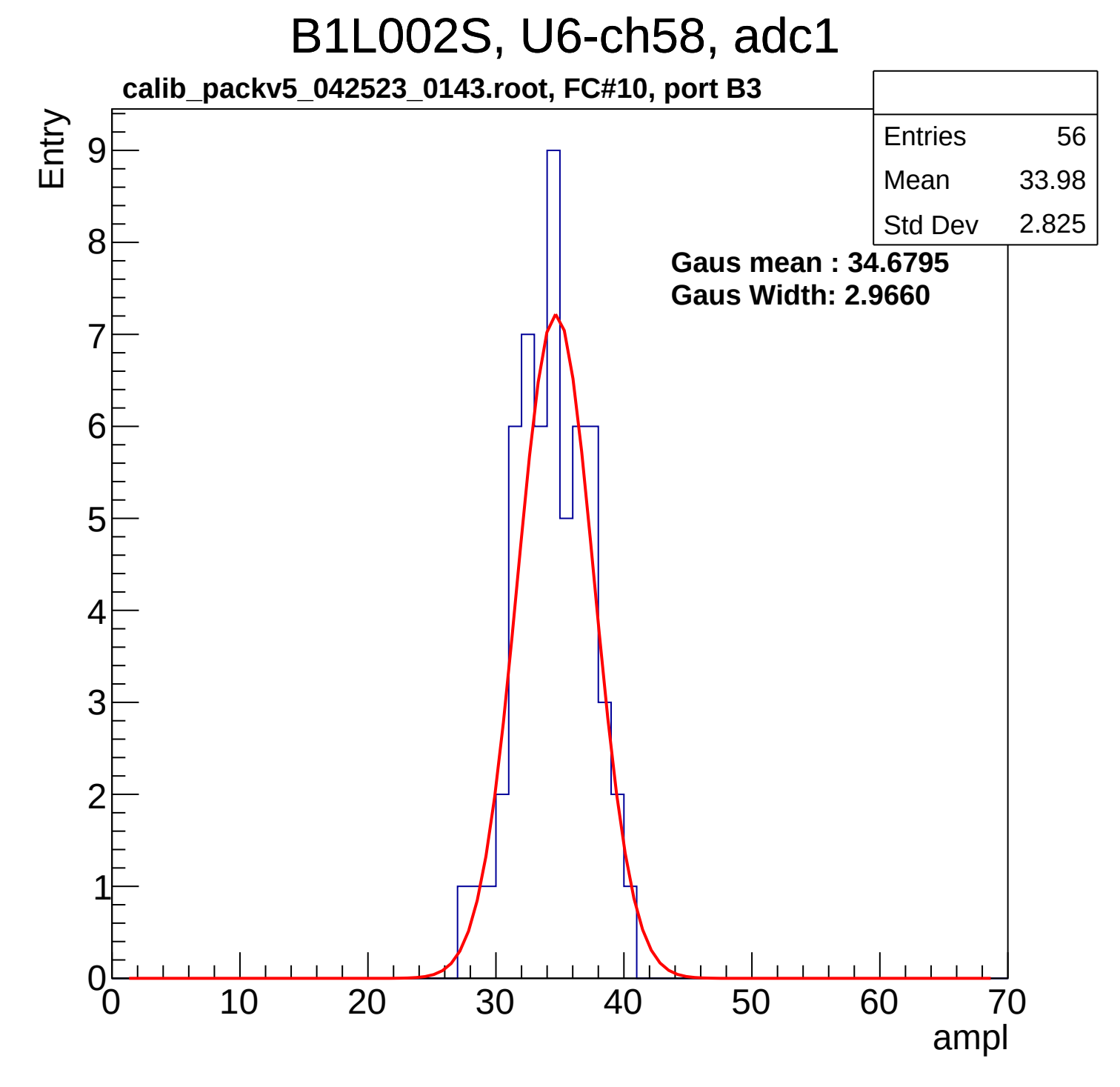

B1L002S, U6-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 54.13
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

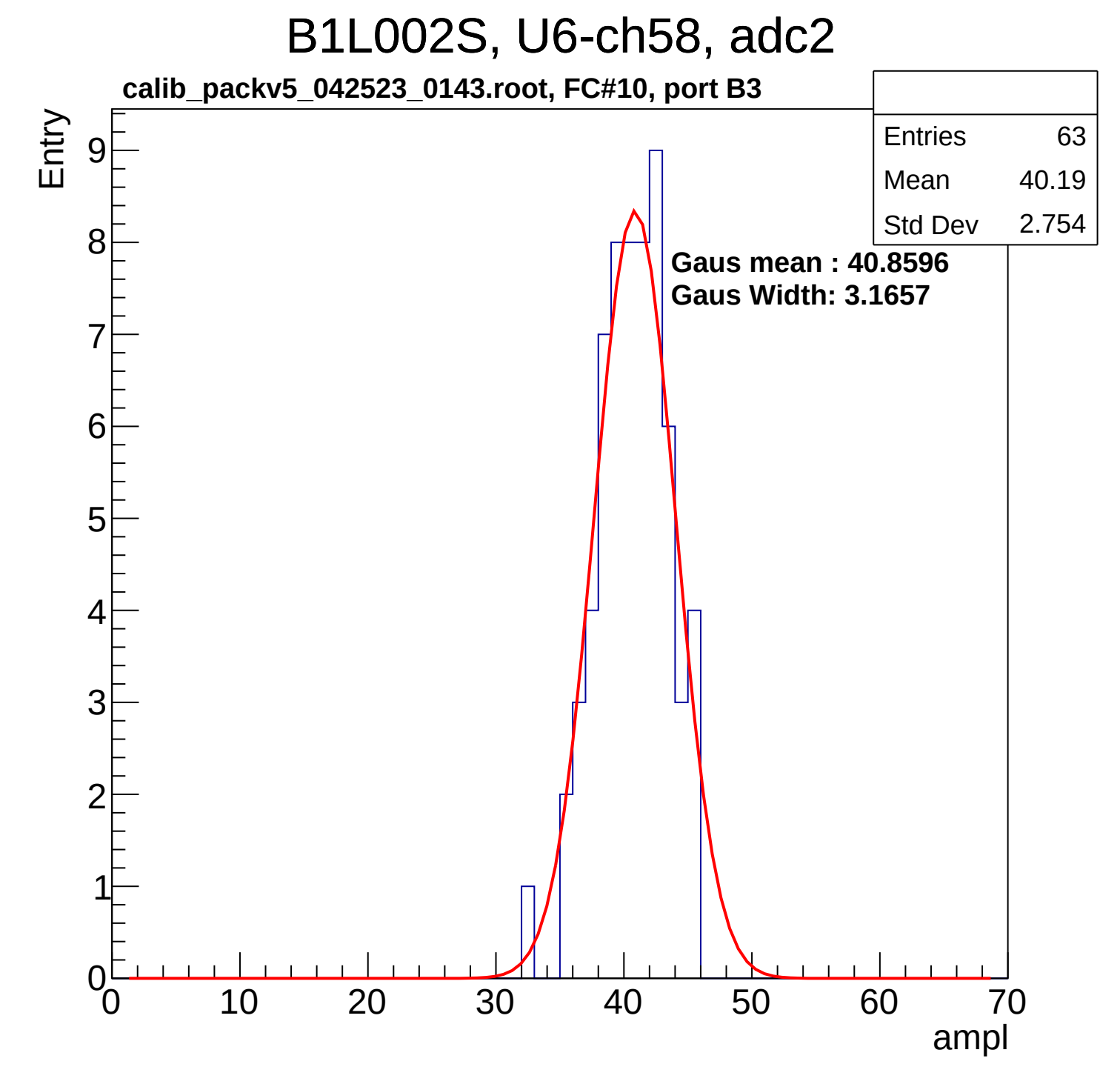

B1L002S, U6-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.74
Std Dev 2.614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

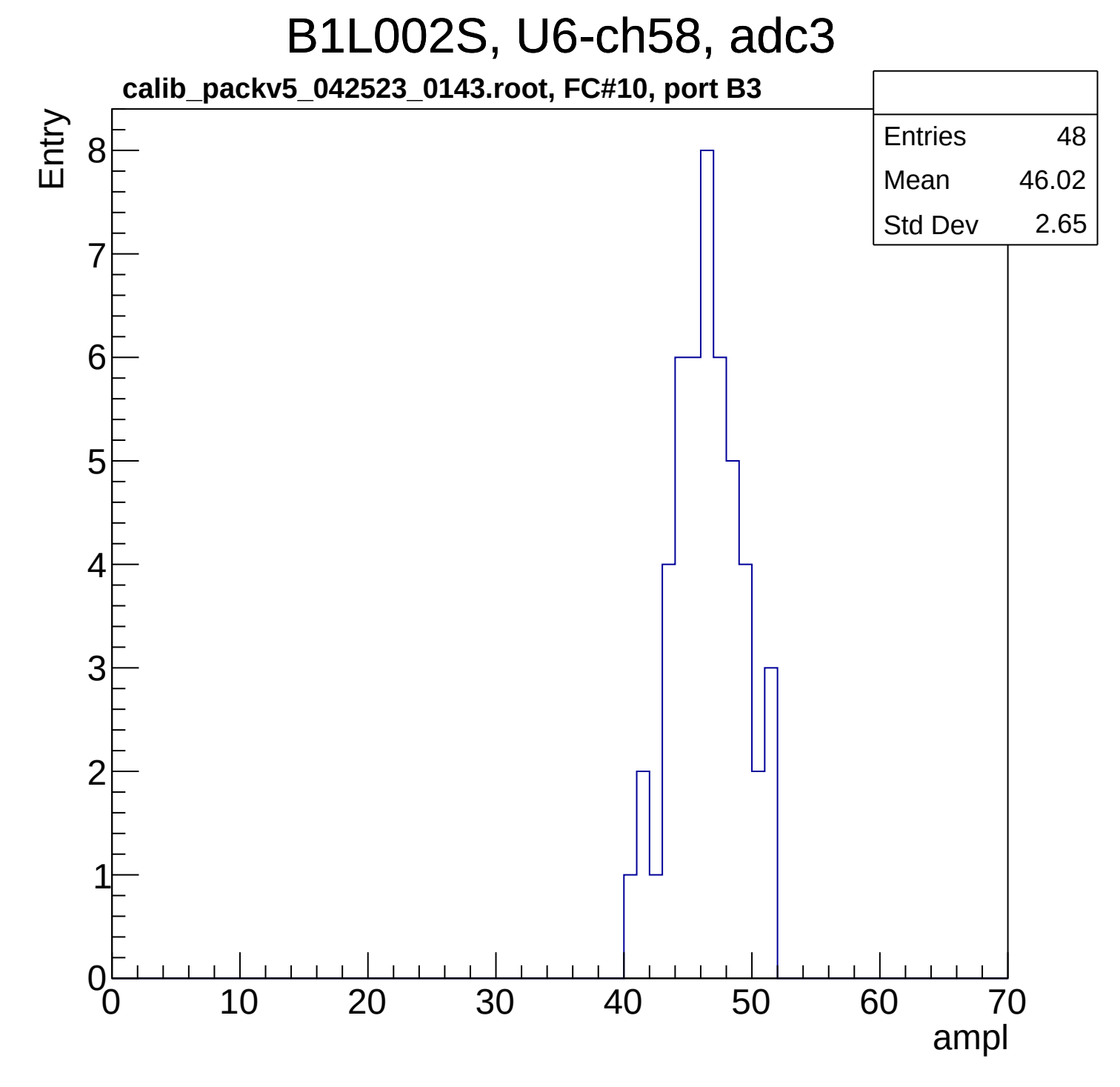

B1L002S, U6-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.25
Std Dev 20.52
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

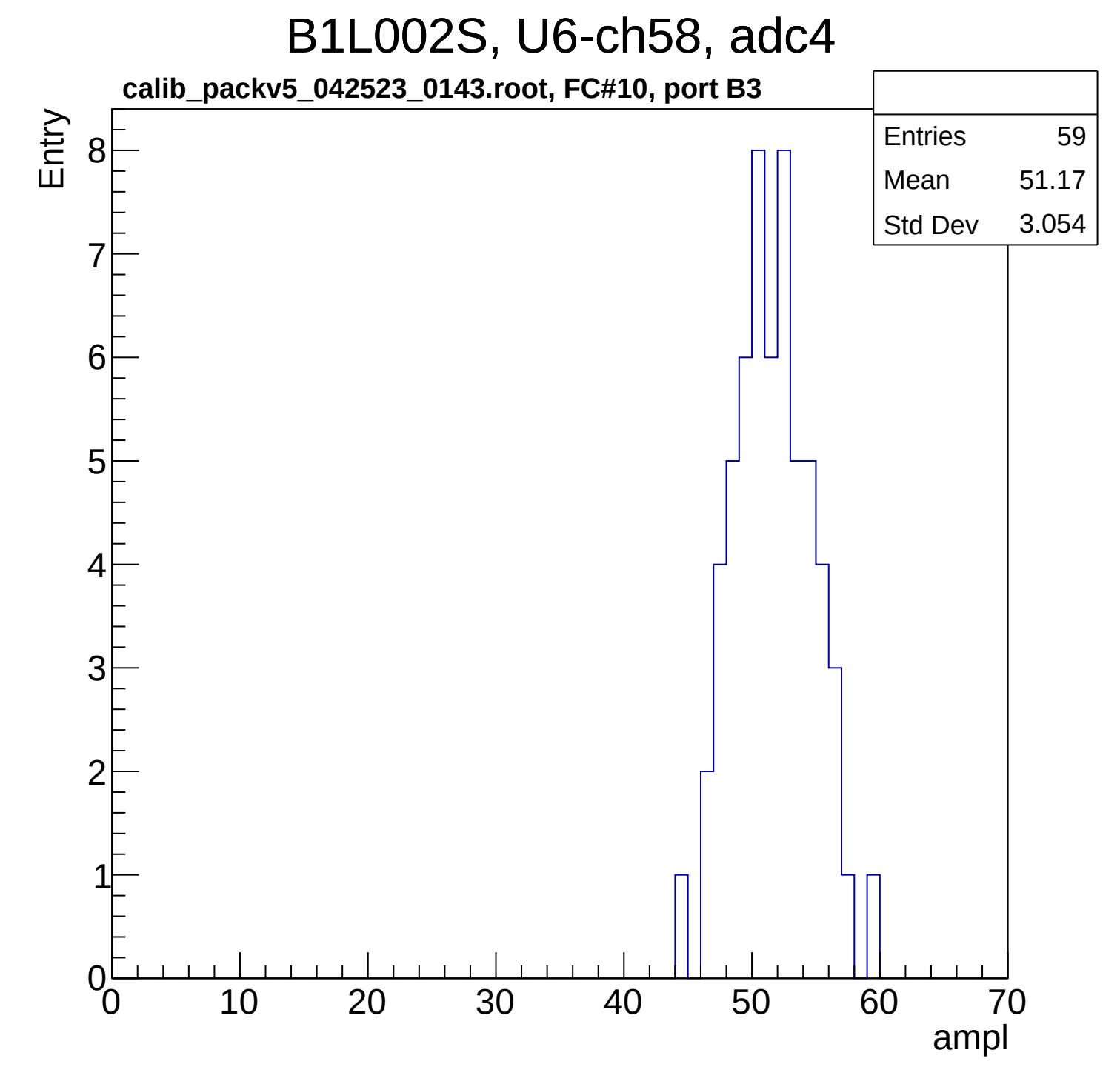

B1L002S, U6-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

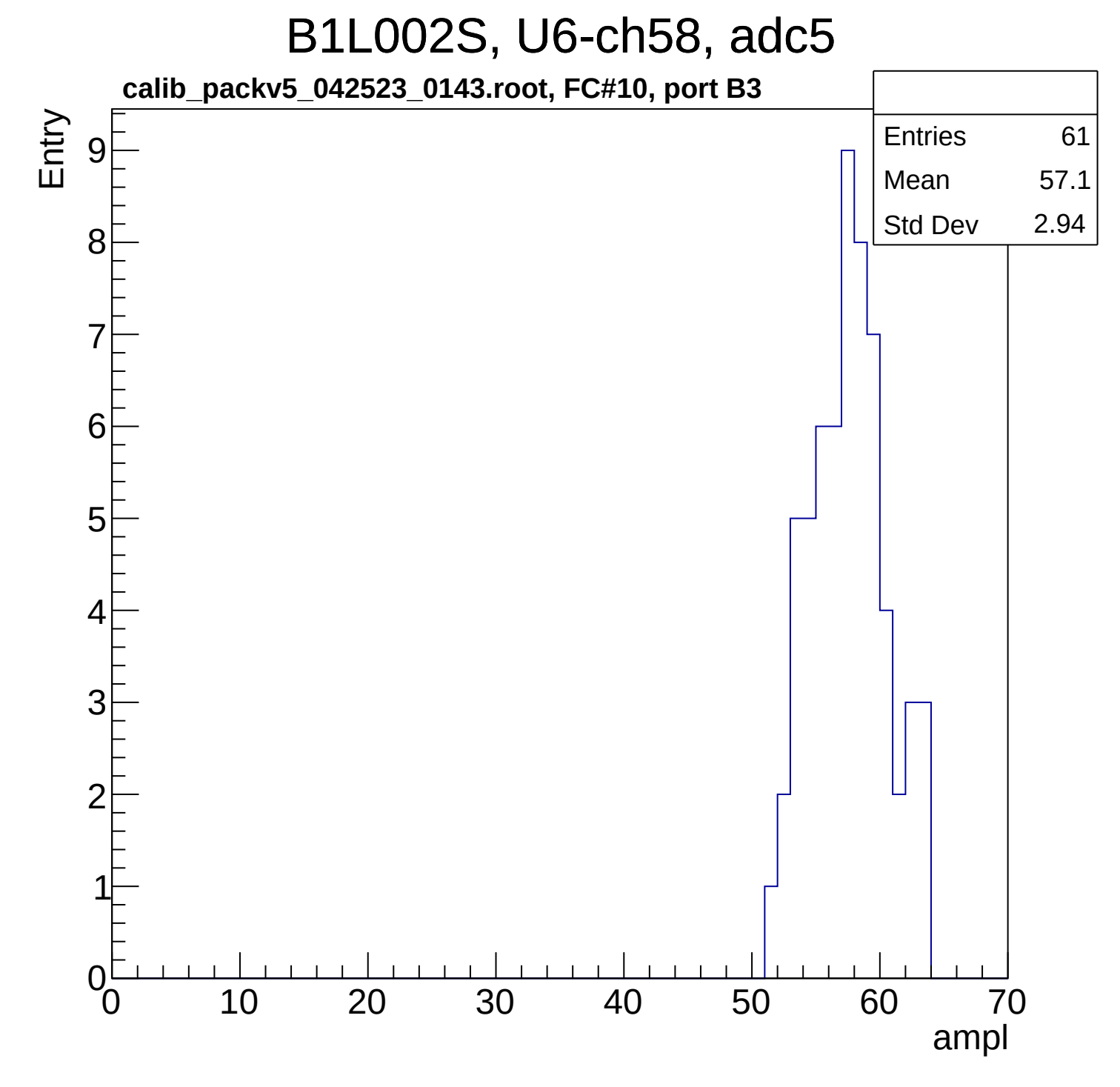

B1L002S, U6-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.86
Std Dev 3.548
Gaus mean : 27.8655
Gaus Width: 3.7152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

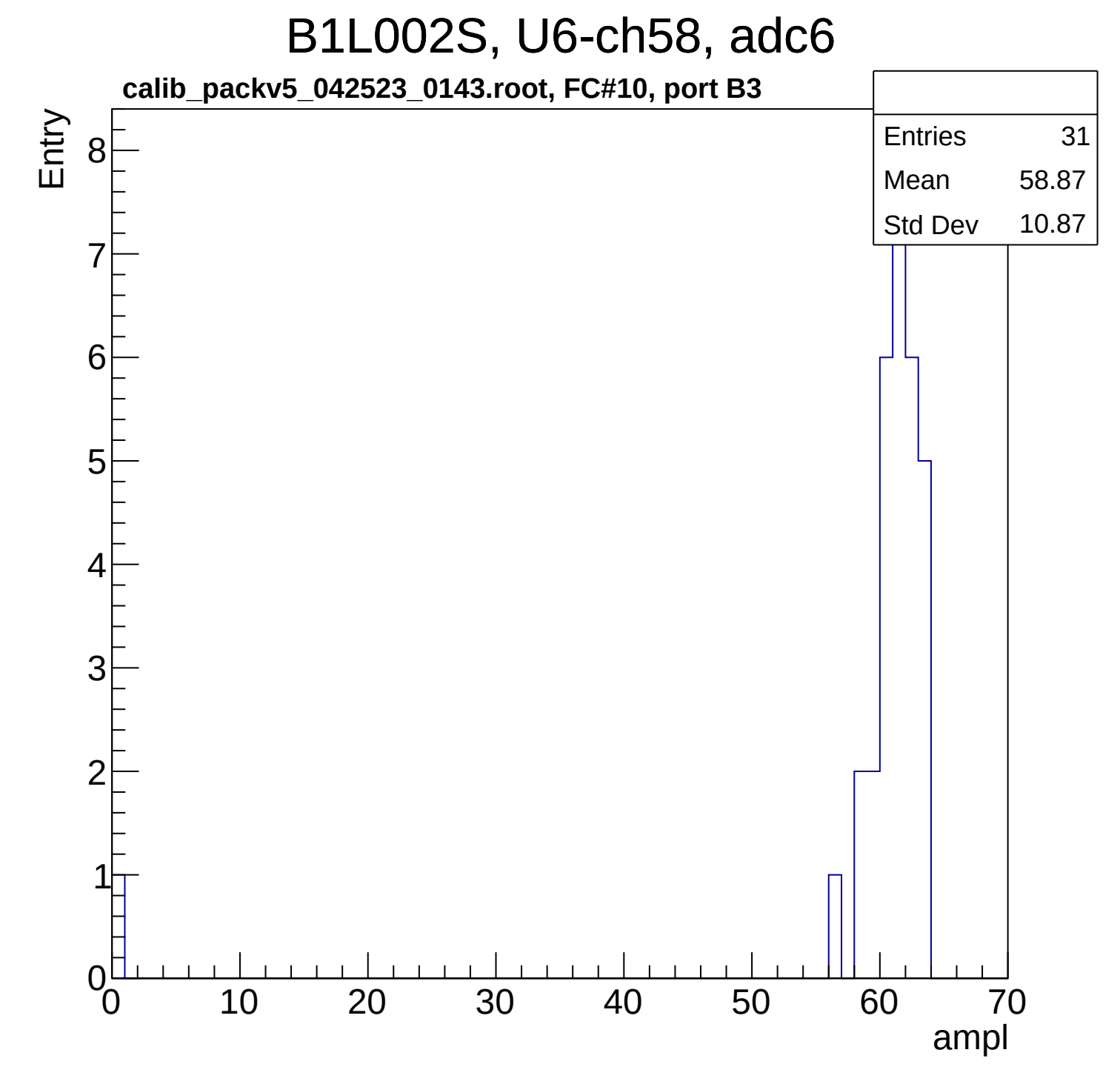

B1L002S, U6-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.98
Std Dev 2.825
Gaus mean : 34.6795
Gaus Width: 2.9660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

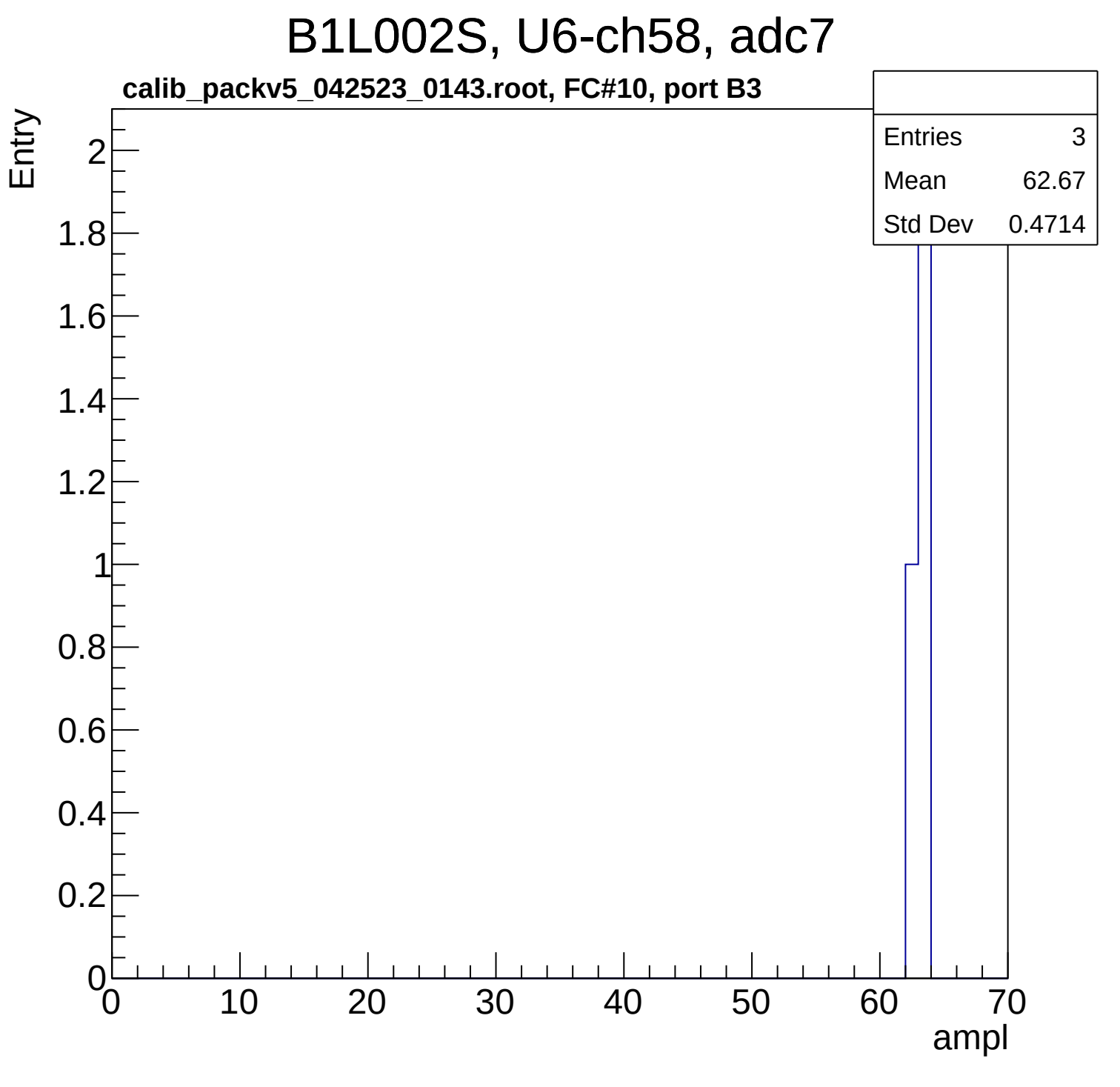

B1L002S, U6-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.19
Std Dev 2.754
Gaus mean : 40.8596
Gaus Width: 3.1657
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

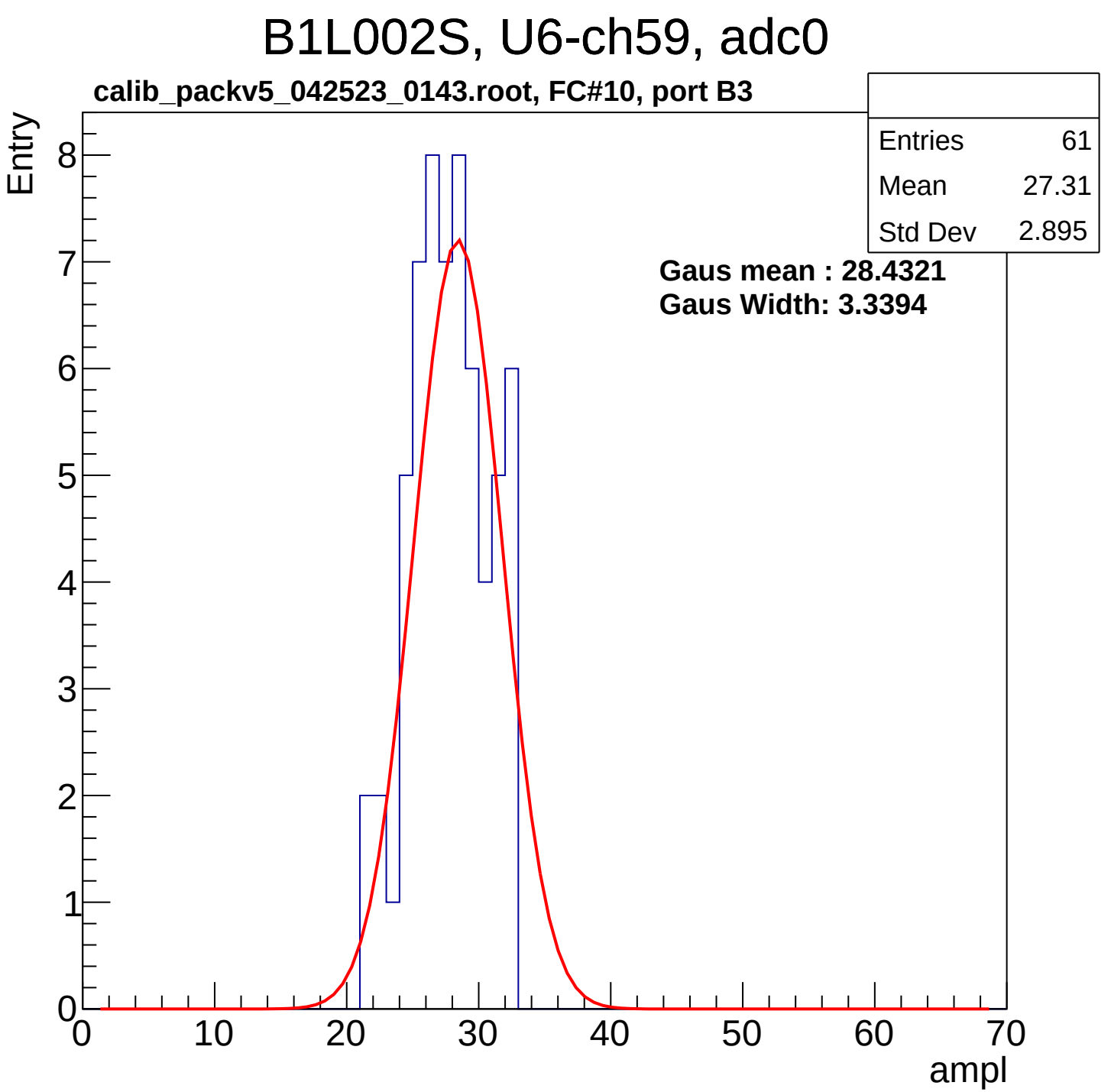

B1L002S, U6-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 46.02
Std Dev 2.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

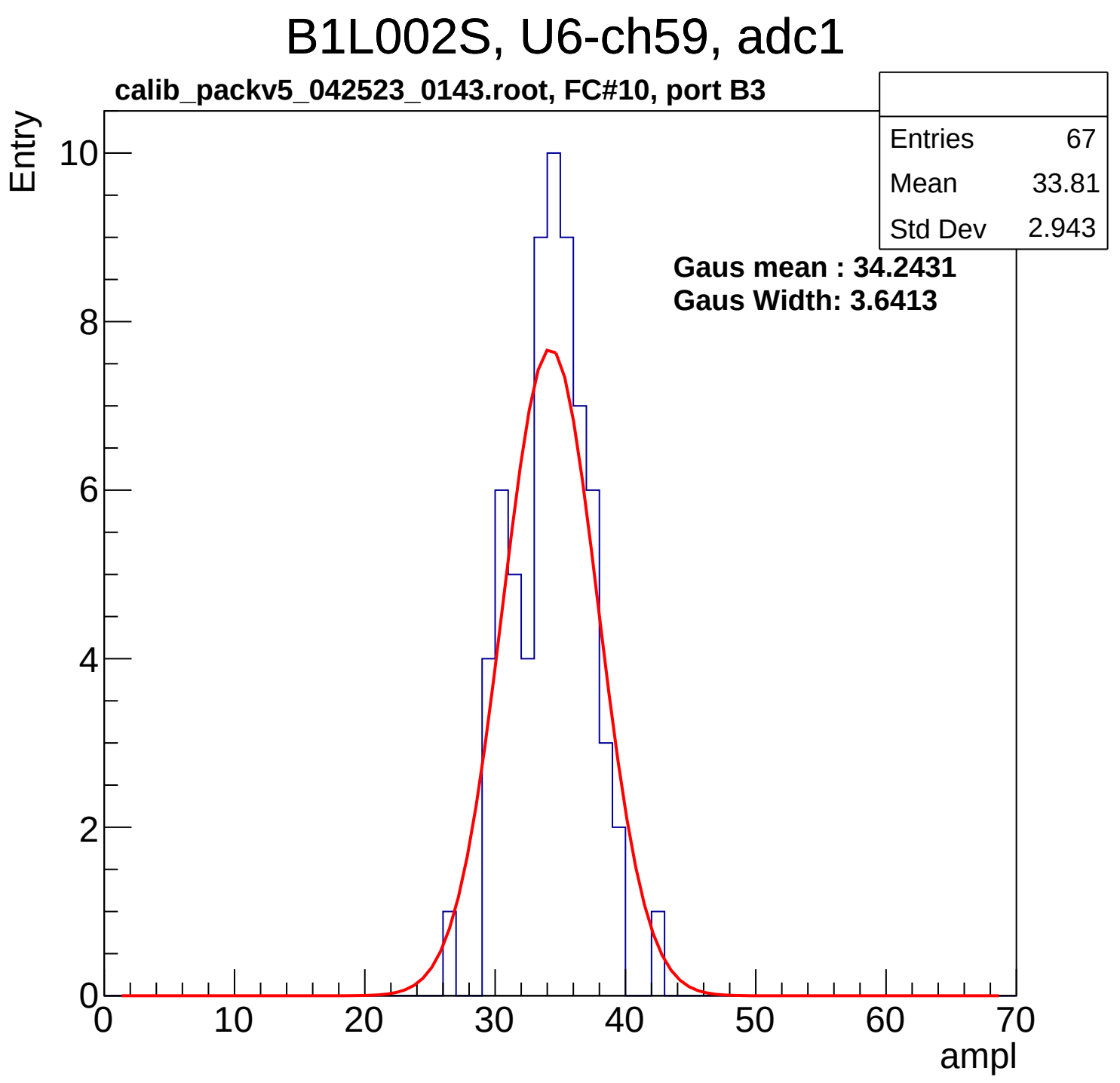

B1L002S, U6-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 51.17
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

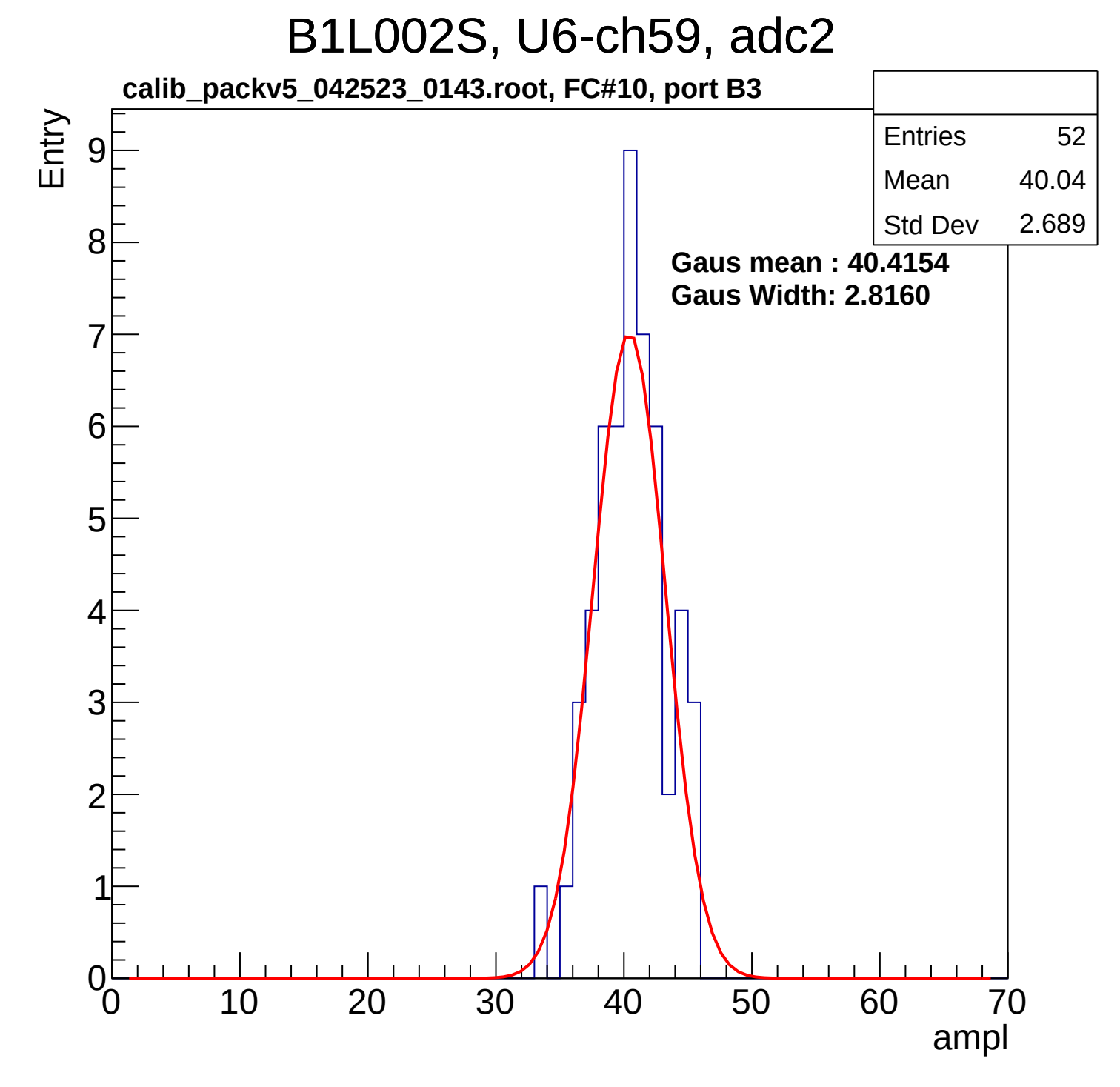

B1L002S, U6-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.1
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

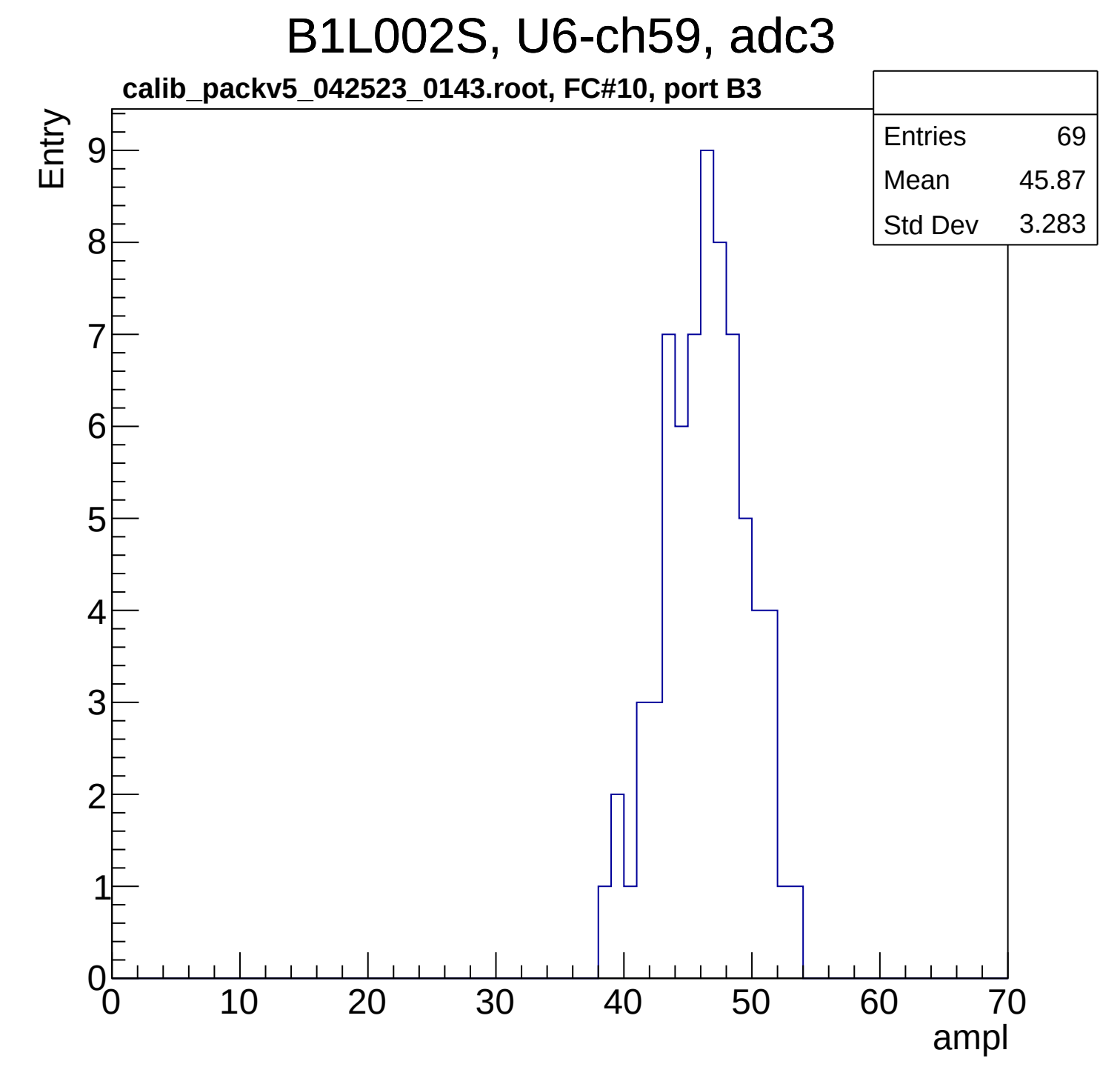

B1L002S, U6-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.87
Std Dev 10.87
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

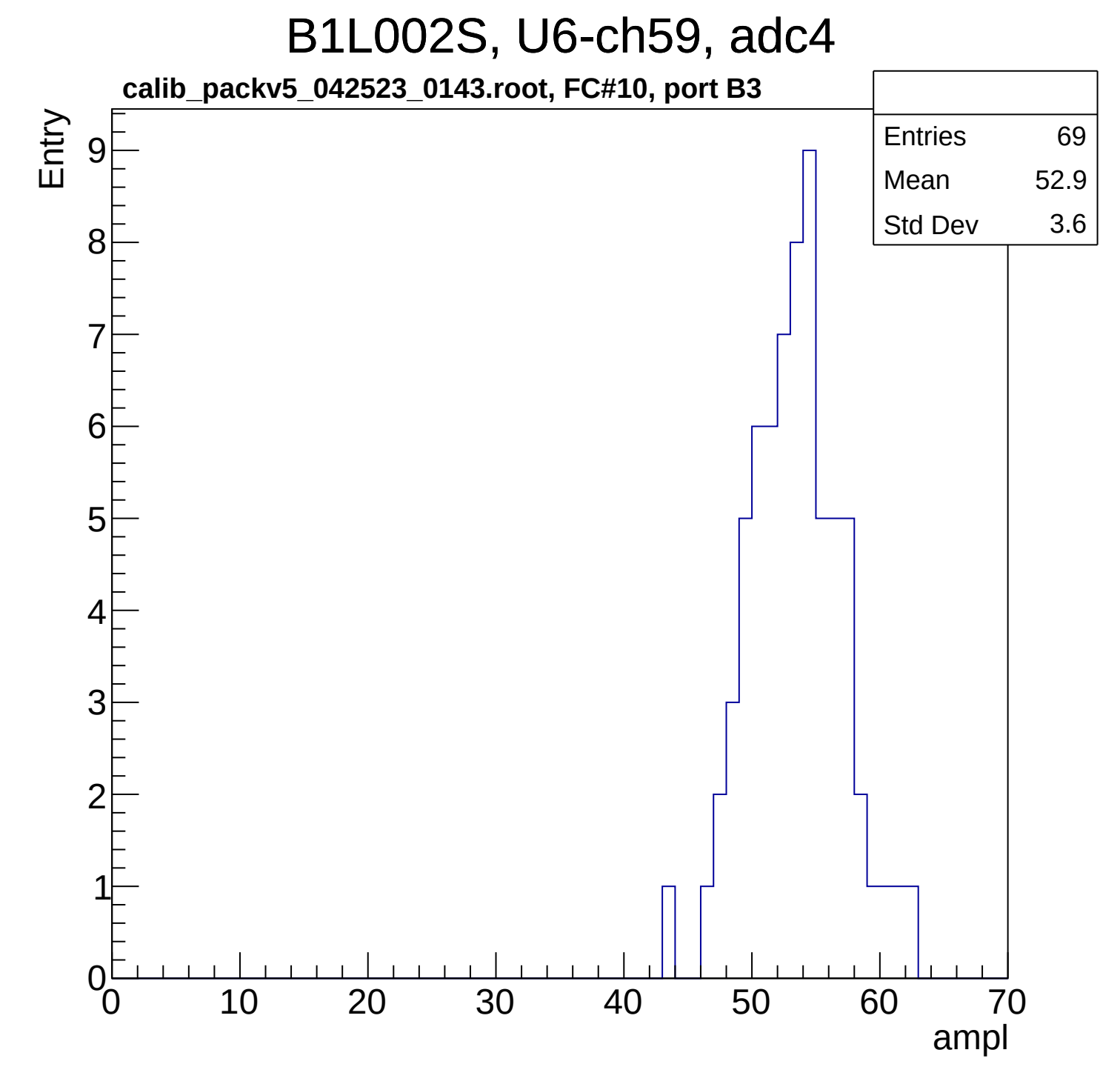

B1L002S, U6-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

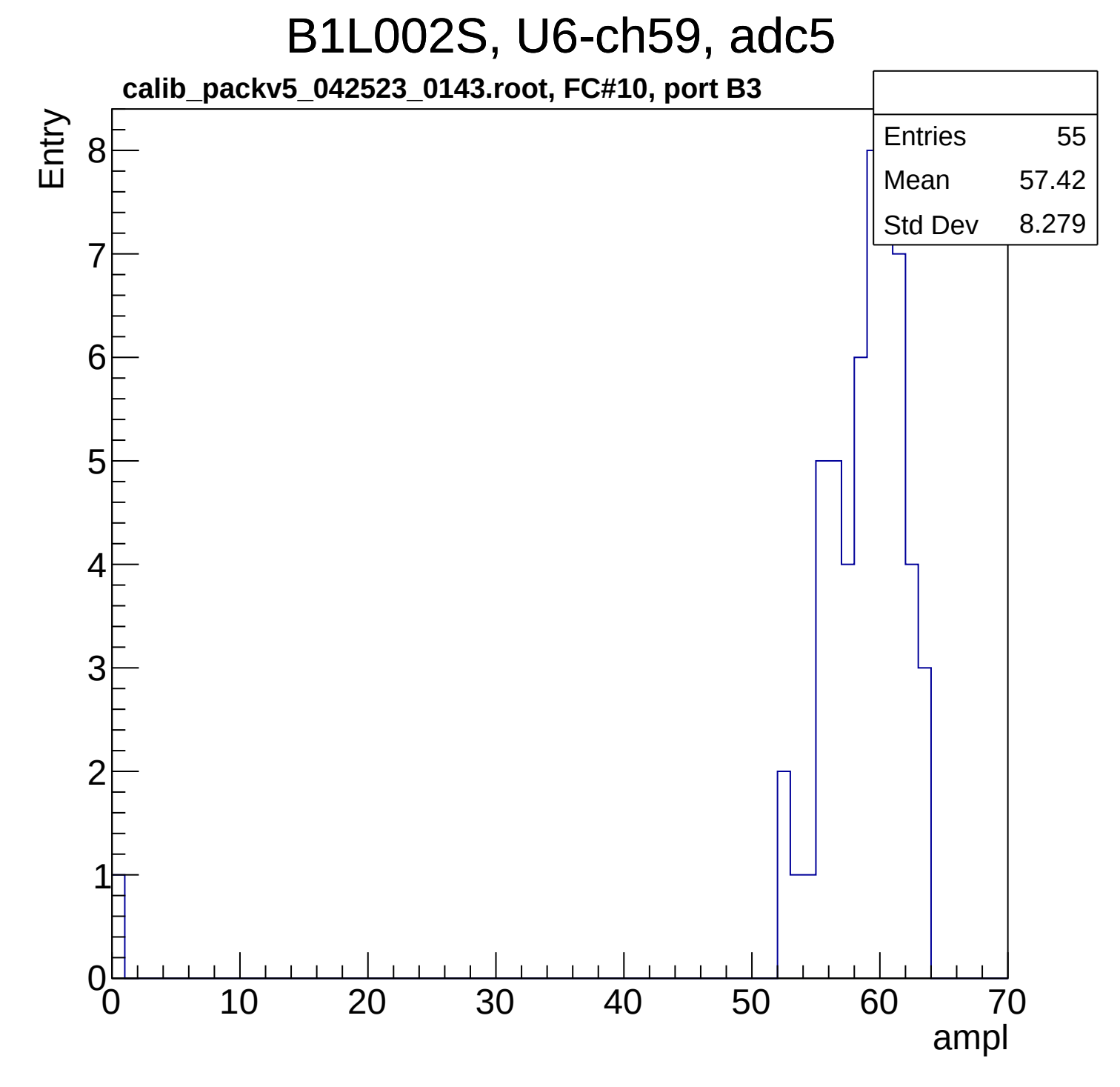

B1L002S, U6-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.31
Std Dev 2.895
Gaus mean : 28.4321
Gaus Width: 3.3394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

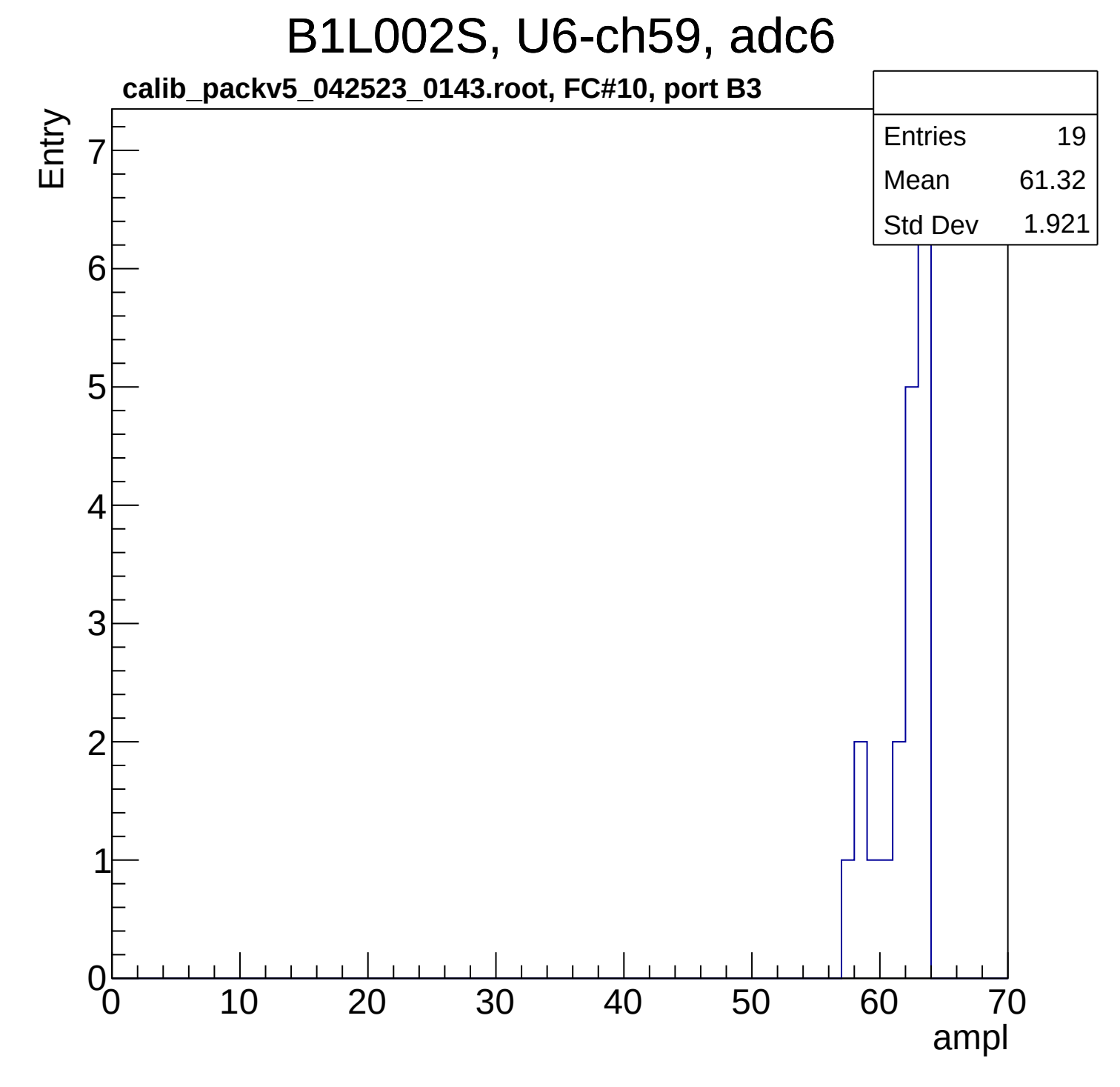

B1L002S, U6-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.81
Std Dev 2.943
Gaus mean : 34.2431
Gaus Width: 3.6413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

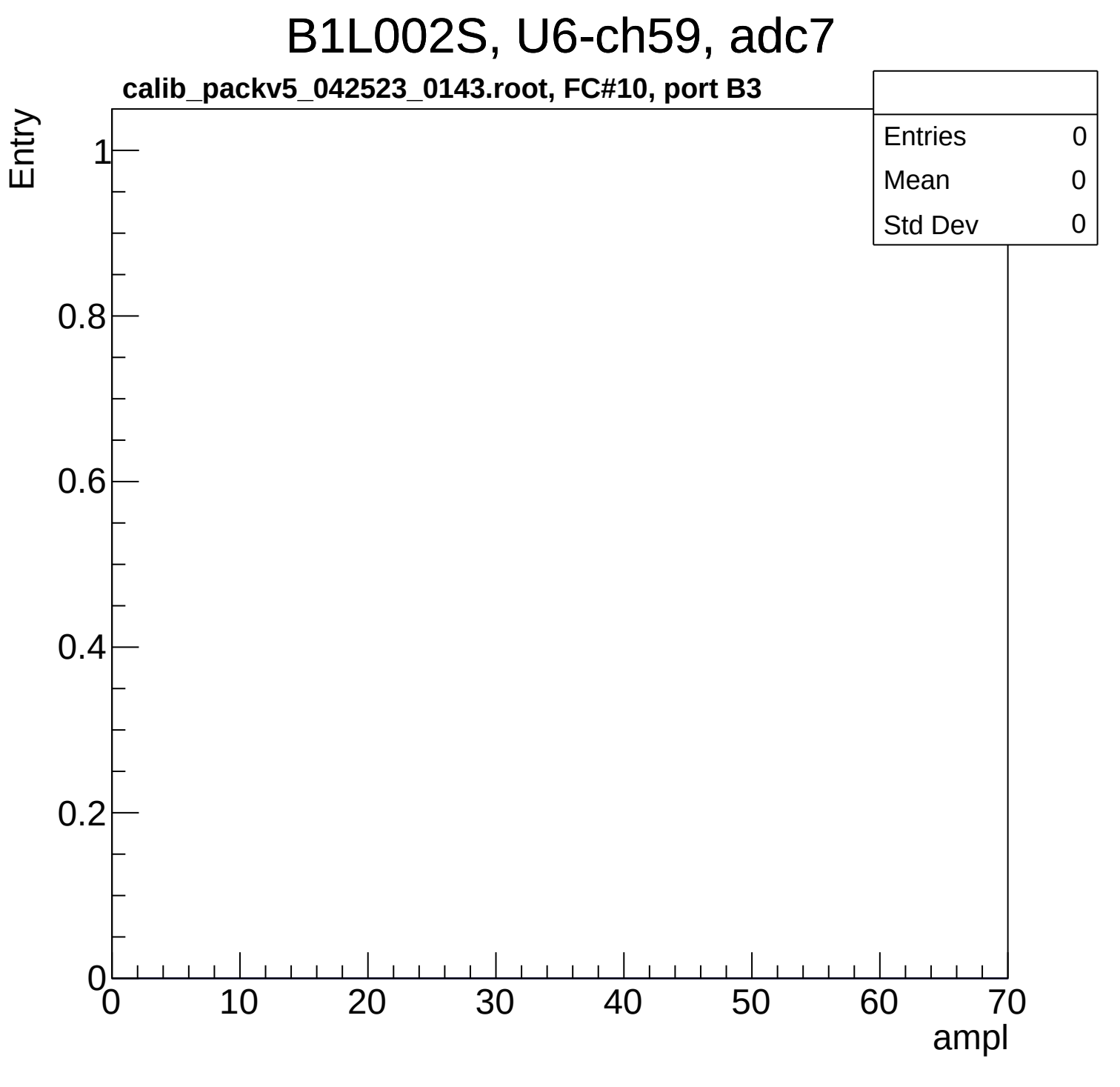

B1L002S, U6-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.04
Std Dev 2.689
Gaus mean : 40.4154
Gaus Width: 2.8160
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

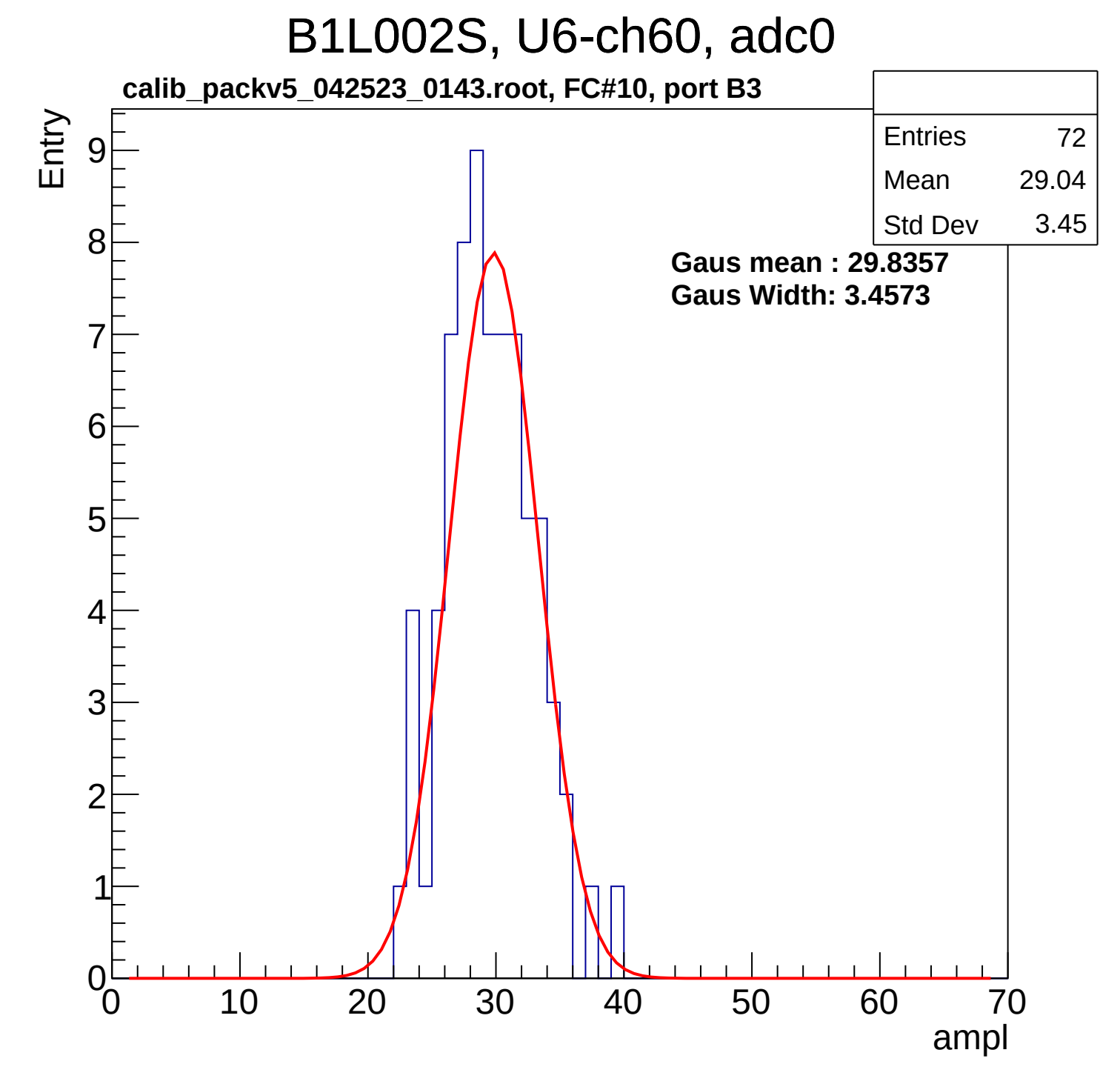

B1L002S, U6-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.87
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

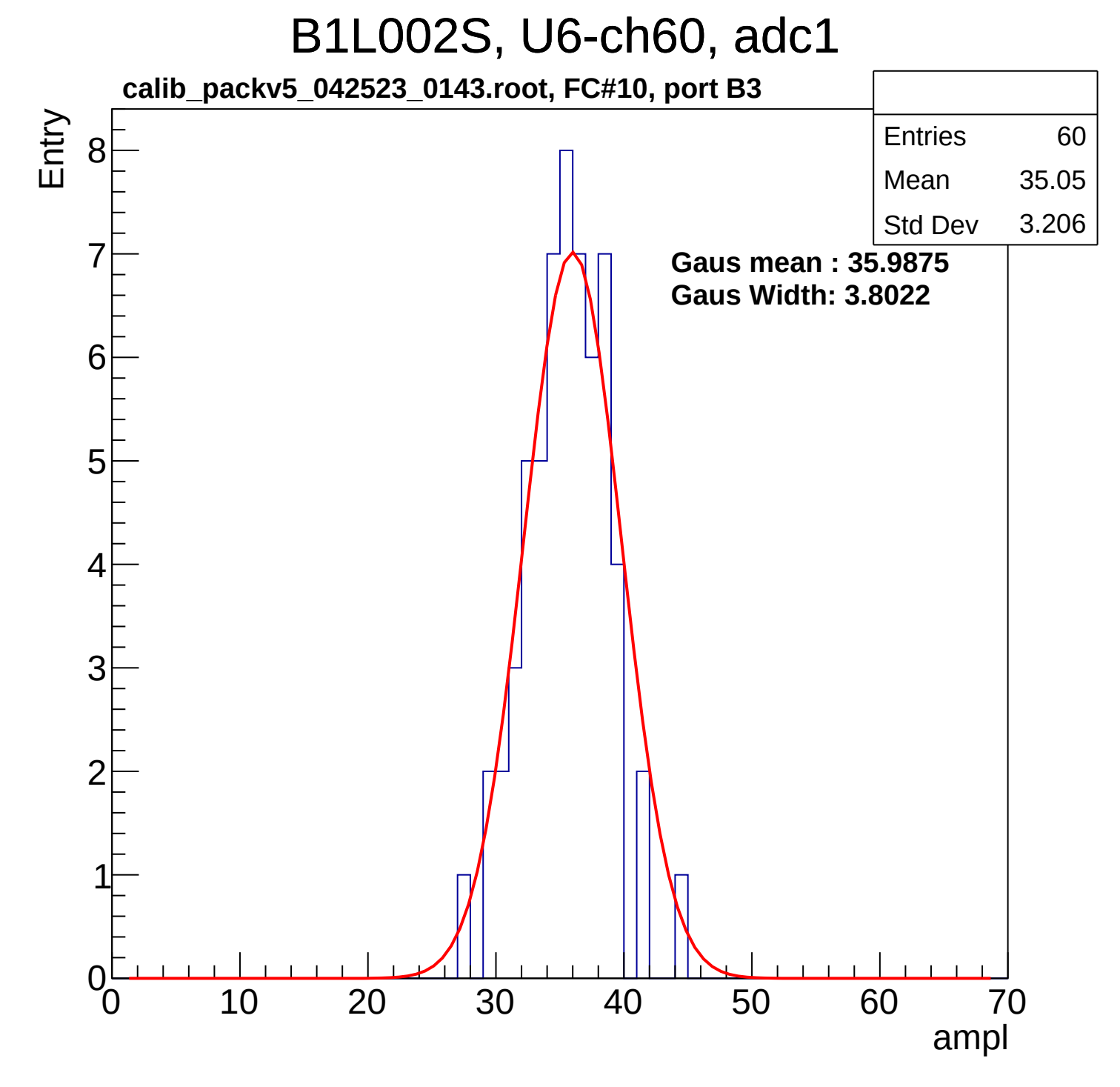

B1L002S, U6-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 52.9
Std Dev 3.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

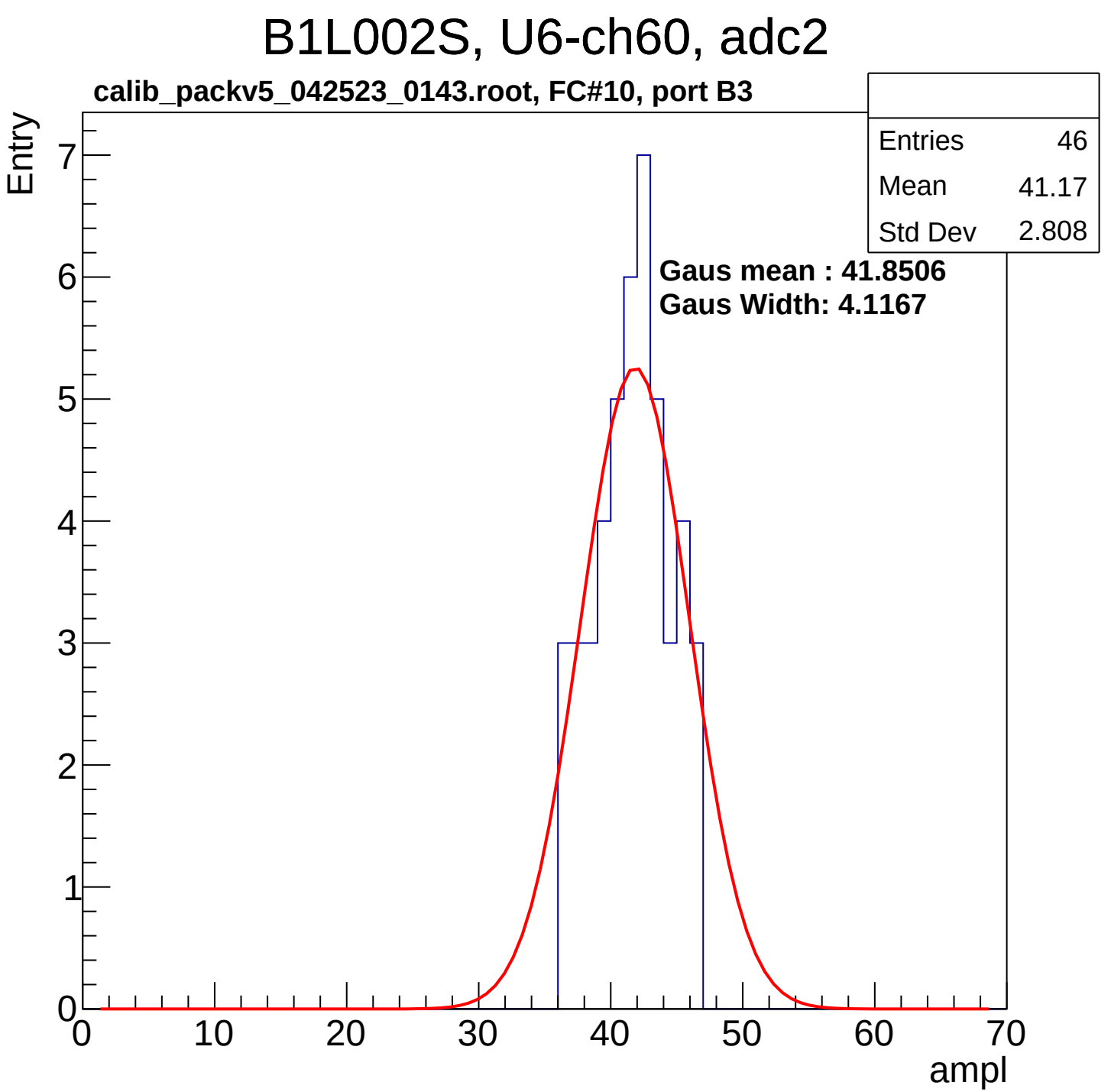

B1L002S, U6-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.42
Std Dev 8.279
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

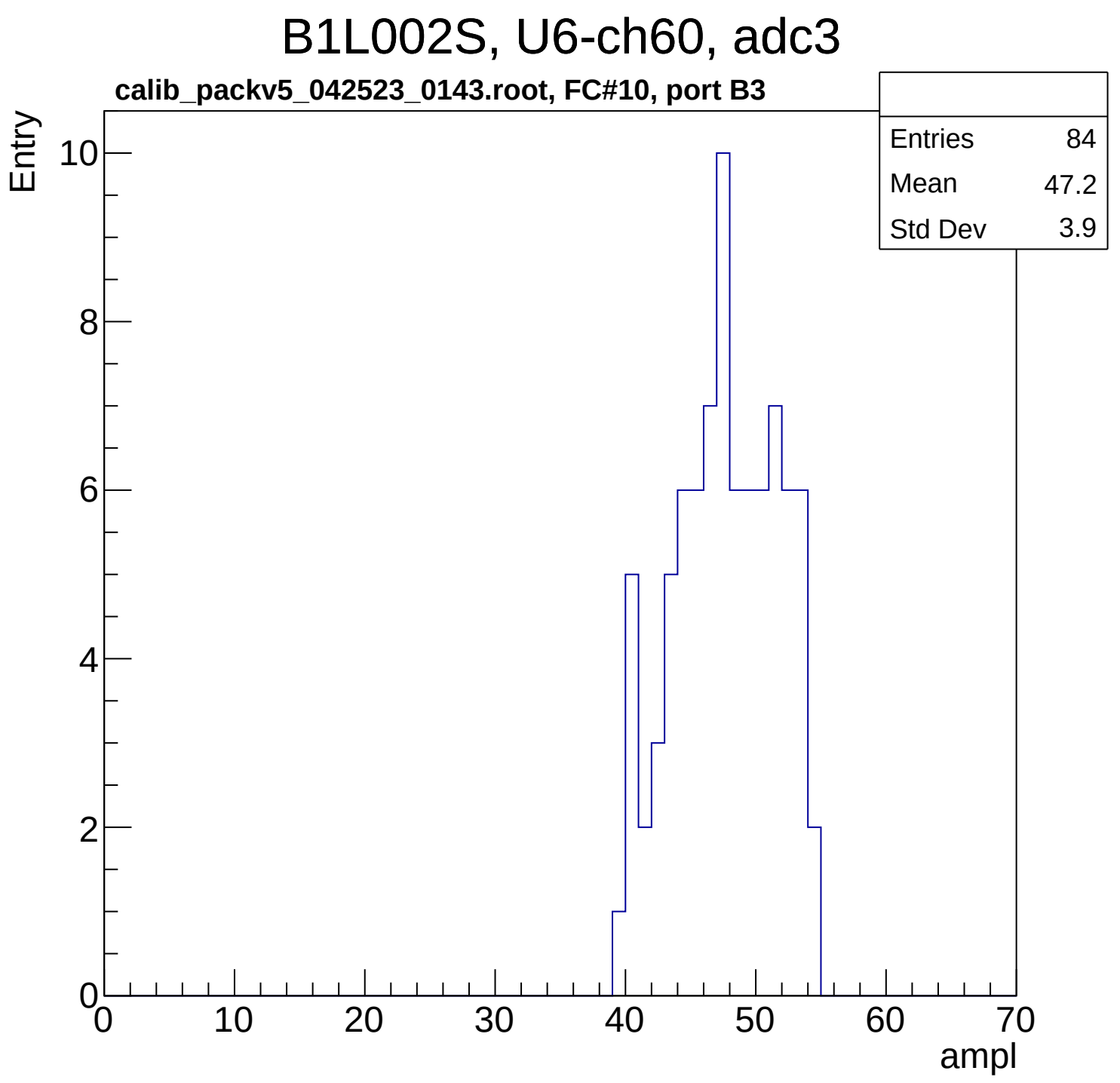

B1L002S, U6-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.32
Std Dev 1.921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

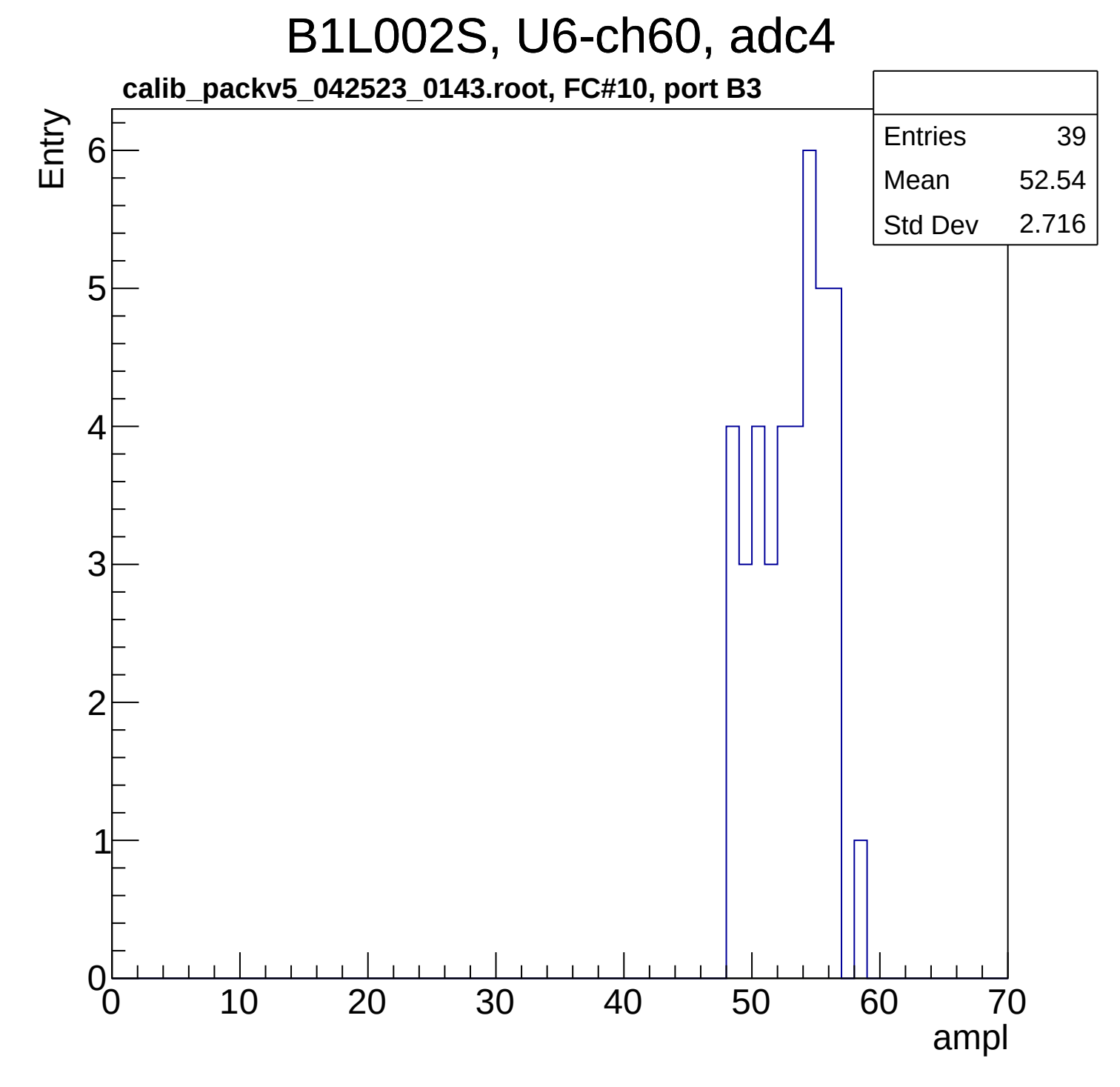

B1L002S, U6-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

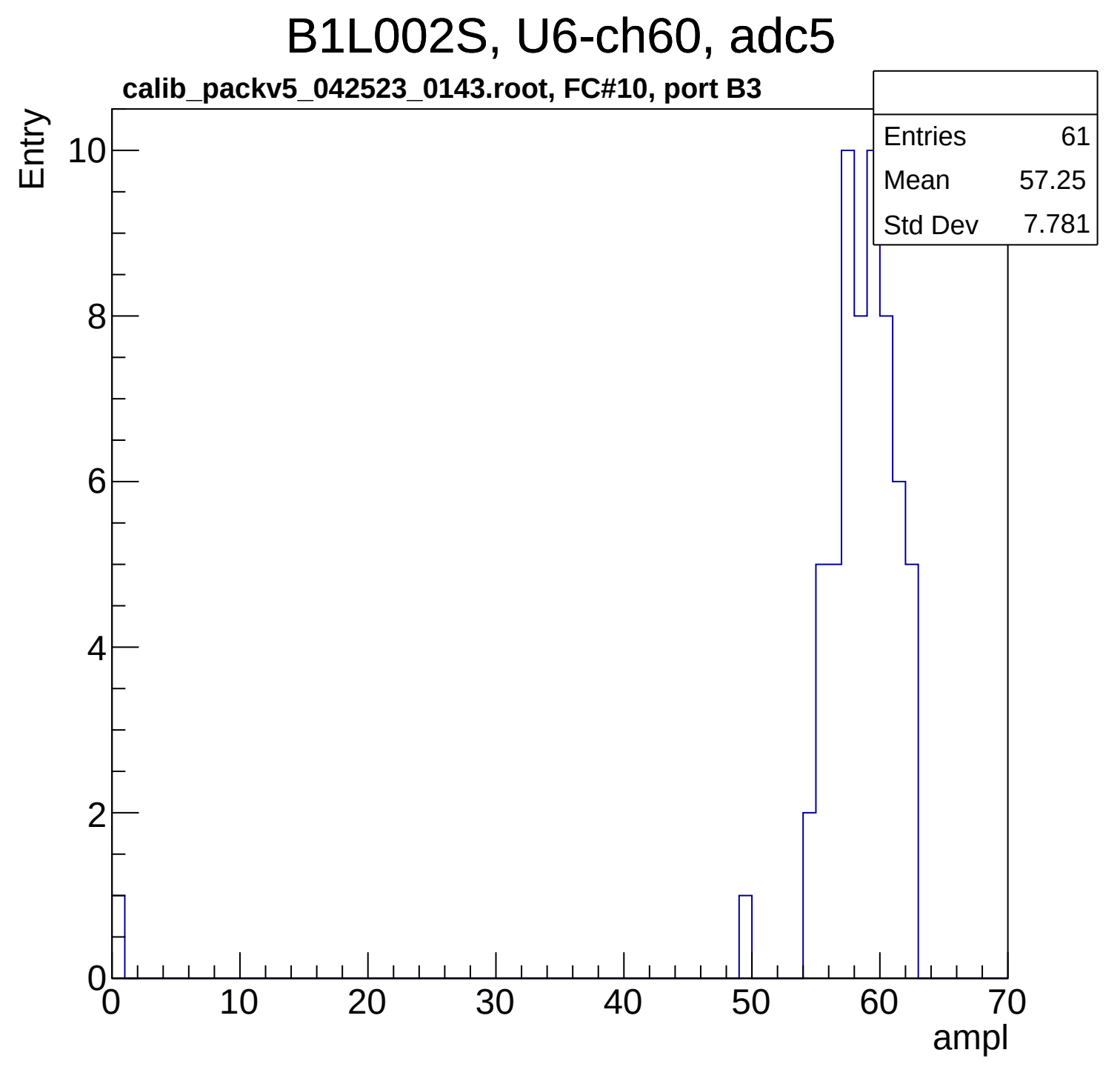

B1L002S, U6-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 29.04
Std Dev 3.45
Gaus mean : 29.8357
Gaus Width: 3.4573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

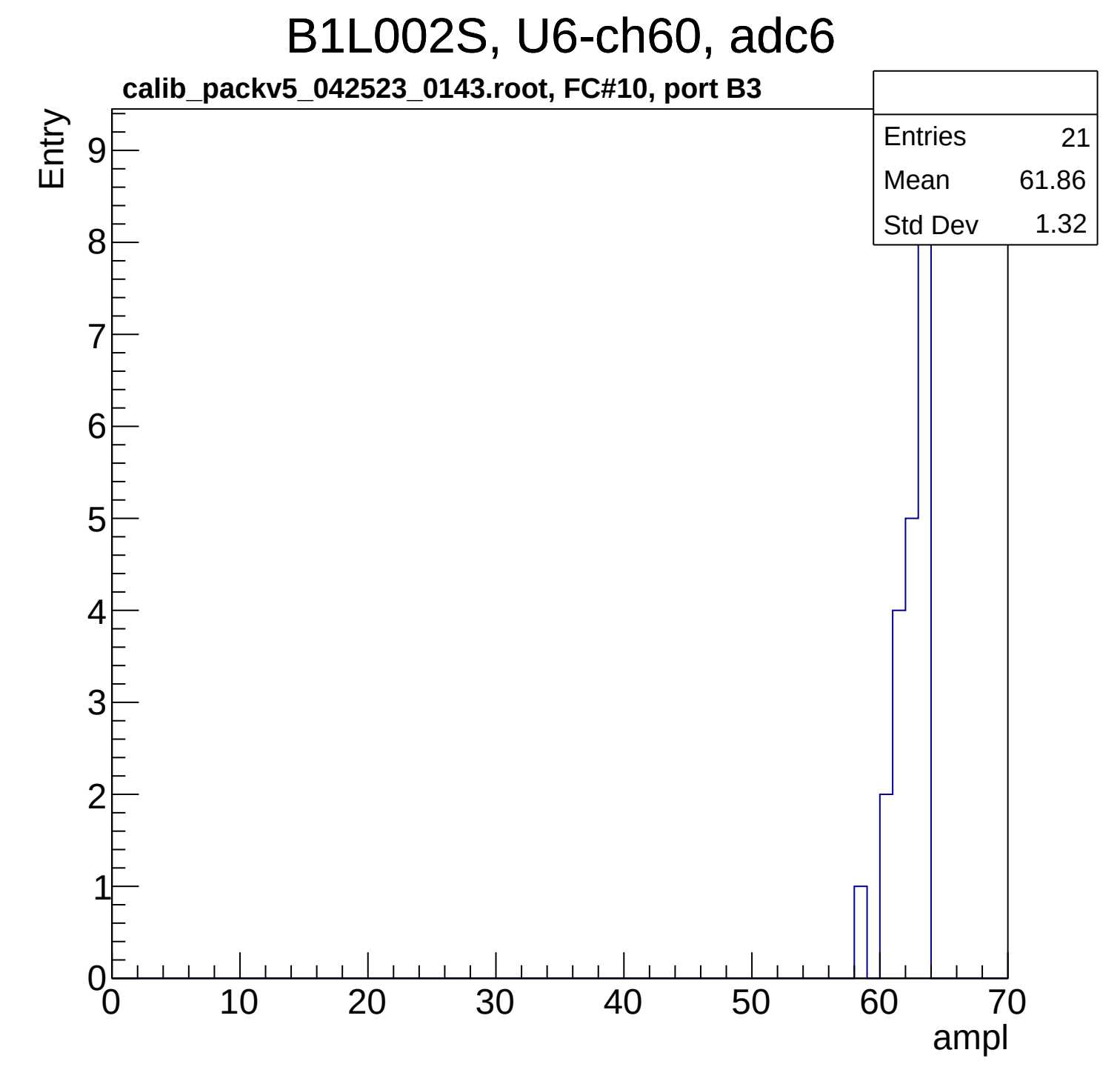

B1L002S, U6-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.05
Std Dev 3.206
Gaus mean : 35.9875
Gaus Width: 3.8022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

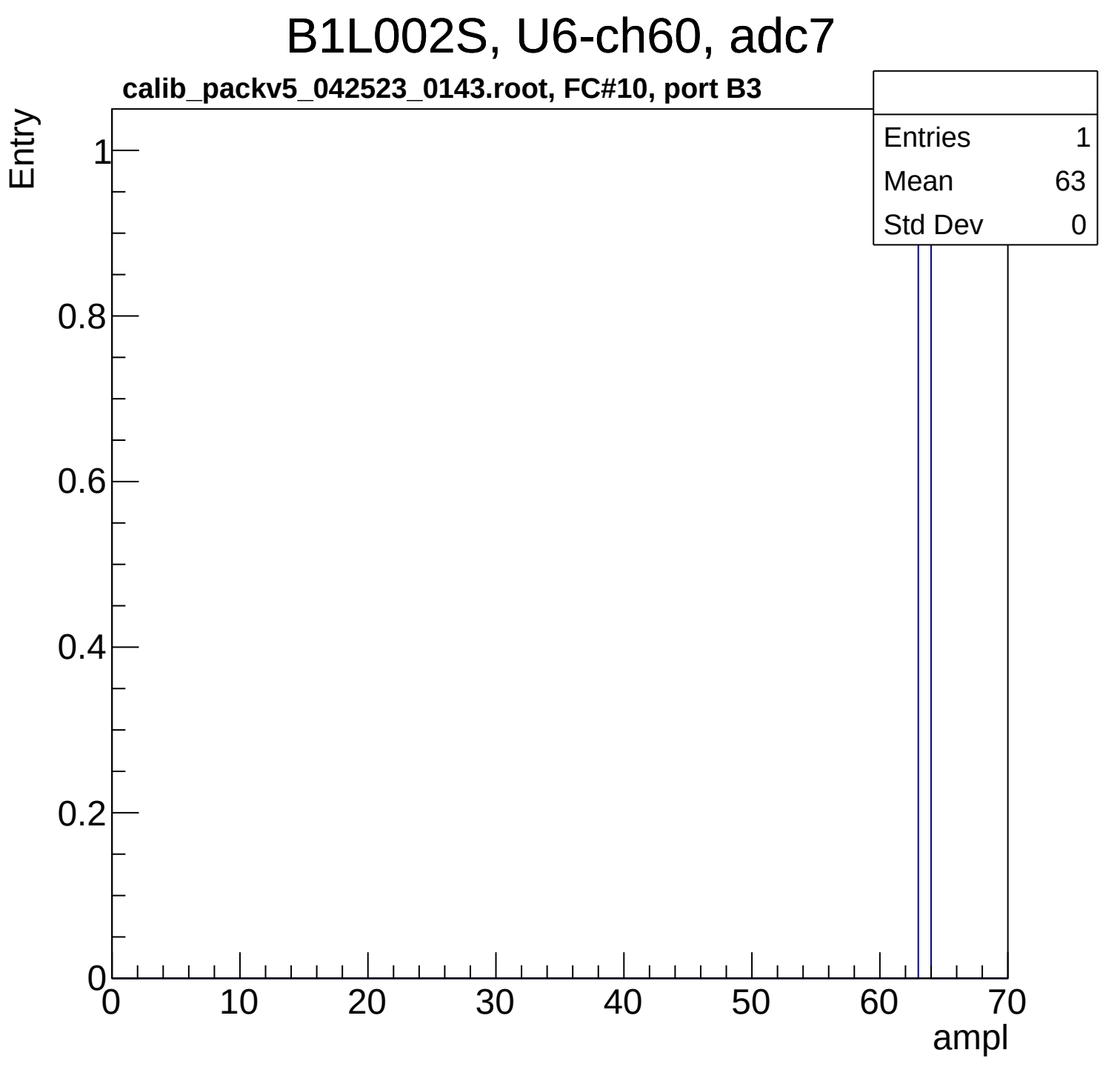

B1L002S, U6-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 41.17
Std Dev 2.808
Gaus mean : 41.8506
Gaus Width: 4.1167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

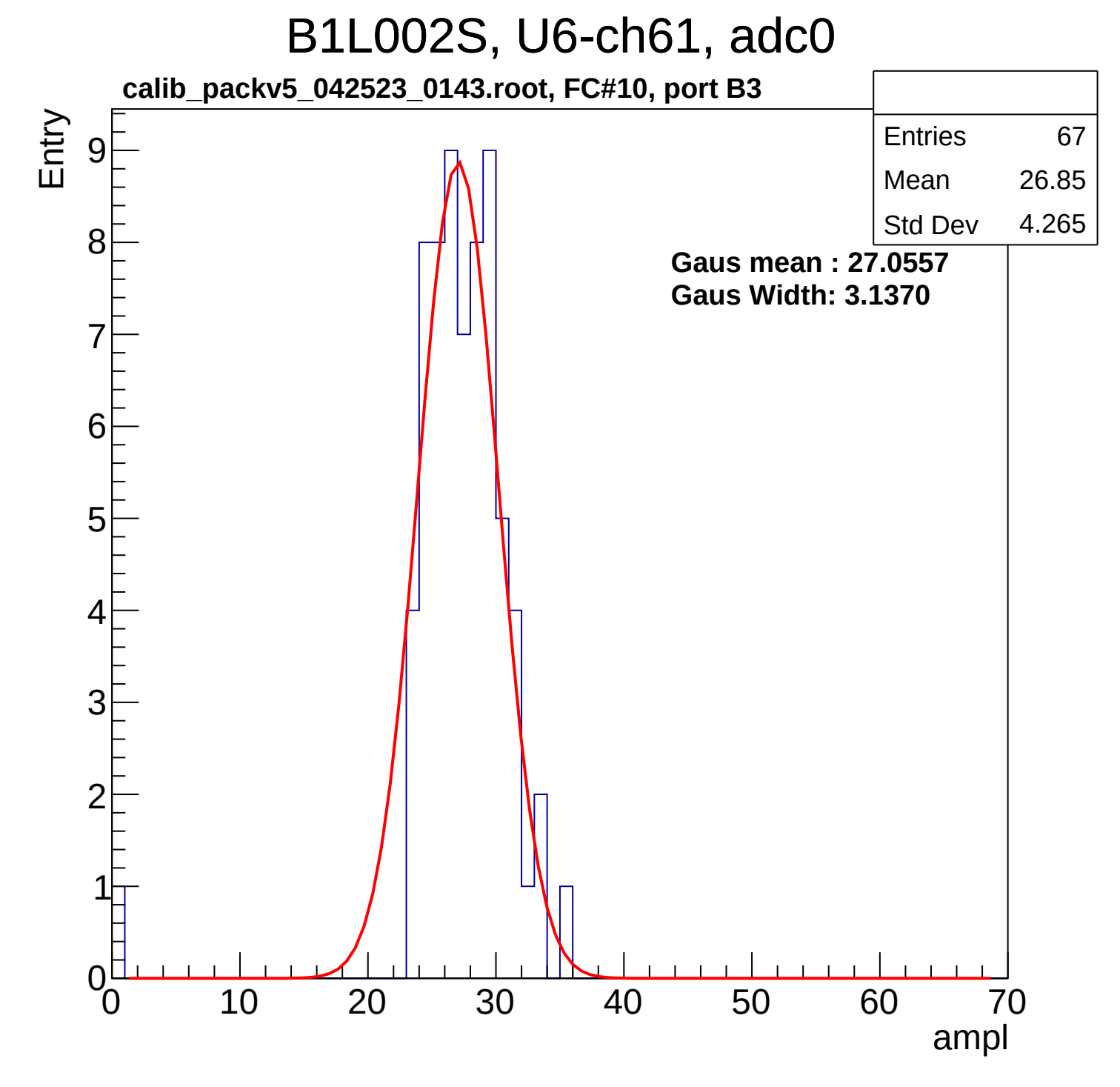

B1L002S, U6-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 47.2
Std Dev 3.9
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

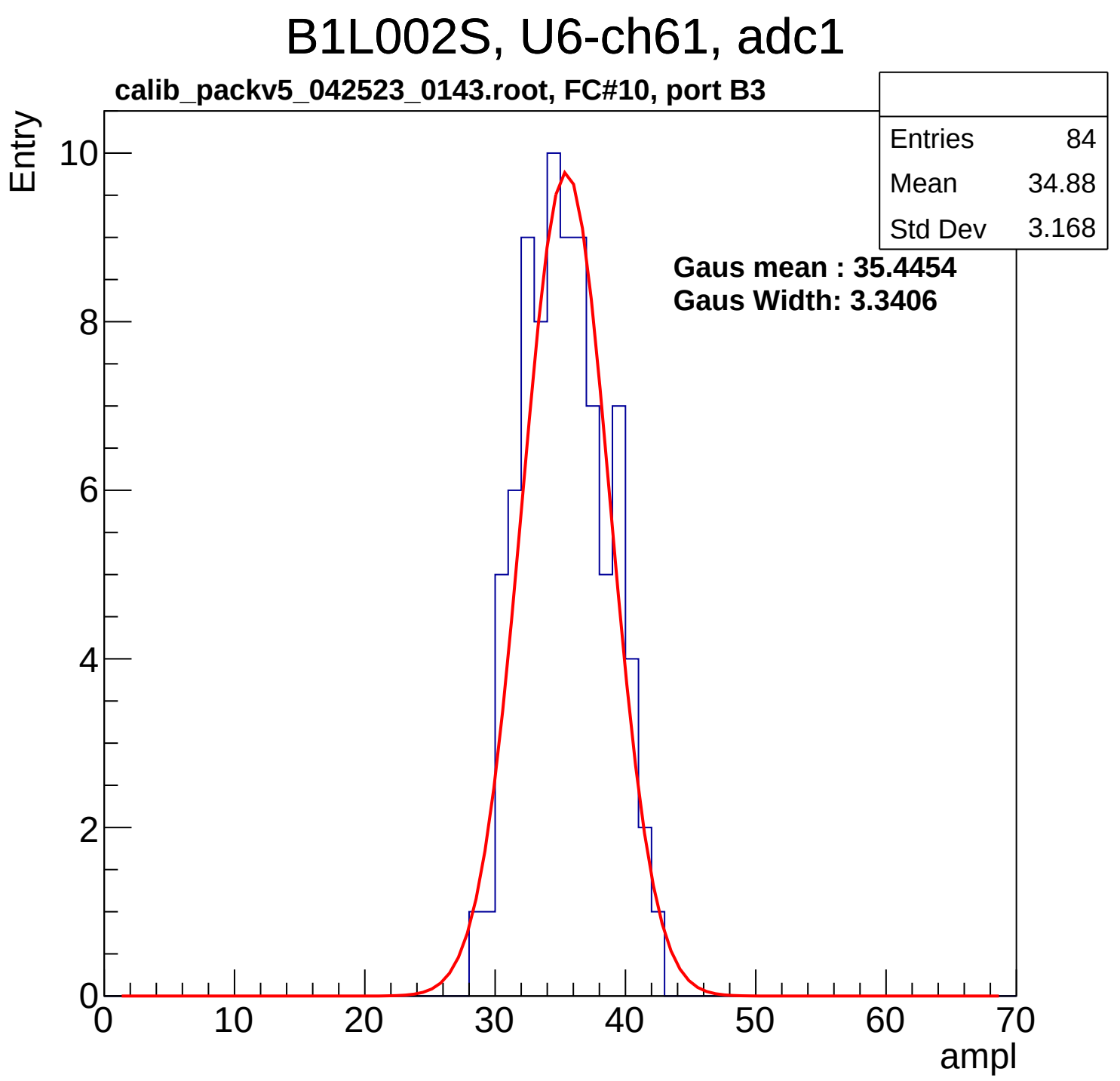

B1L002S, U6-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 52.54
Std Dev 2.716
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

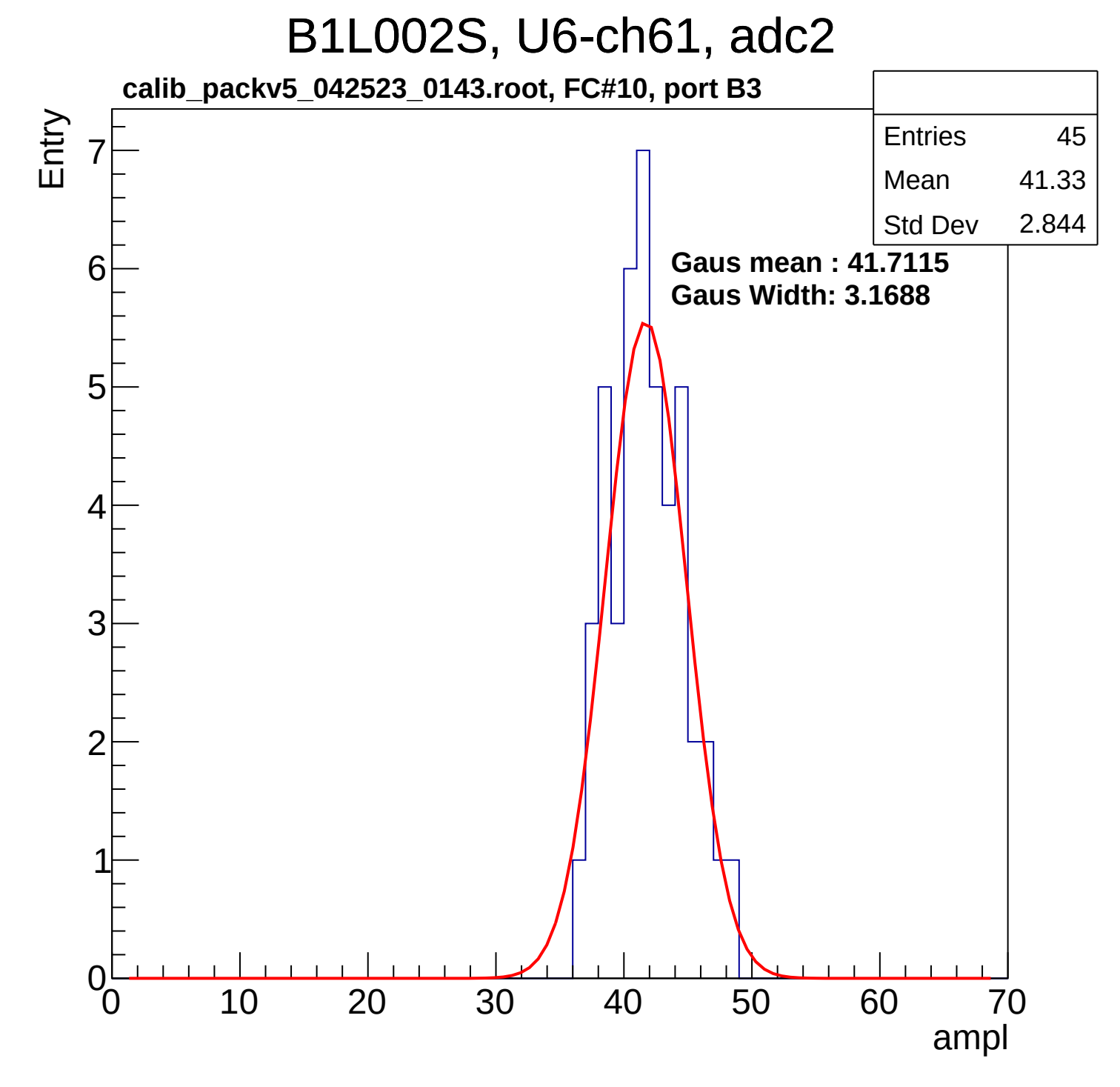

B1L002S, U6-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.25
Std Dev 7.781
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

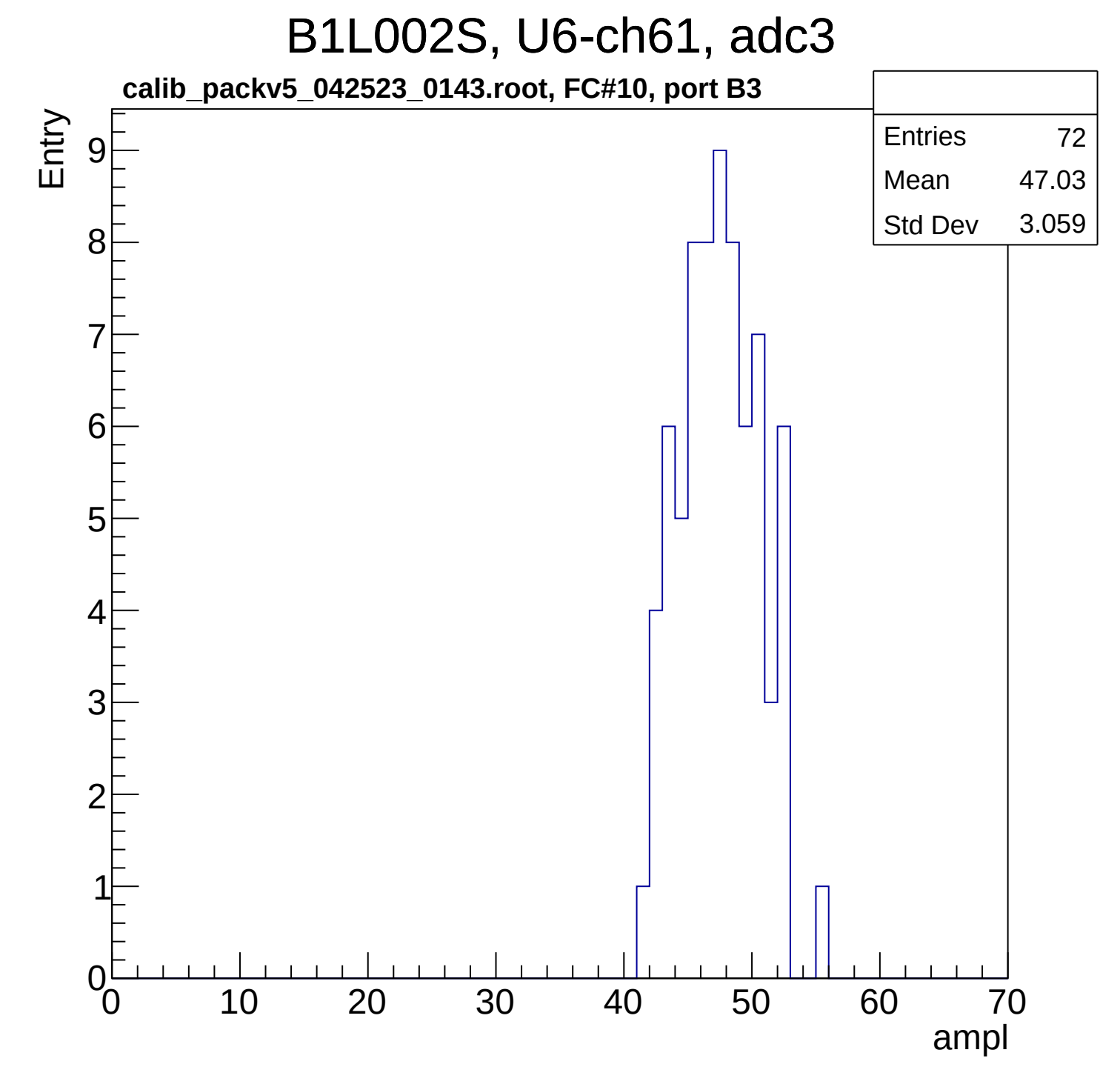

B1L002S, U6-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.86
Std Dev 1.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

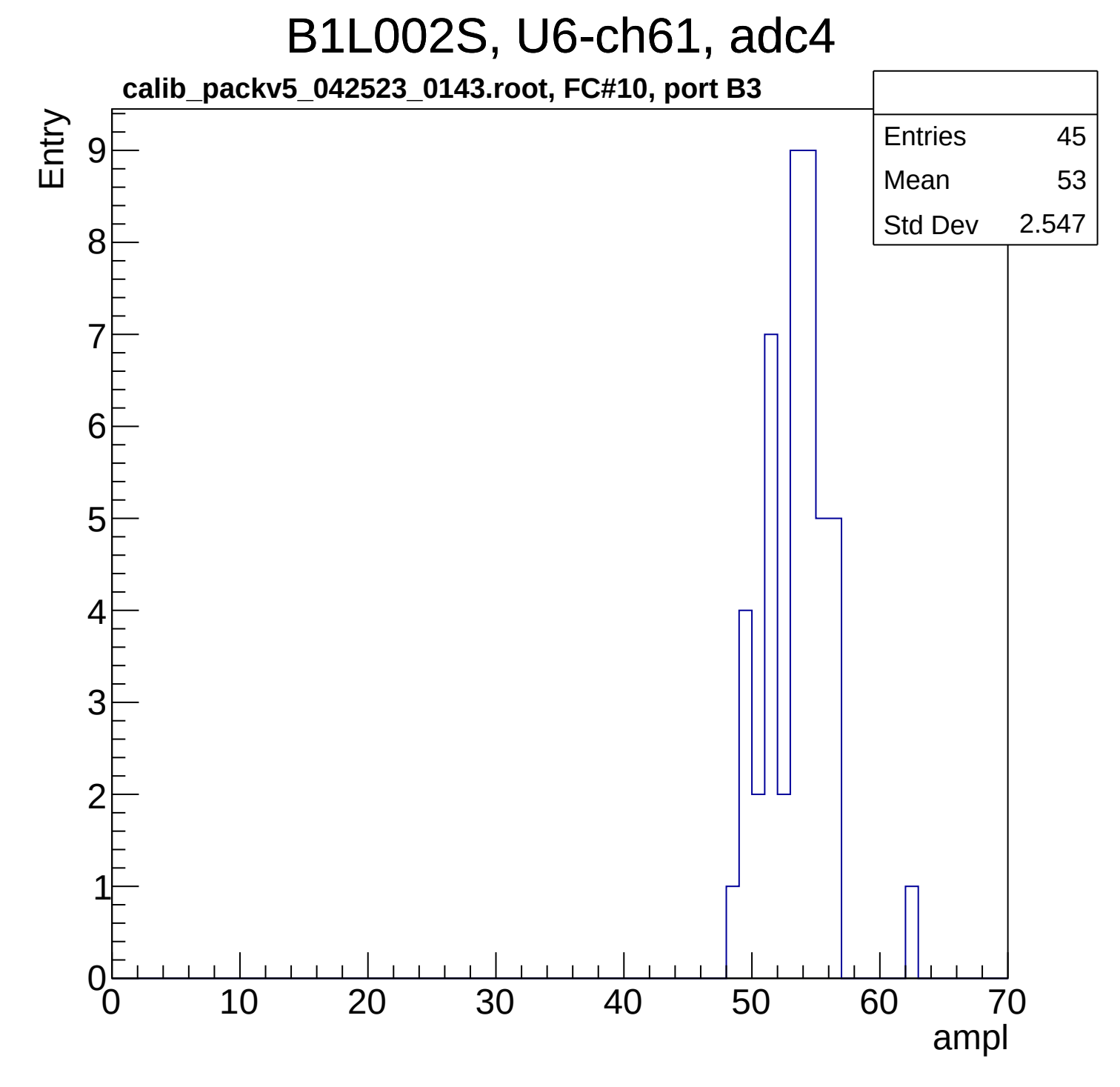

B1L002S, U6-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

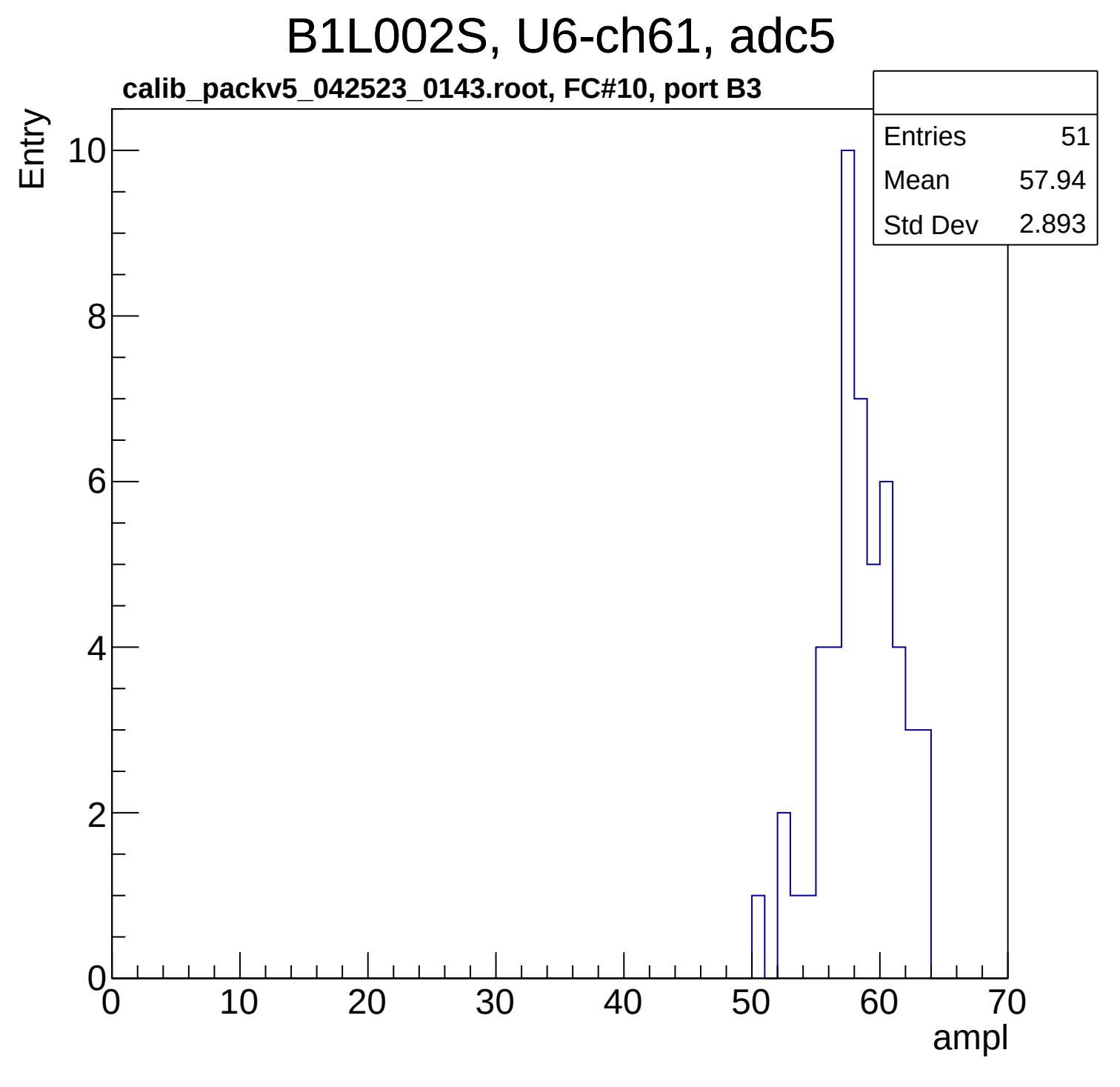

B1L002S, U6-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 26.85
Std Dev 4.265
Gaus mean : 27.0557
Gaus Width: 3.1370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

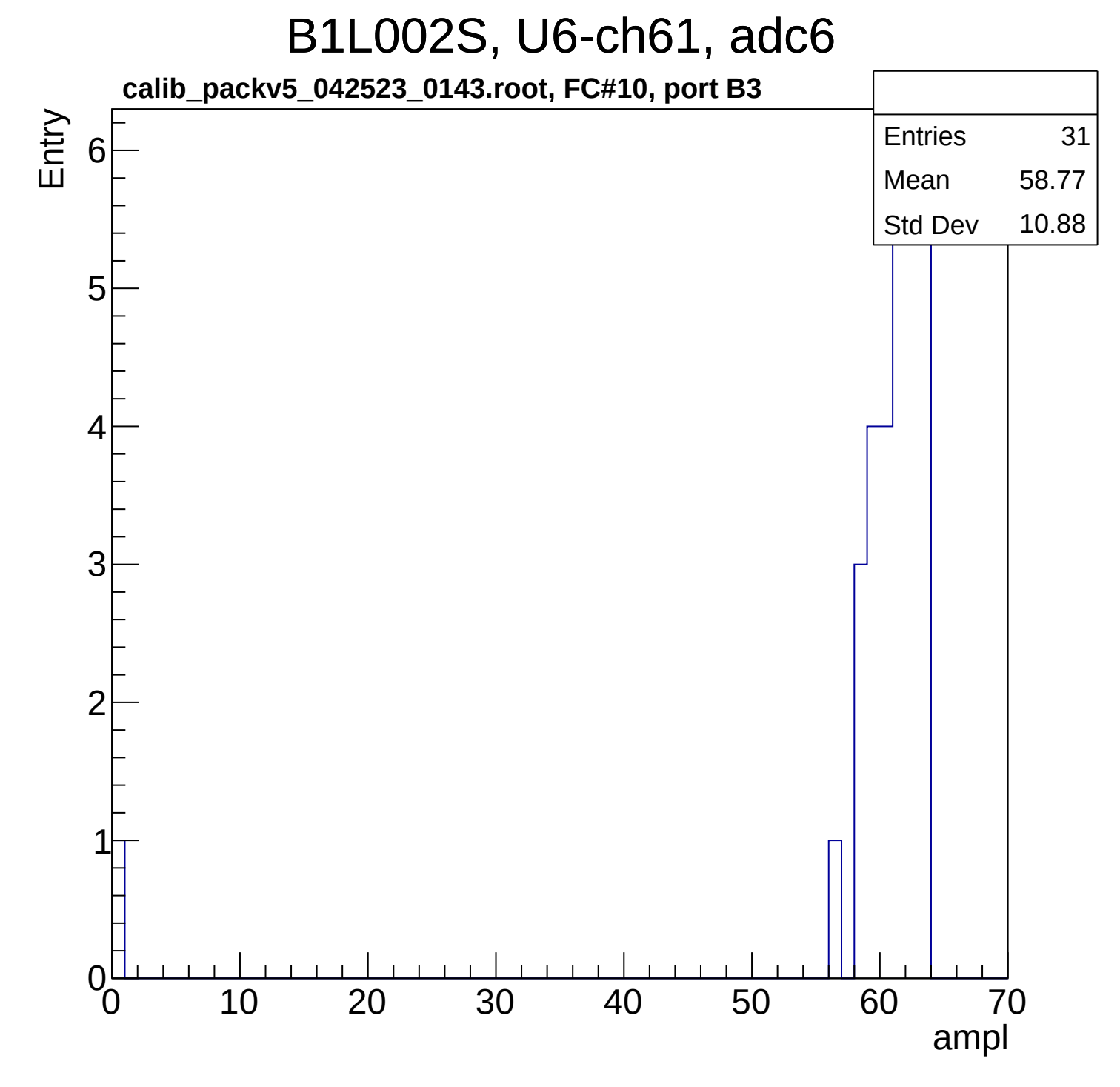

B1L002S, U6-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 34.88
Std Dev 3.168
Gaus mean : 35.4454
Gaus Width: 3.3406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

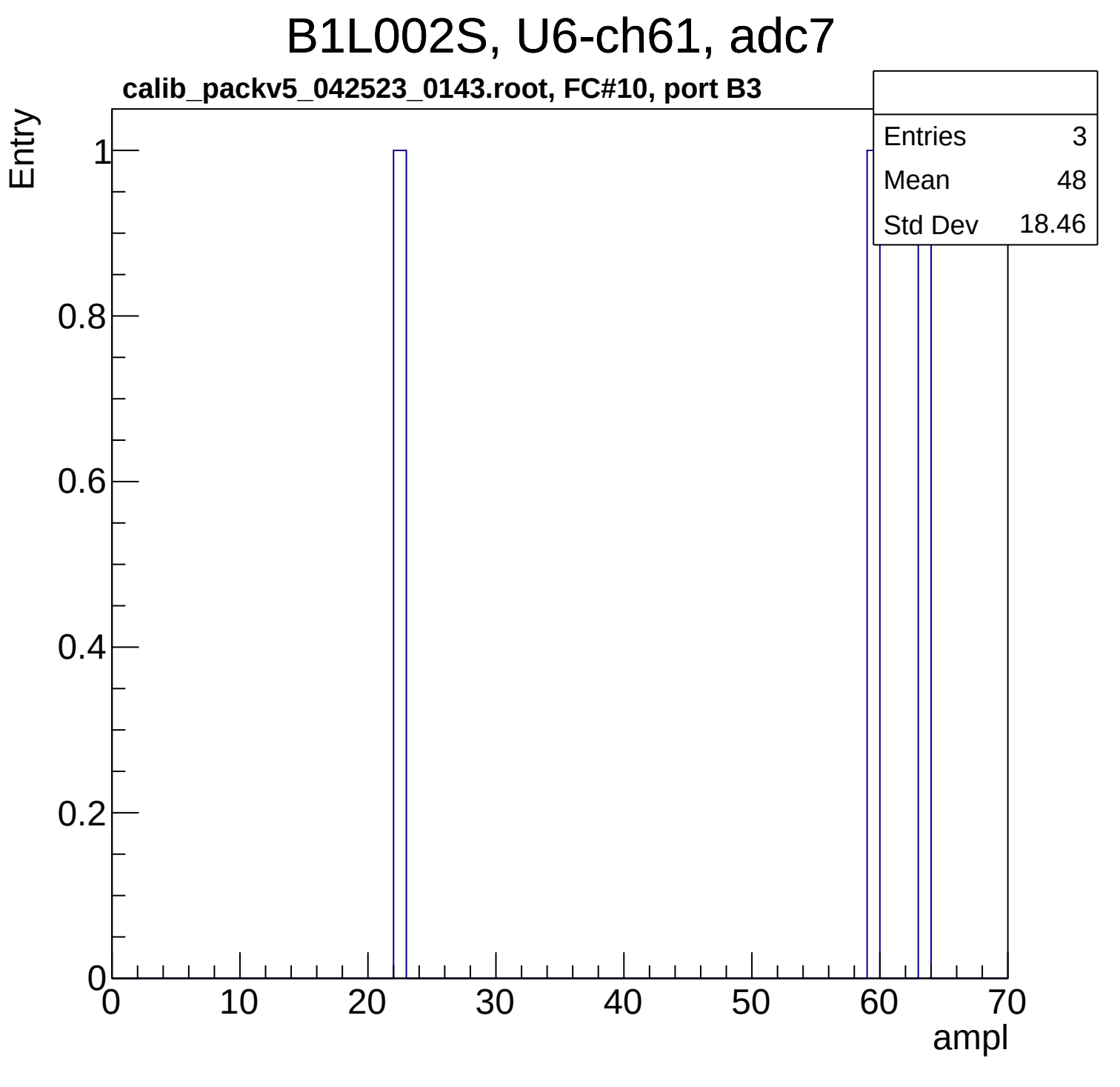

B1L002S, U6-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 41.33
Std Dev 2.844
Gaus mean : 41.7115
Gaus Width: 3.1688
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

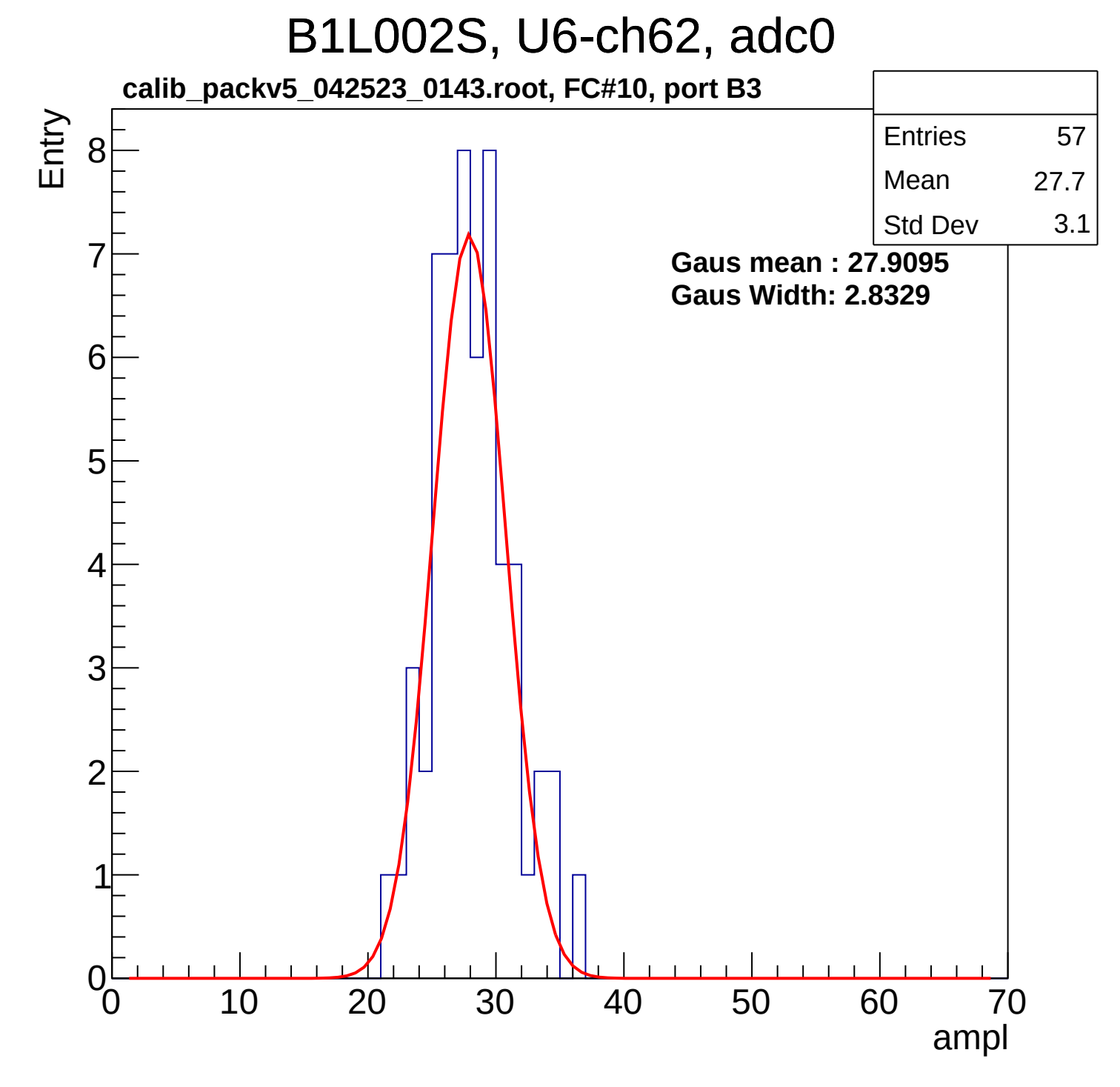

B1L002S, U6-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.03
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

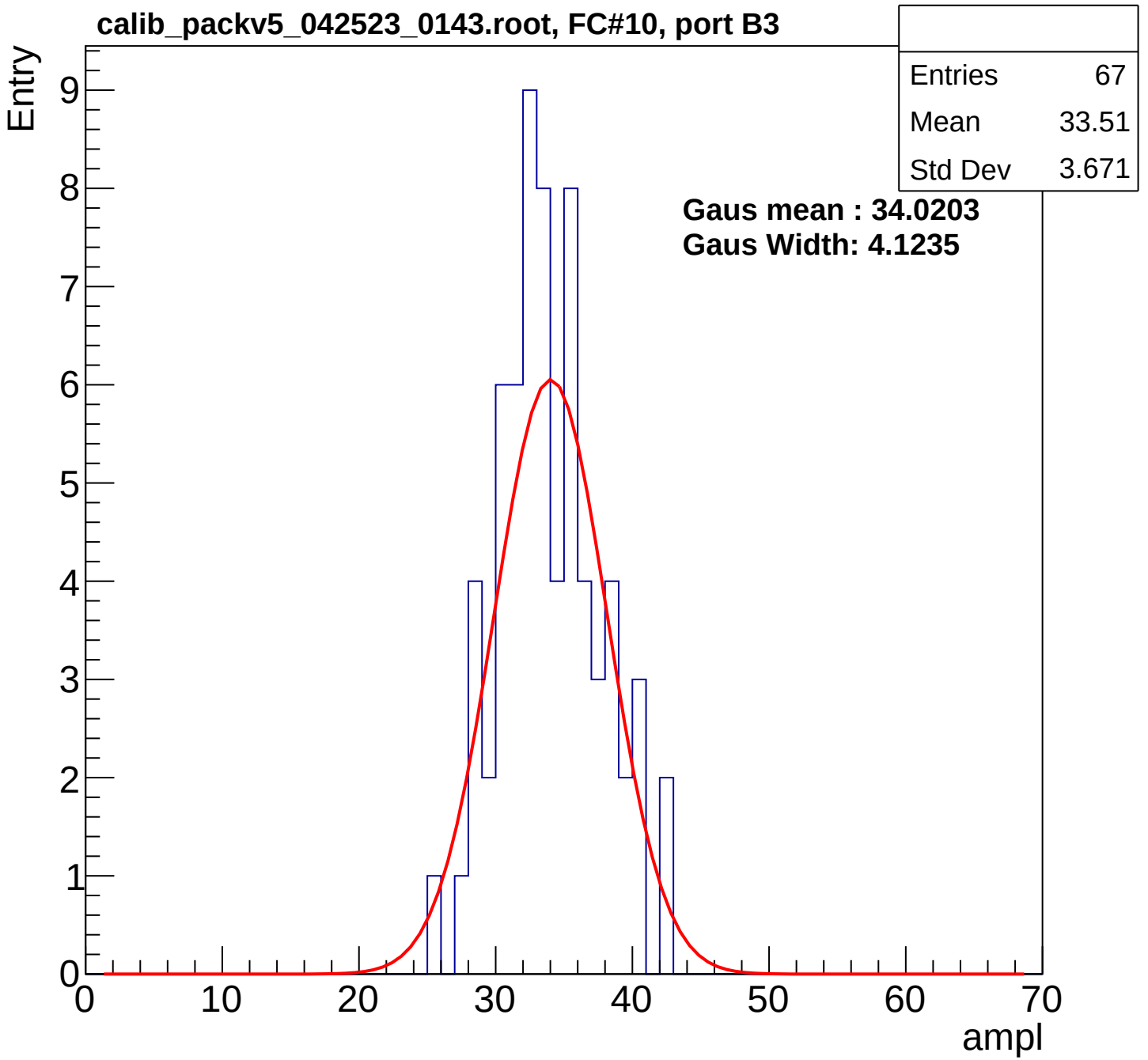

B1L002S, U6-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53
Std Dev 2.547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

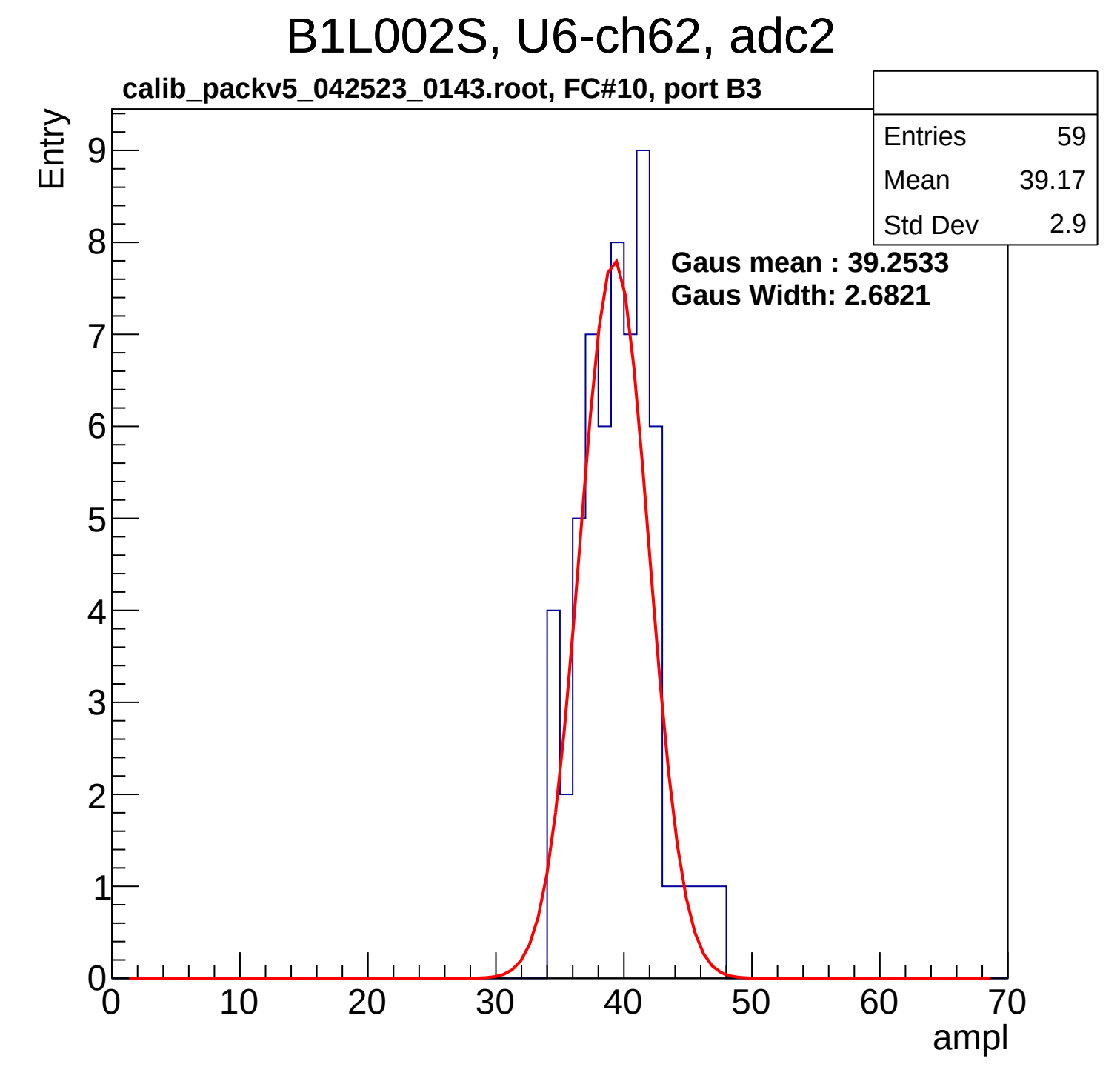

B1L002S, U6-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.94
Std Dev 2.893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

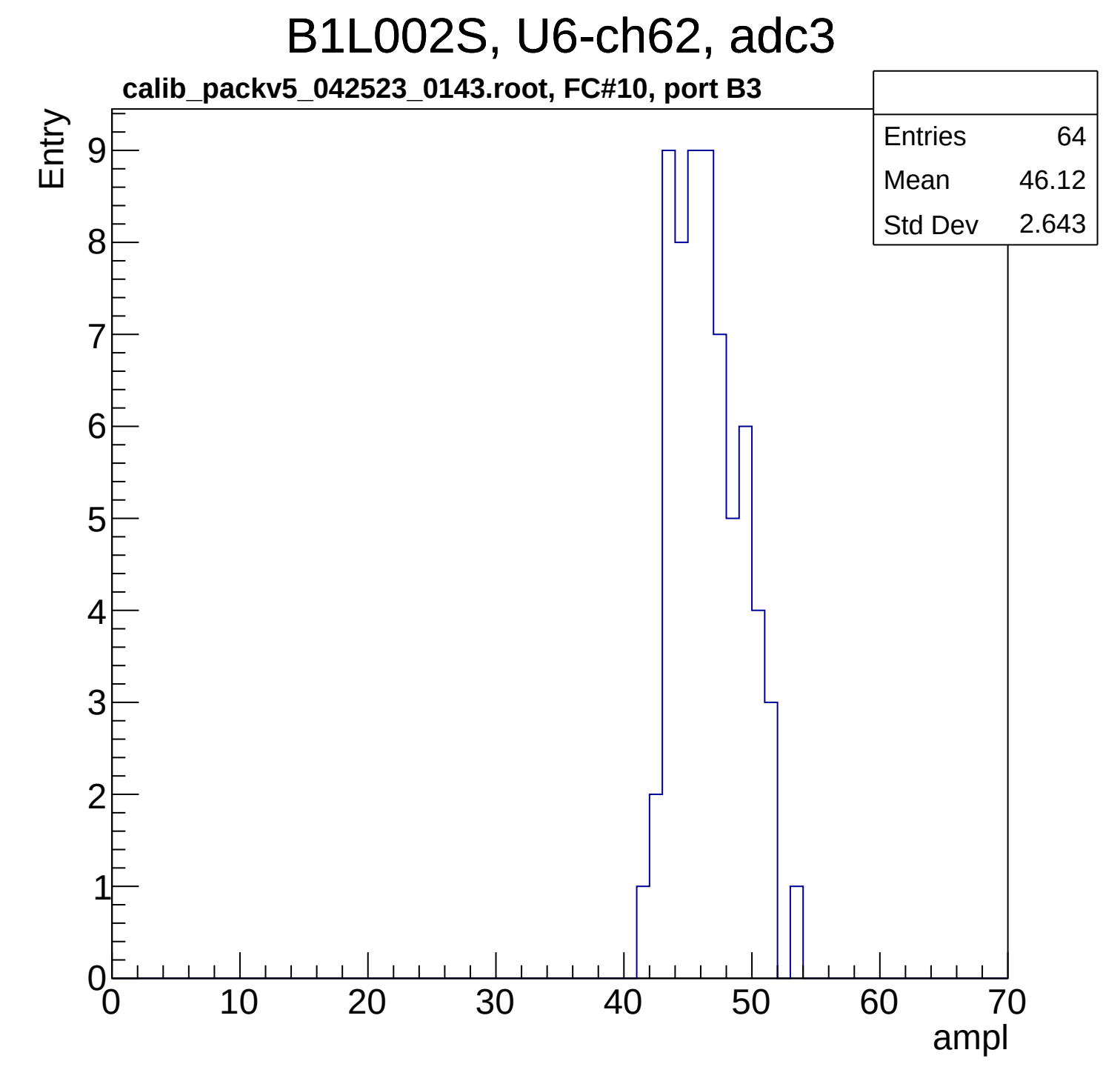

B1L002S, U6-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.77
Std Dev 10.88
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

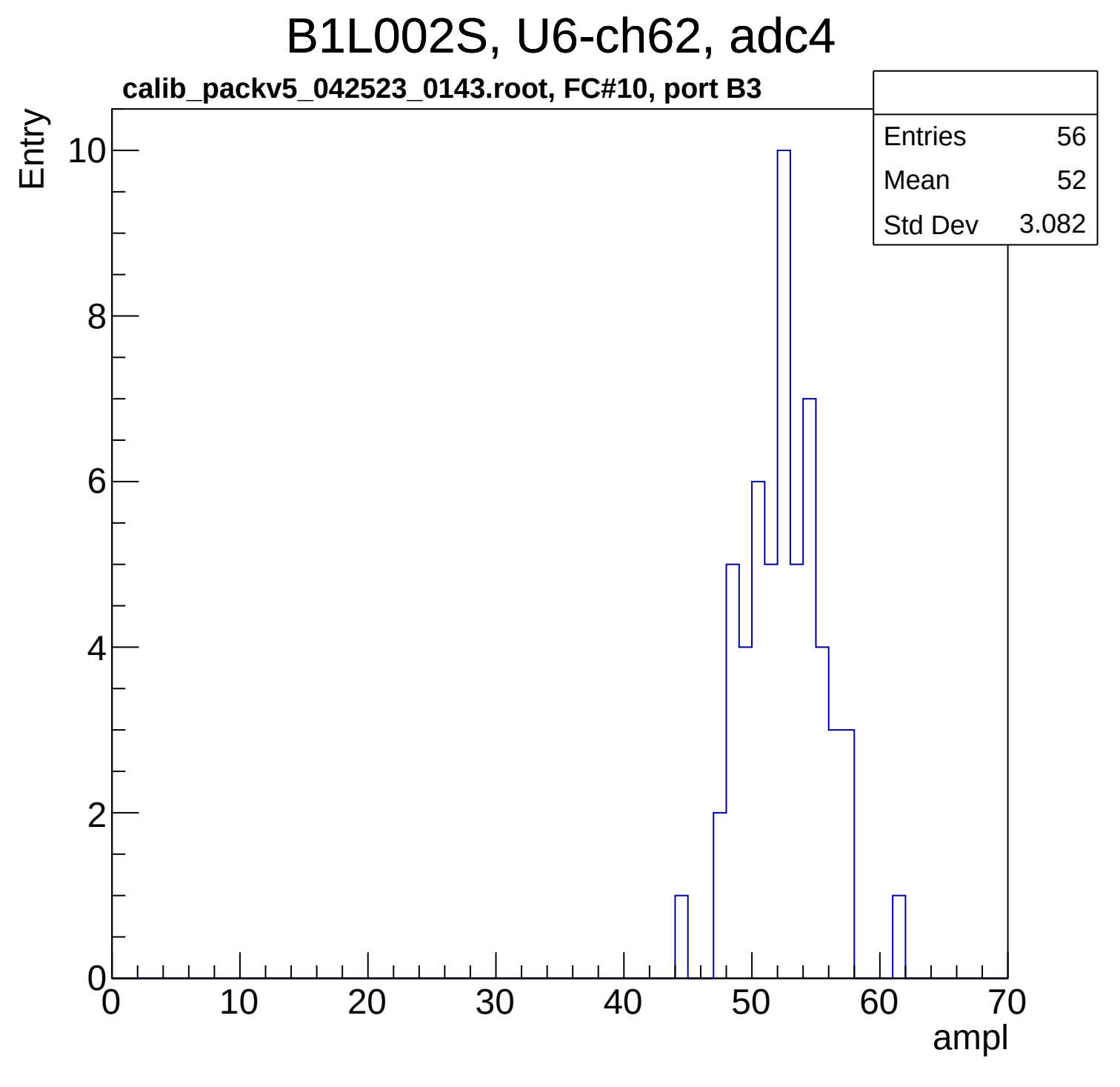

B1L002S, U6-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48
Std Dev 18.46
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

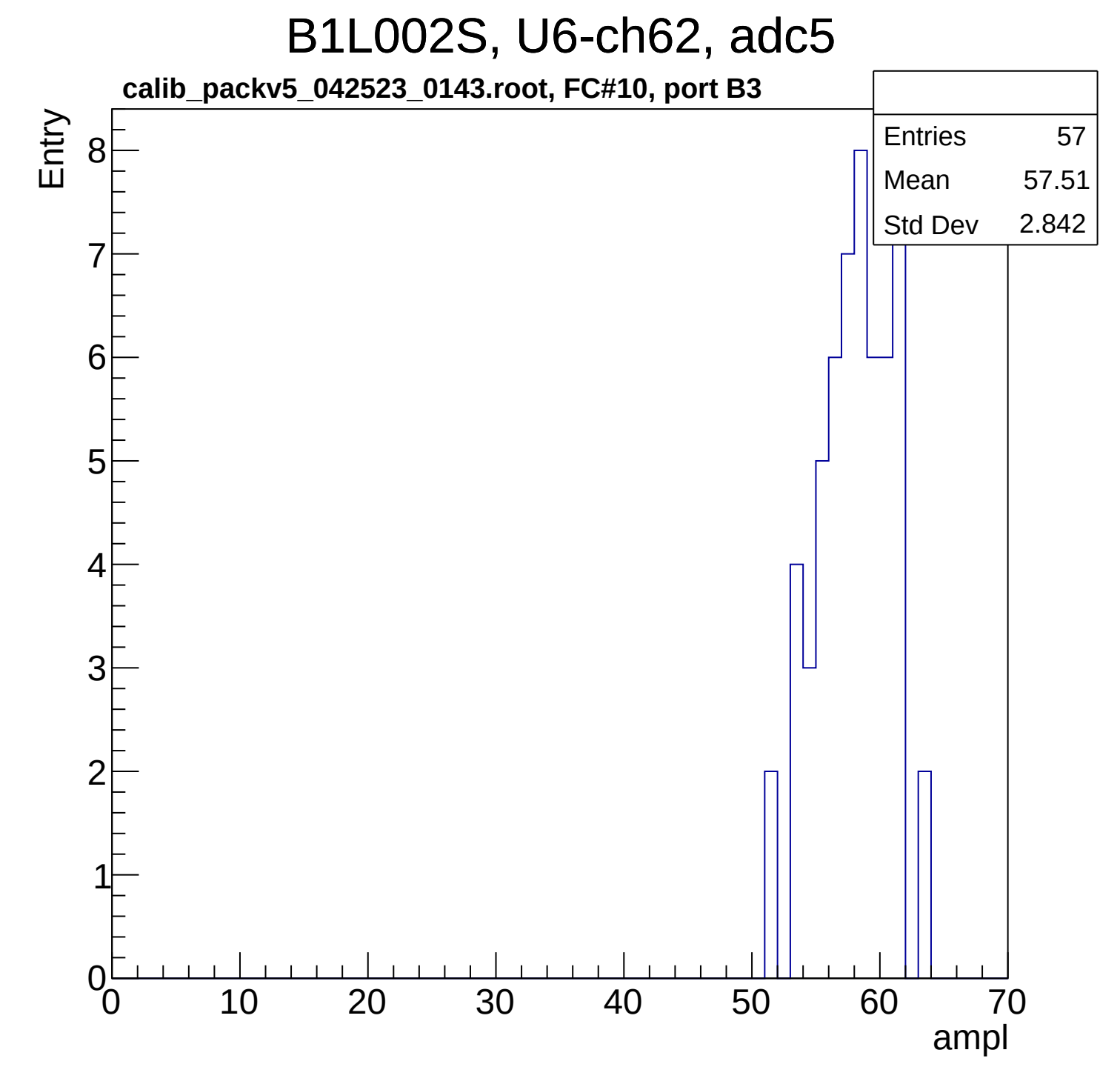

B1L002S, U6-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.7
Std Dev 3.1
Gaus mean : 27.9095
Gaus Width: 2.8329
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

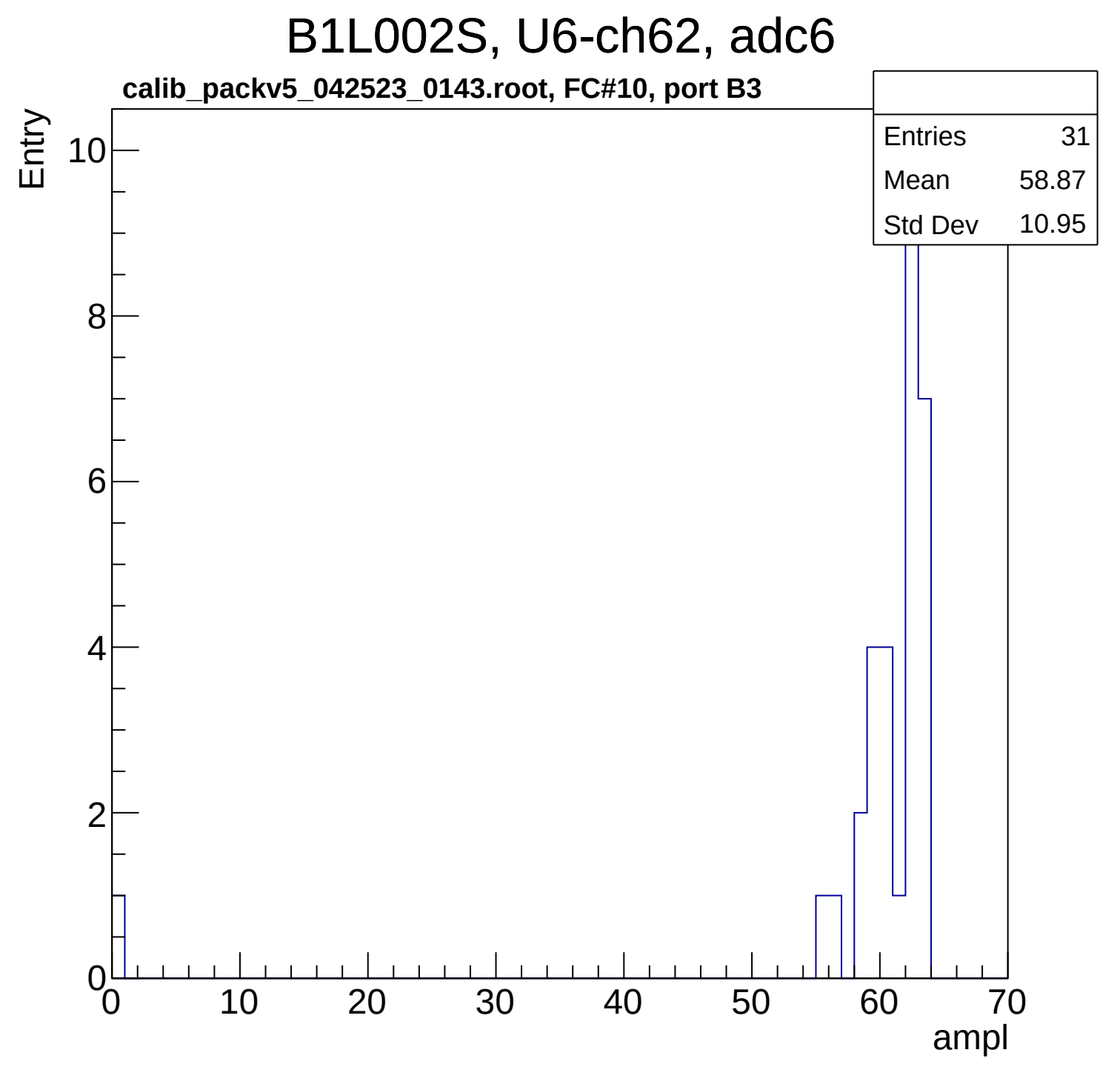

B1L002S, U6-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.51
Std Dev 3.671
Gaus mean : 34.0203
Gaus Width: 4.1235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

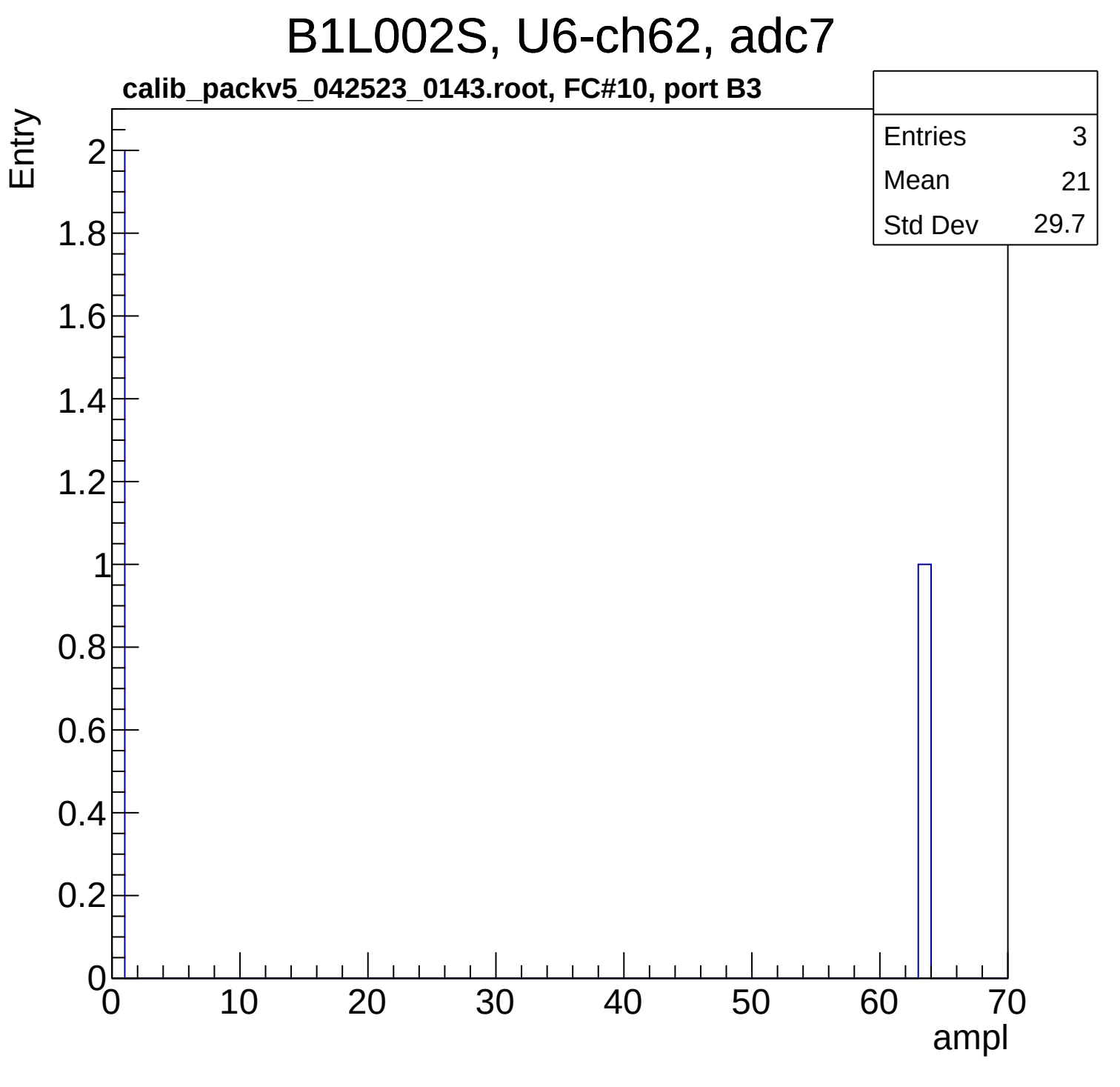

B1L002S, U6-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.17
Std Dev 2.9
Gaus mean : 39.2533
Gaus Width: 2.6821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

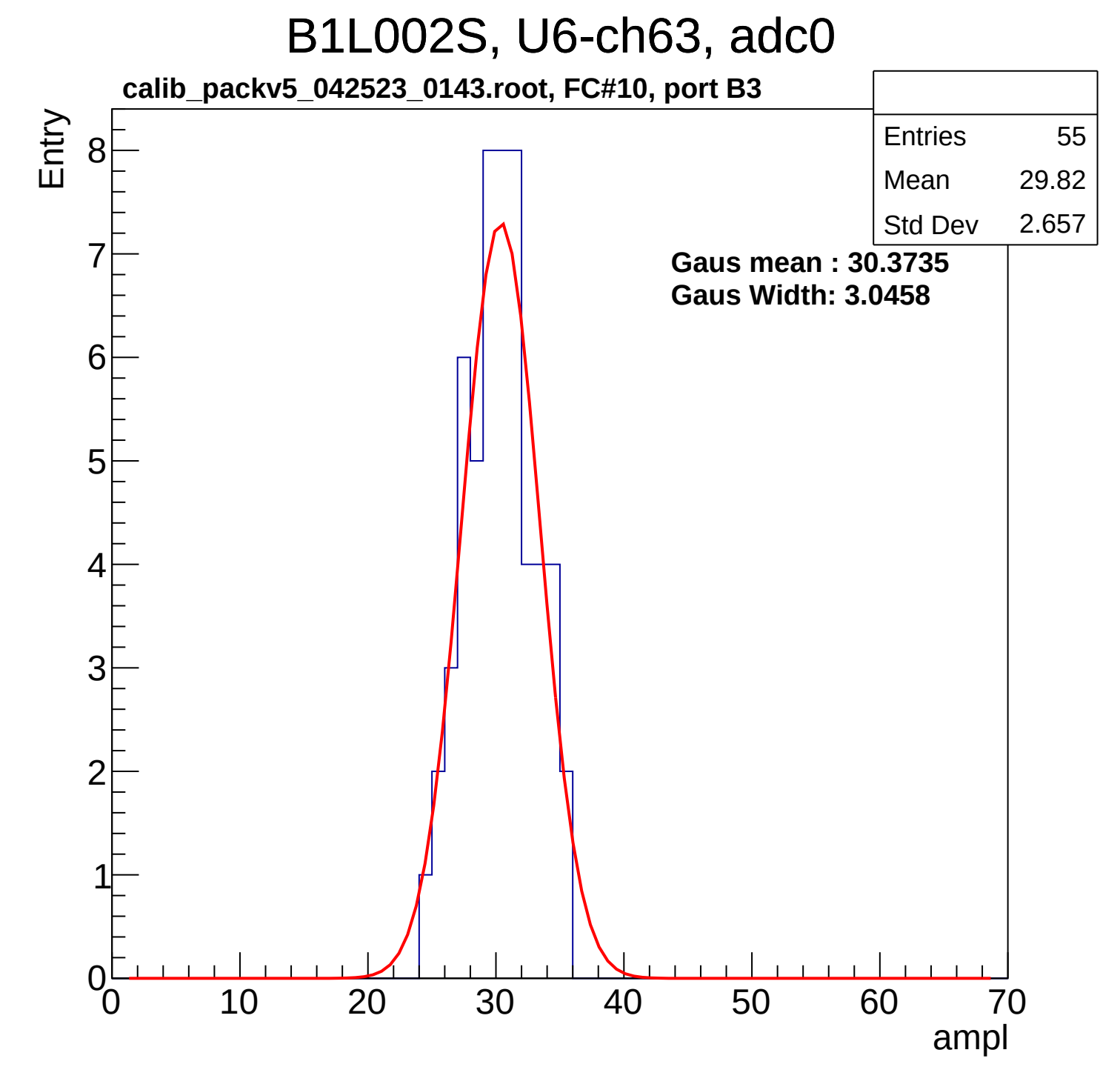

B1L002S, U6-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.12
Std Dev 2.643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

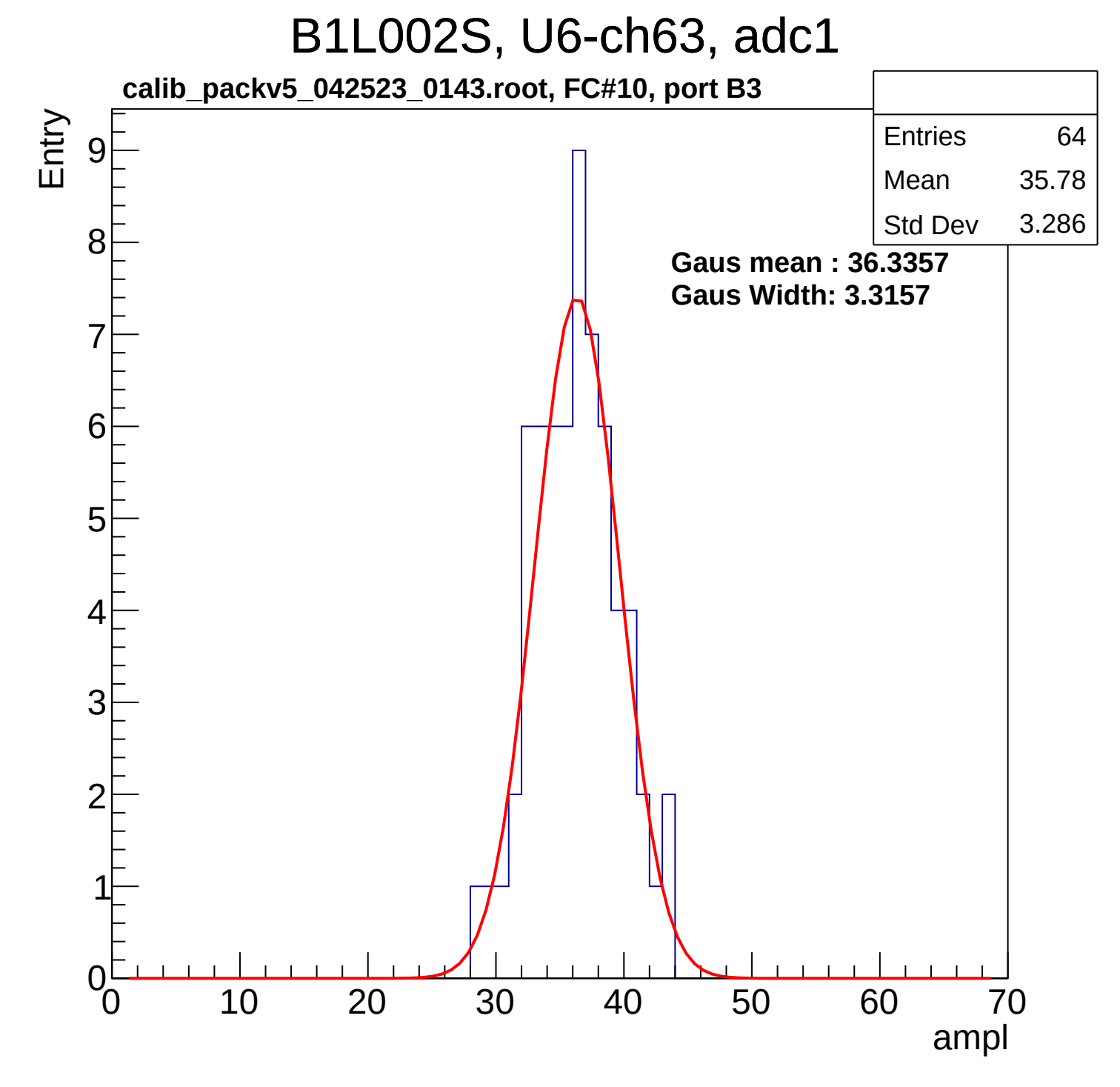

B1L002S, U6-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52
Std Dev 3.082
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

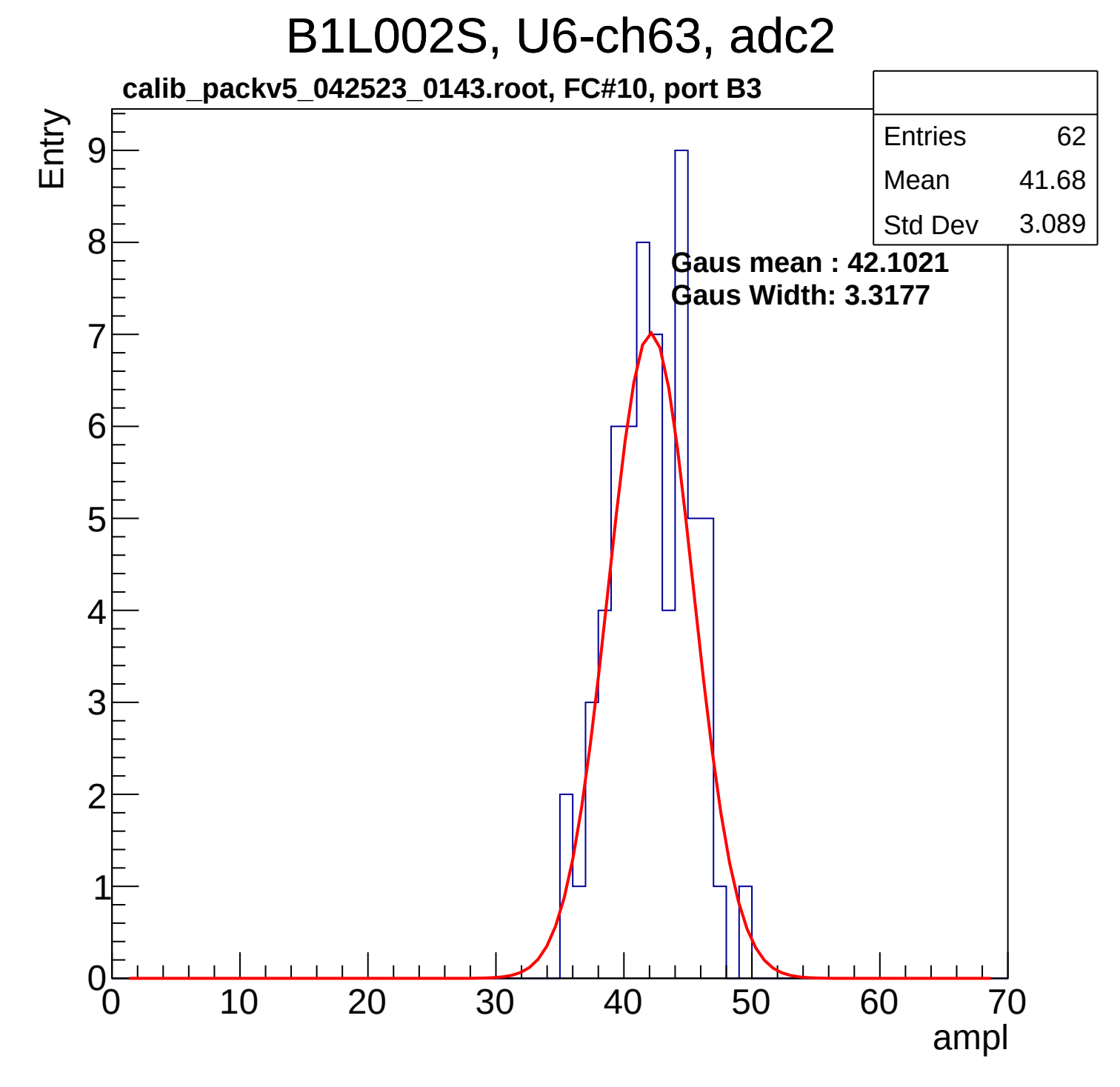

B1L002S, U6-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.51
Std Dev 2.842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

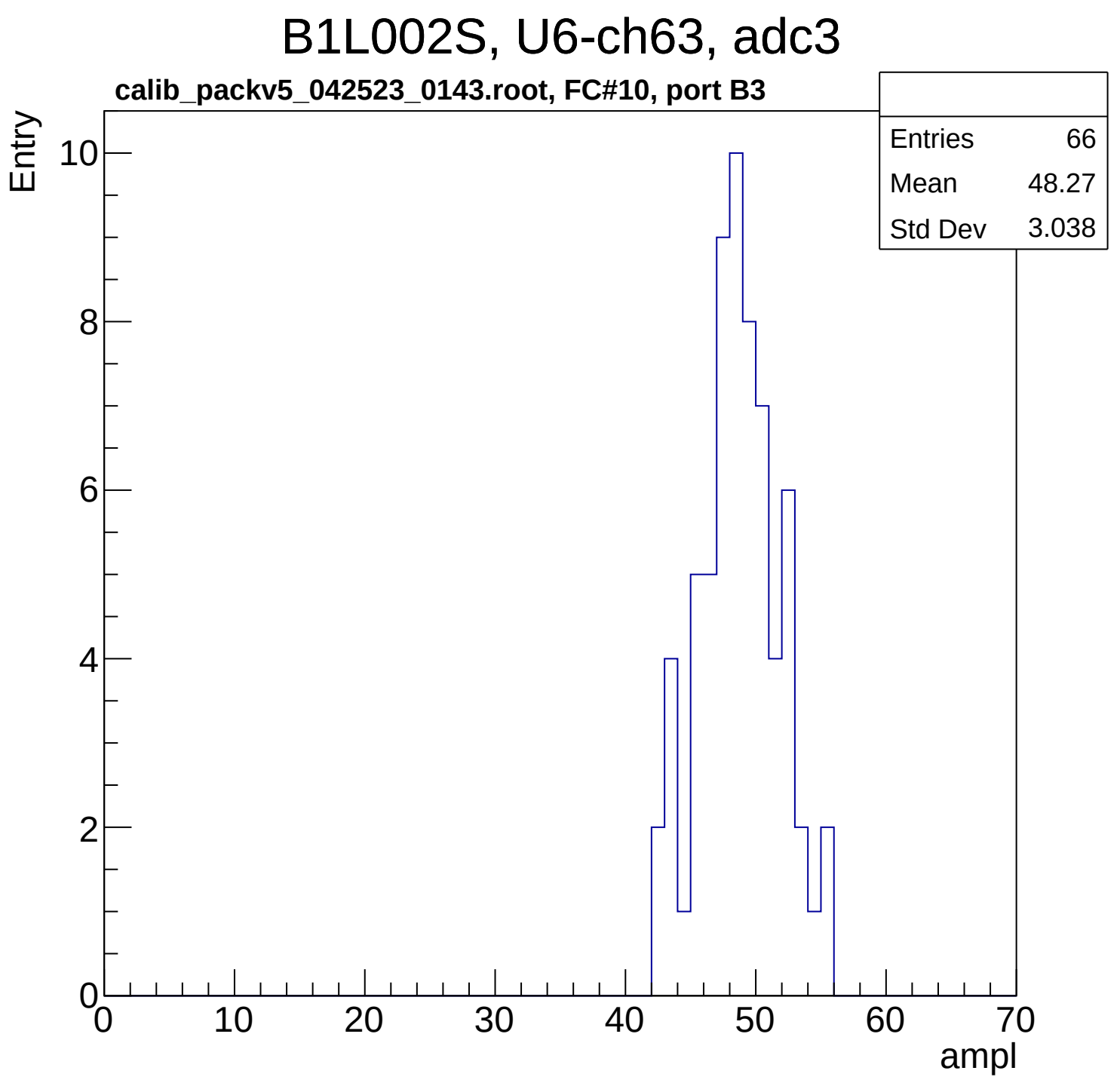

B1L002S, U6-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.87
Std Dev 10.95
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

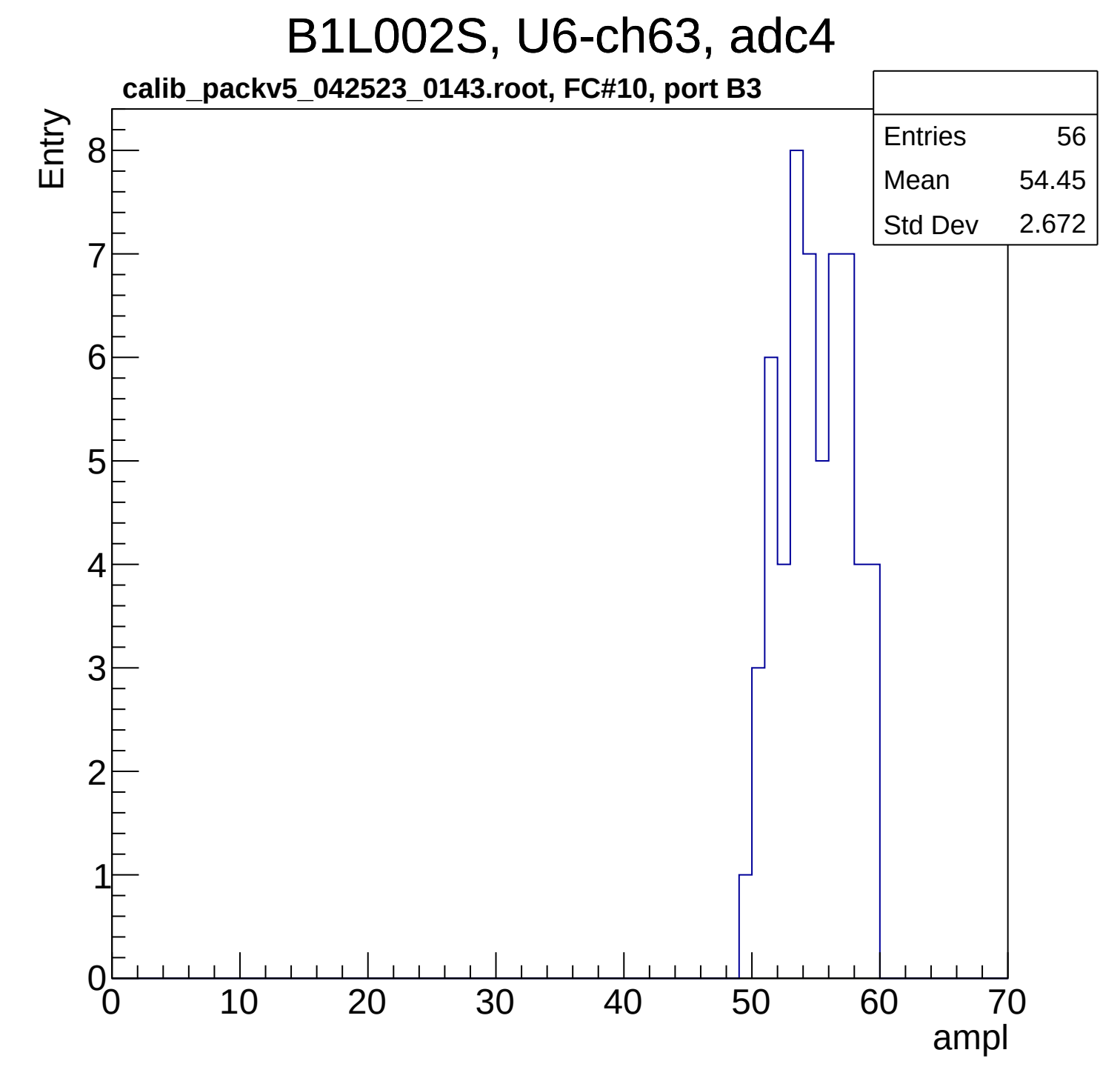

B1L002S, U6-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

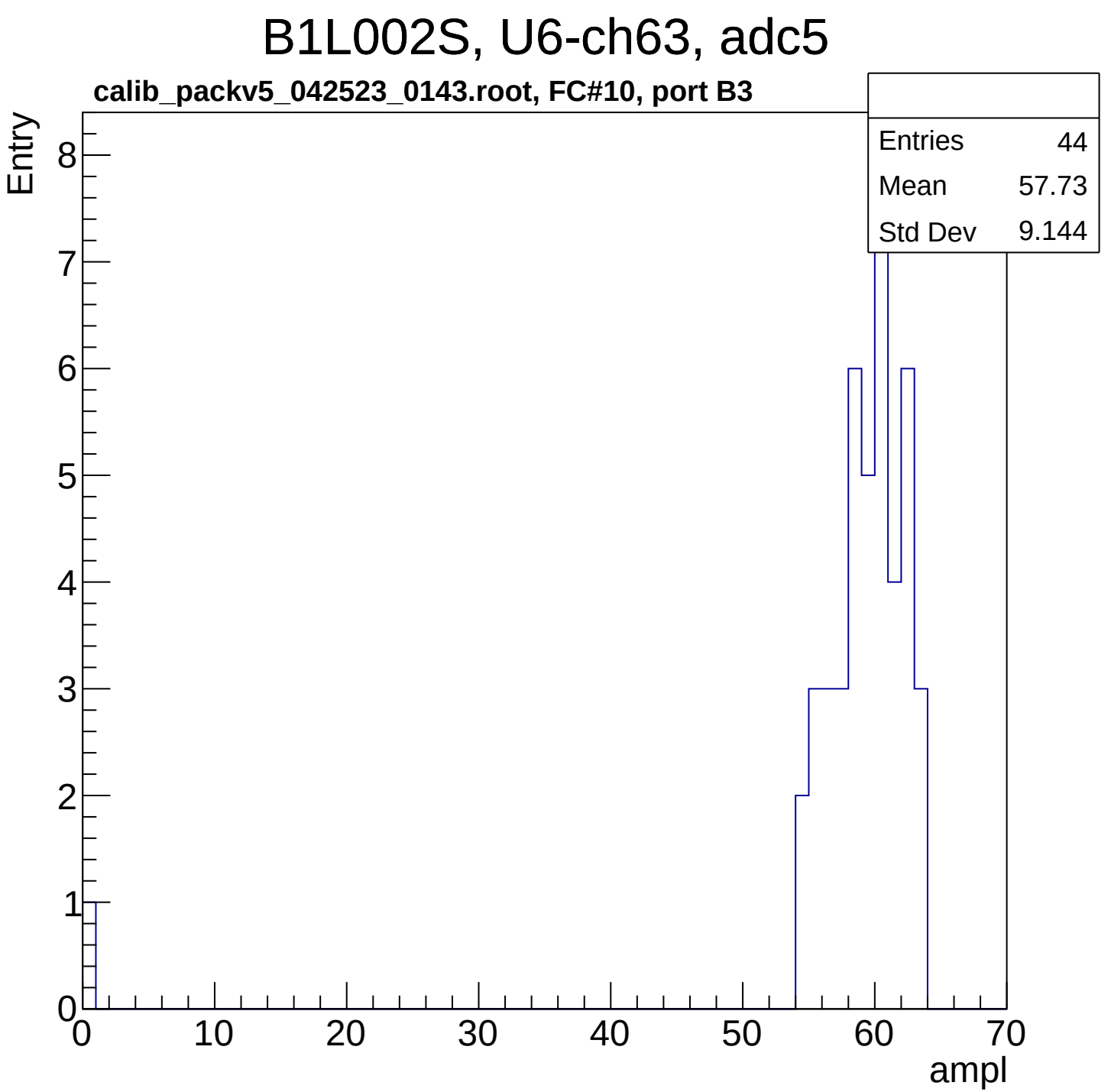

B1L002S, U6-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 29.82
Std Dev 2.657
Gaus mean : 30.3735
Gaus Width: 3.0458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

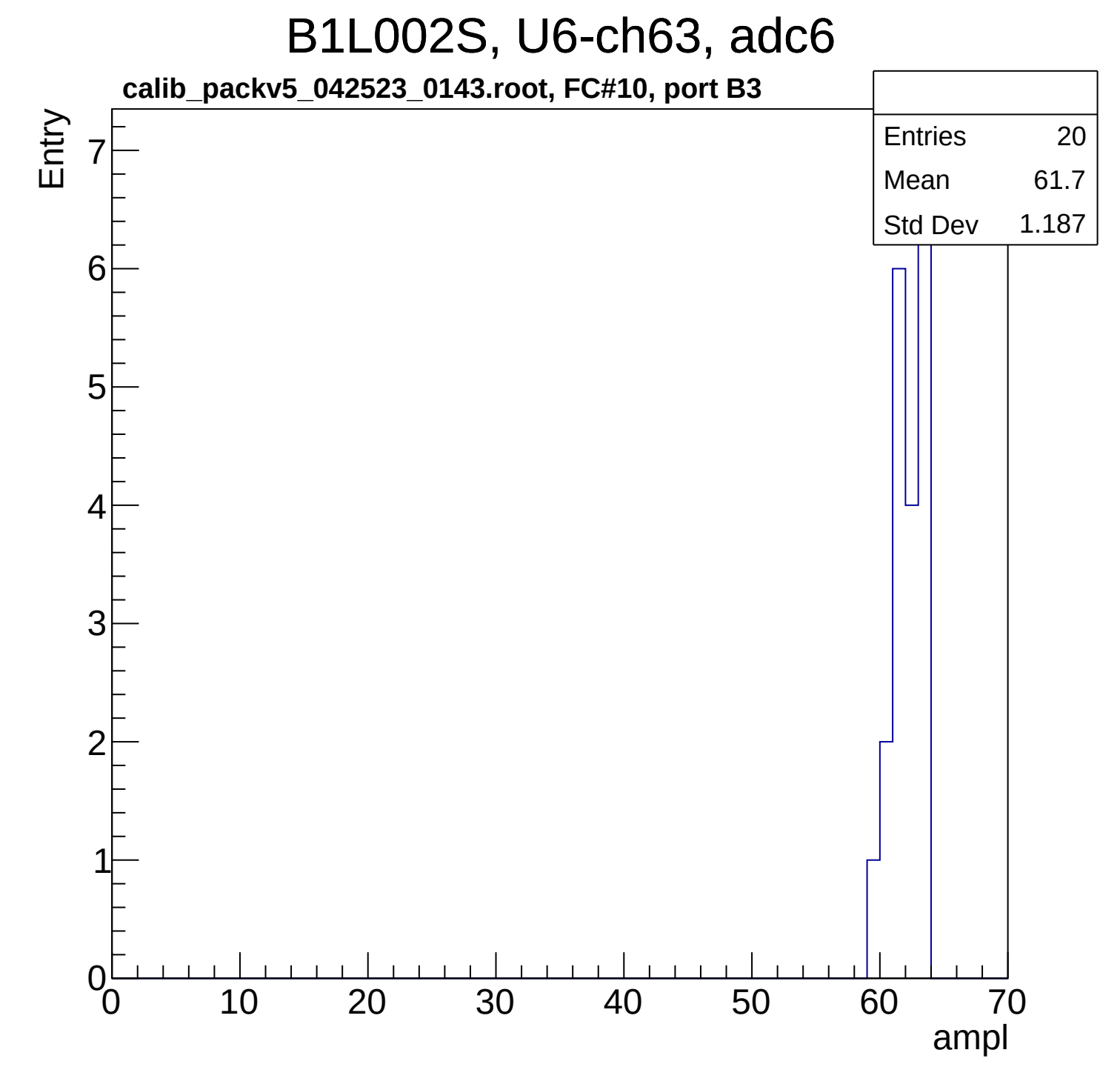

B1L002S, U6-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.78
Std Dev 3.286
Gaus mean : 36.3357
Gaus Width: 3.3157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

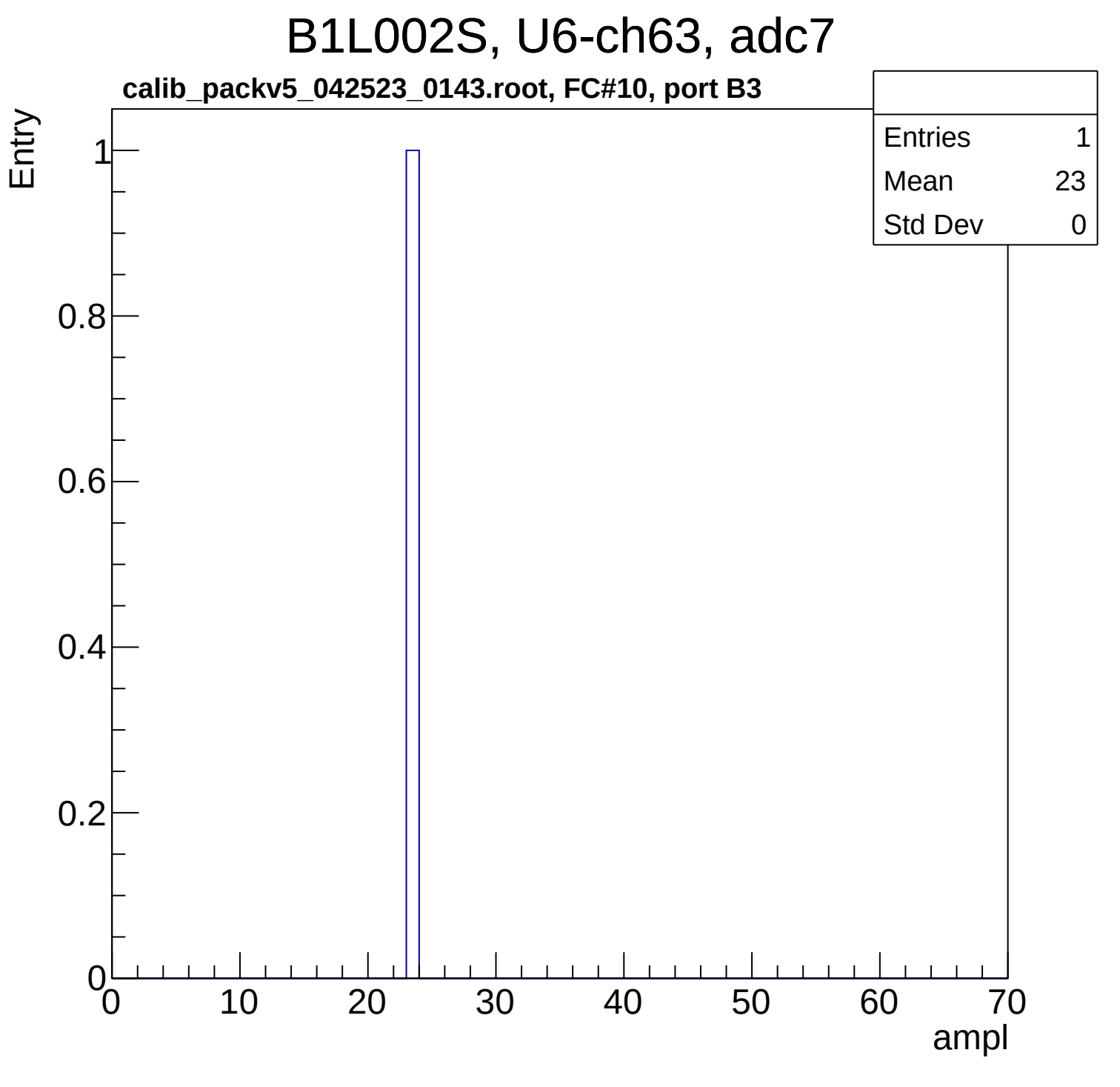

B1L002S, U6-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.68
Std Dev 3.089
Gaus mean : 42.1021
Gaus Width: 3.3177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

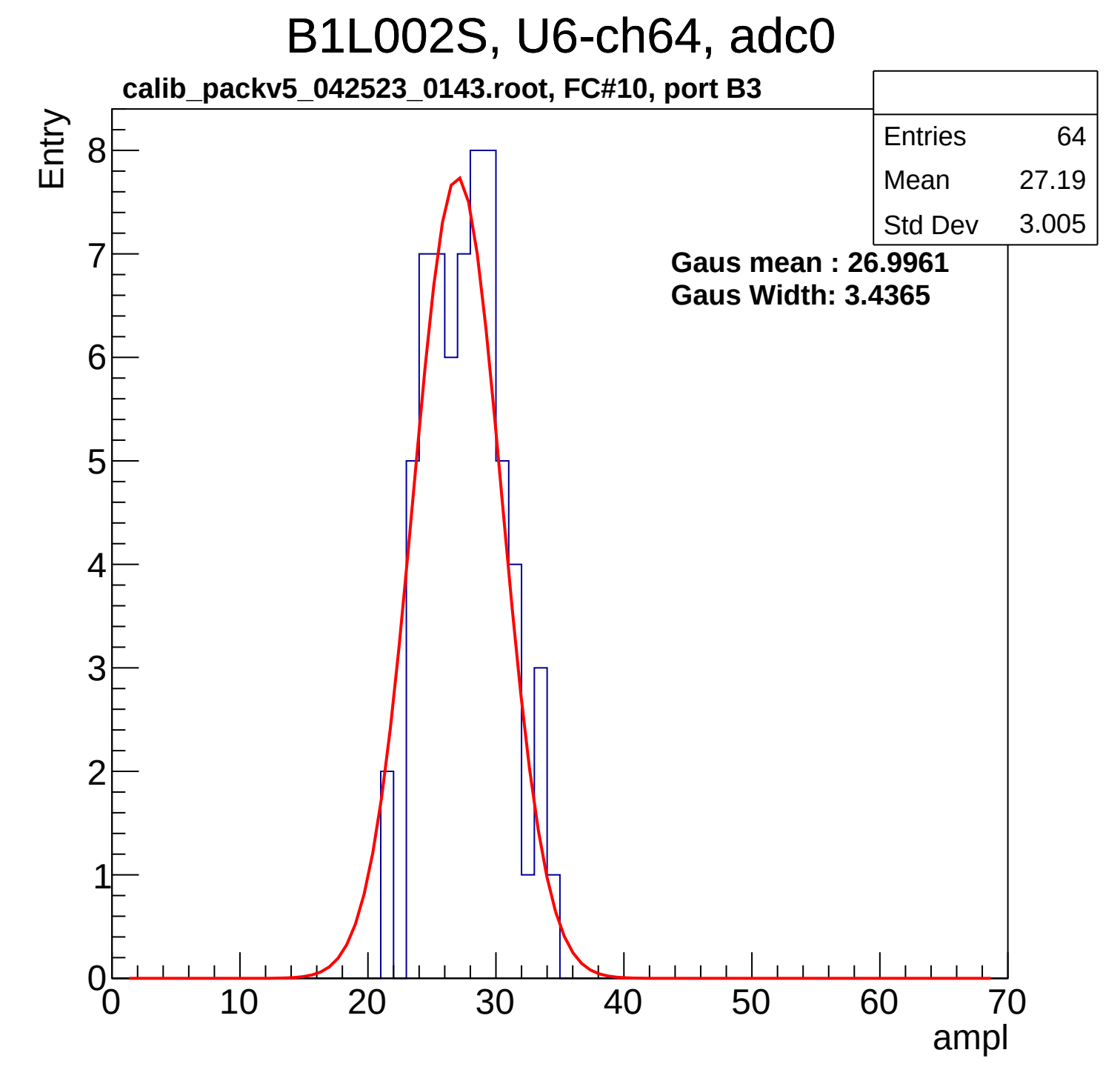

B1L002S, U6-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.27
Std Dev 3.038
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

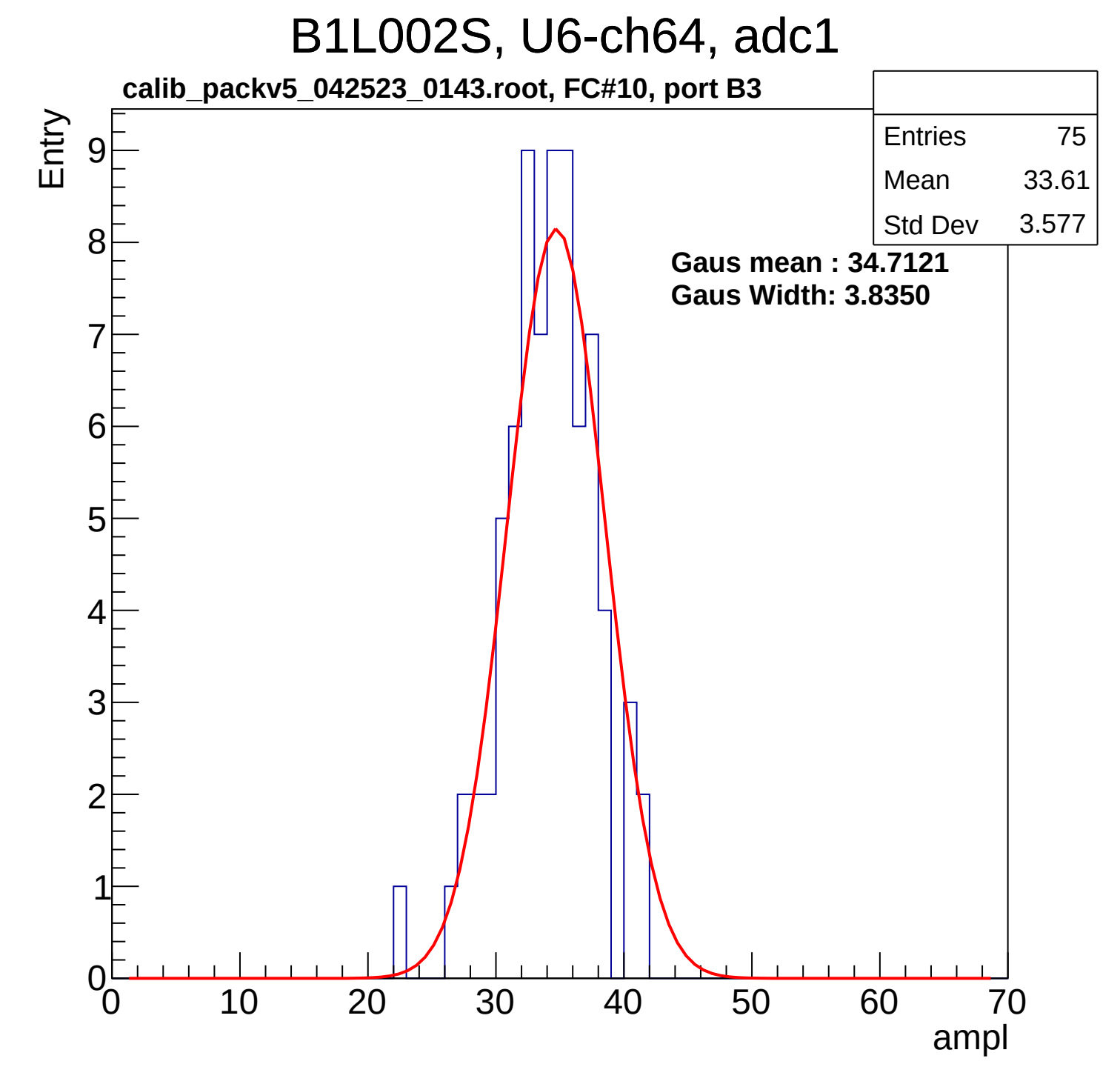

B1L002S, U6-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.45
Std Dev 2.672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

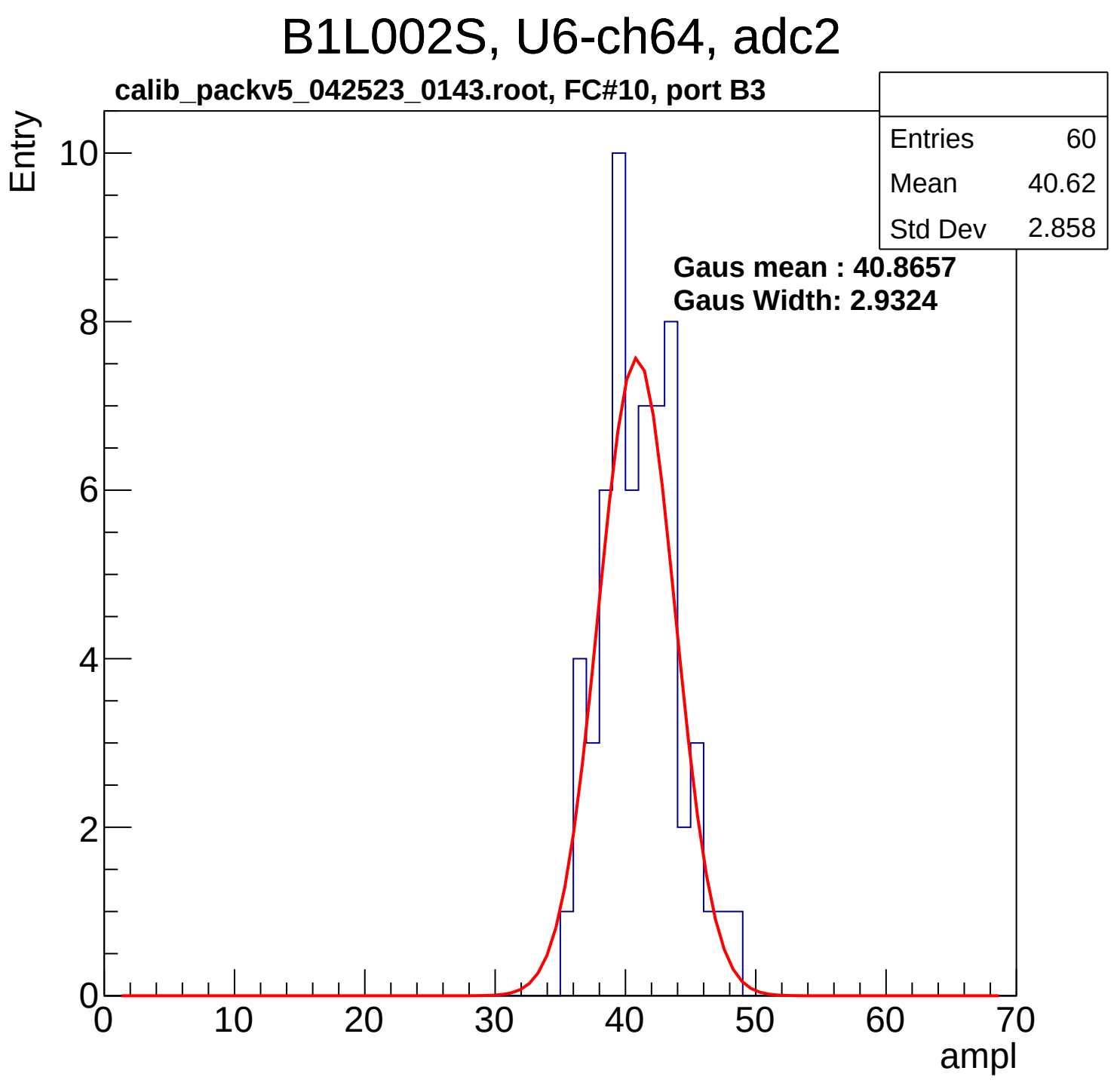

B1L002S, U6-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.73
Std Dev 9.144
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

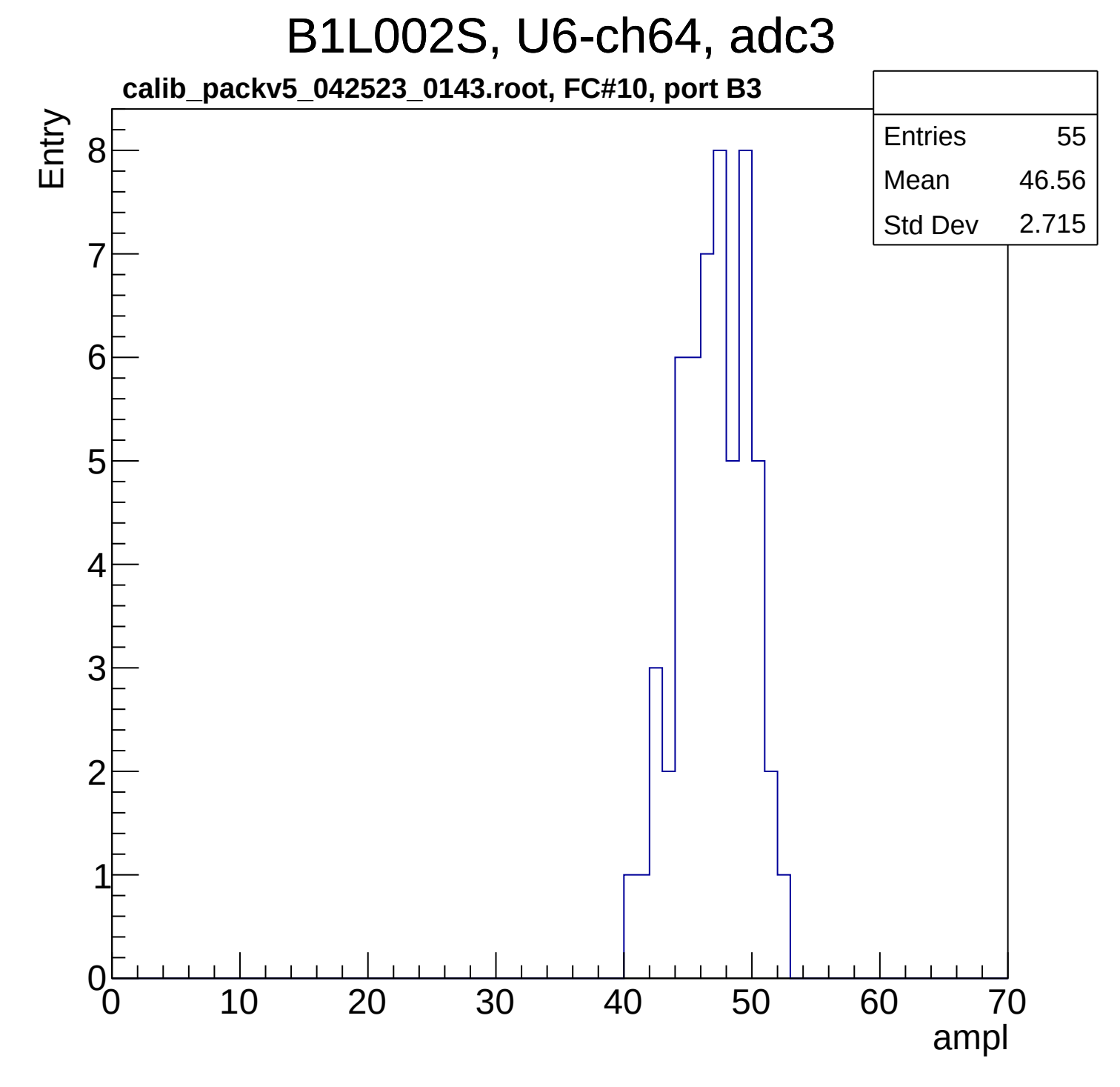

B1L002S, U6-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.7
Std Dev 1.187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

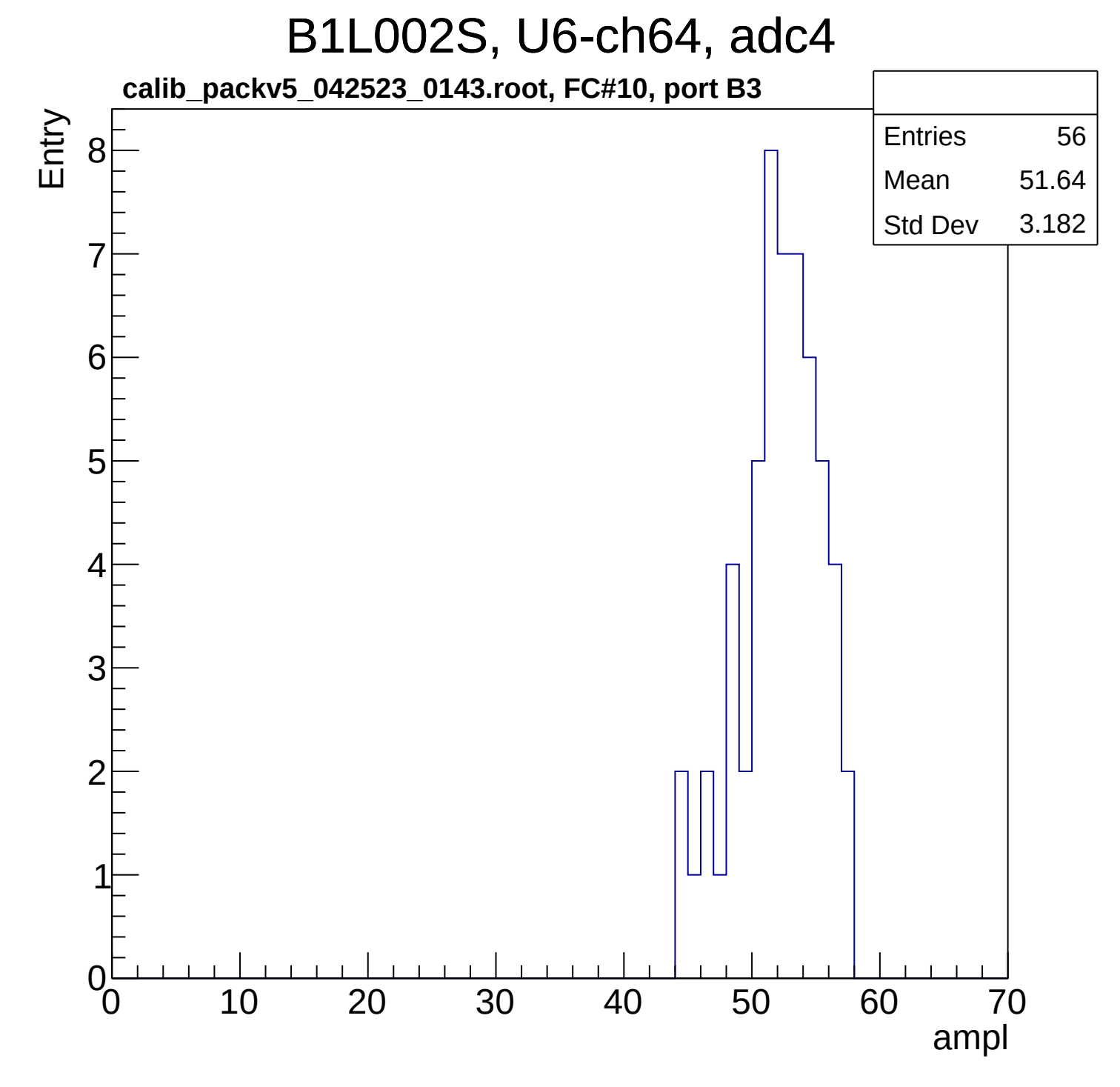

B1L002S, U6-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

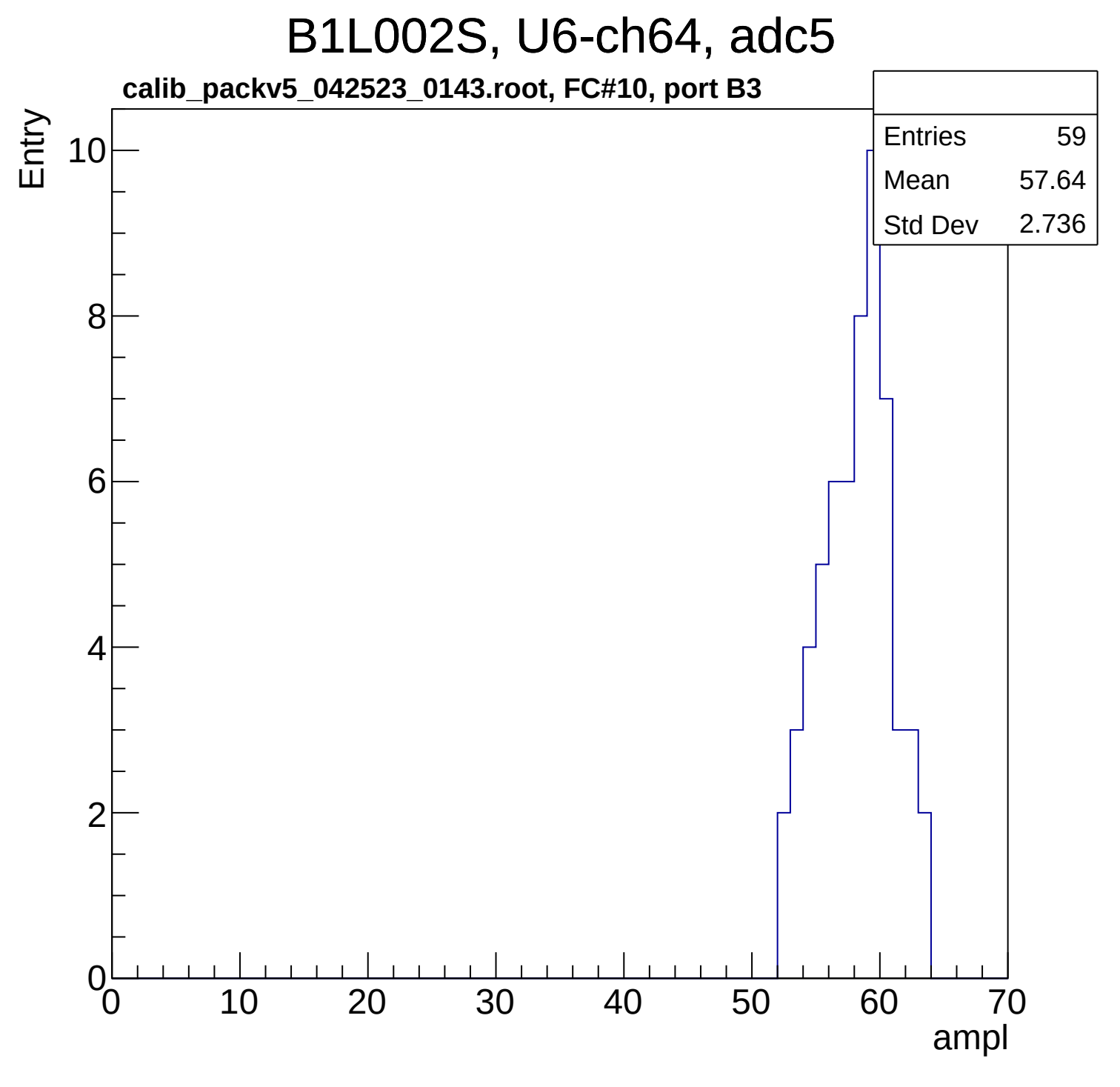

B1L002S, U6-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.19
Std Dev 3.005
Gaus mean : 26.9961
Gaus Width: 3.4365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

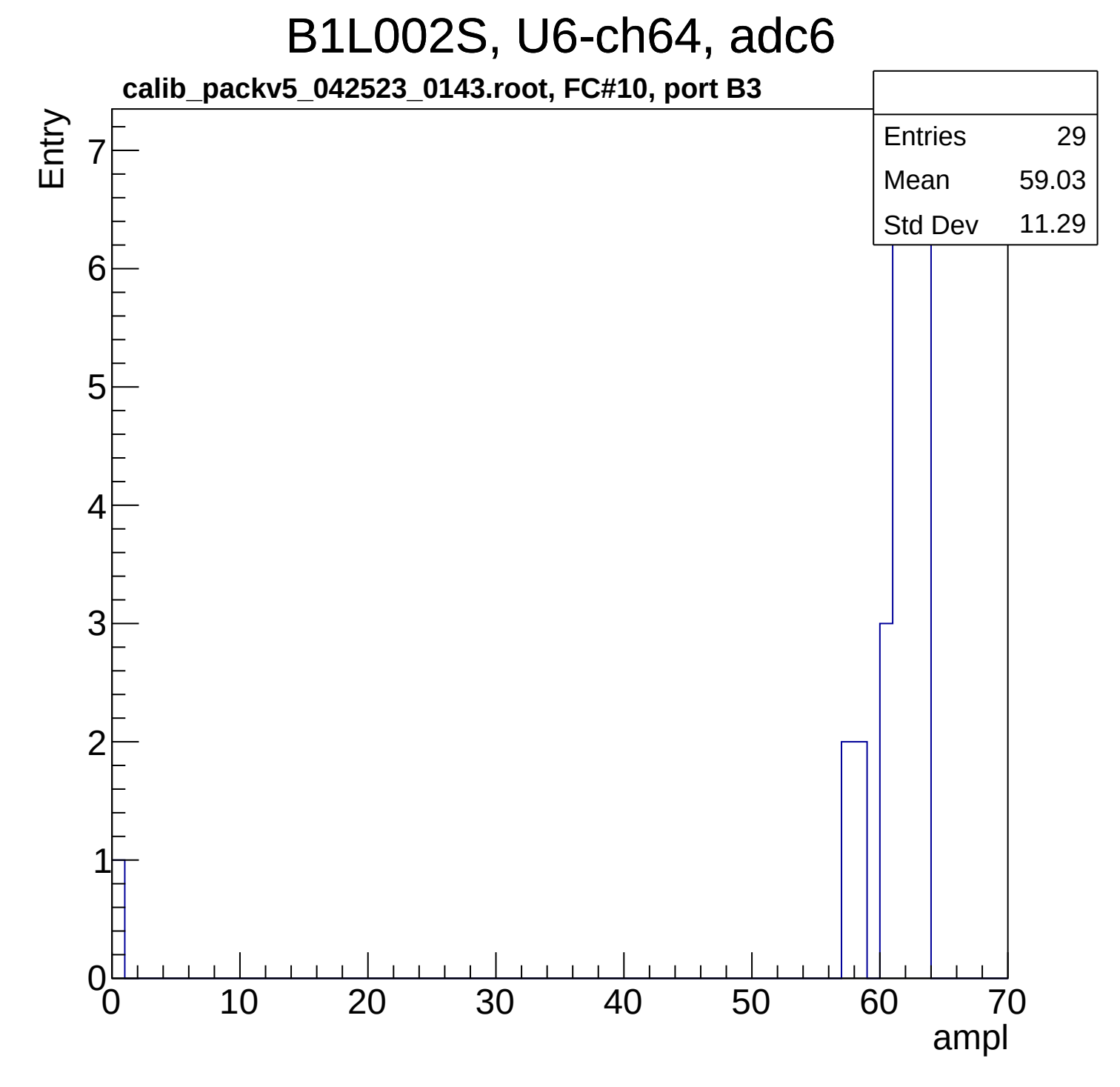

B1L002S, U6-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.61
Std Dev 3.577
Gaus mean : 34.7121
Gaus Width: 3.8350
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

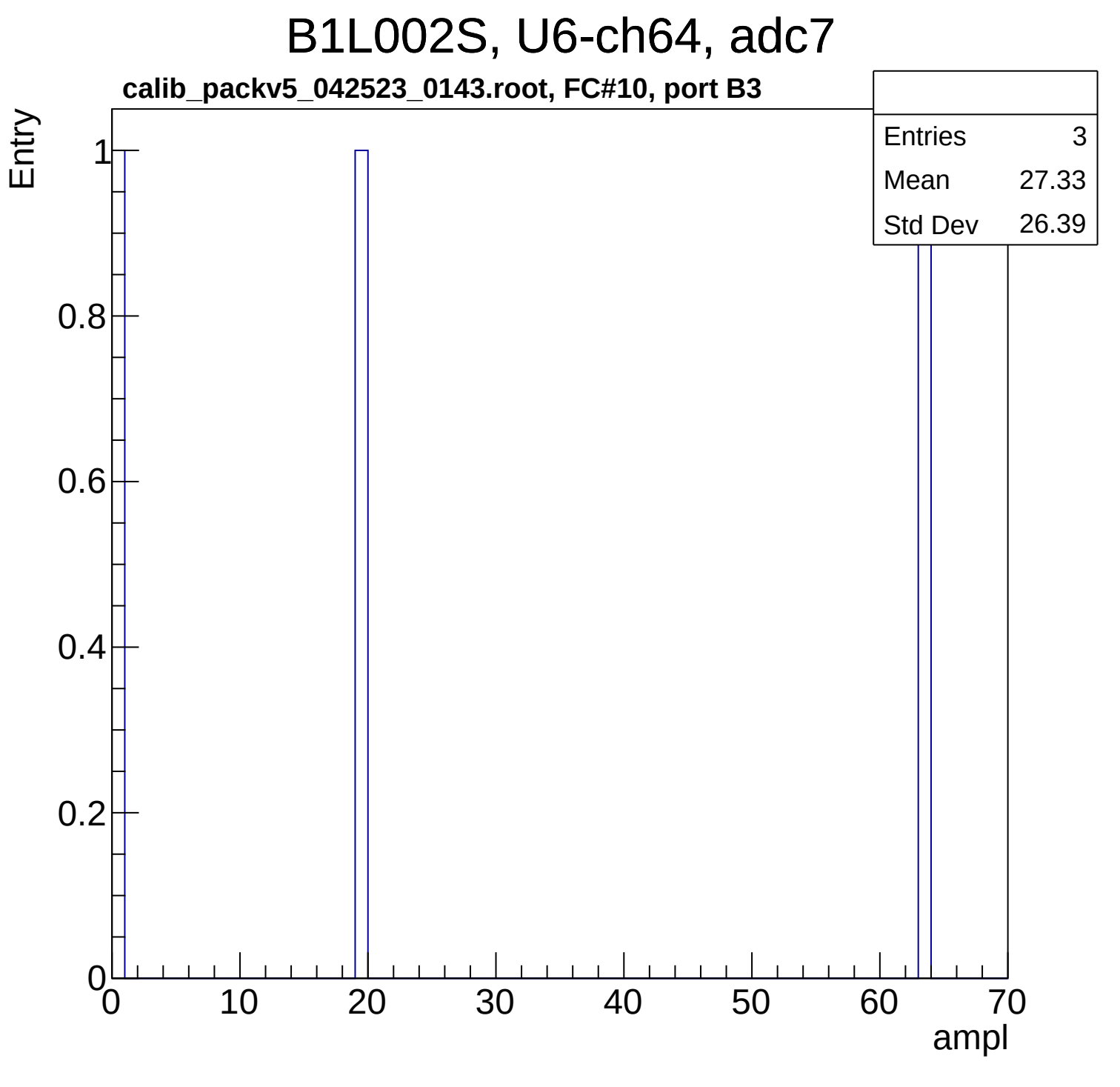

B1L002S, U6-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.62
Std Dev 2.858
Gaus mean : 40.8657
Gaus Width: 2.9324
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

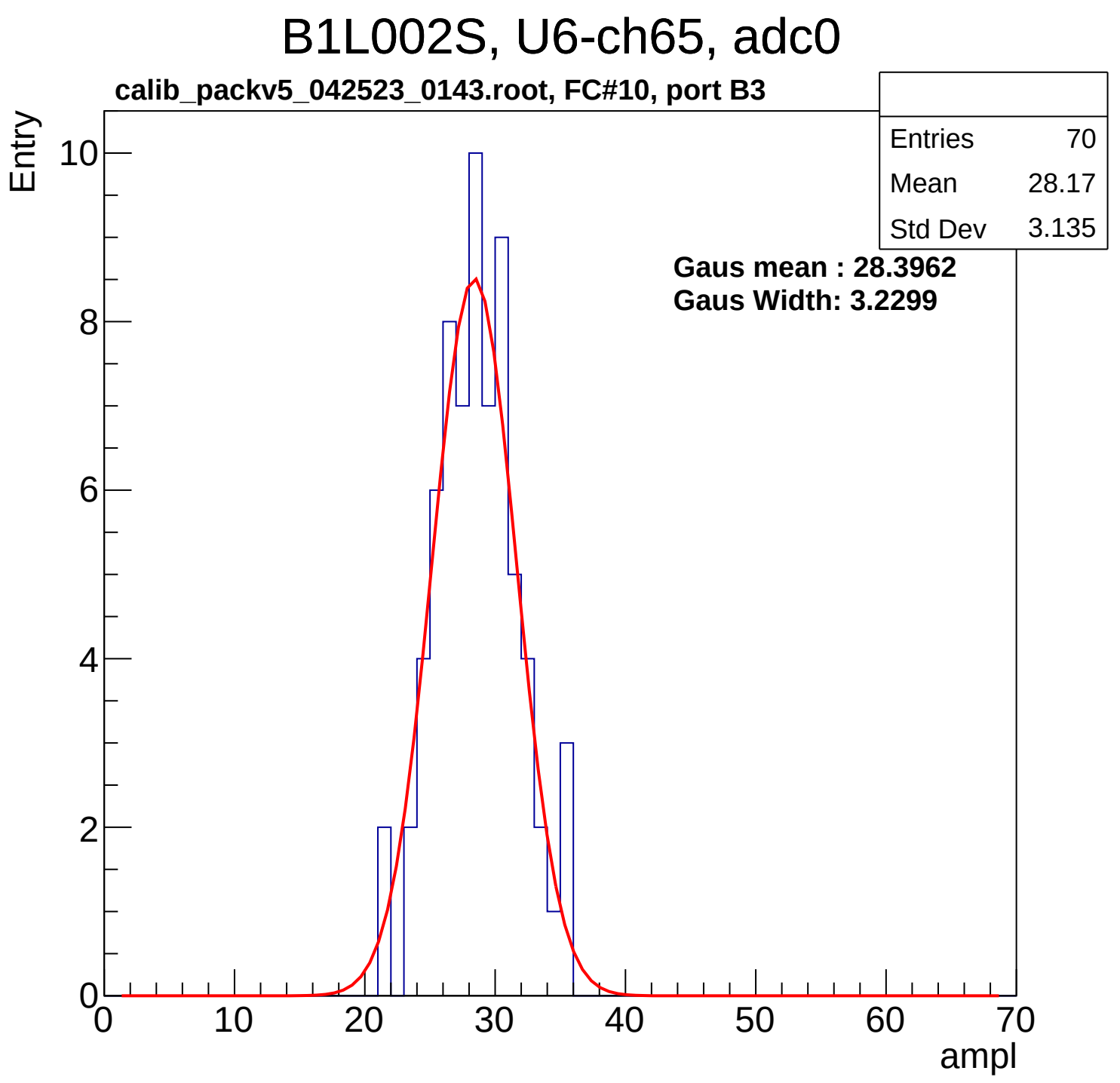

B1L002S, U6-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.56
Std Dev 2.715
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

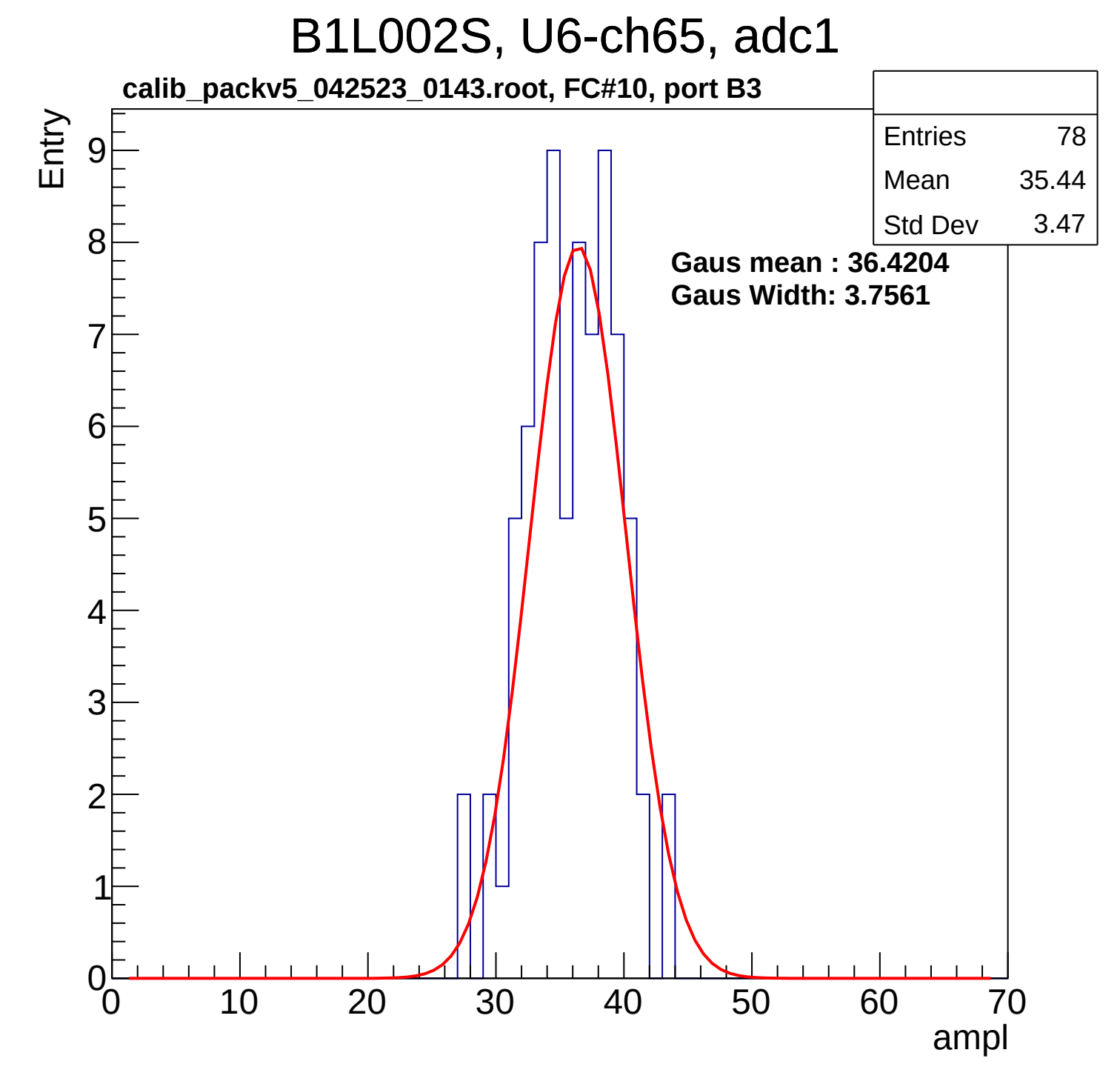

B1L002S, U6-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.64
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

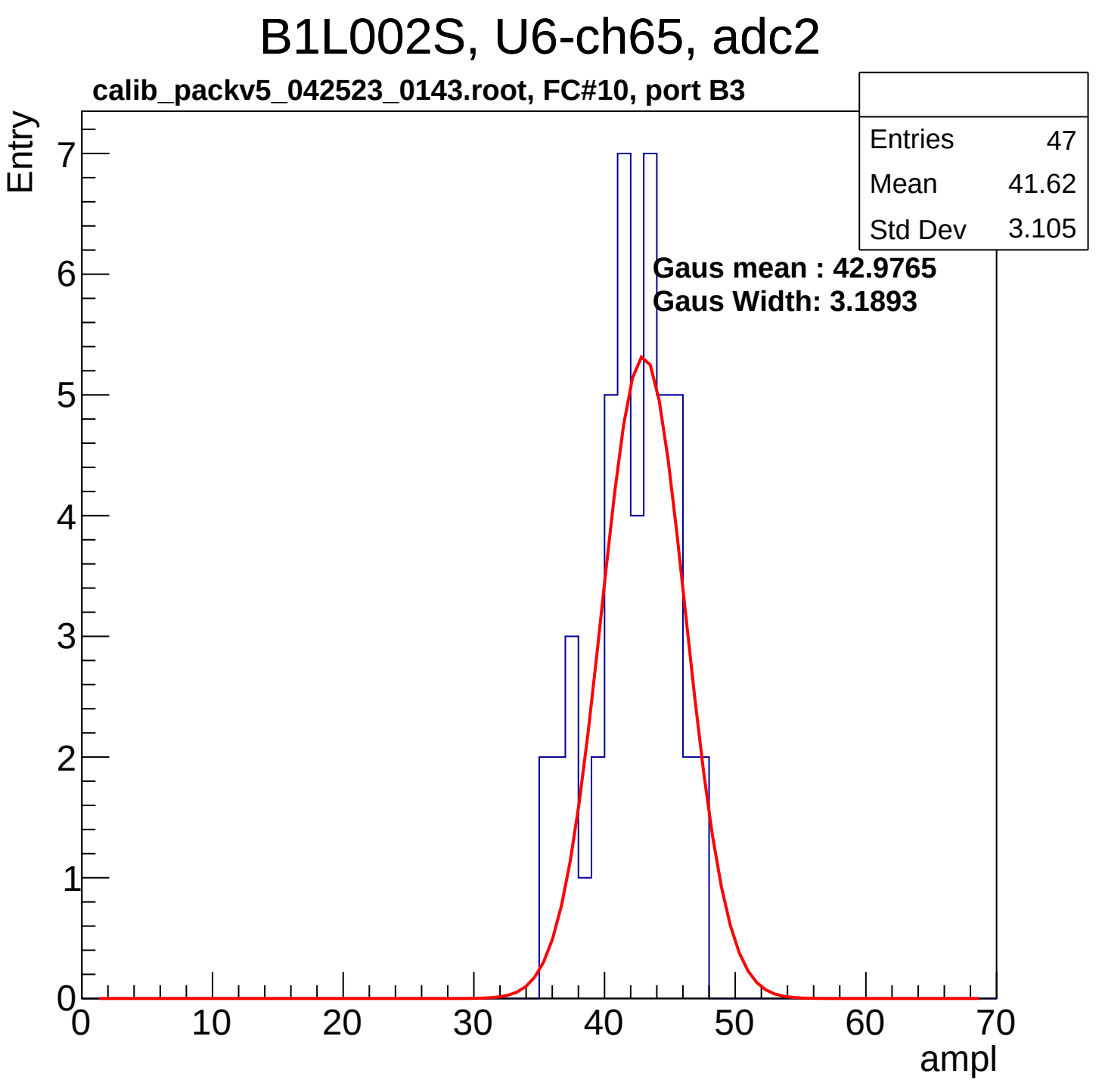

B1L002S, U6-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.64
Std Dev 2.736
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

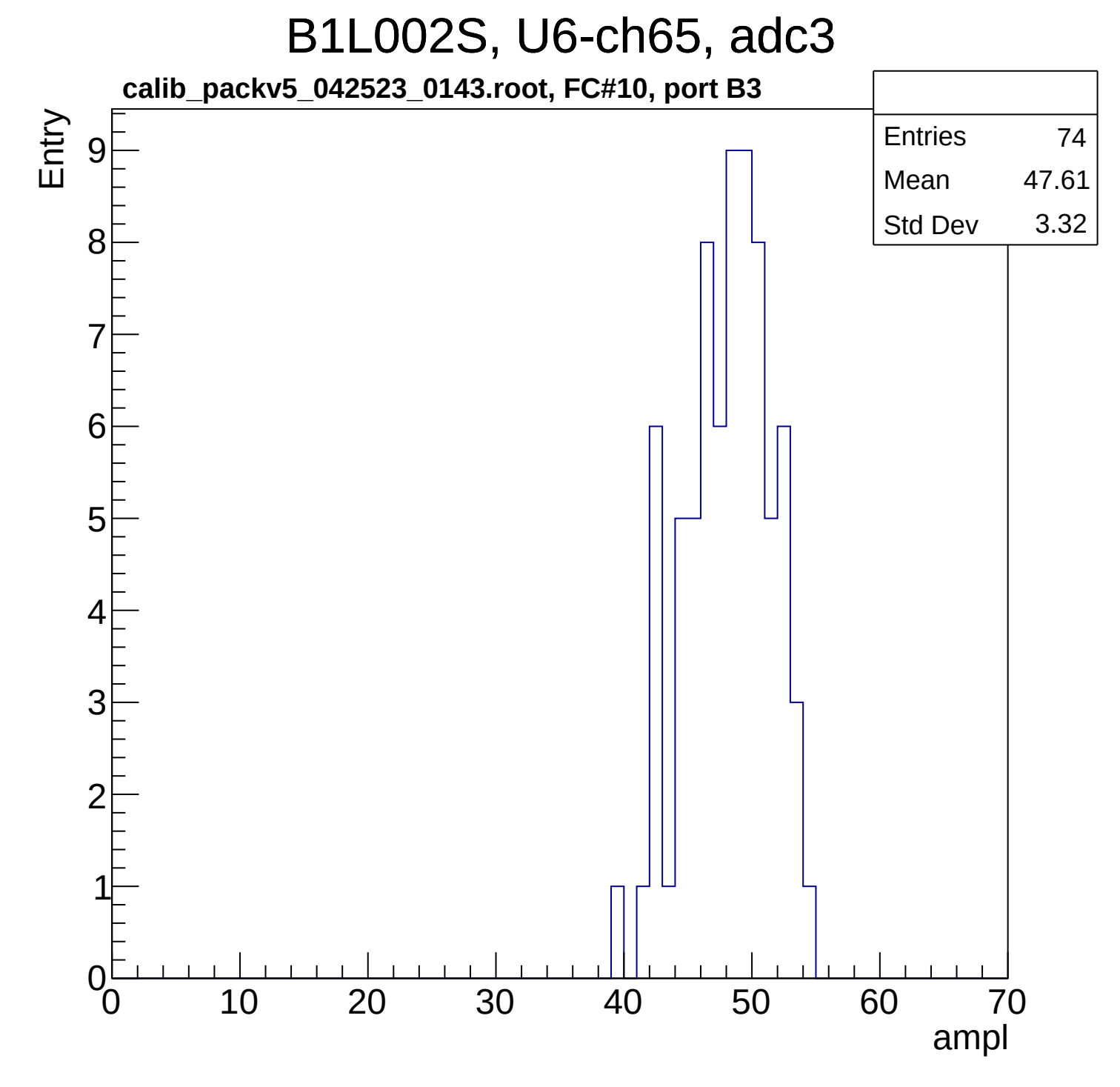

B1L002S, U6-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.03
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

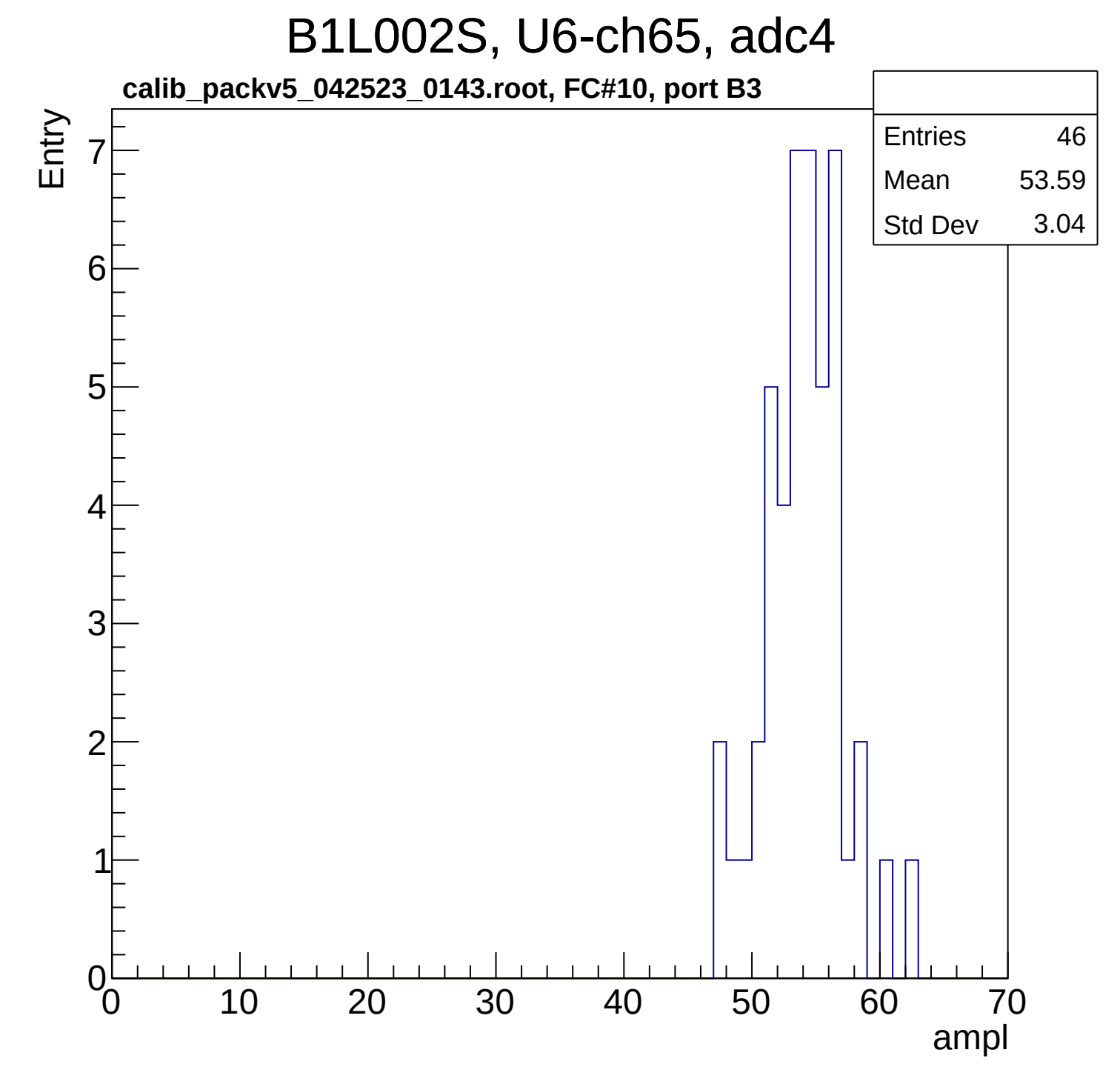

B1L002S, U6-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 26.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

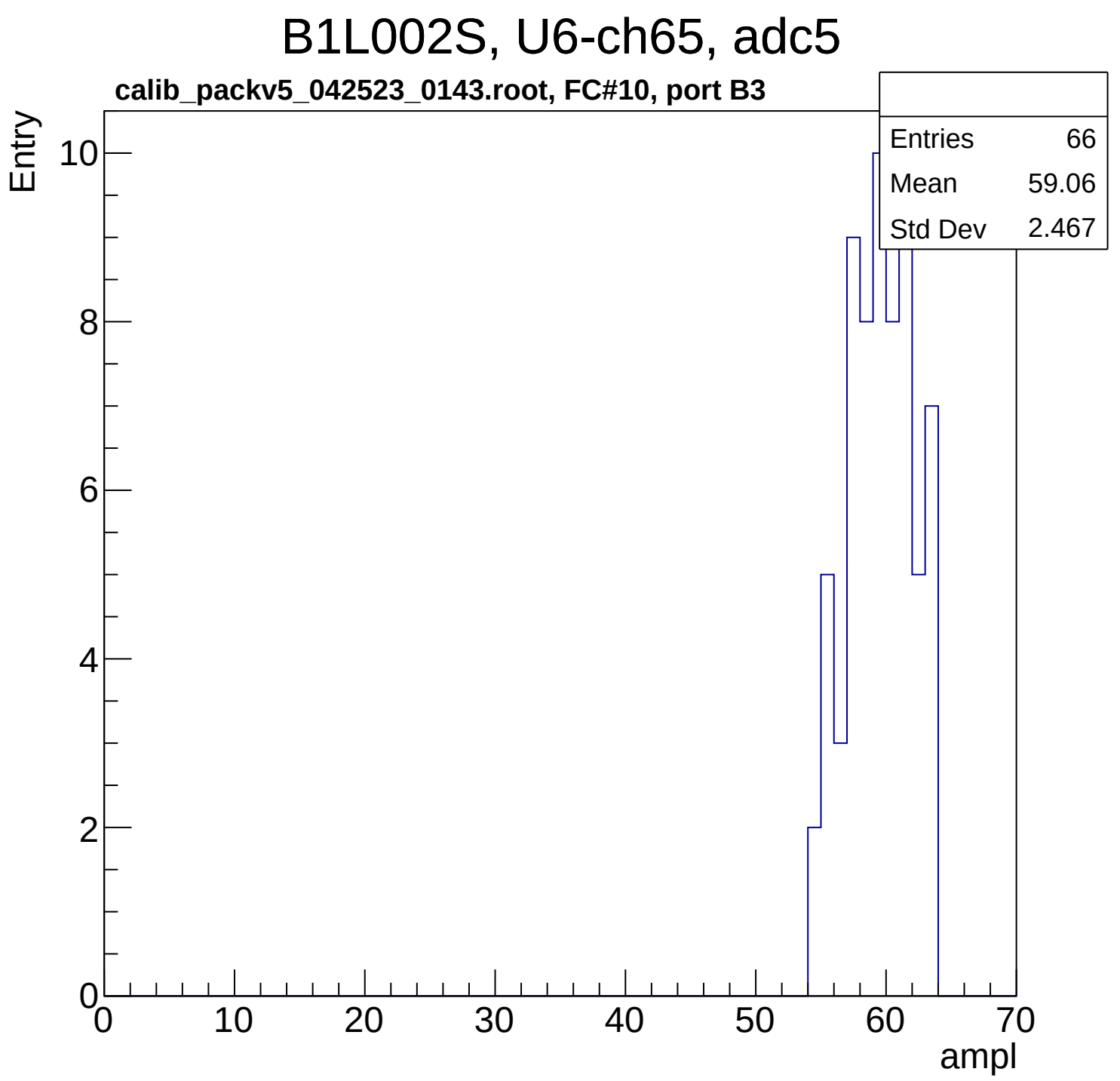

B1L002S, U6-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.17
Std Dev 3.135
Gaus mean : 28.3962
Gaus Width: 3.2299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

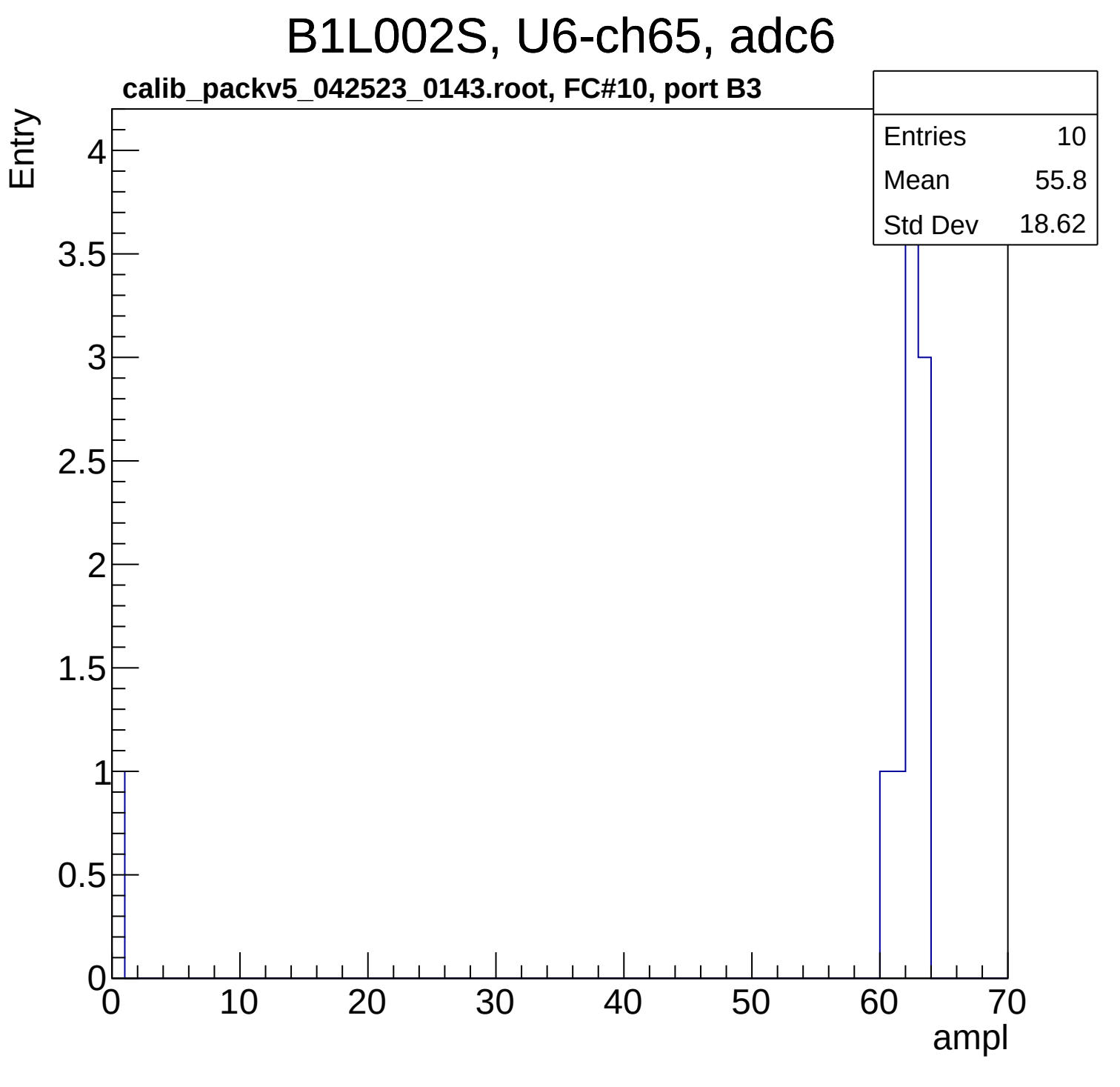

B1L002S, U6-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 35.44
Std Dev 3.47
Gaus mean : 36.4204
Gaus Width: 3.7561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

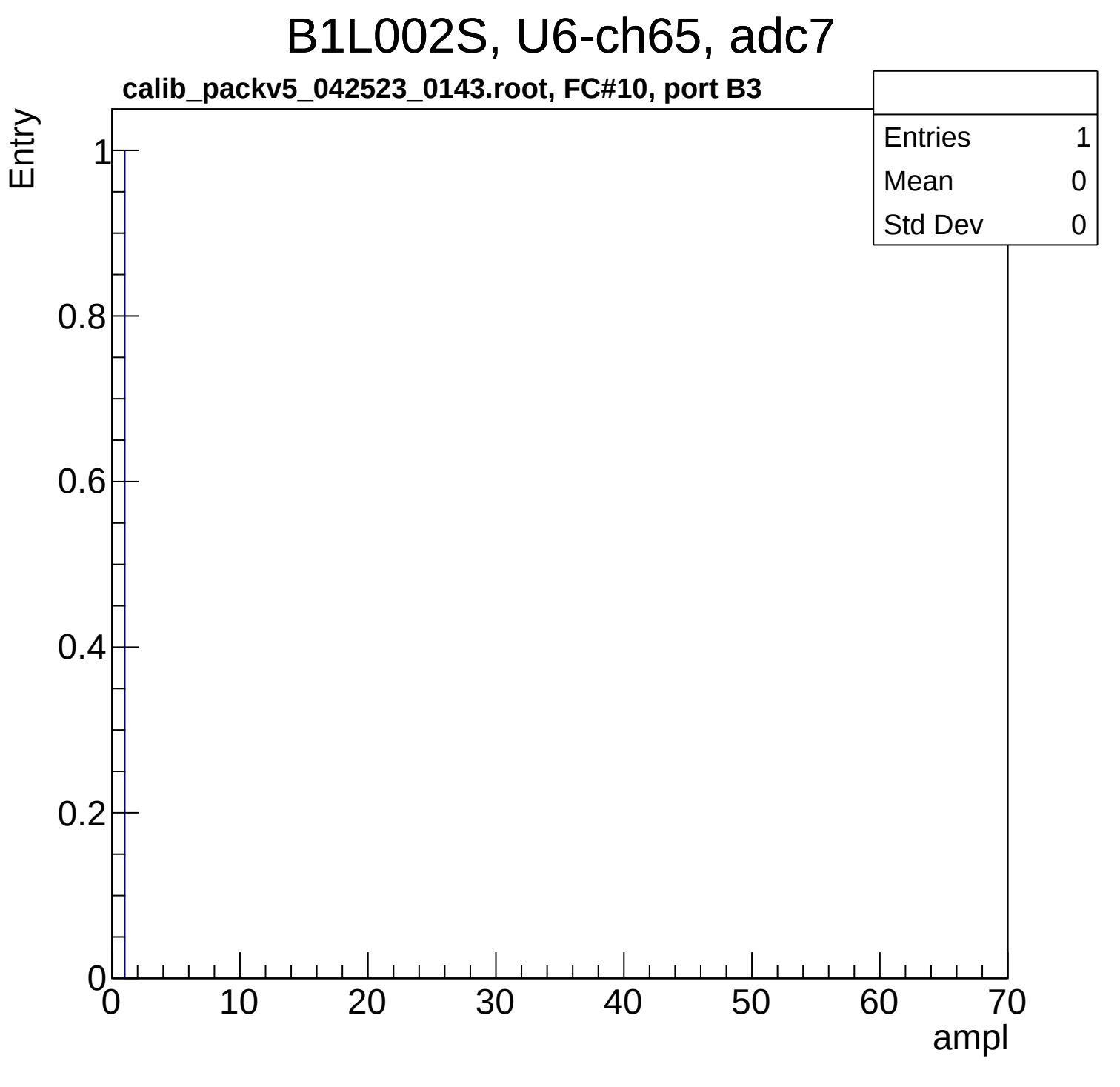

B1L002S, U6-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 41.62
Std Dev 3.105
Gaus mean : 42.9765
Gaus Width: 3.1893
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

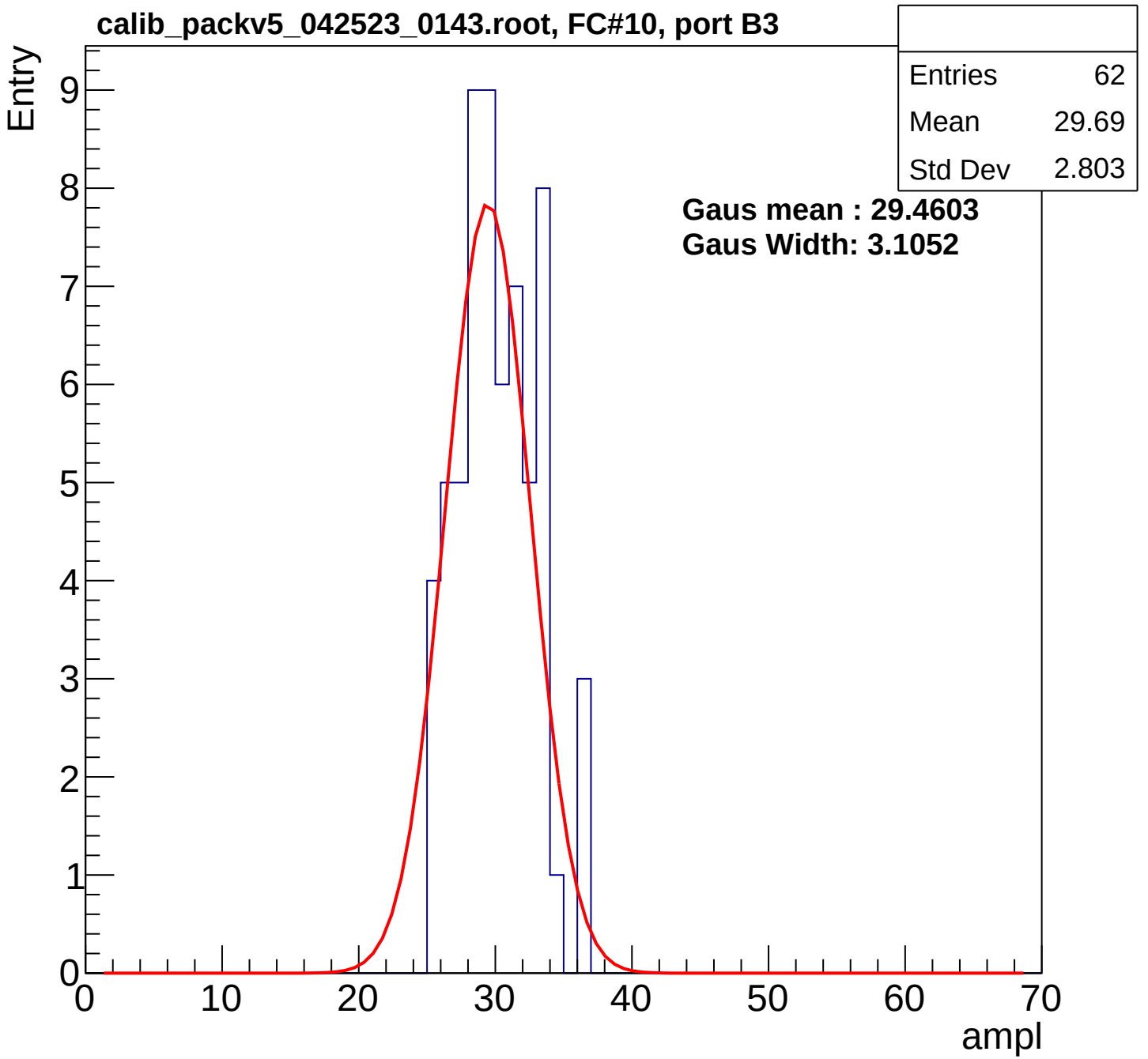

B1L002S, U6-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.61
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

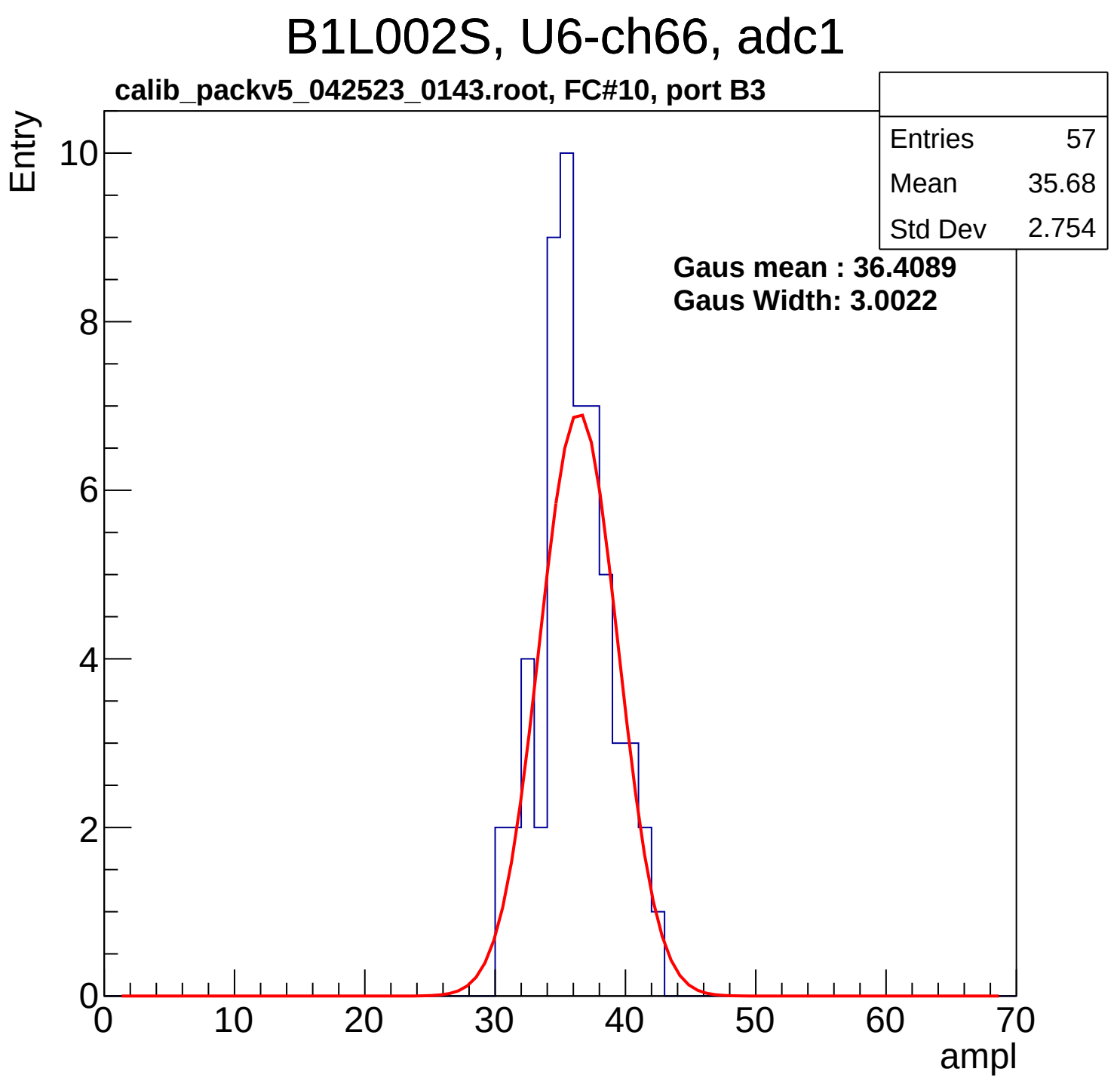

B1L002S, U6-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 53.59
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

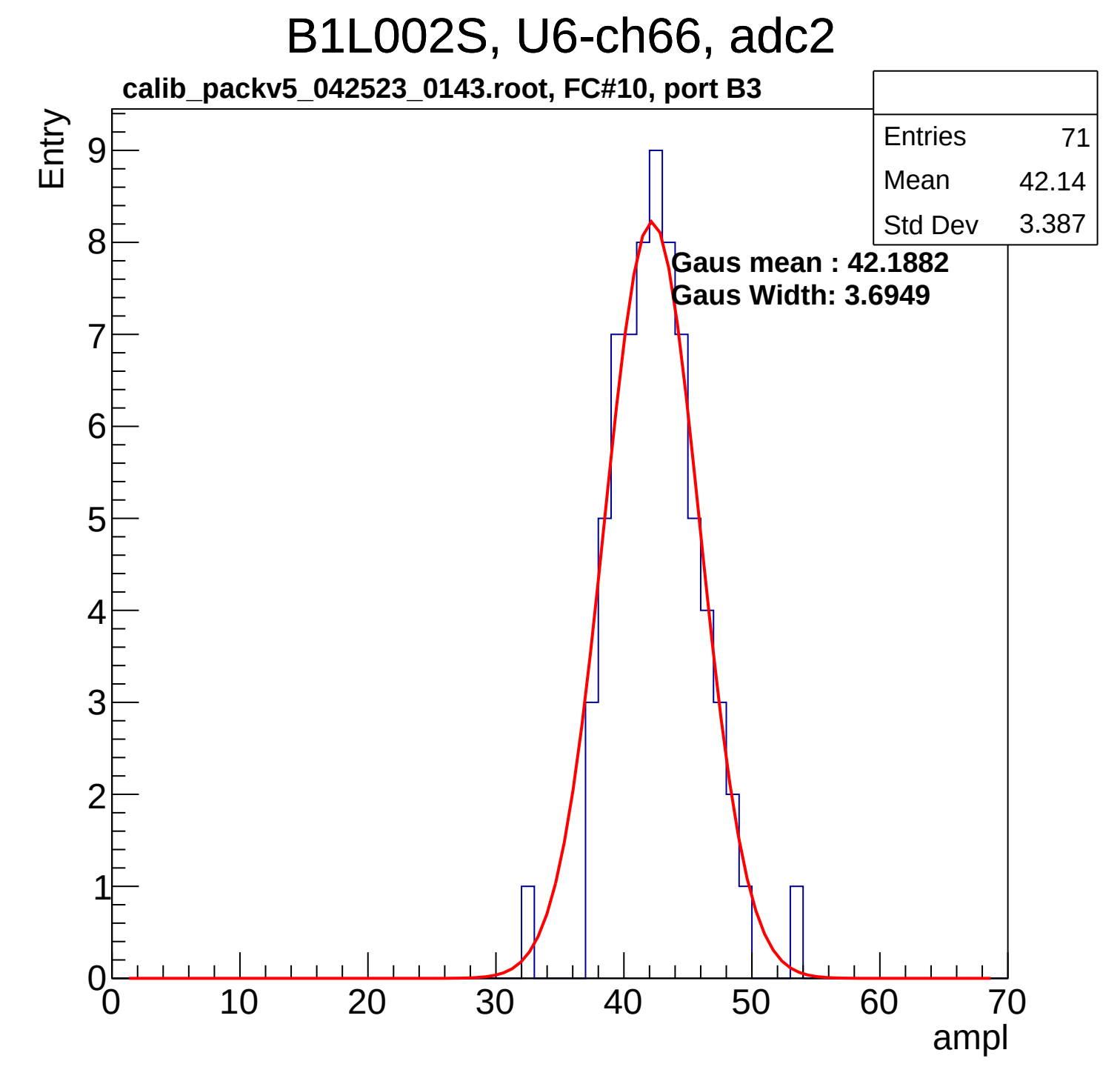

B1L002S, U6-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 59.06
Std Dev 2.467
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

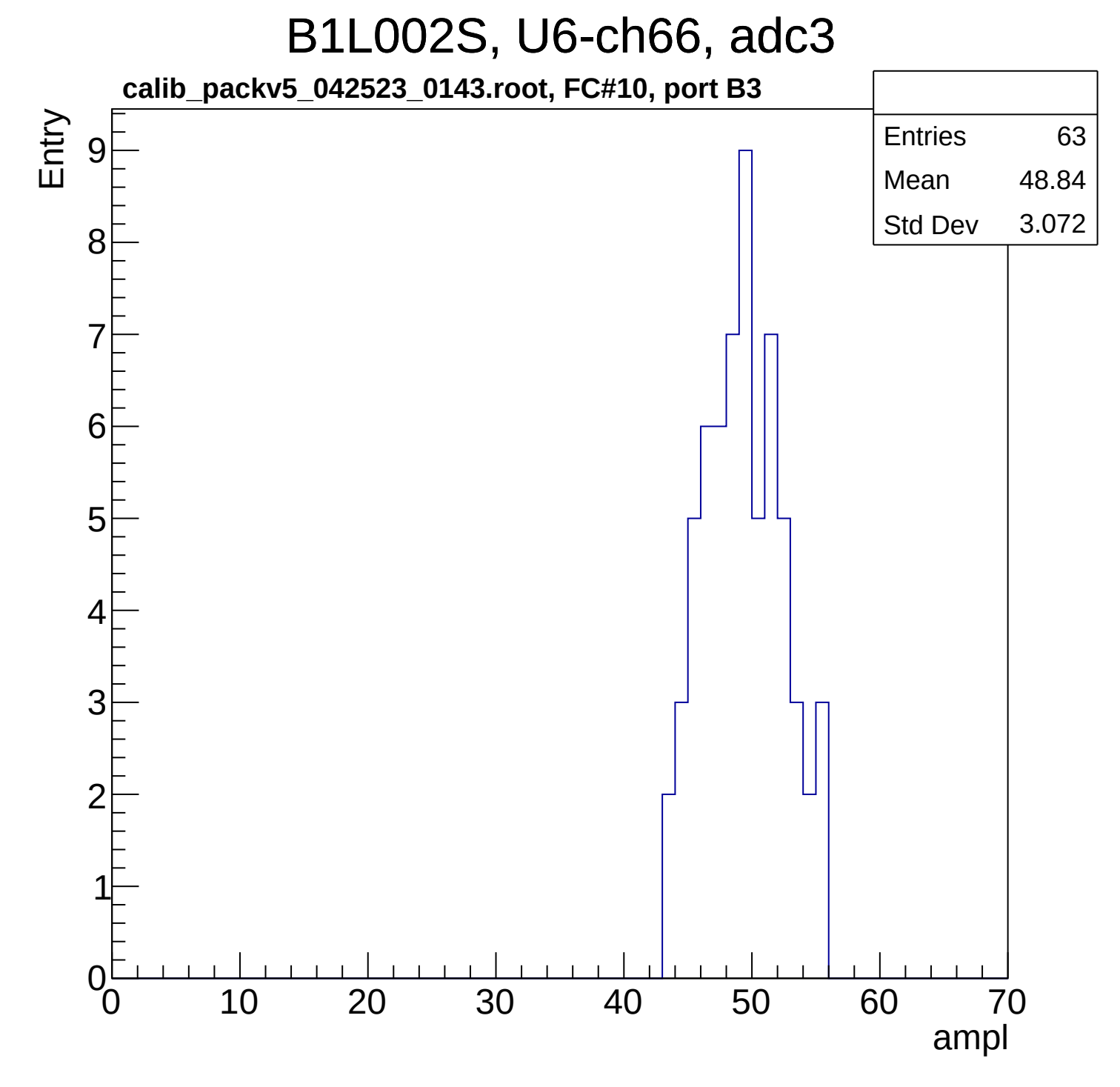

B1L002S, U6-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

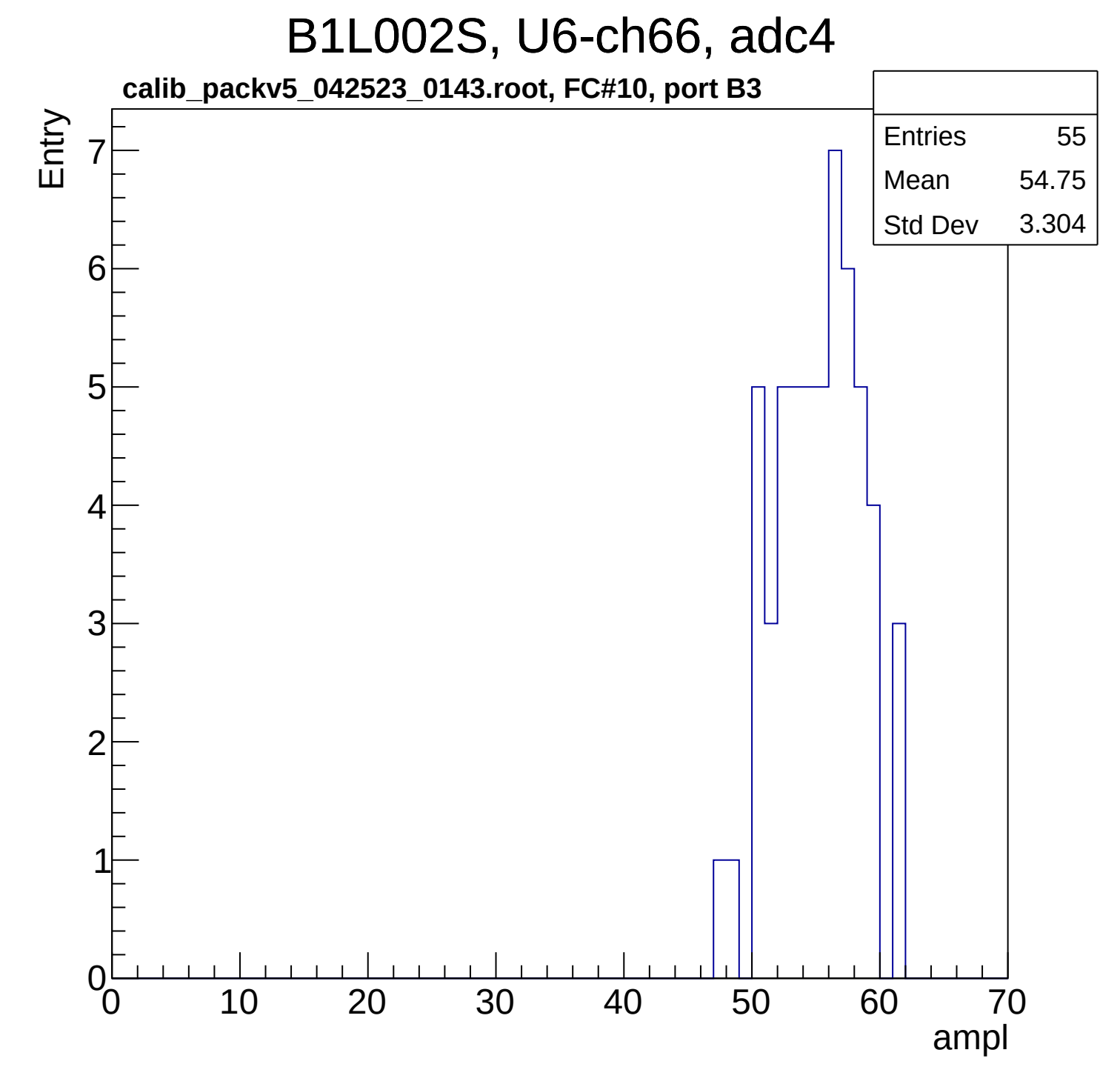

B1L002S, U6-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

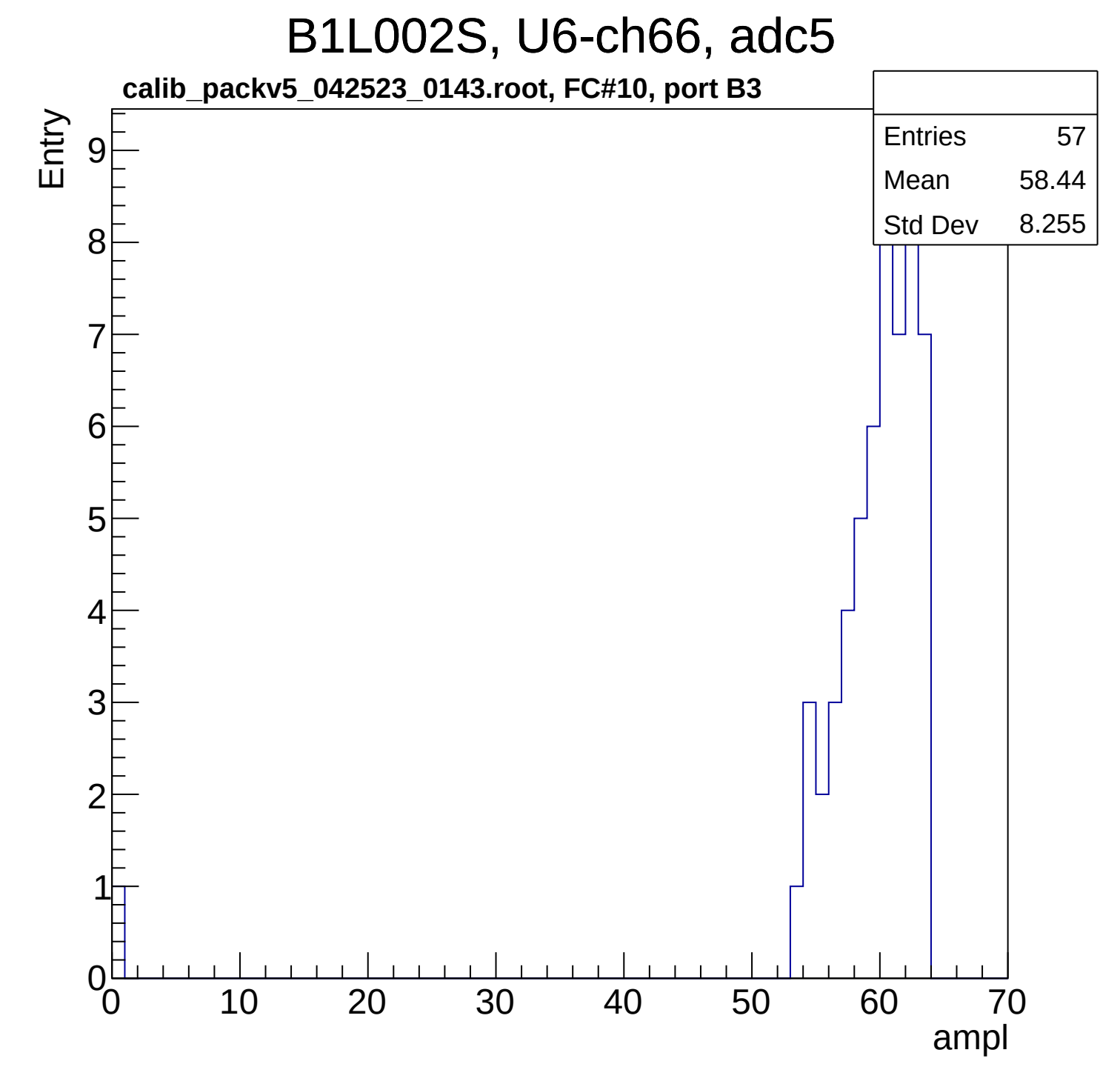

B1L002S, U6-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 29.69
Std Dev 2.803
Gaus mean : 29.4603
Gaus Width: 3.1052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

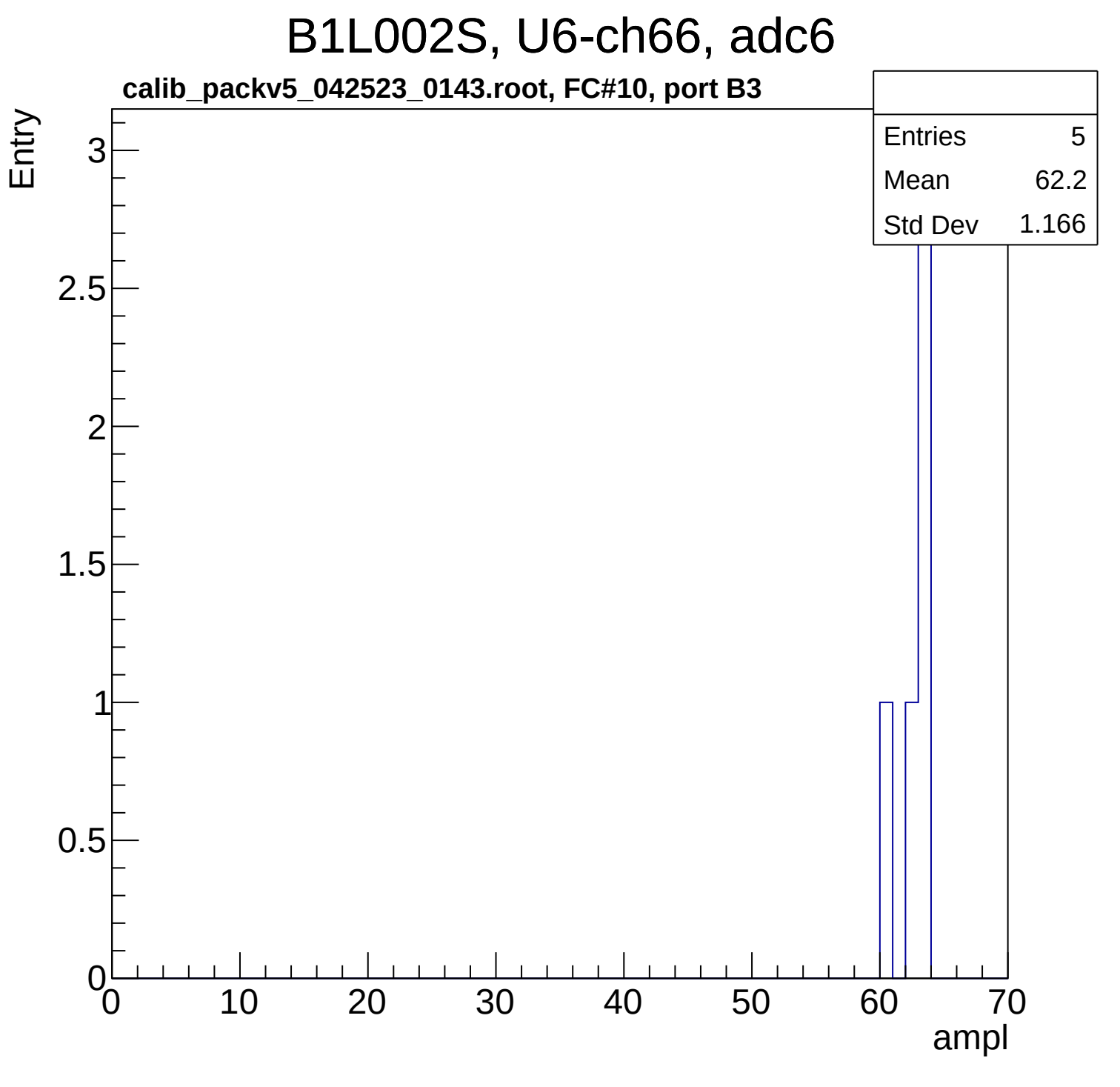

B1L002S, U6-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.68
Std Dev 2.754
Gaus mean : 36.4089
Gaus Width: 3.0022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

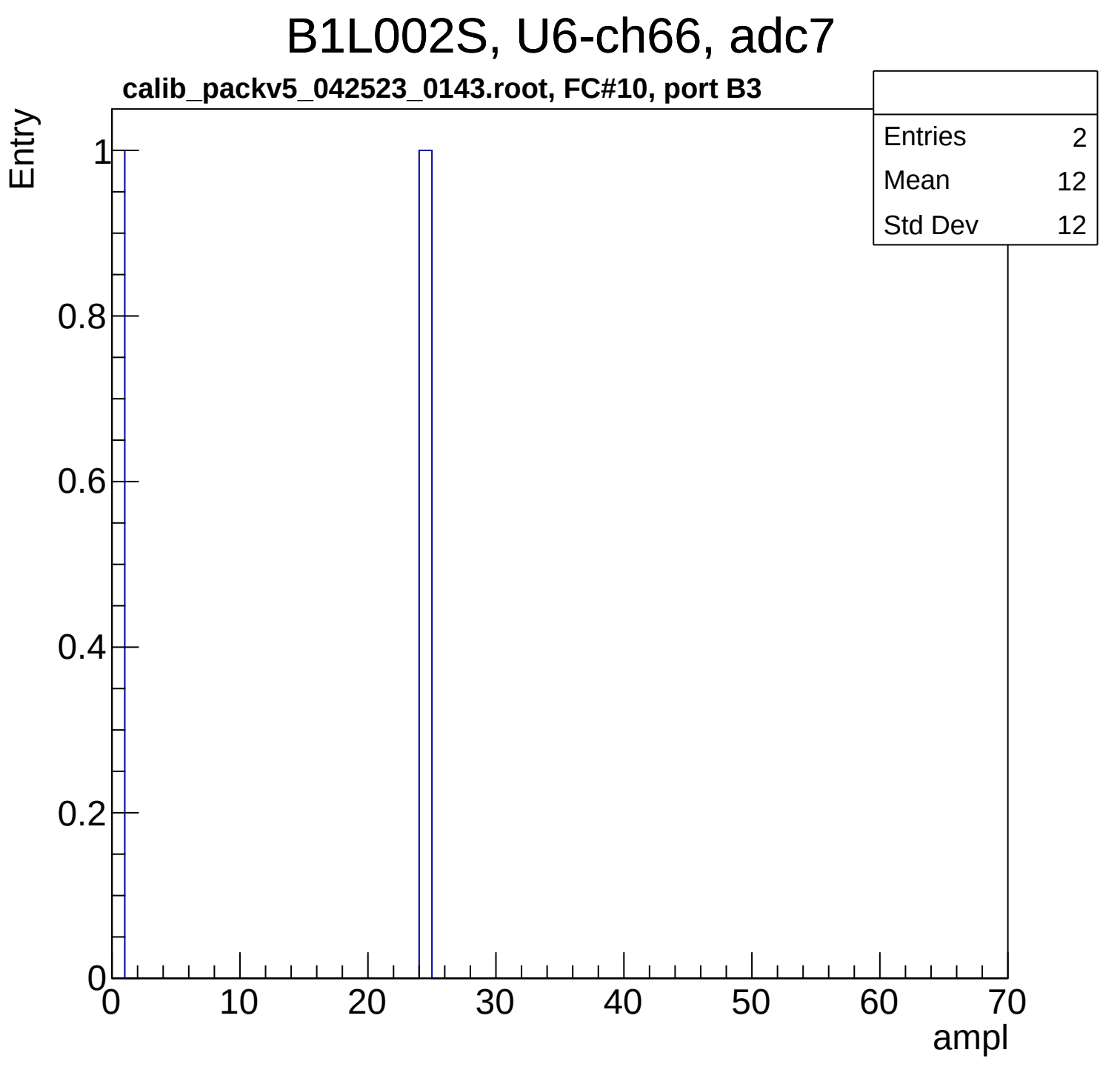

B1L002S, U6-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 42.14
Std Dev 3.387
Gaus mean : 42.1882
Gaus Width: 3.6949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

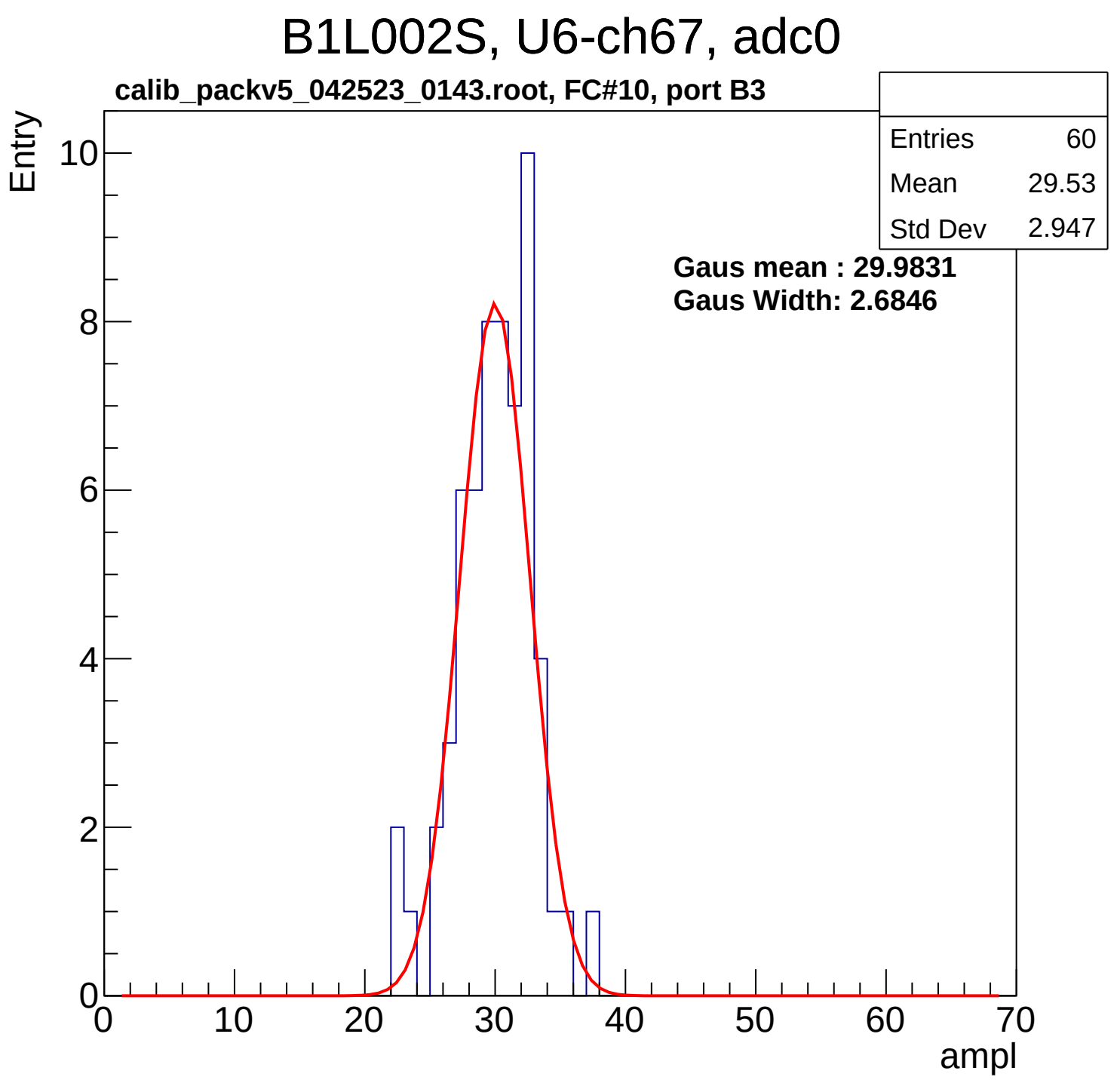

B1L002S, U6-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.84
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

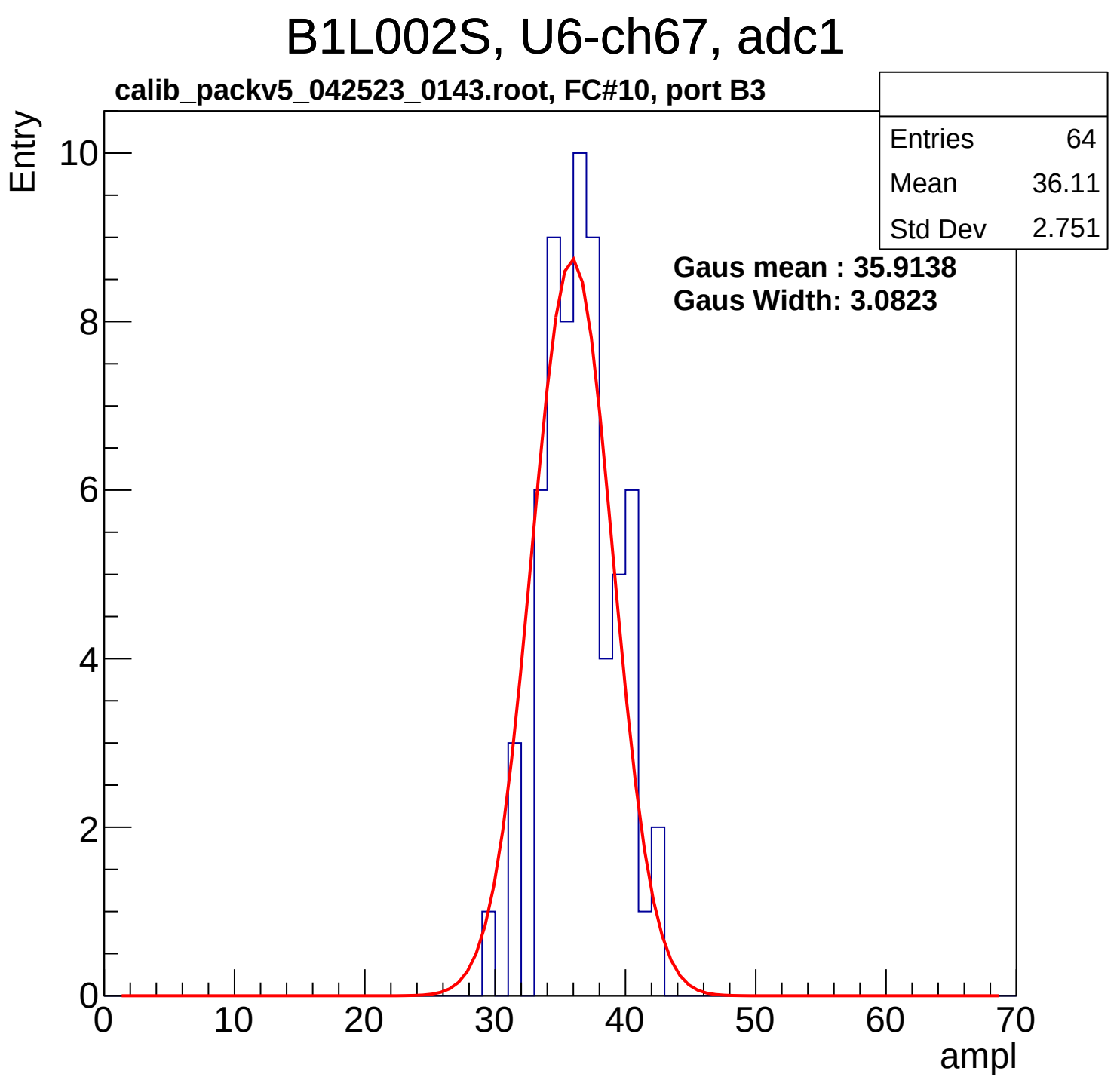

B1L002S, U6-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.75
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

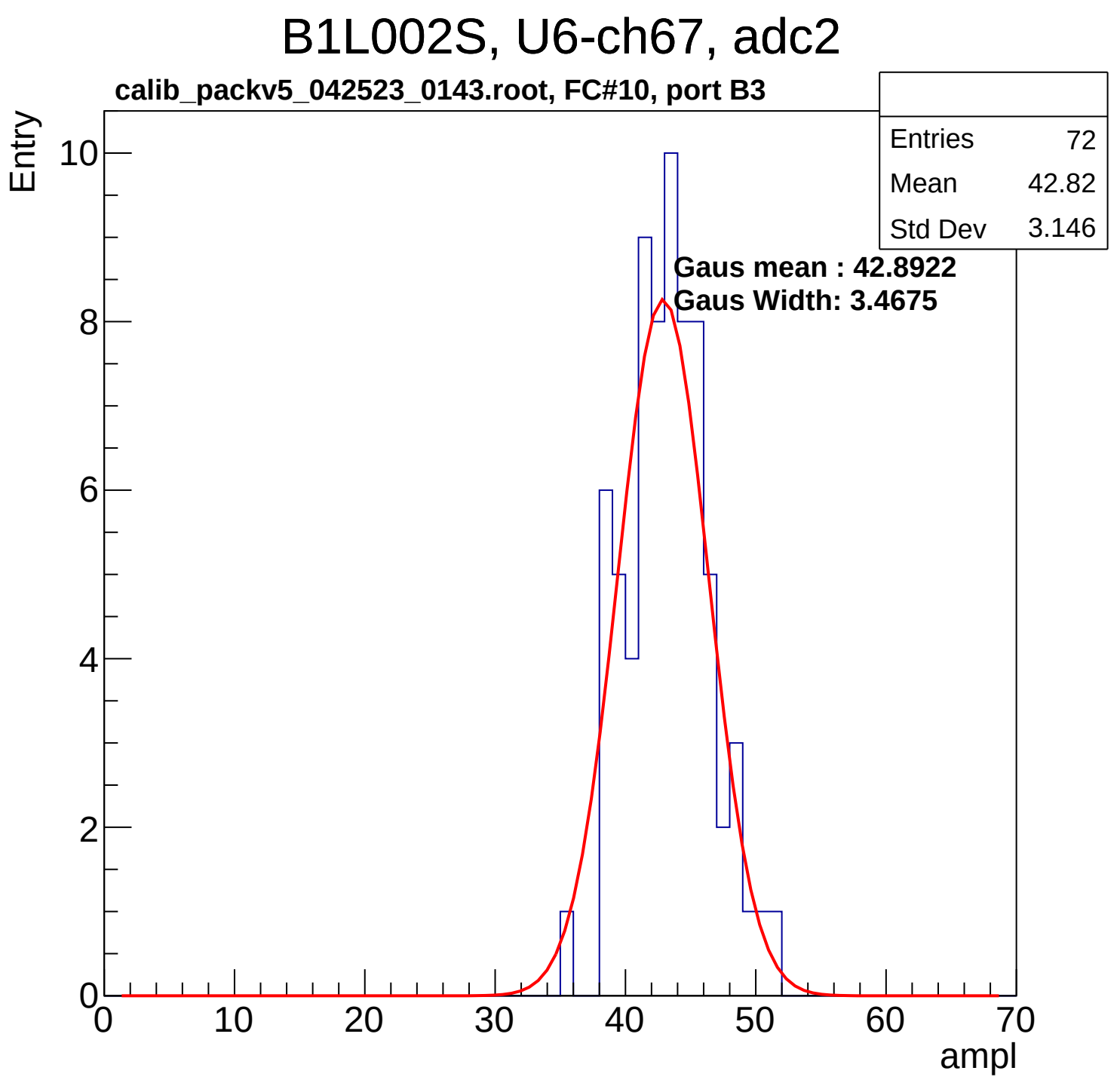

B1L002S, U6-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.44
Std Dev 8.255
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

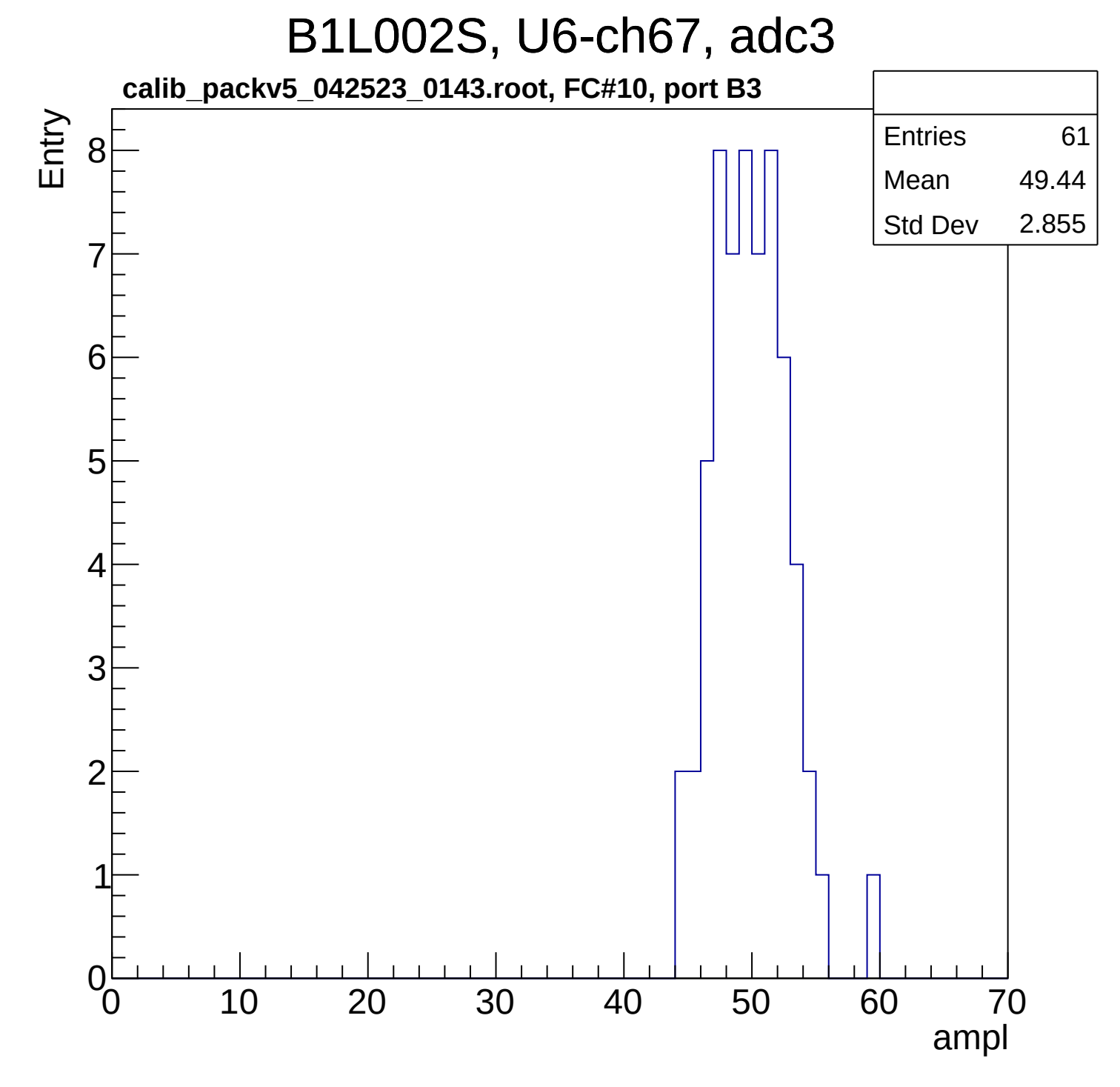

B1L002S, U6-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

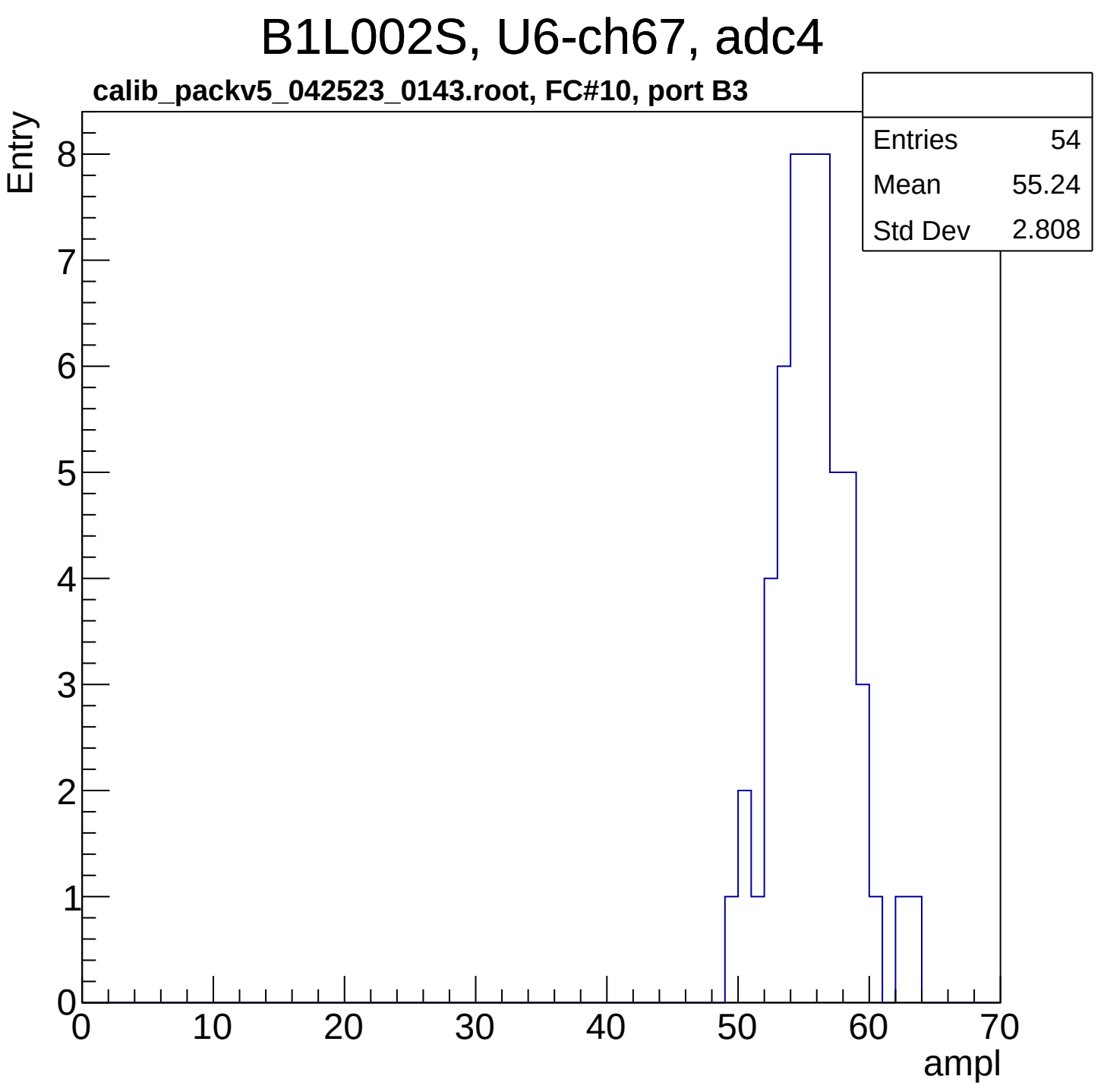

B1L002S, U6-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

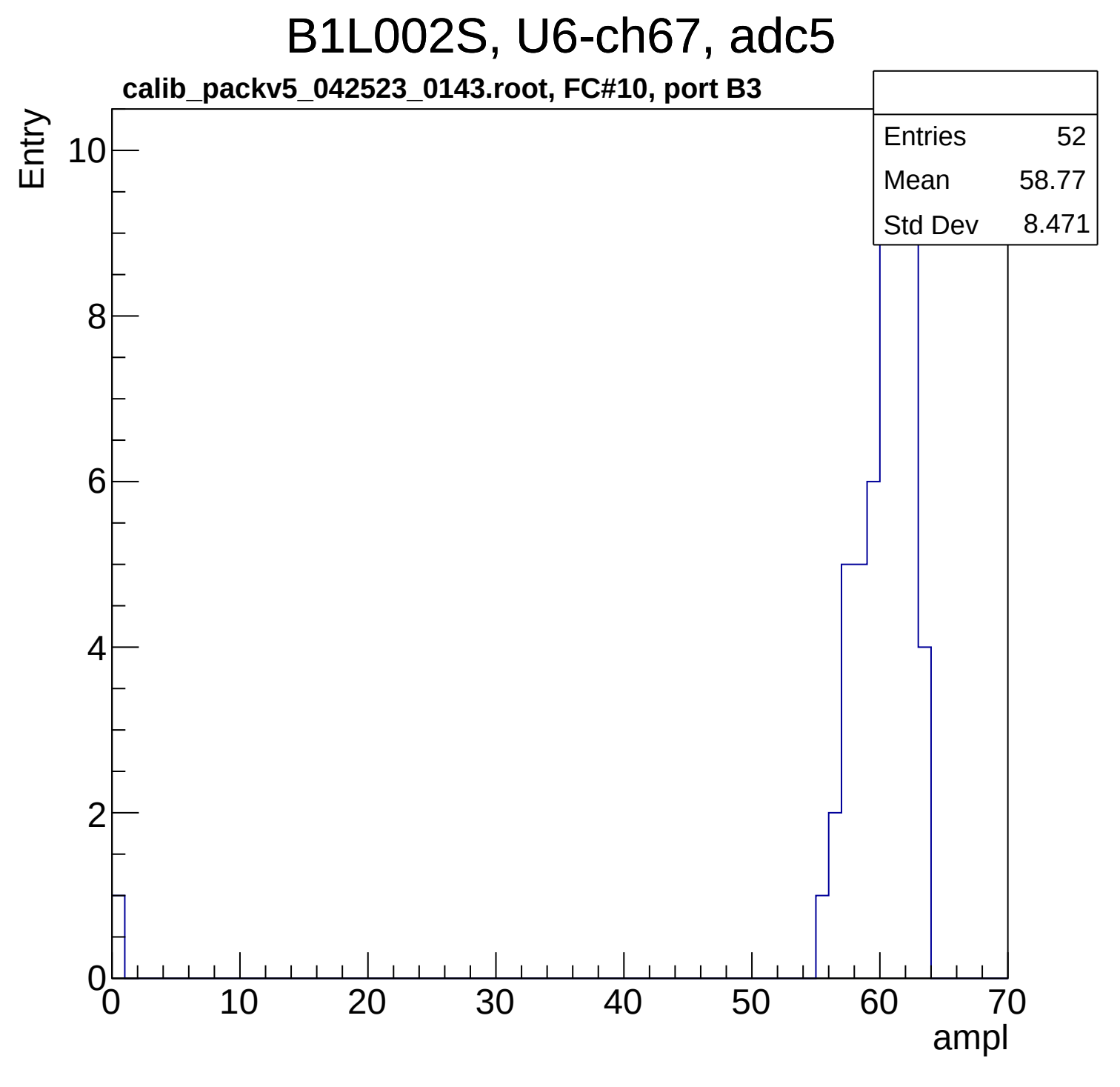

B1L002S, U6-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.53
Std Dev 2.947
Gaus mean : 29.9831
Gaus Width: 2.6846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

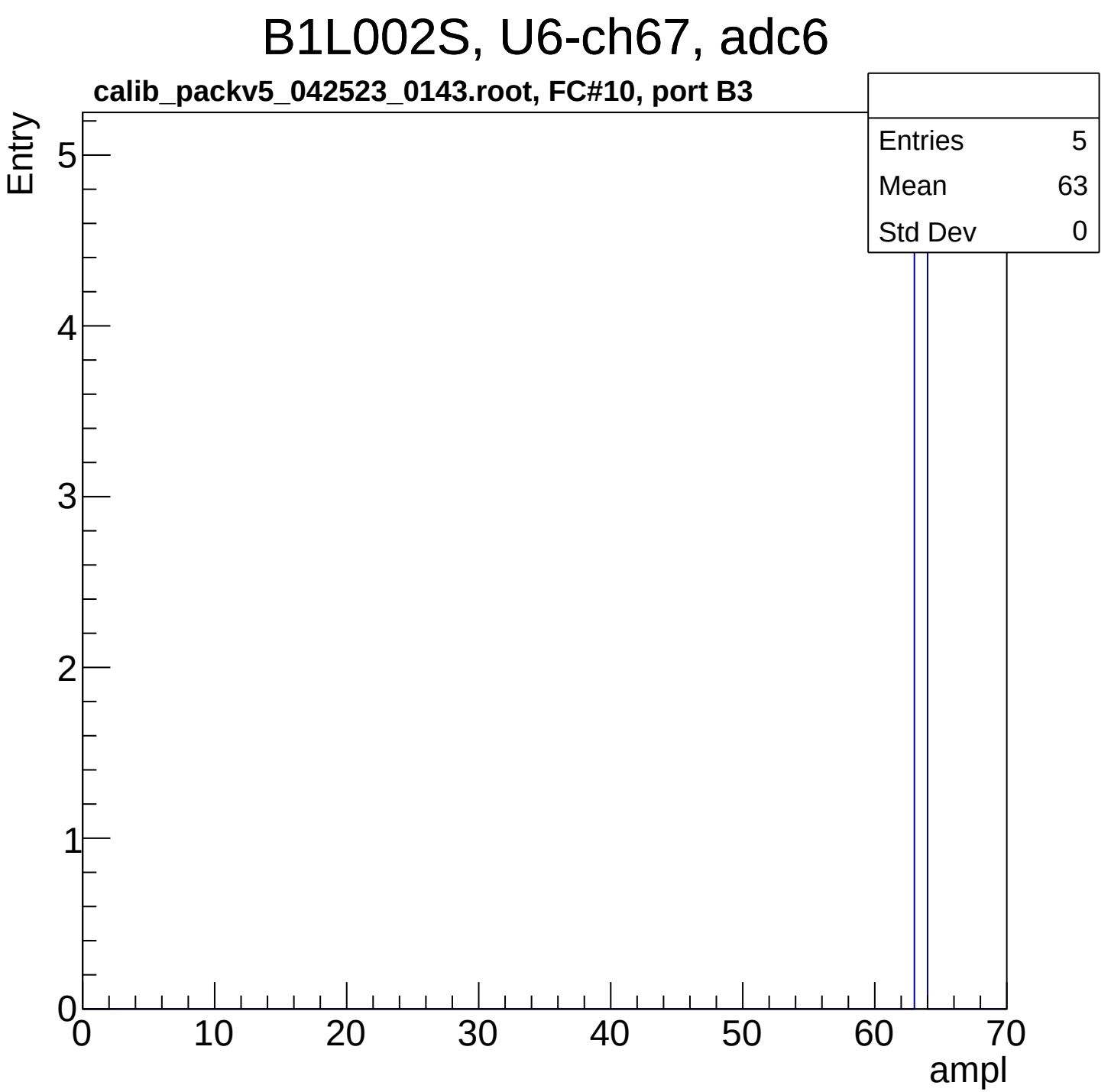

B1L002S, U6-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 36.11
Std Dev 2.751
Gaus mean : 35.9138
Gaus Width: 3.0823
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

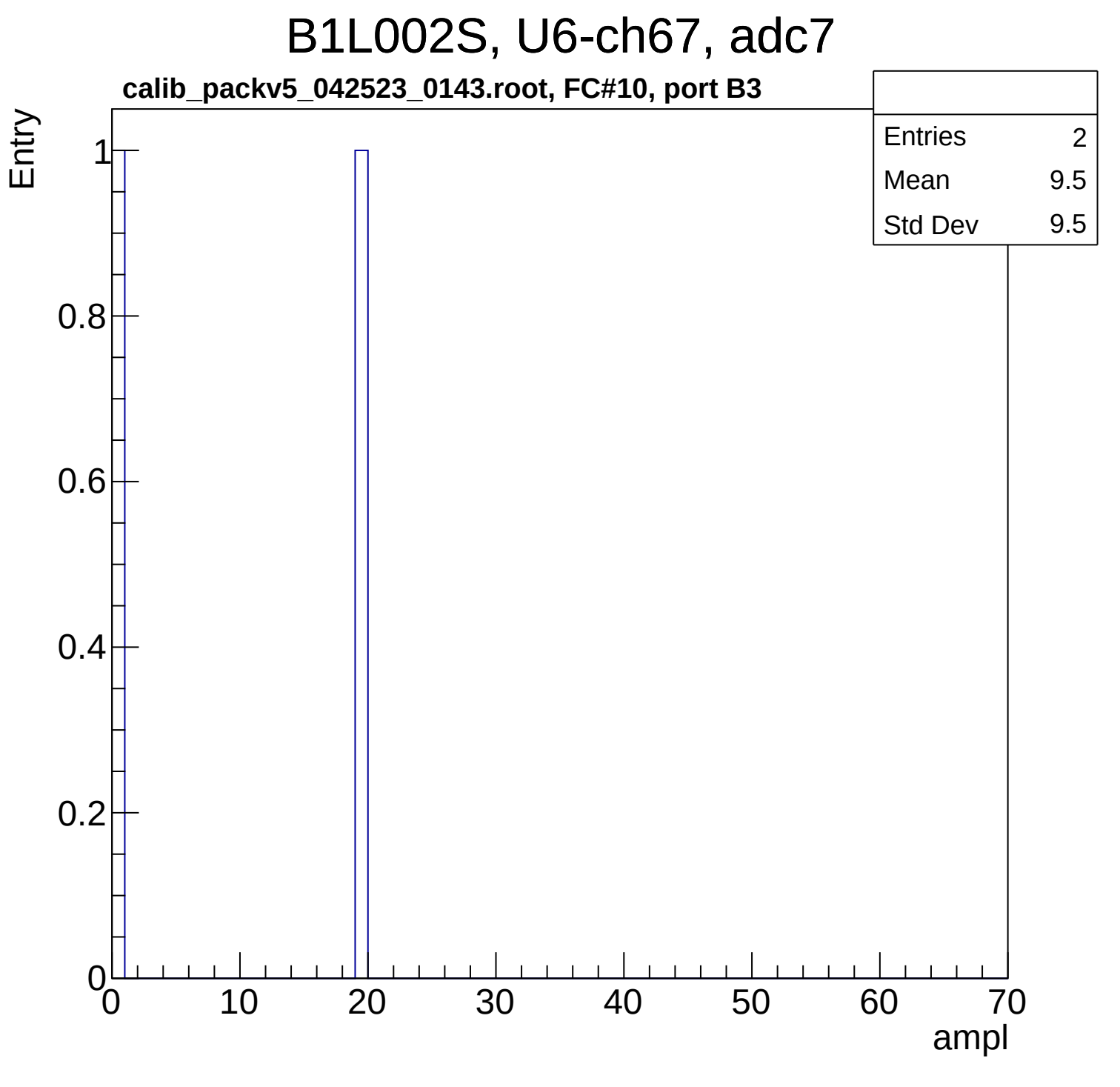

B1L002S, U6-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 42.82
Std Dev 3.146
Gaus mean : 42.8922
Gaus Width: 3.4675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

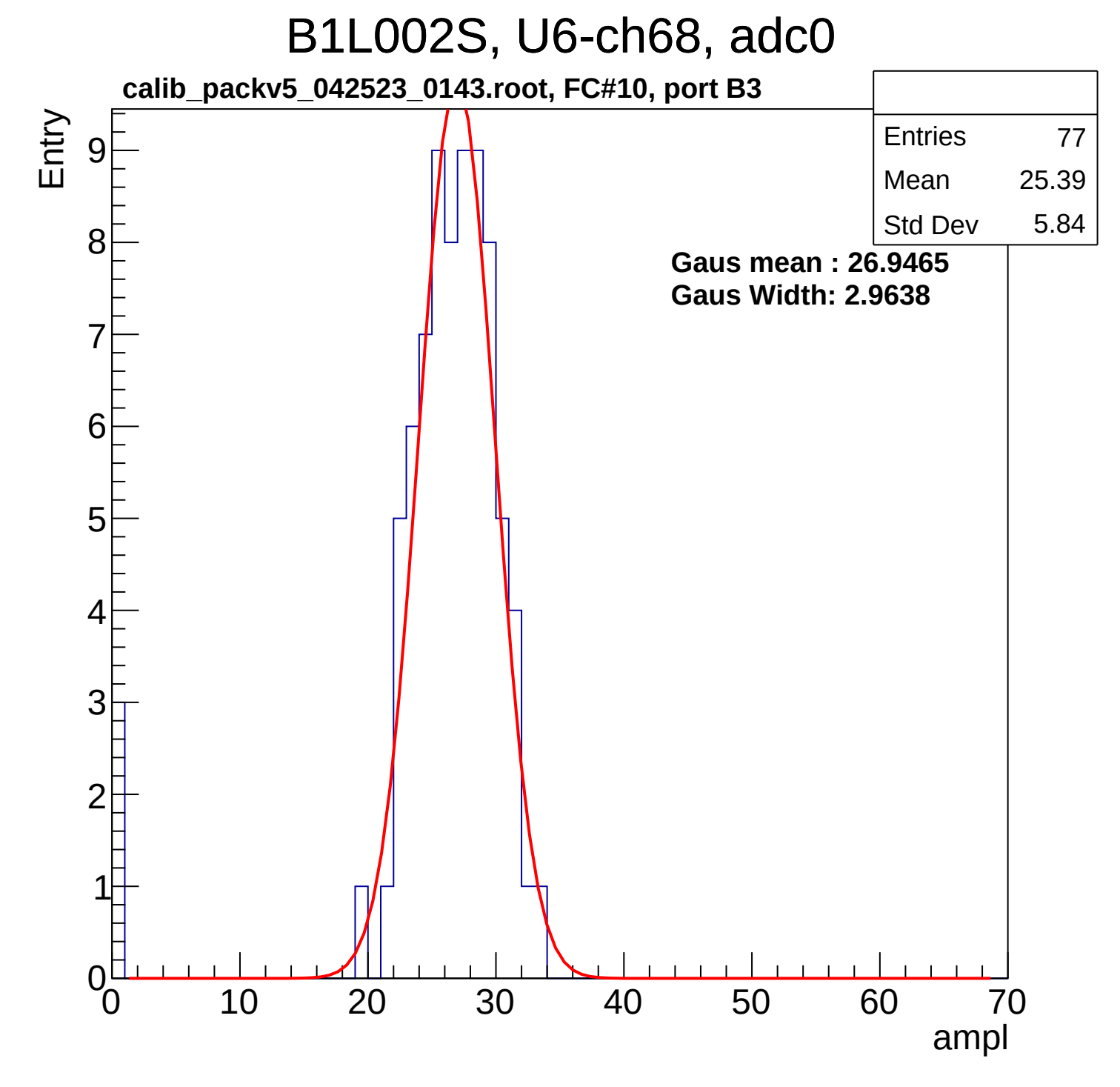

B1L002S, U6-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 49.44
Std Dev 2.855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

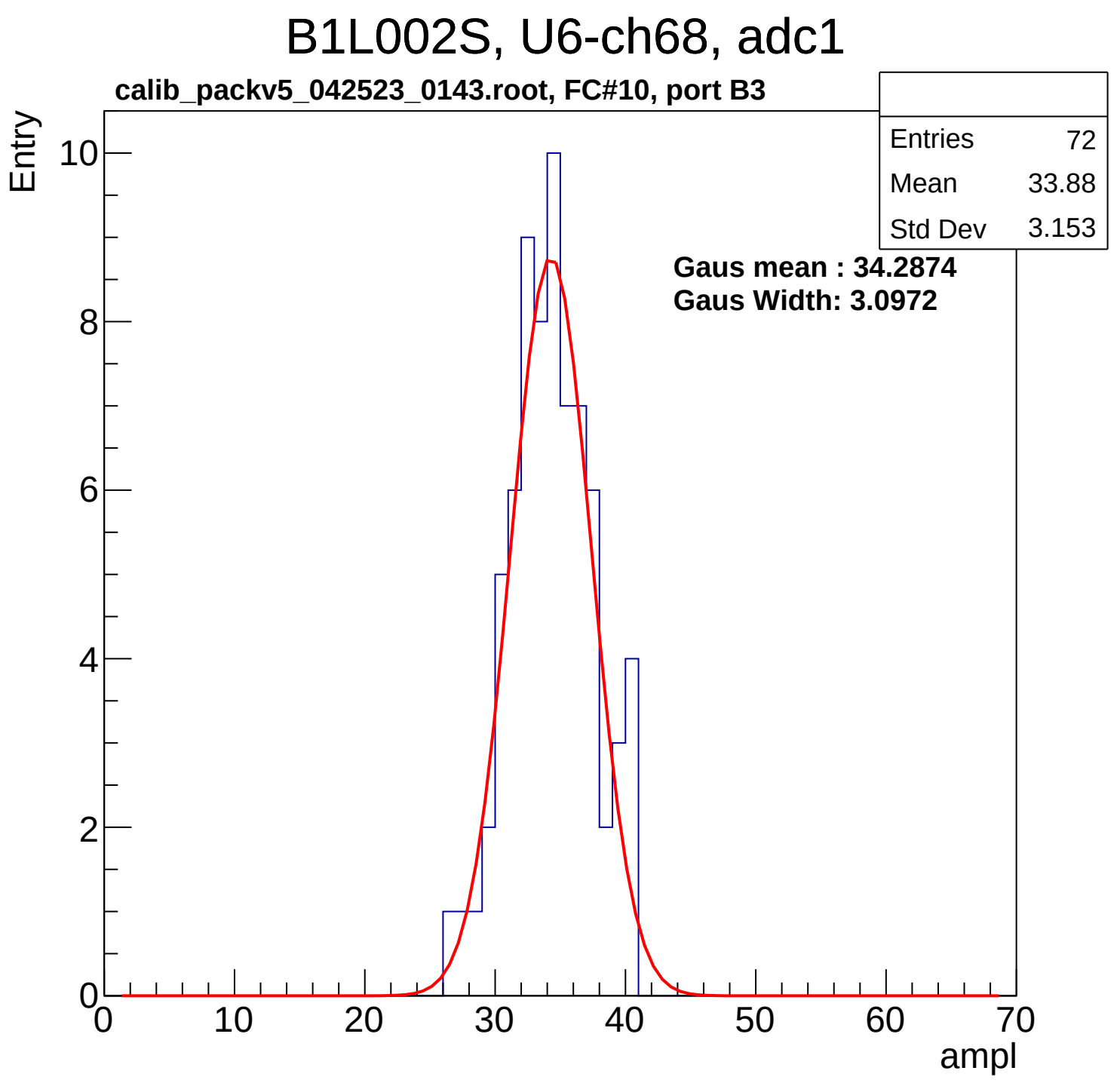

B1L002S, U6-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 55.24
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

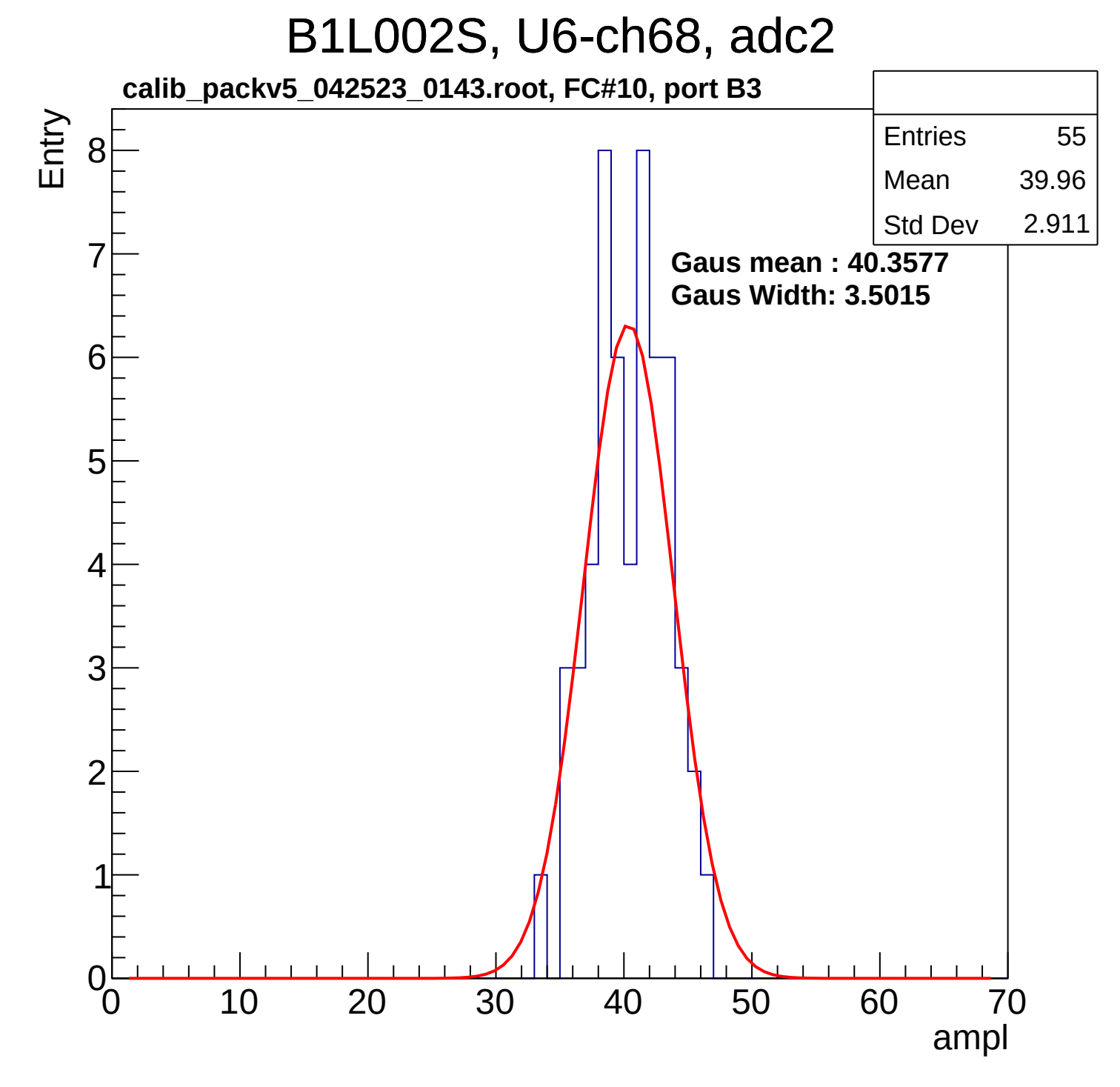

B1L002S, U6-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.77
Std Dev 8.471
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

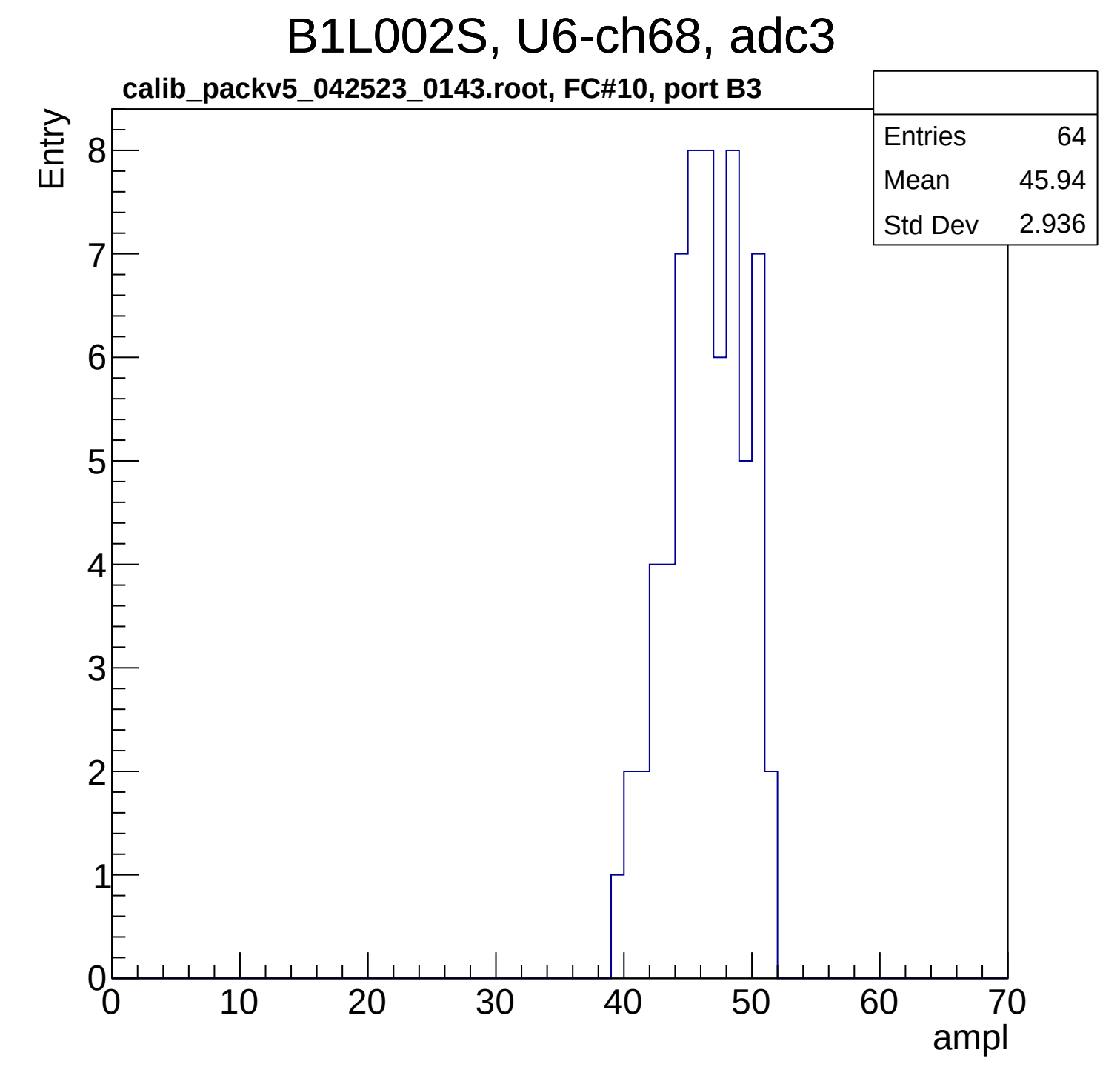

B1L002S, U6-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 63
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

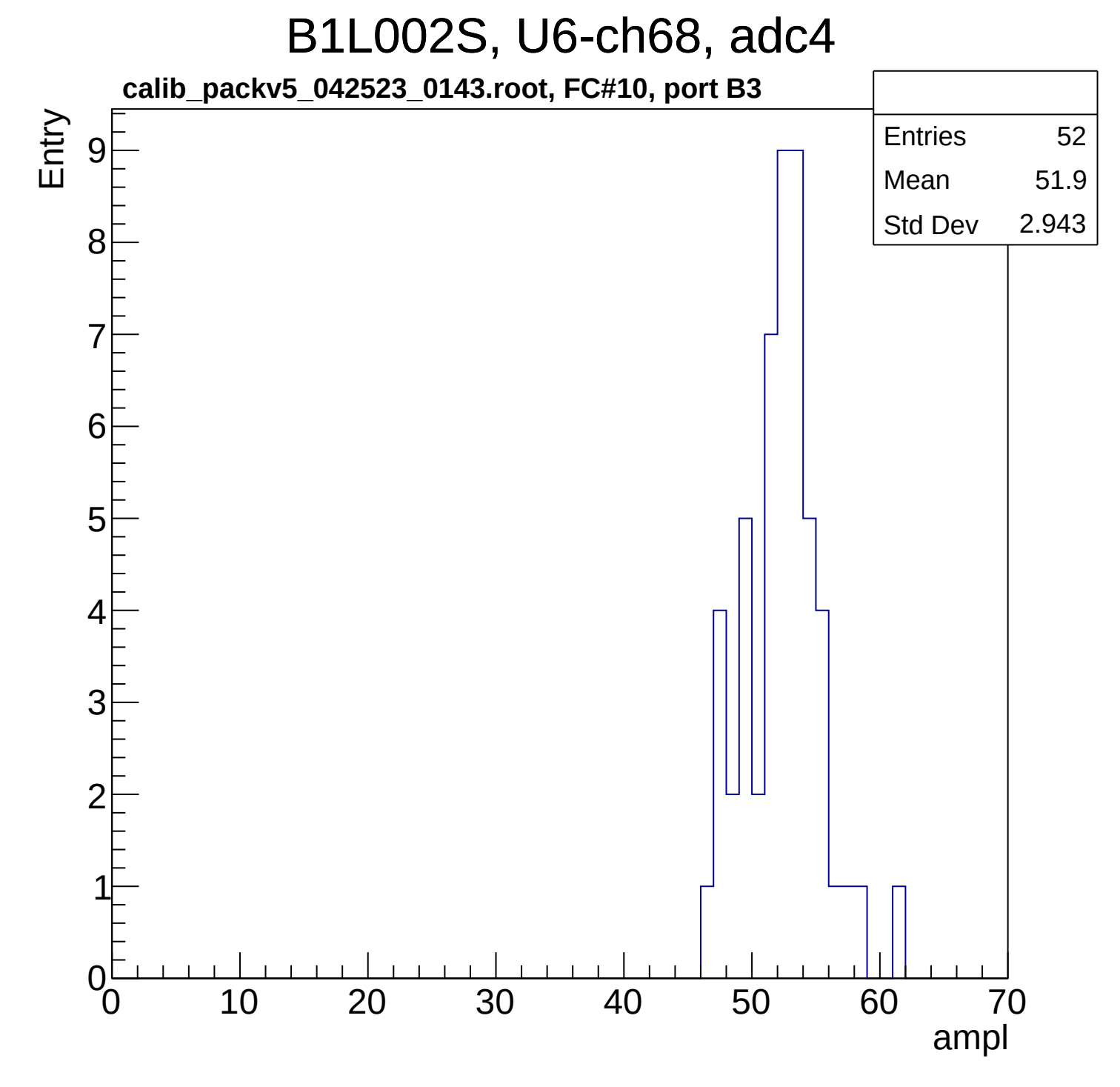

B1L002S, U6-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

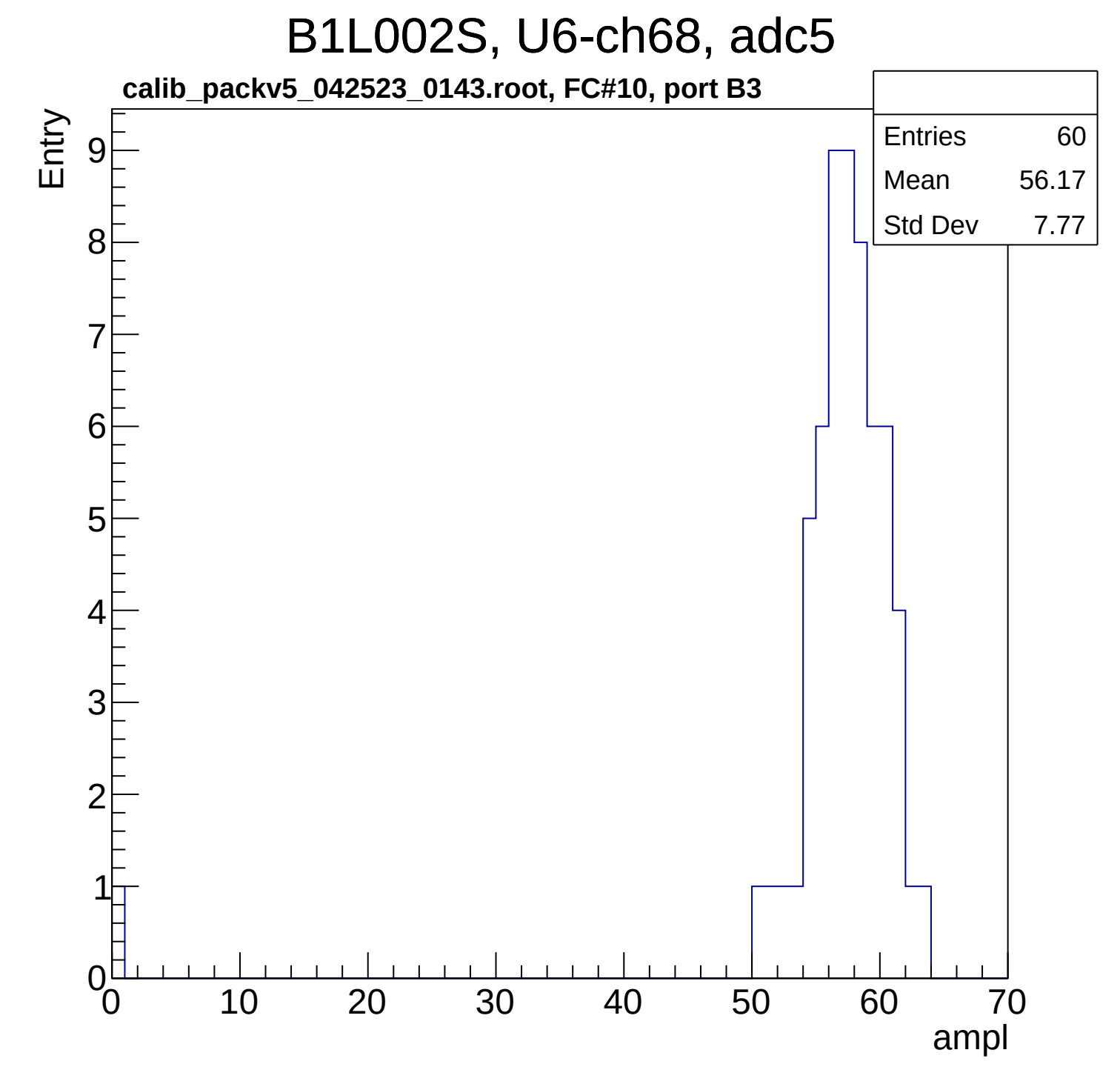

B1L002S, U6-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 25.39
Std Dev 5.84
Gaus mean : 26.9465
Gaus Width: 2.9638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

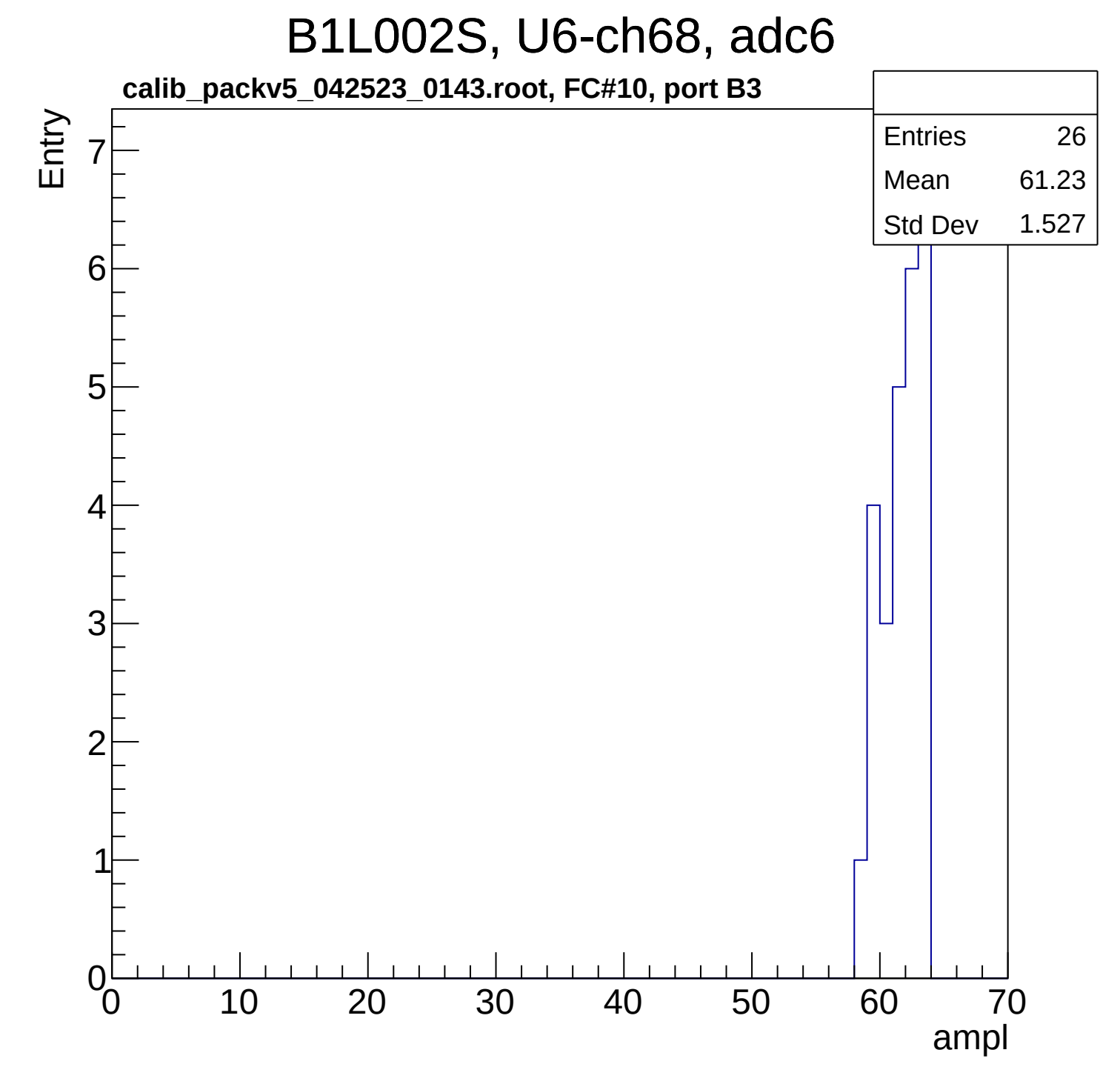

B1L002S, U6-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.88
Std Dev 3.153
Gaus mean : 34.2874
Gaus Width: 3.0972
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

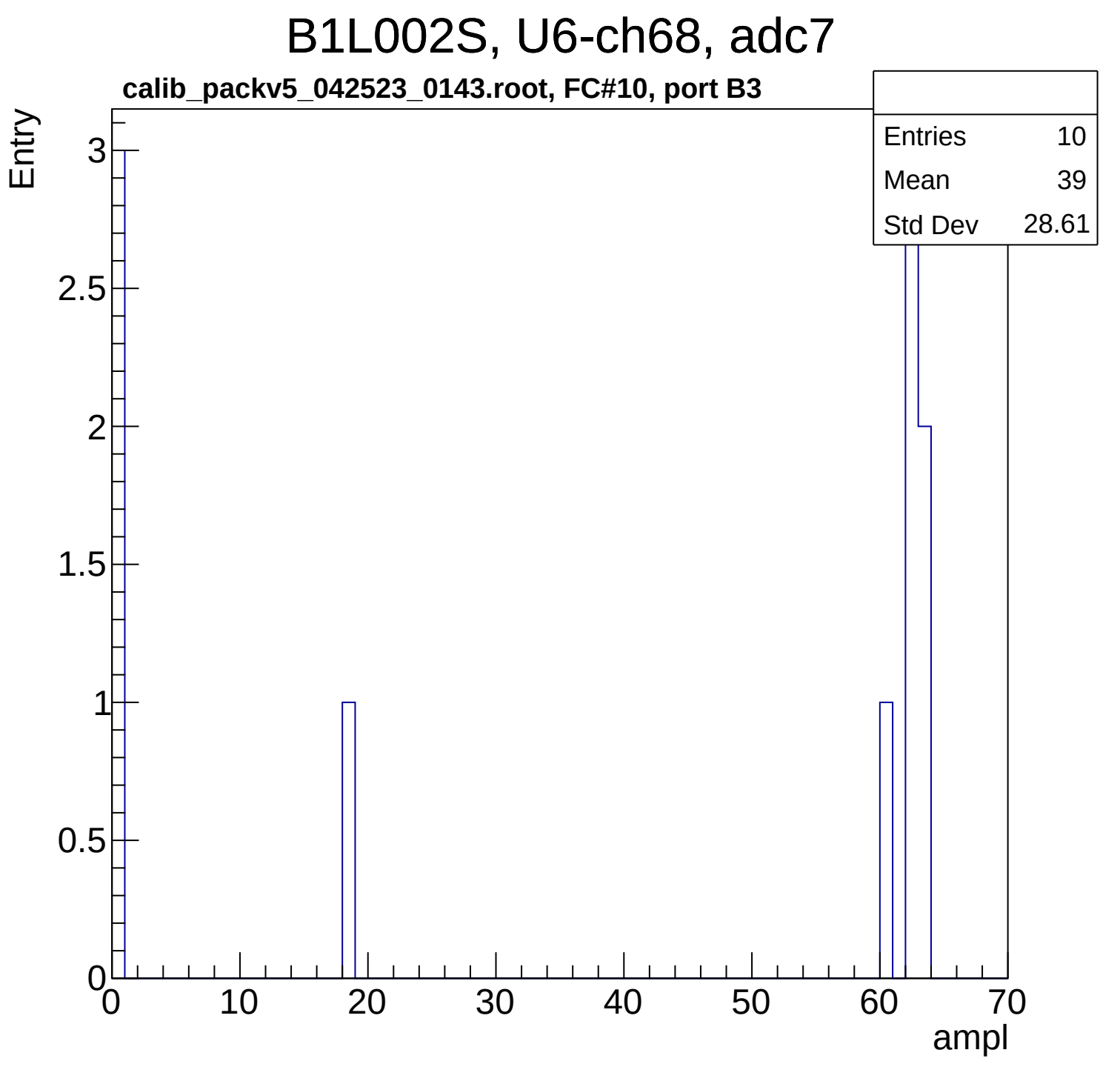

B1L002S, U6-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 39.96
Std Dev 2.911
Gaus mean : 40.3577
Gaus Width: 3.5015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

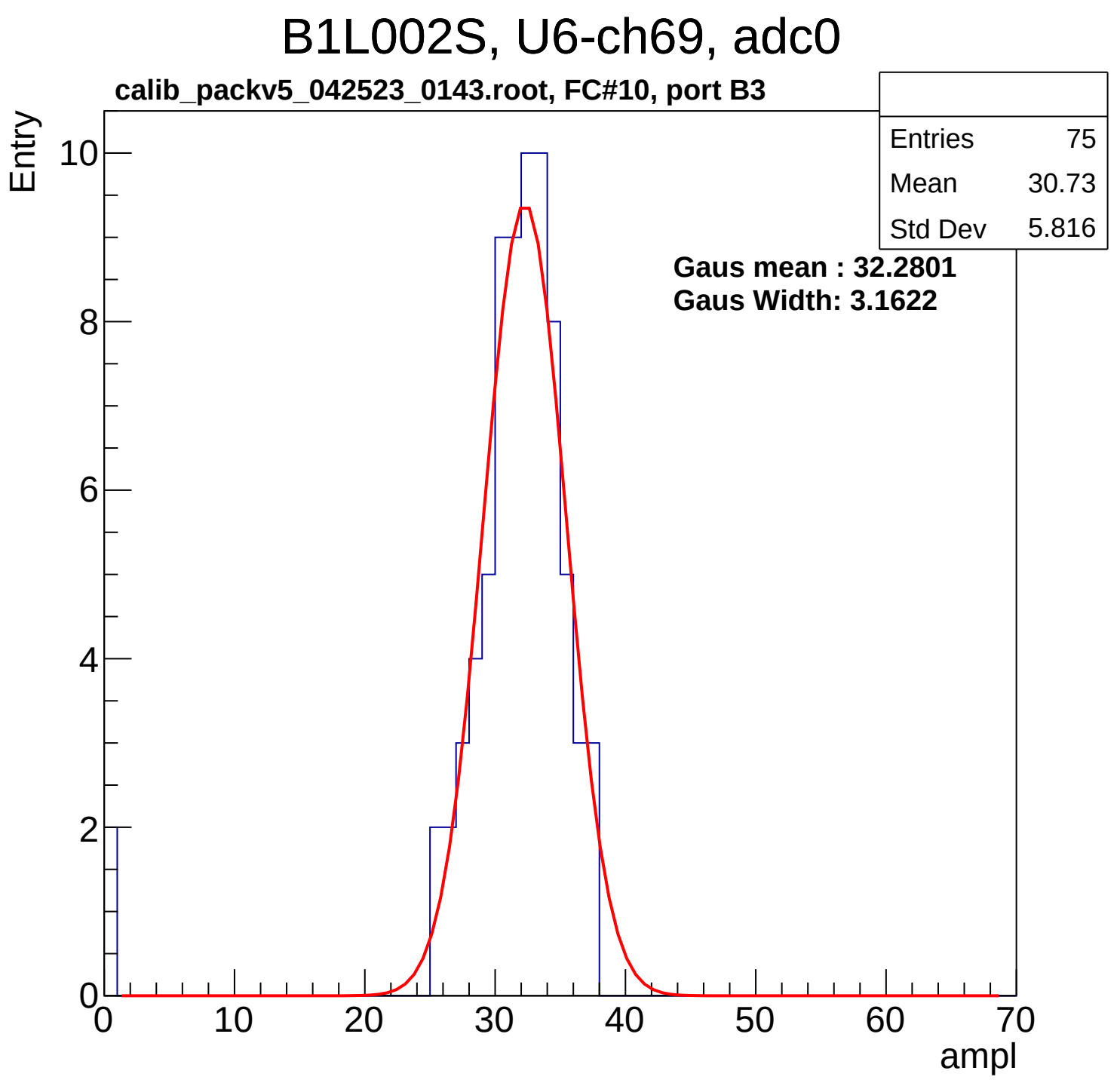

B1L002S, U6-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.94
Std Dev 2.936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

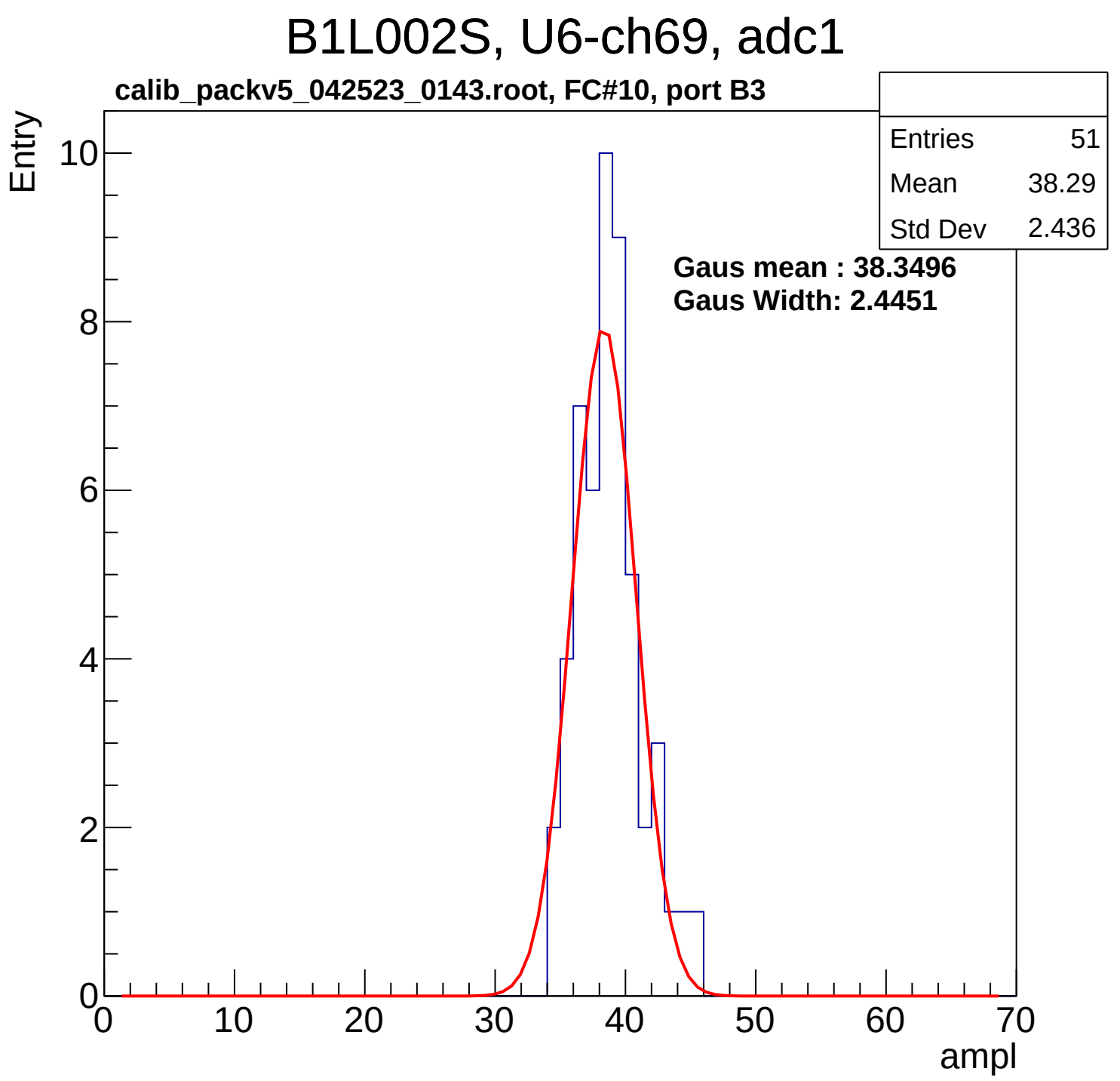

B1L002S, U6-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.9
Std Dev 2.943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

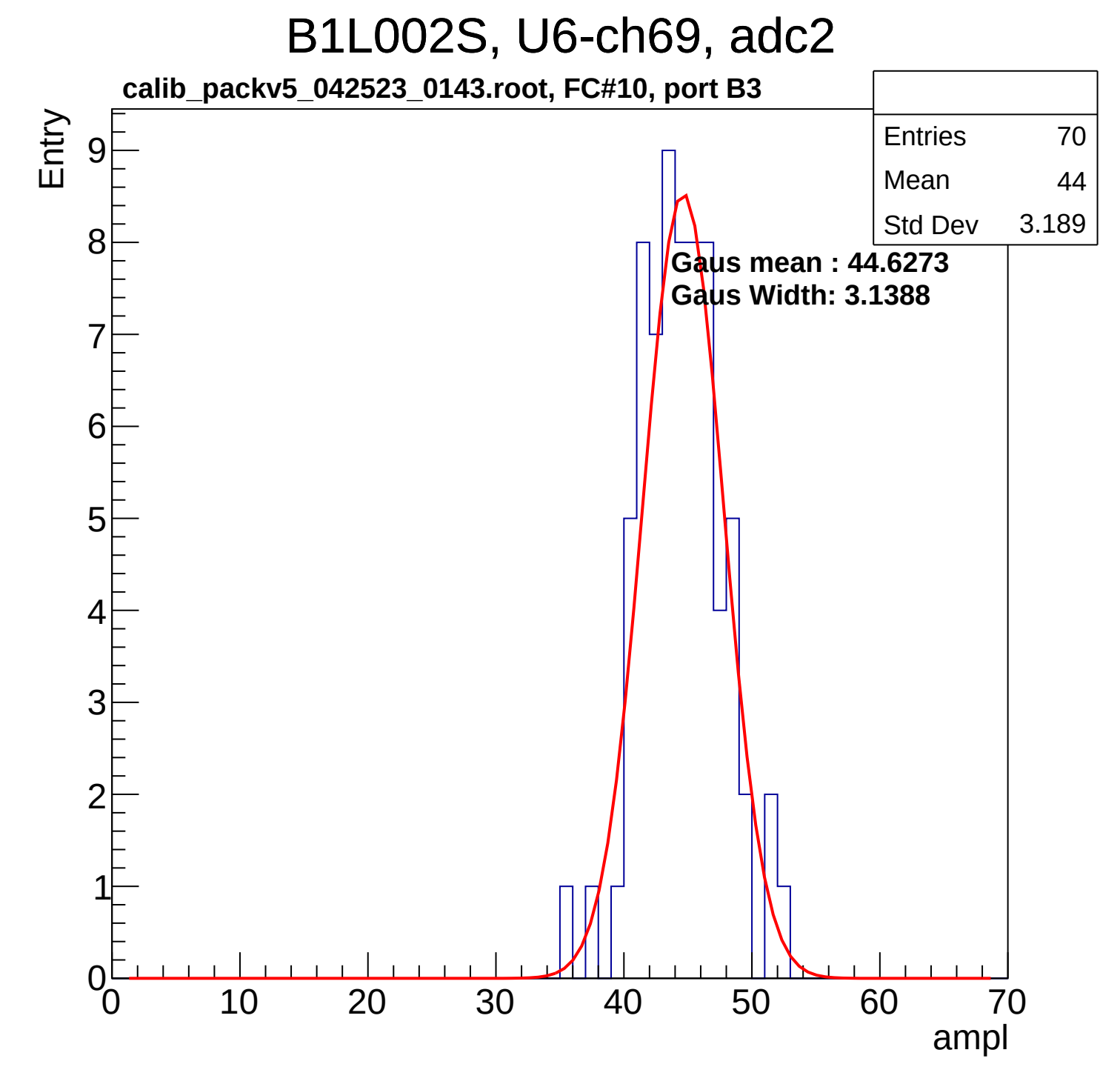

B1L002S, U6-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 56.17
Std Dev 7.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

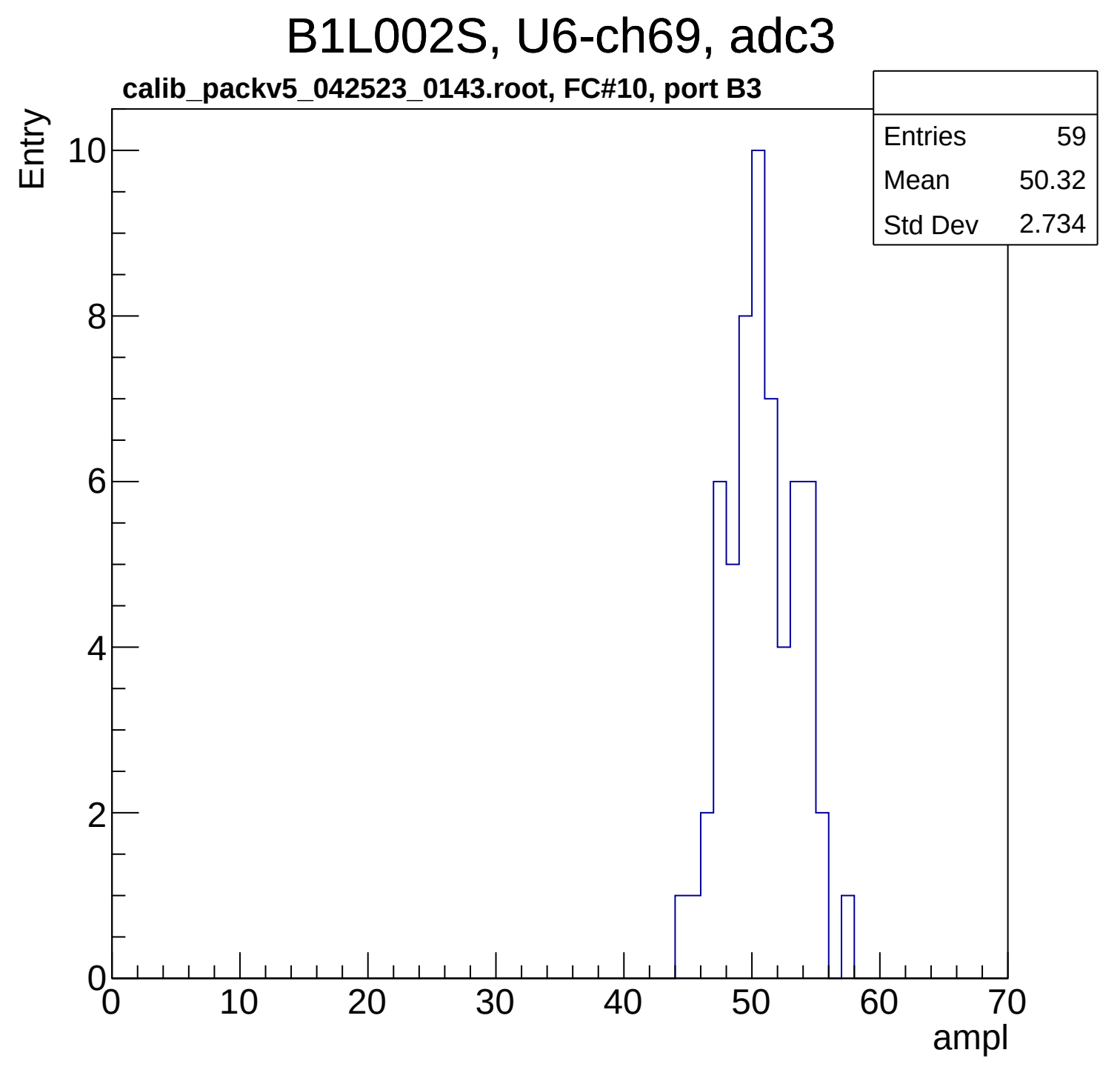

B1L002S, U6-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.23
Std Dev 1.527
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

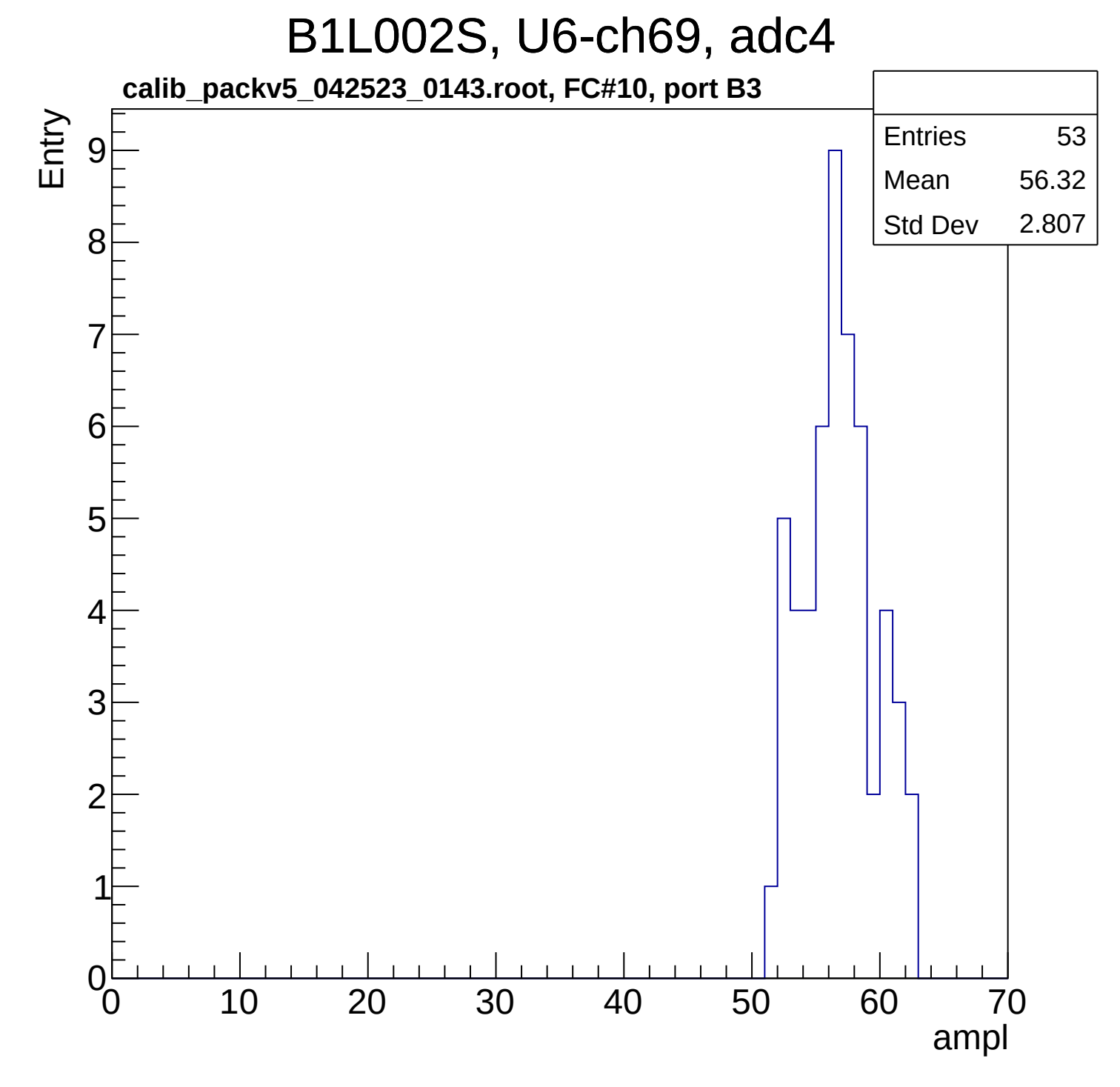

B1L002S, U6-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 39
Std Dev 28.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

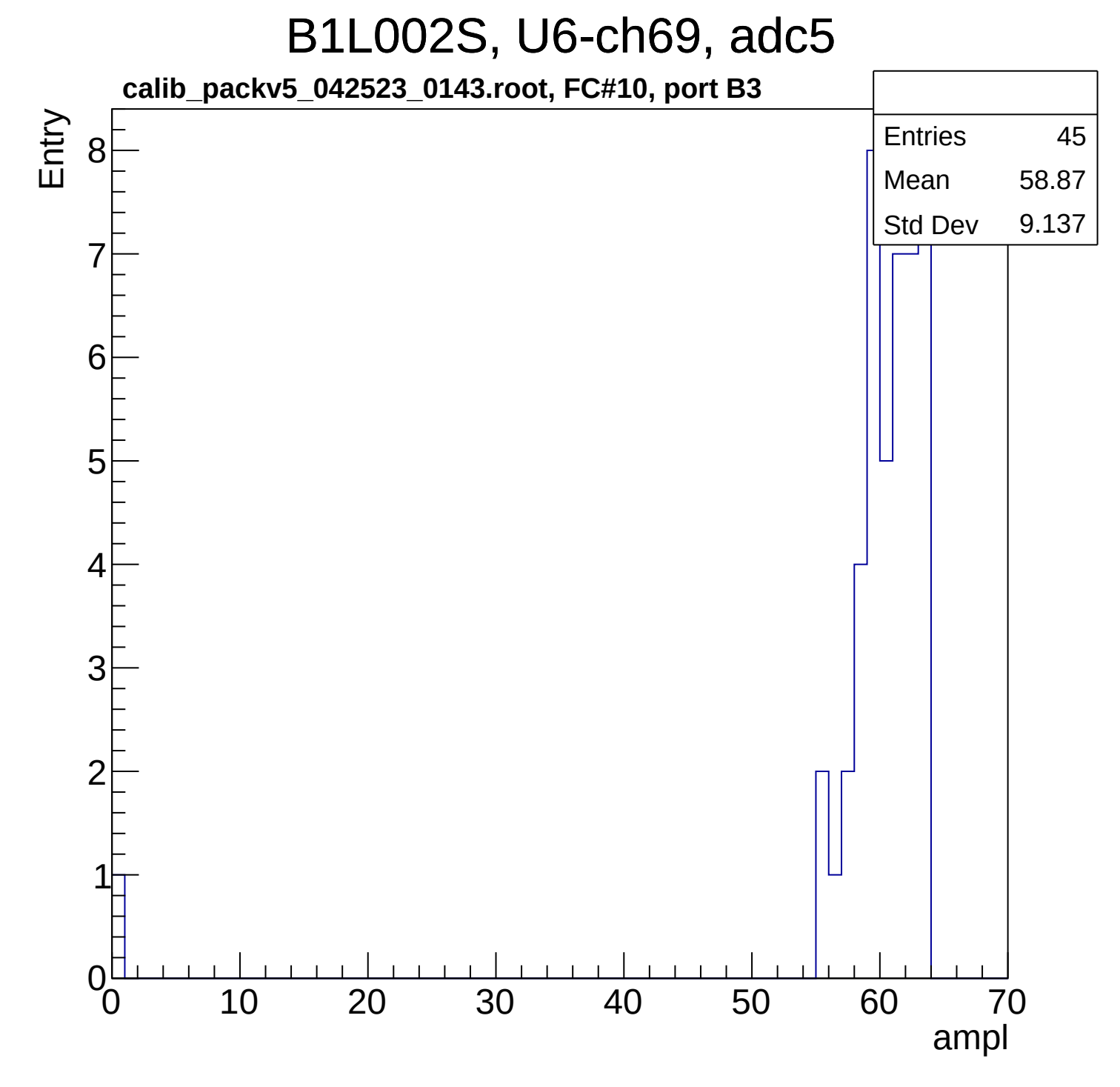

B1L002S, U6-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 30.73
Std Dev 5.816
Gaus mean : 32.2801
Gaus Width: 3.1622
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

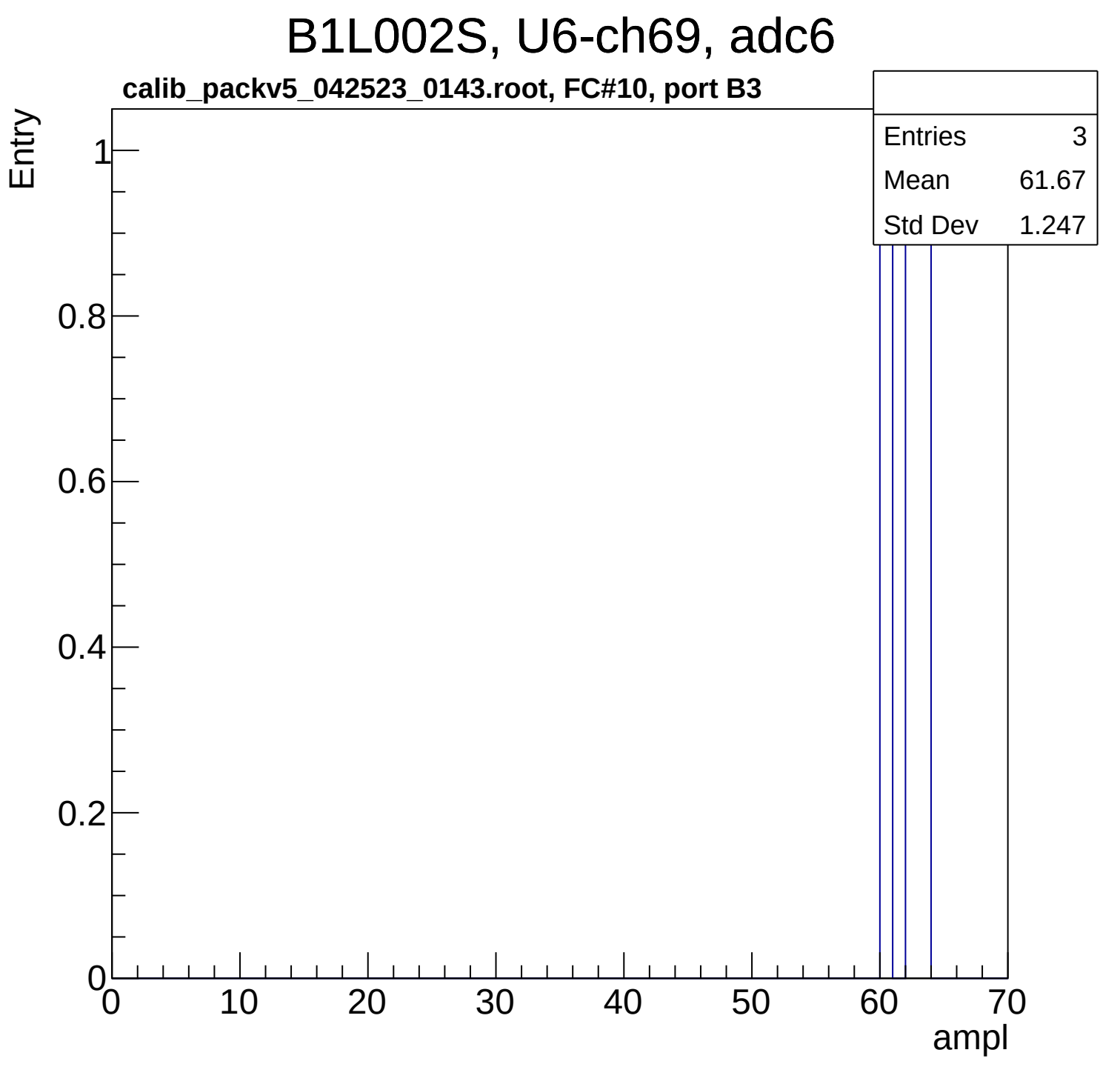

B1L002S, U6-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 38.29
Std Dev 2.436
Gaus mean : 38.3496
Gaus Width: 2.4451
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

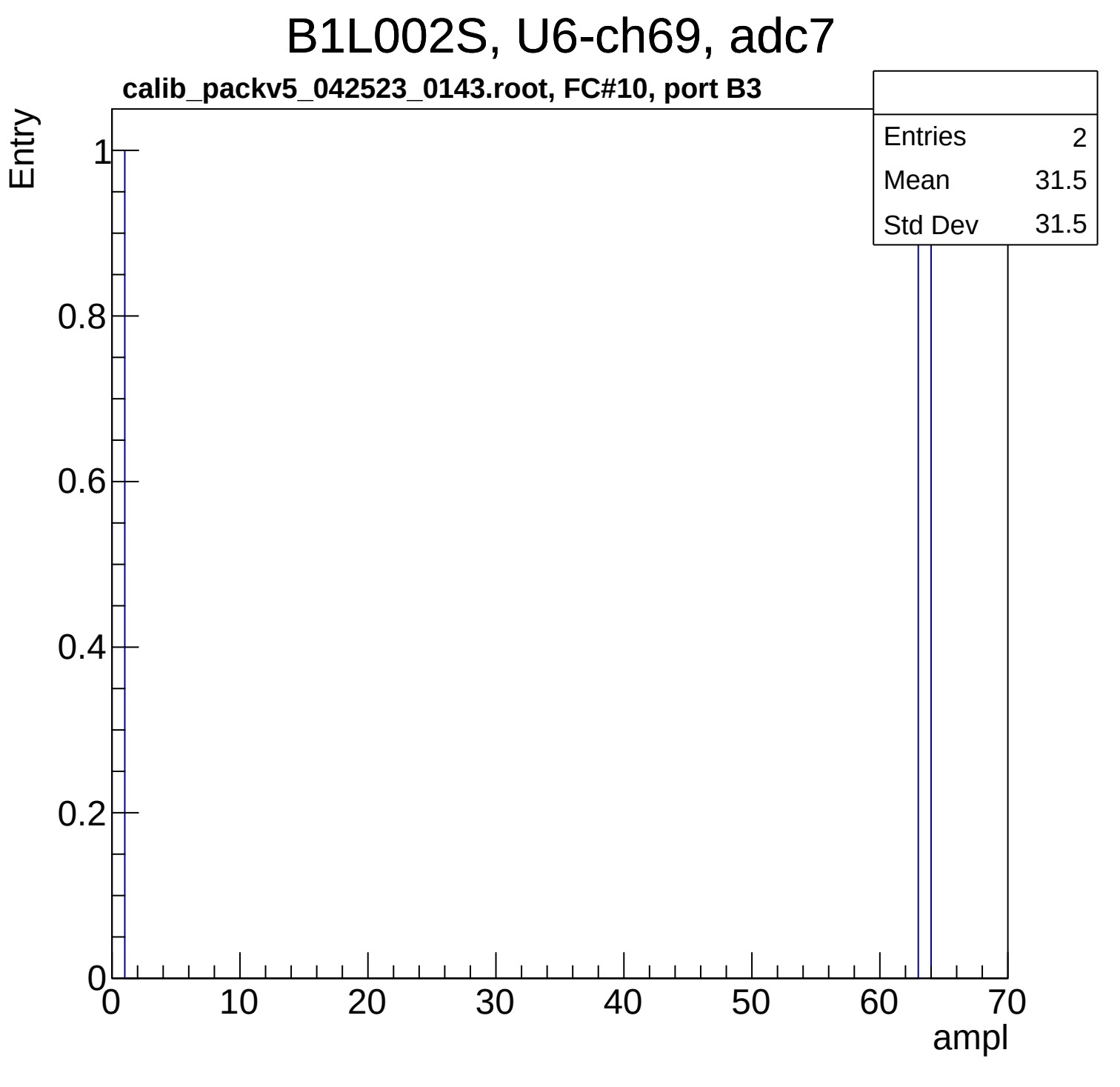

B1L002S, U6-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 44
Std Dev 3.189
Gaus mean : 44.6273
Gaus Width: 3.1388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

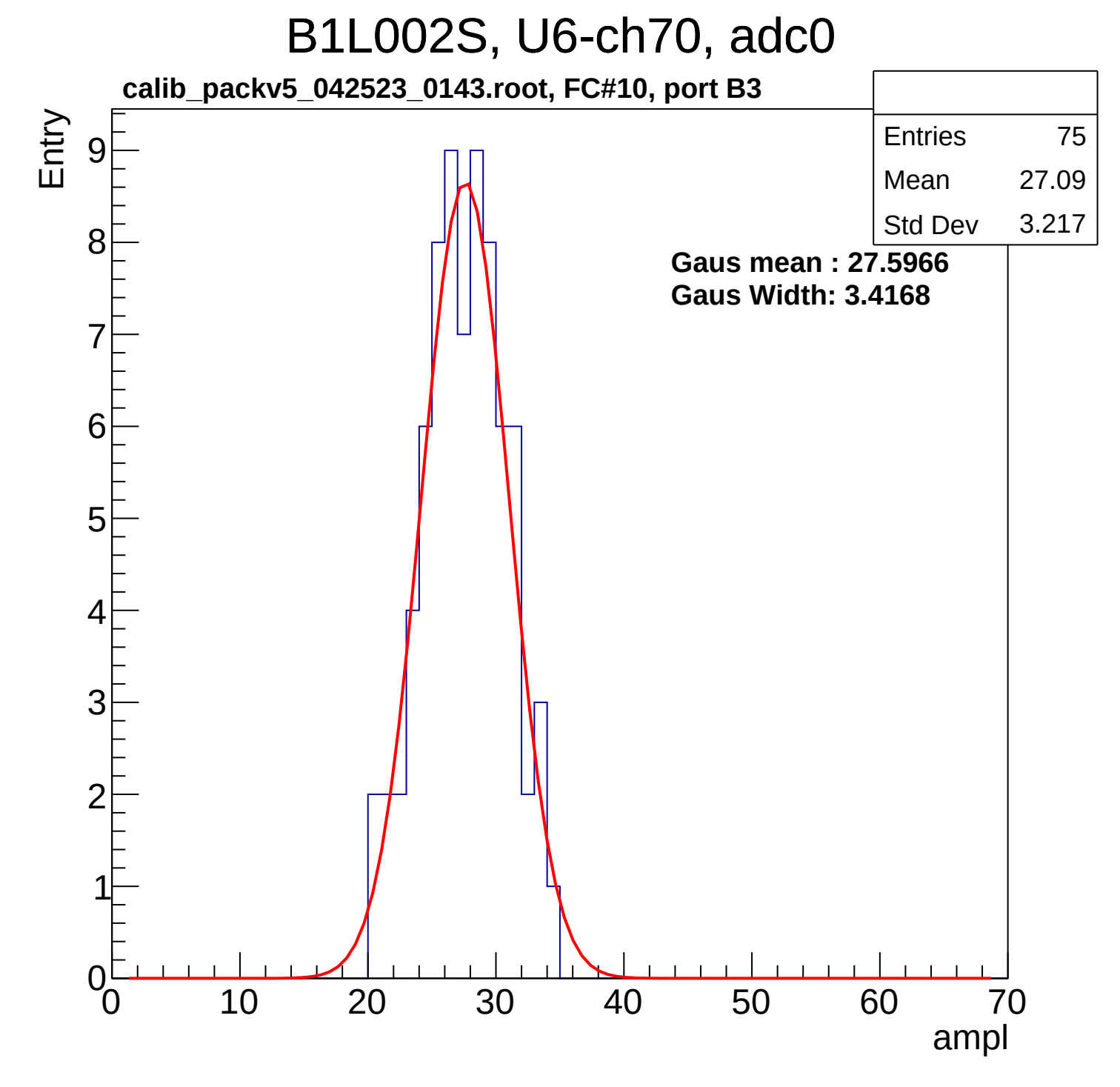

B1L002S, U6-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 50.32
Std Dev 2.734
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

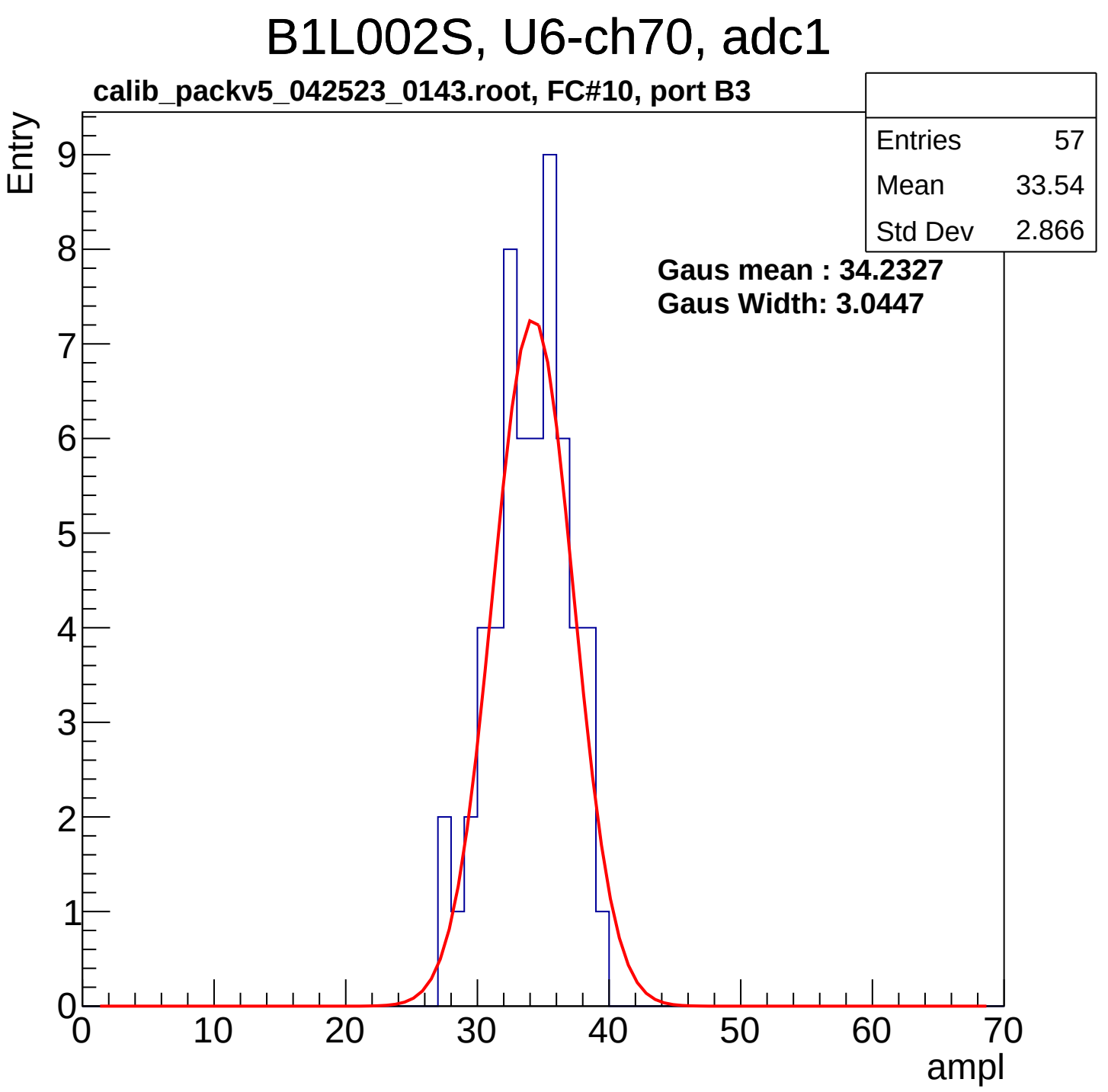

B1L002S, U6-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.32
Std Dev 2.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

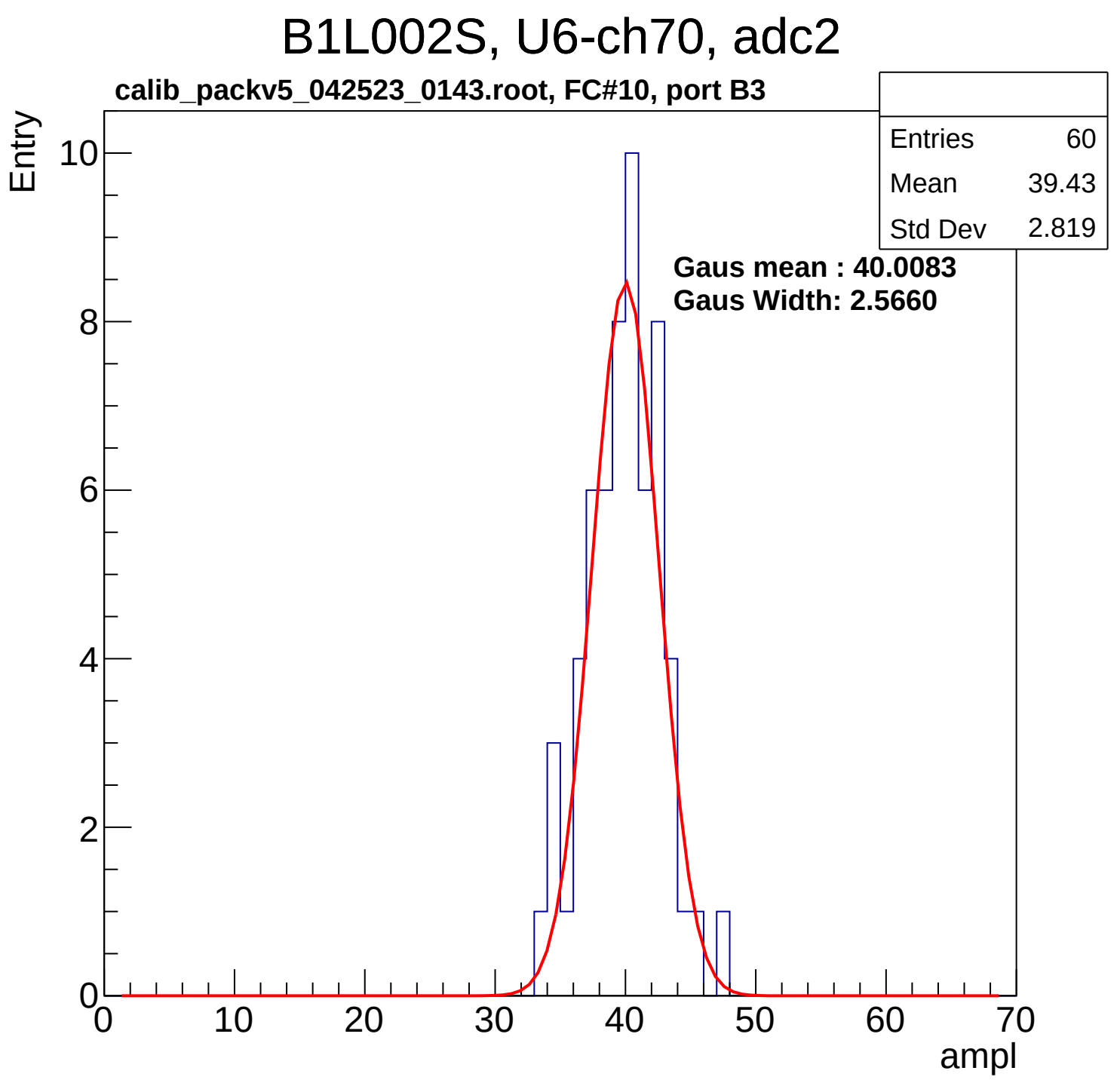

B1L002S, U6-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.87
Std Dev 9.137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

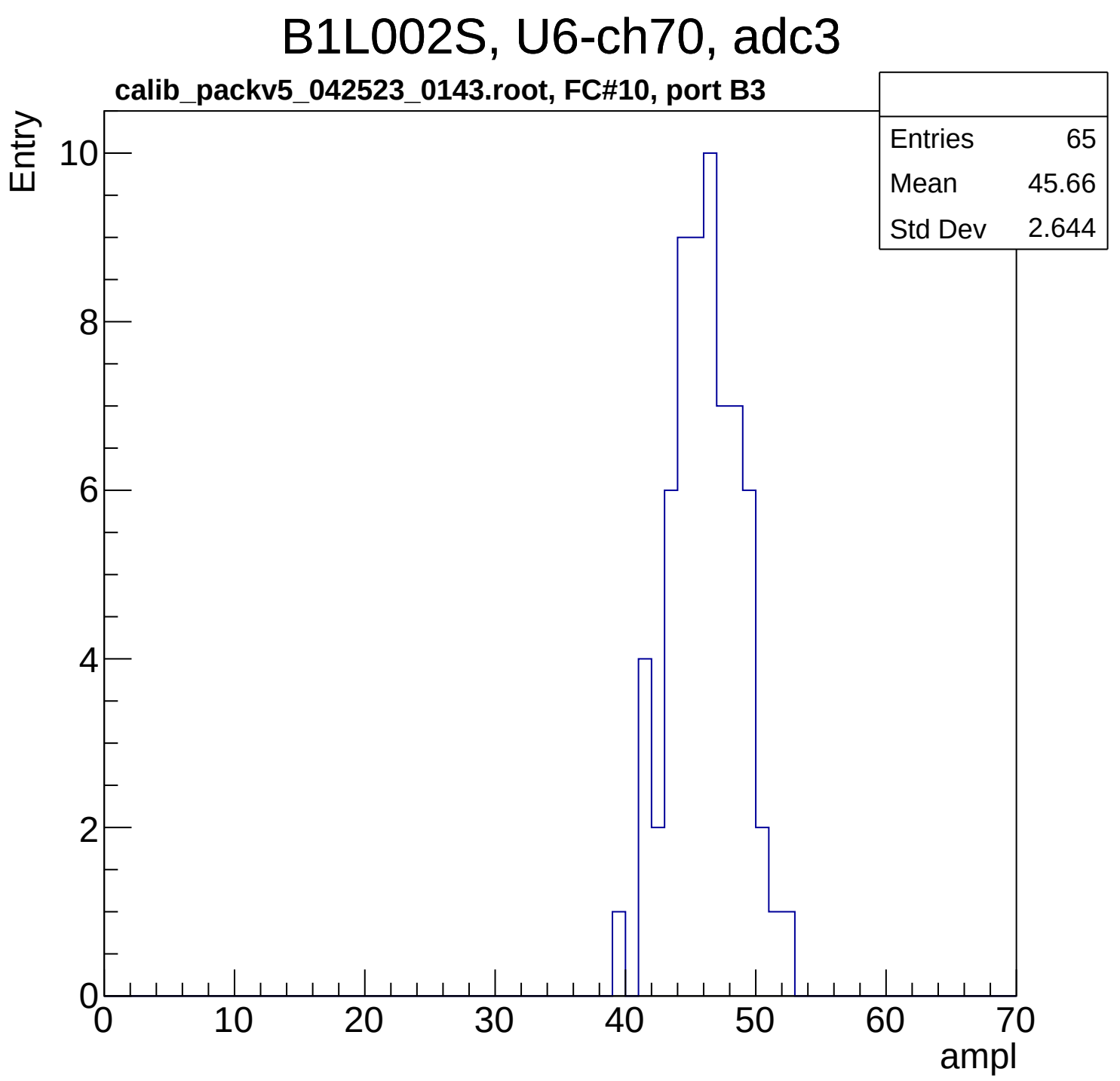

B1L002S, U6-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

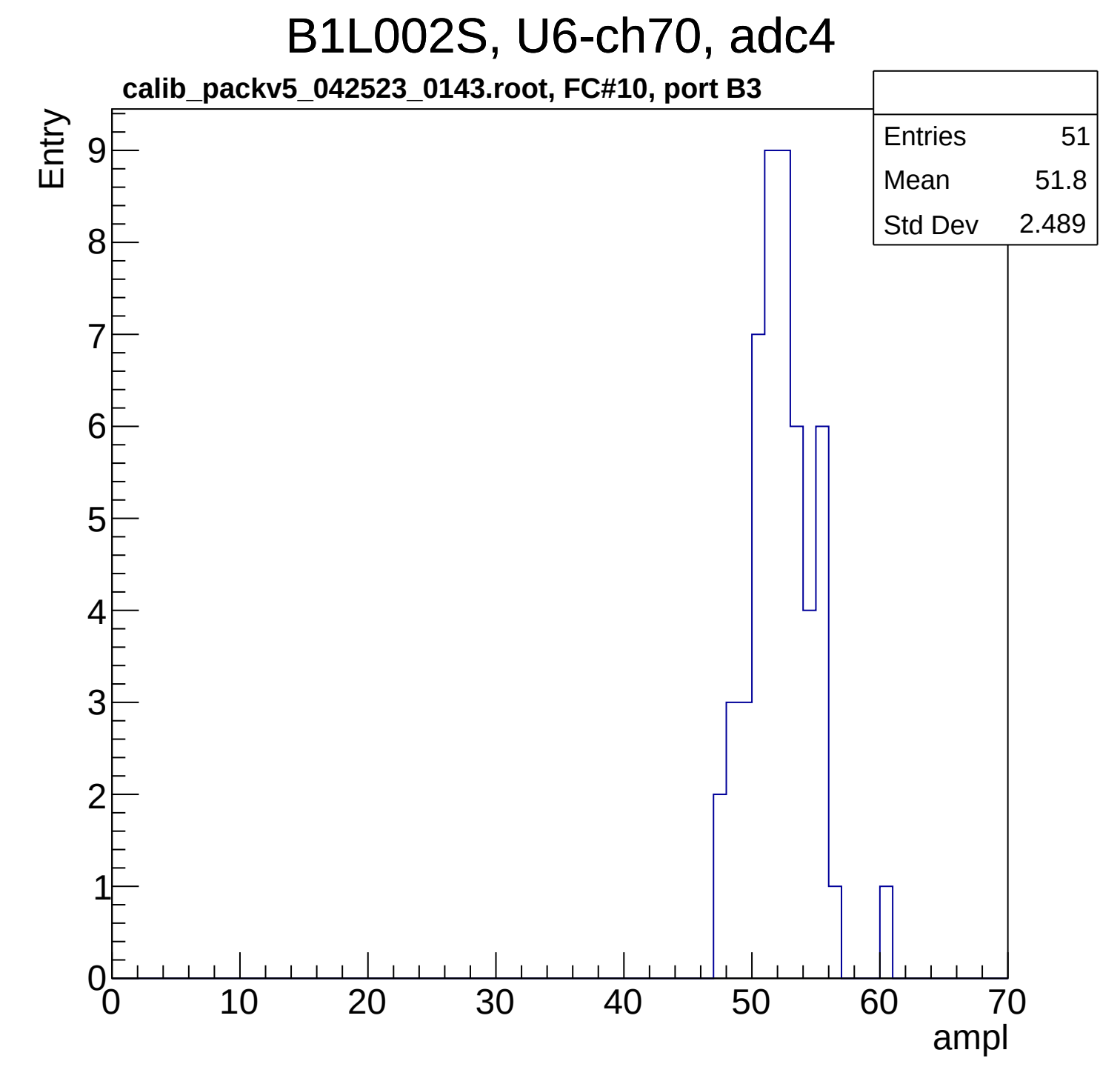

B1L002S, U6-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

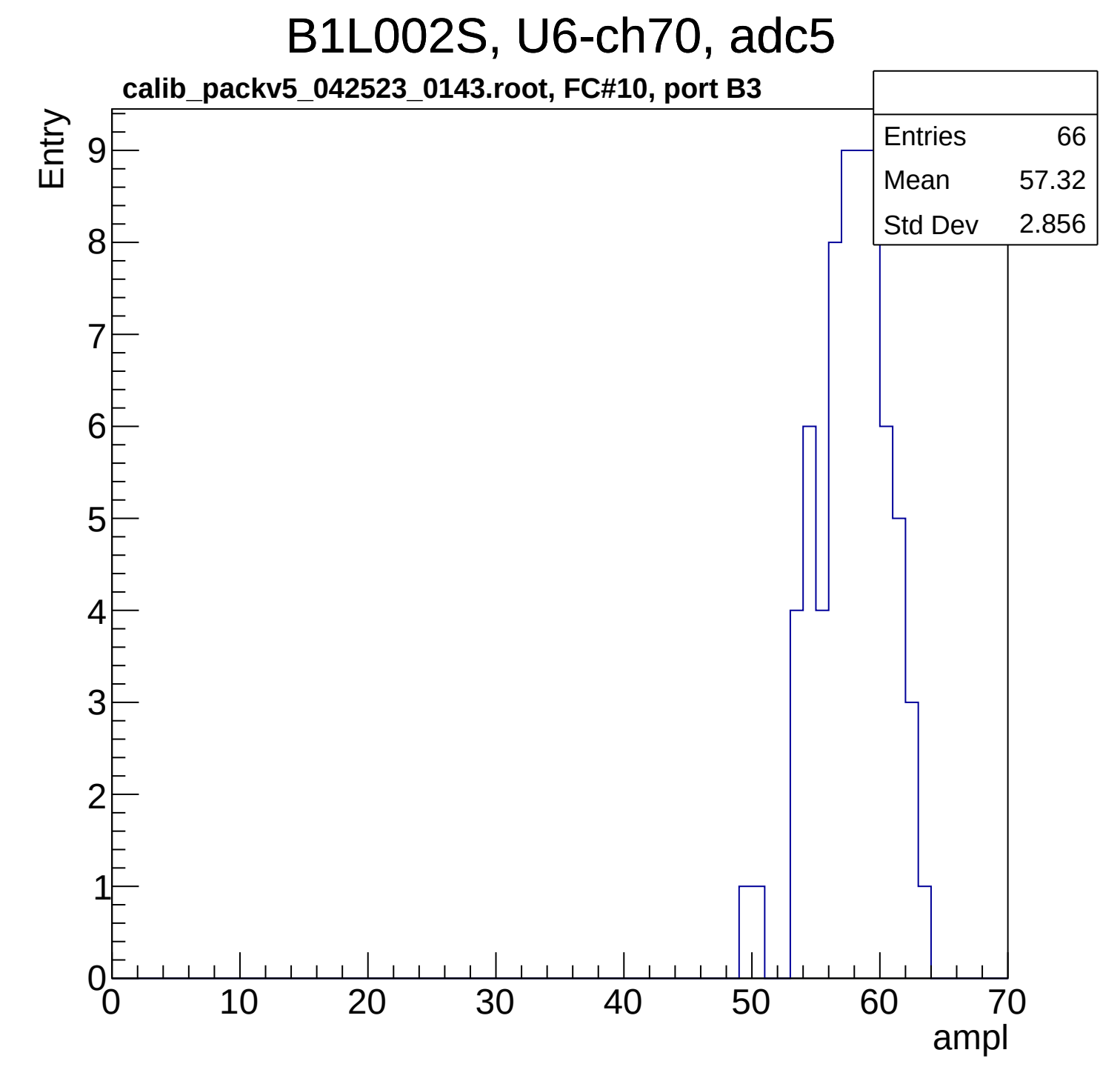

B1L002S, U6-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.09
Std Dev 3.217
Gaus mean : 27.5966
Gaus Width: 3.4168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

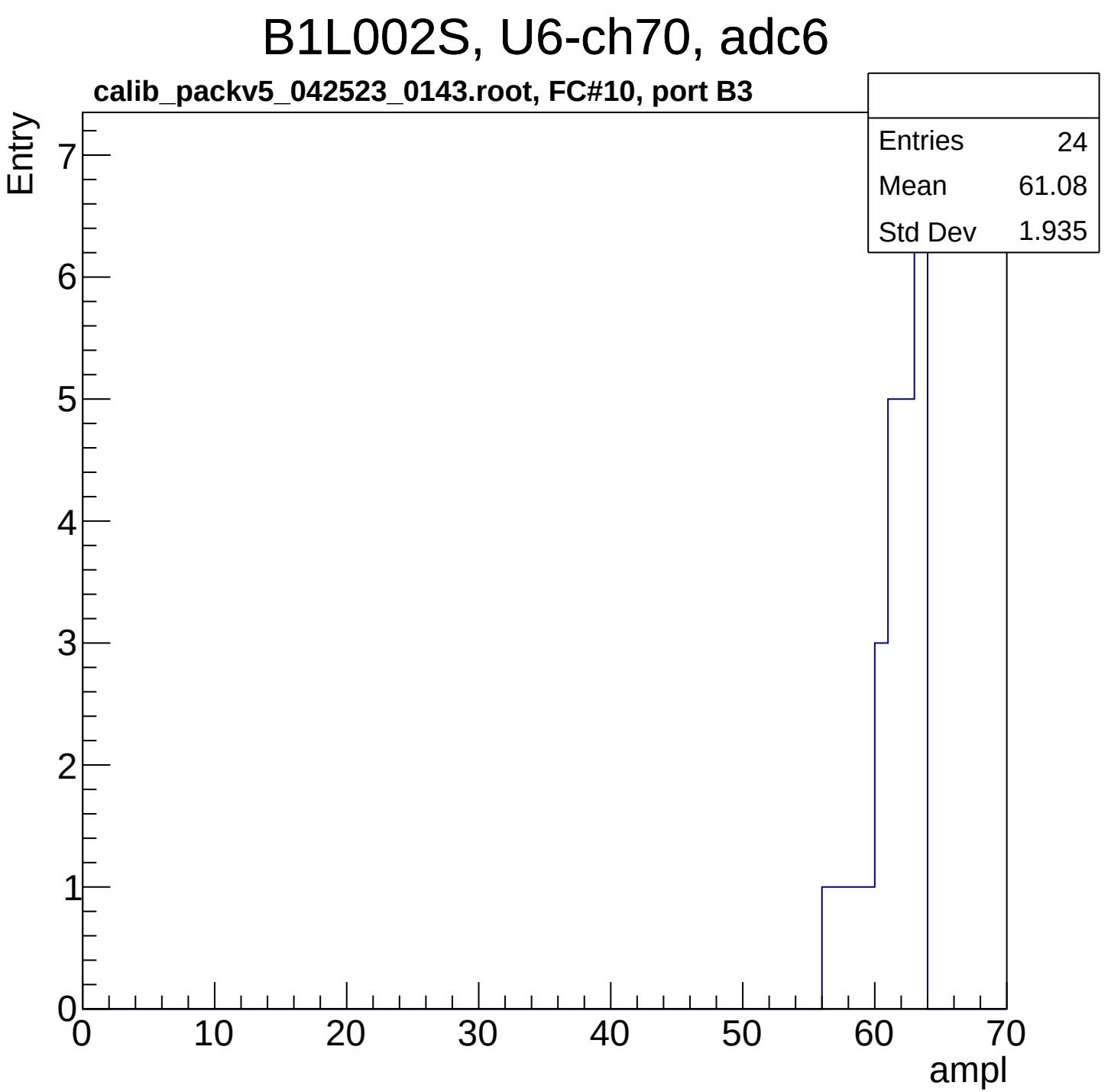

B1L002S, U6-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.54
Std Dev 2.866
Gaus mean : 34.2327
Gaus Width: 3.0447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

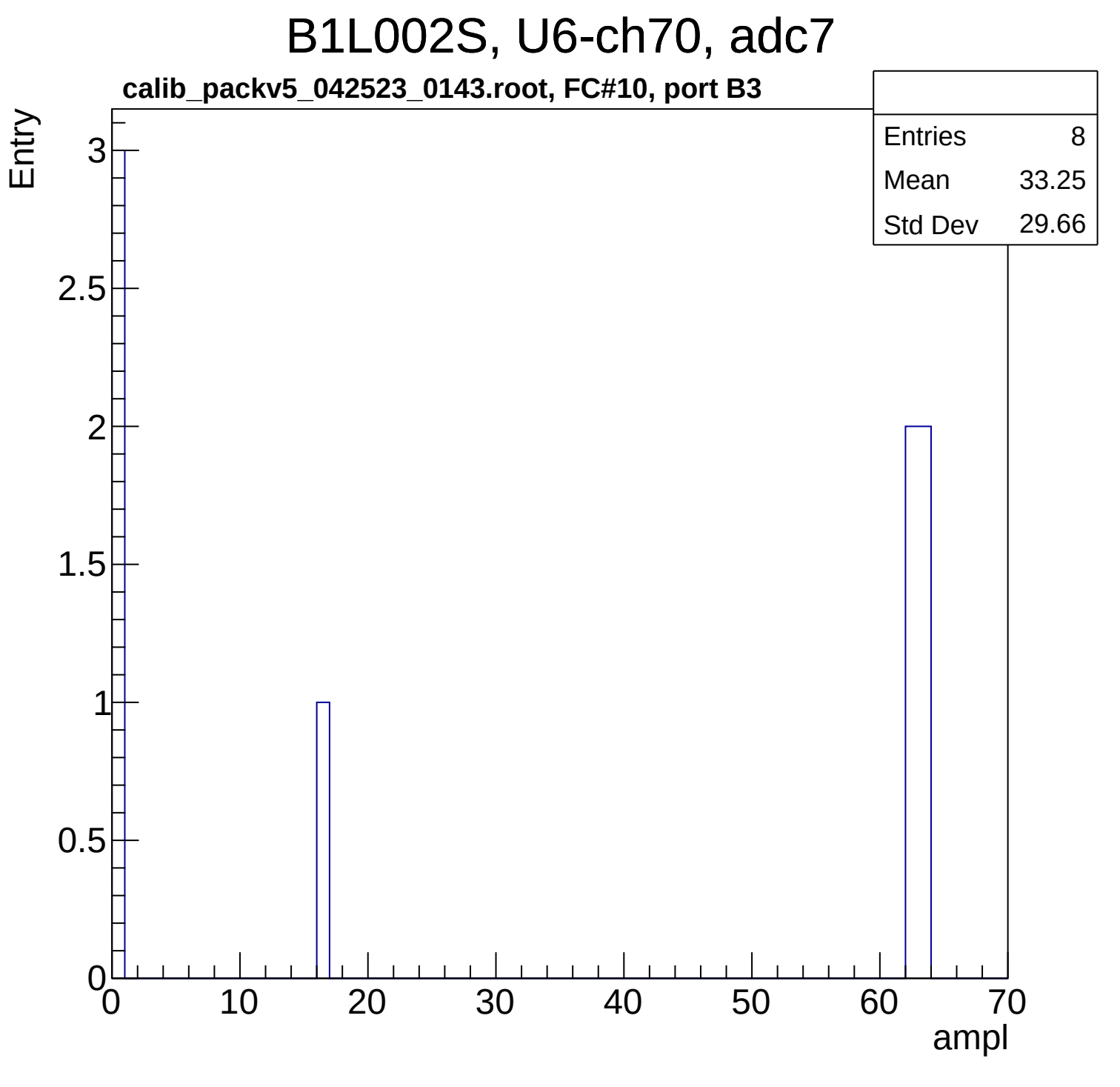

B1L002S, U6-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 39.43
Std Dev 2.819
Gaus mean : 40.0083
Gaus Width: 2.5660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

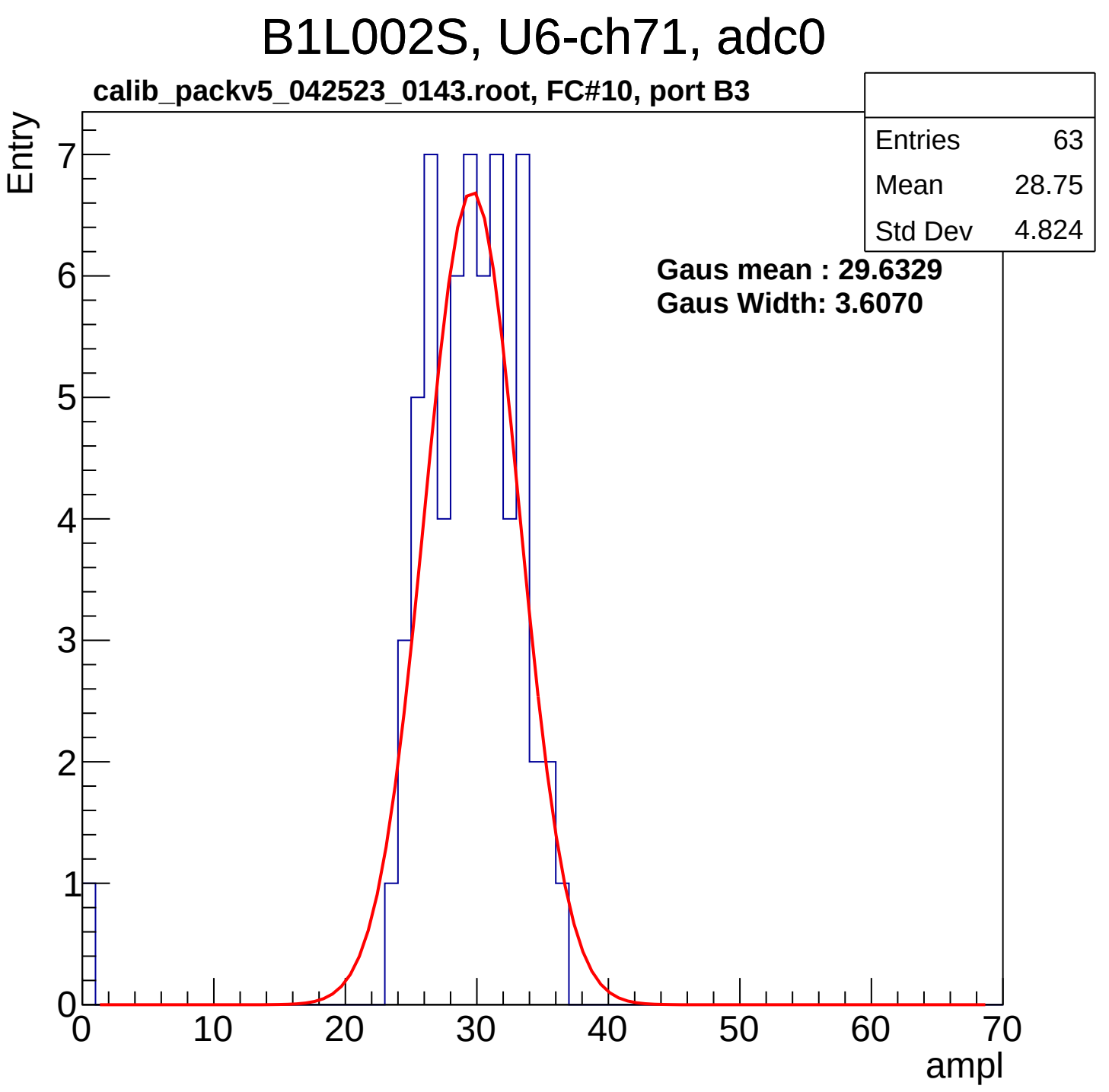

B1L002S, U6-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.66
Std Dev 2.644
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

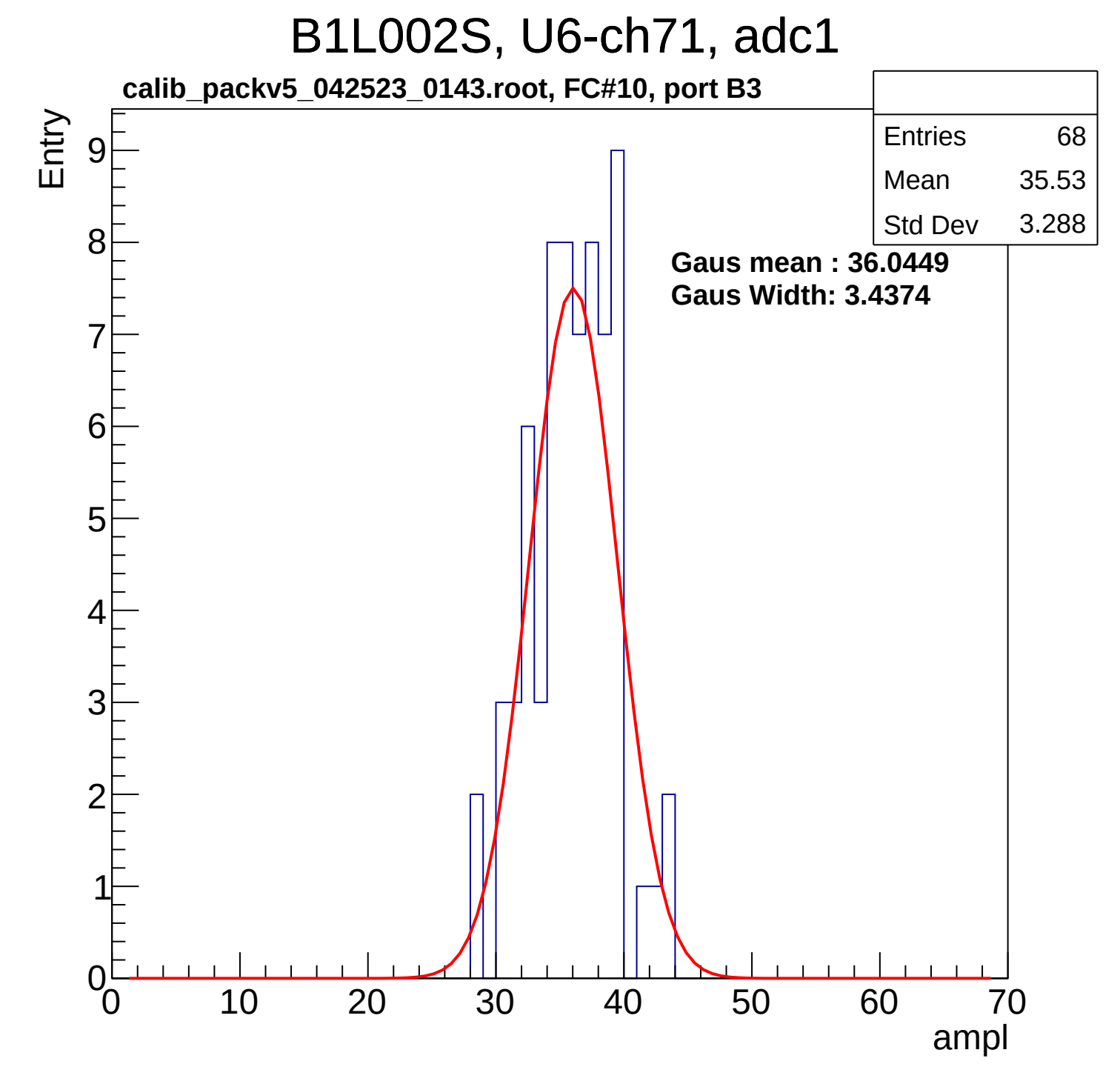

B1L002S, U6-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.8
Std Dev 2.489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

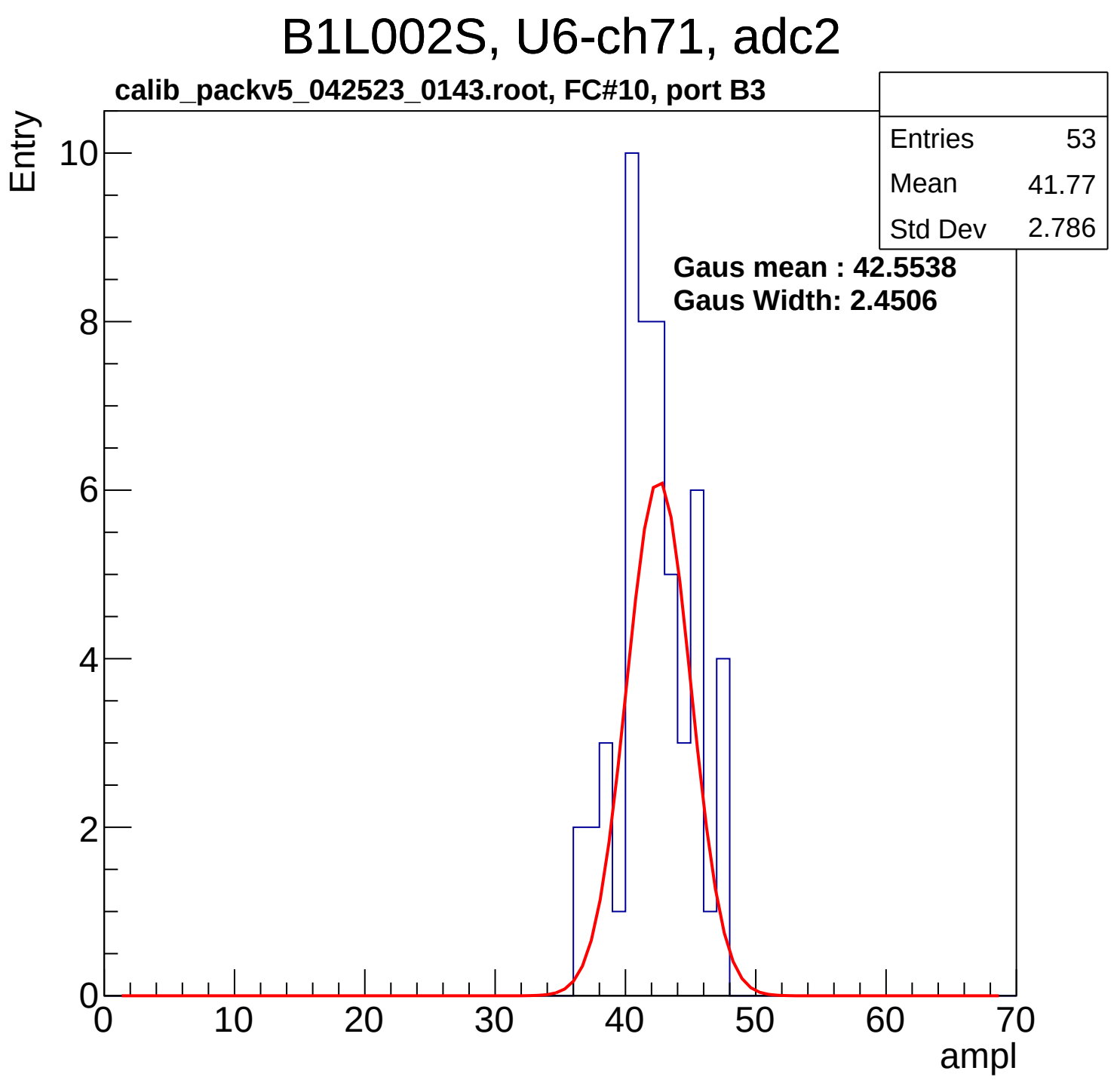

B1L002S, U6-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.32
Std Dev 2.856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

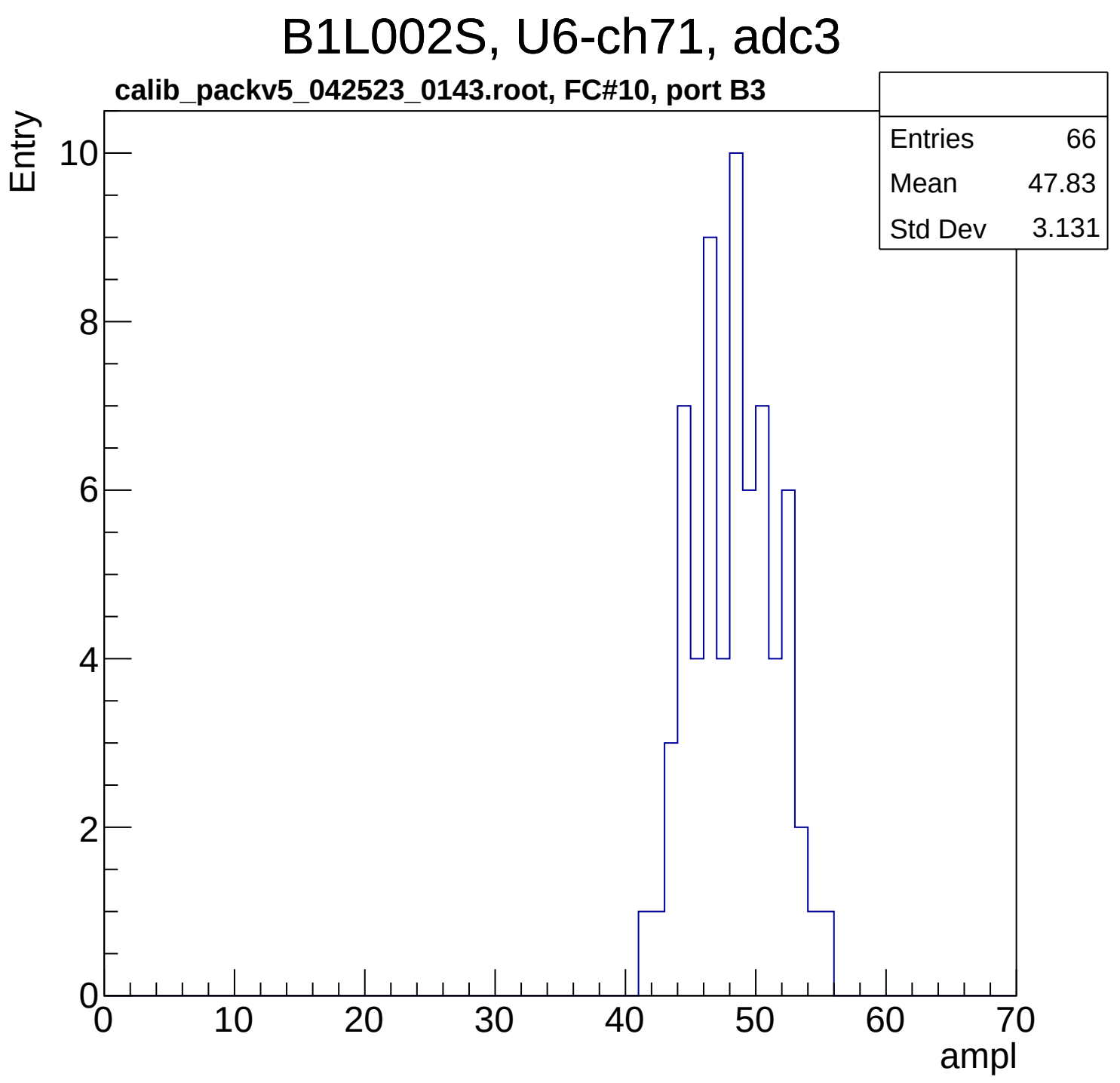

B1L002S, U6-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.08
Std Dev 1.935
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

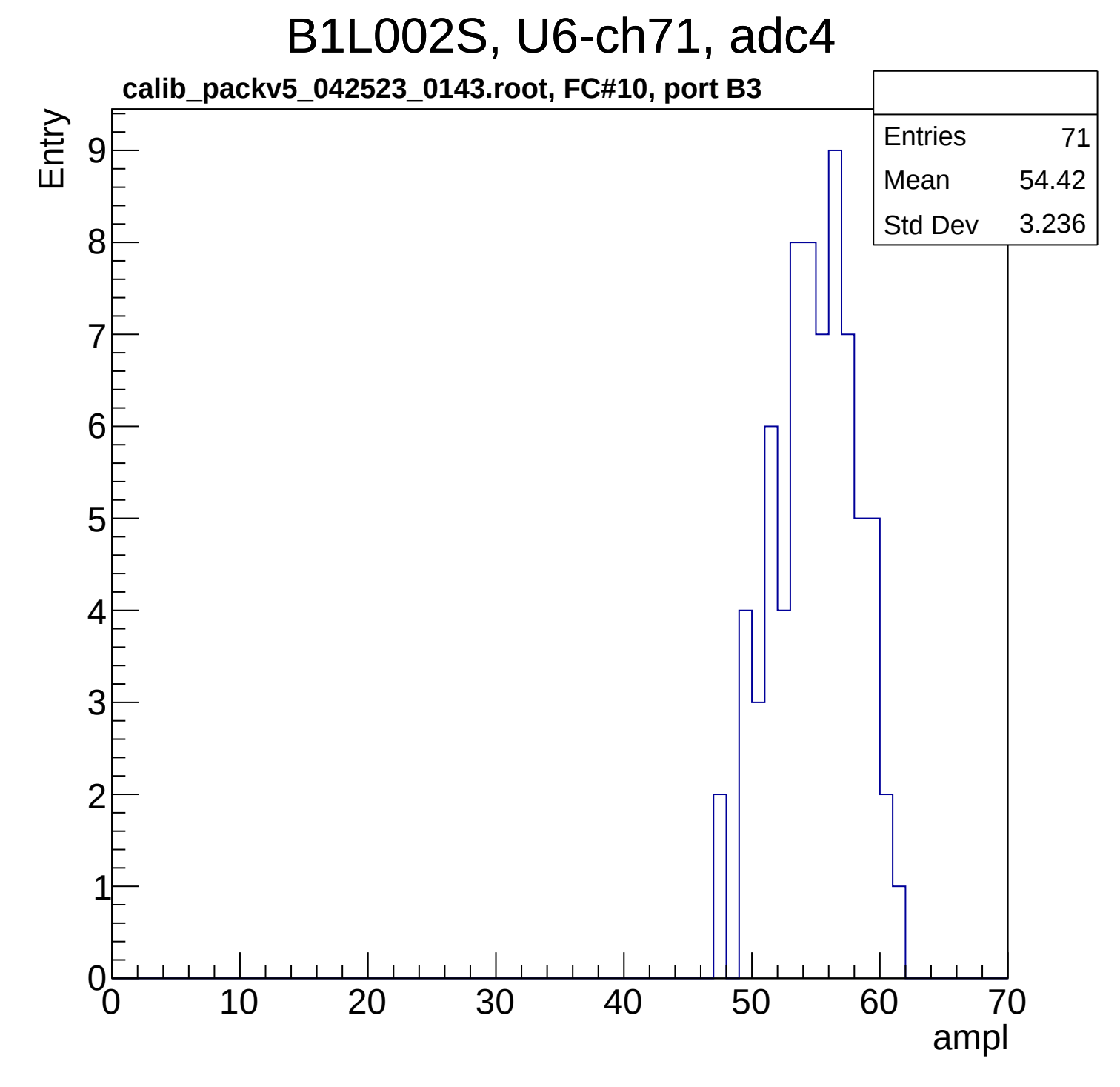

B1L002S, U6-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 33.25
Std Dev 29.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

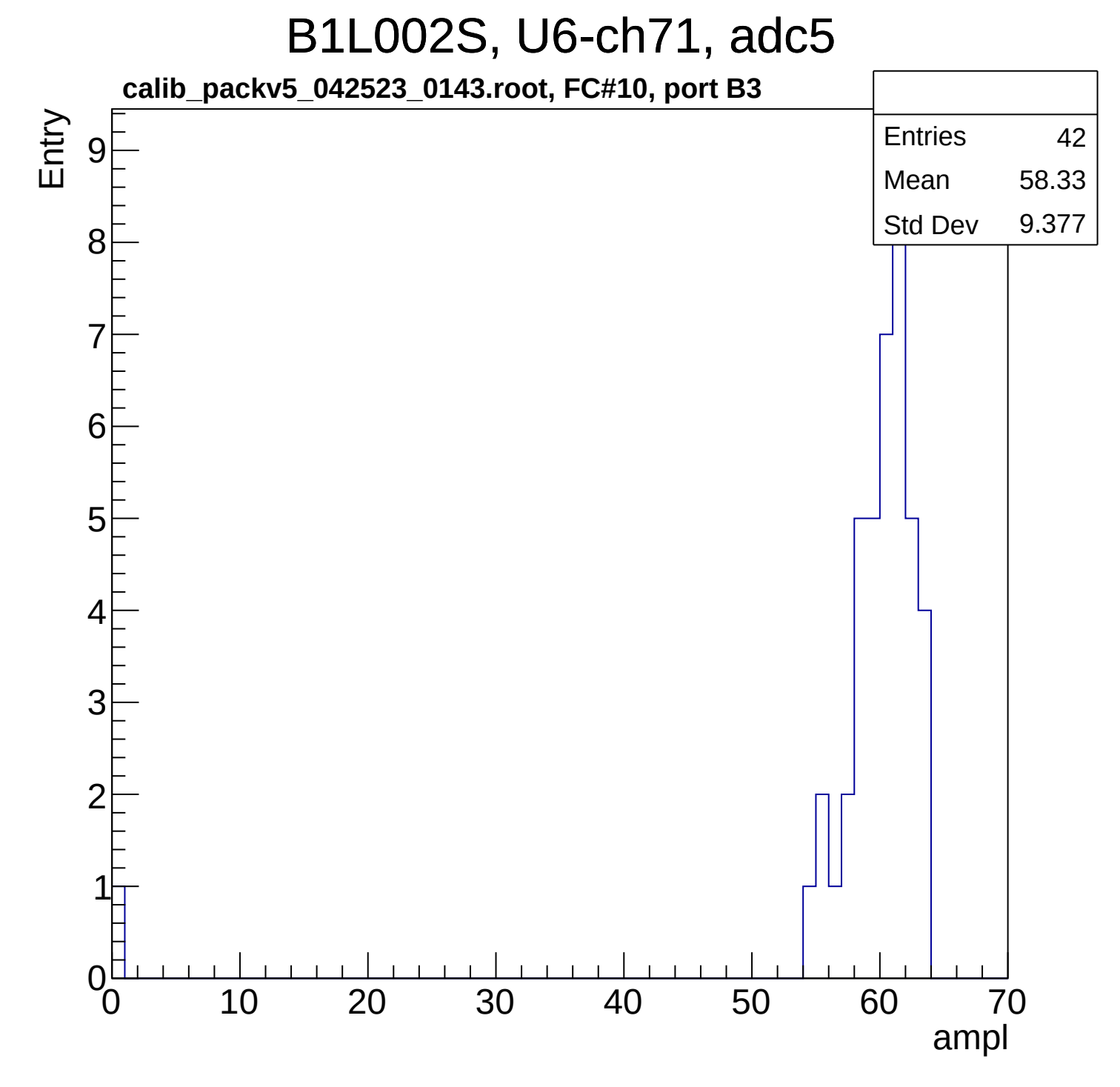

B1L002S, U6-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.75
Std Dev 4.824
Gaus mean : 29.6329
Gaus Width: 3.6070
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

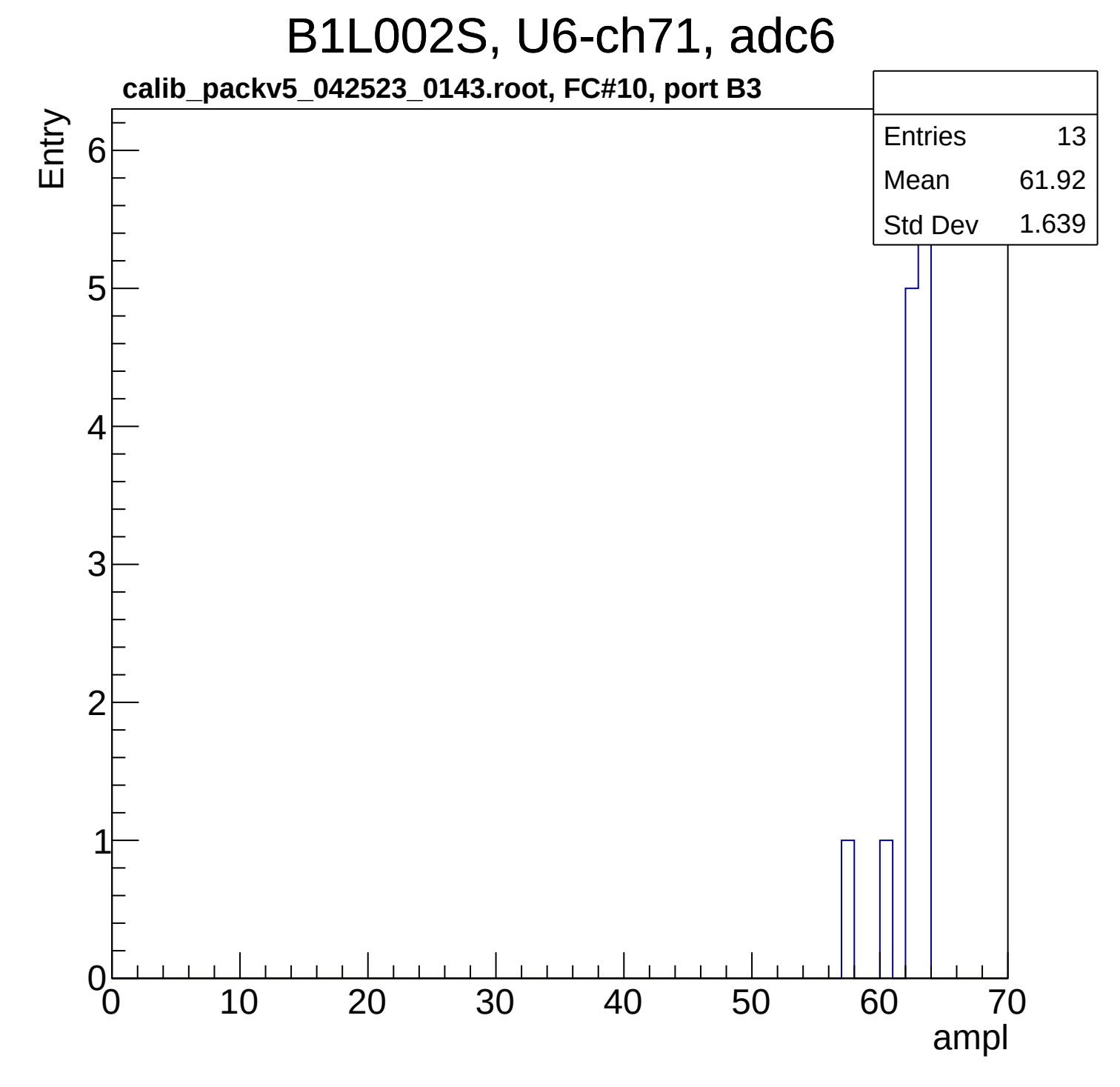

B1L002S, U6-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.53
Std Dev 3.288
Gaus mean : 36.0449
Gaus Width: 3.4374
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

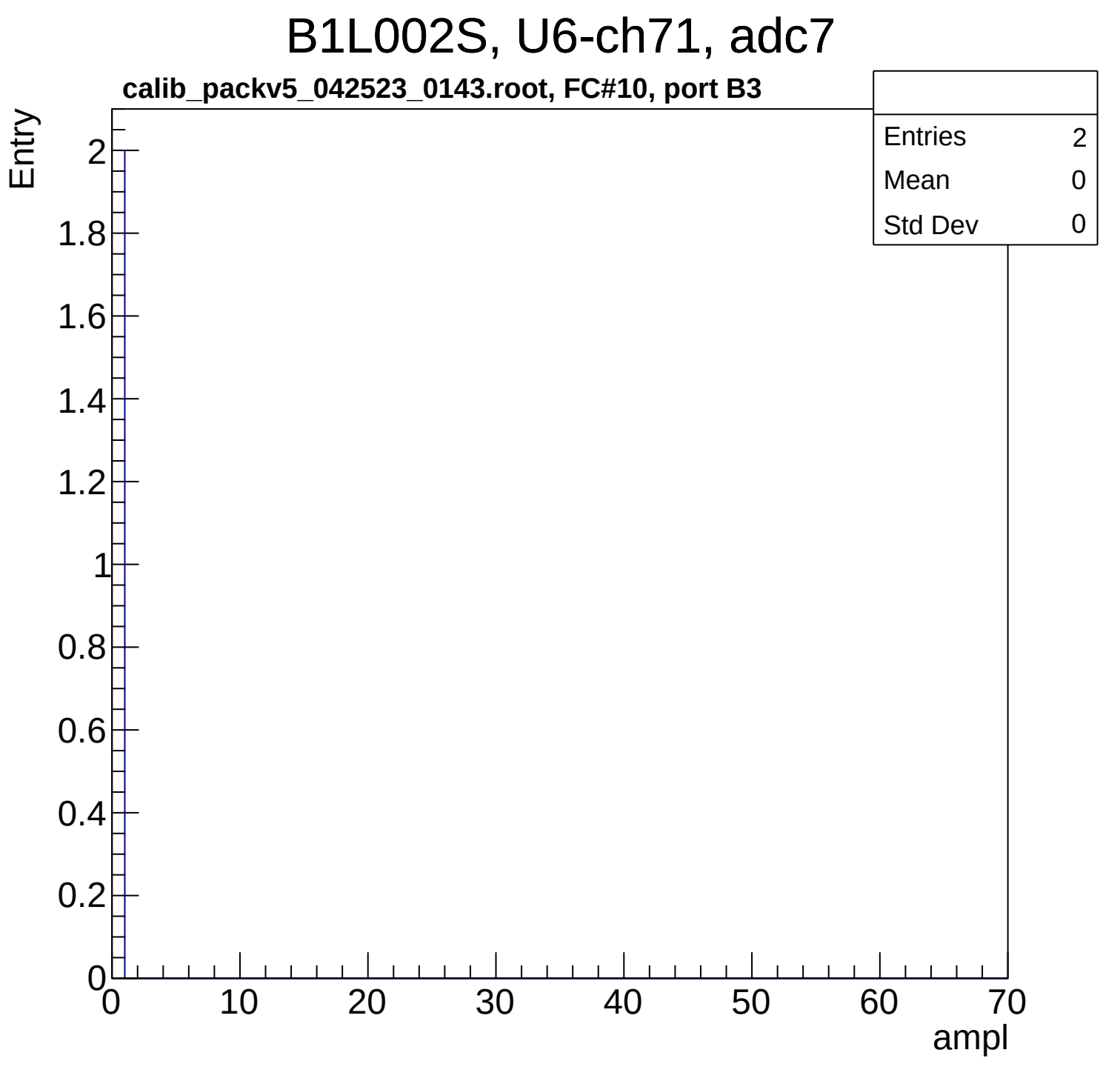

B1L002S, U6-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.77
Std Dev 2.786
Gaus mean : 42.5538
Gaus Width: 2.4506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

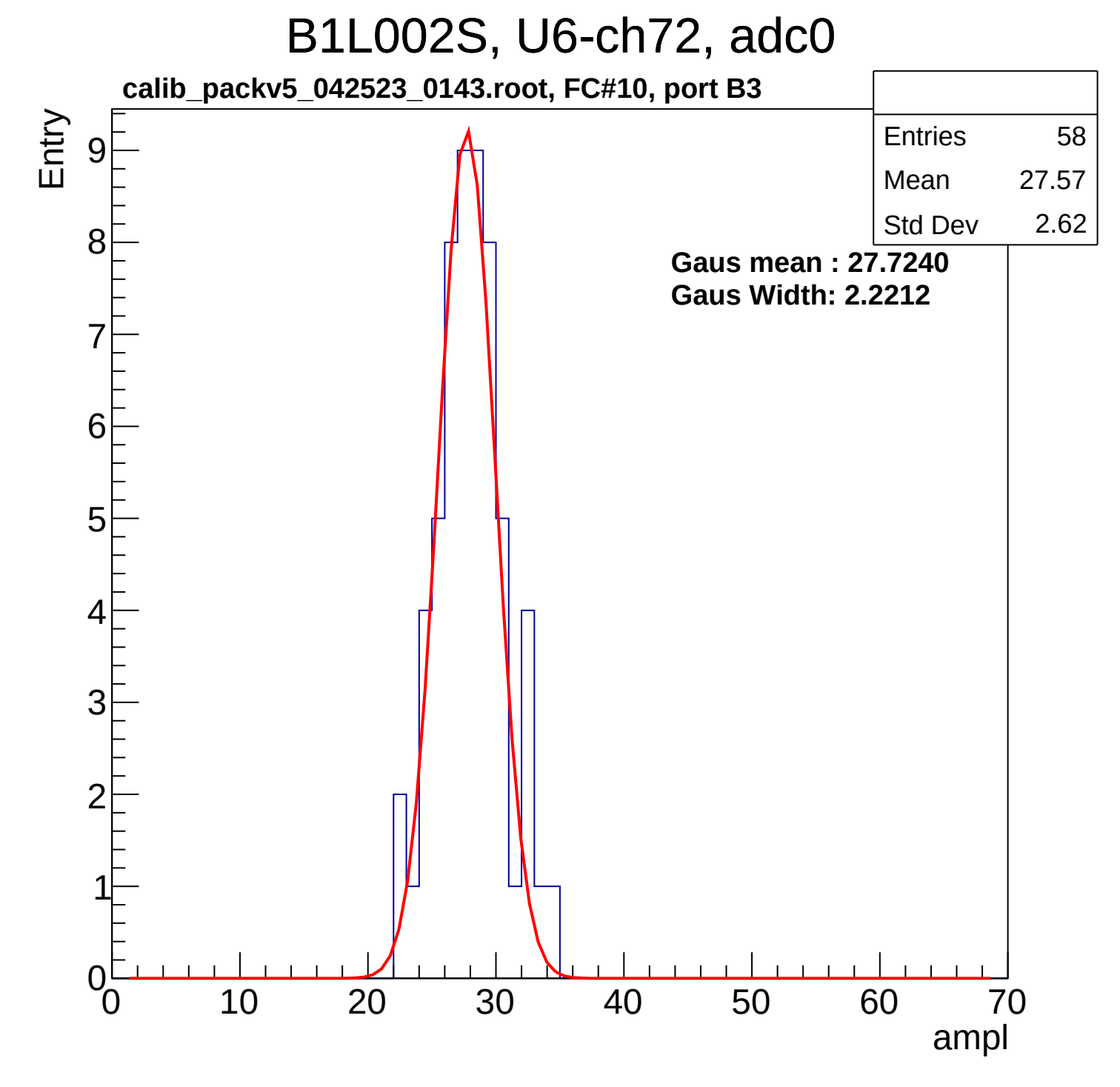

B1L002S, U6-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.83
Std Dev 3.131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

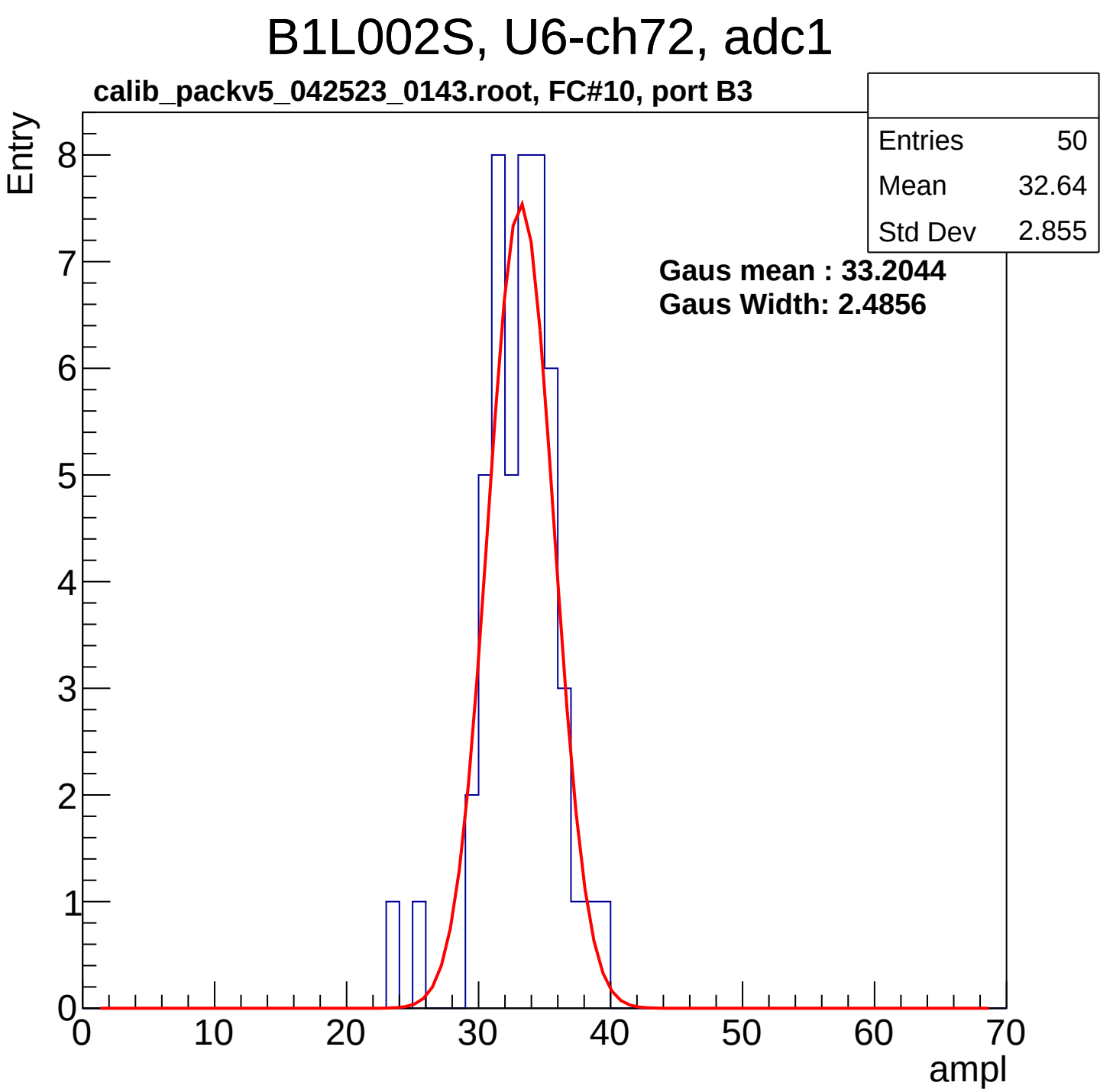

B1L002S, U6-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 54.42
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

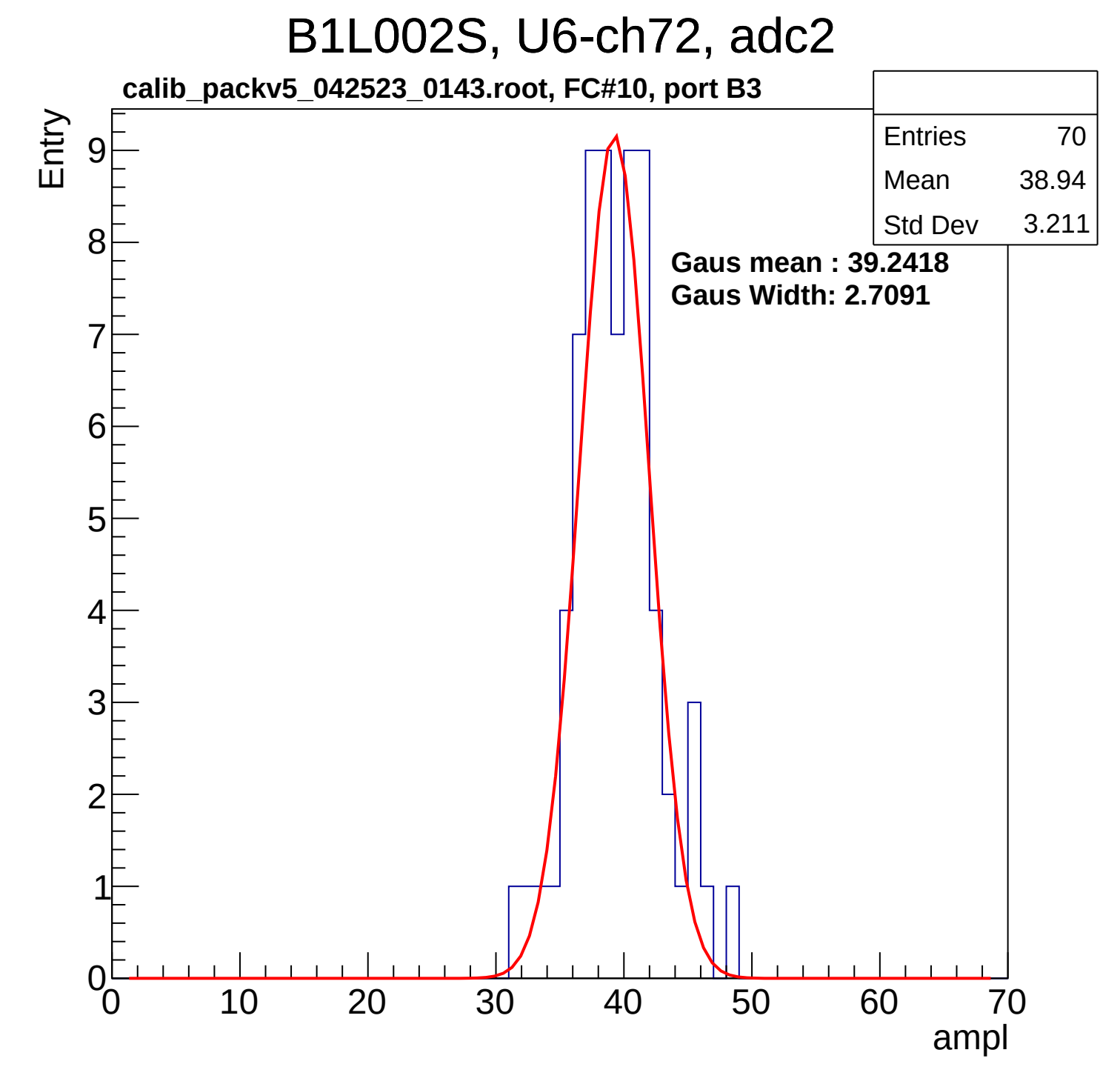

B1L002S, U6-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.33
Std Dev 9.377
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

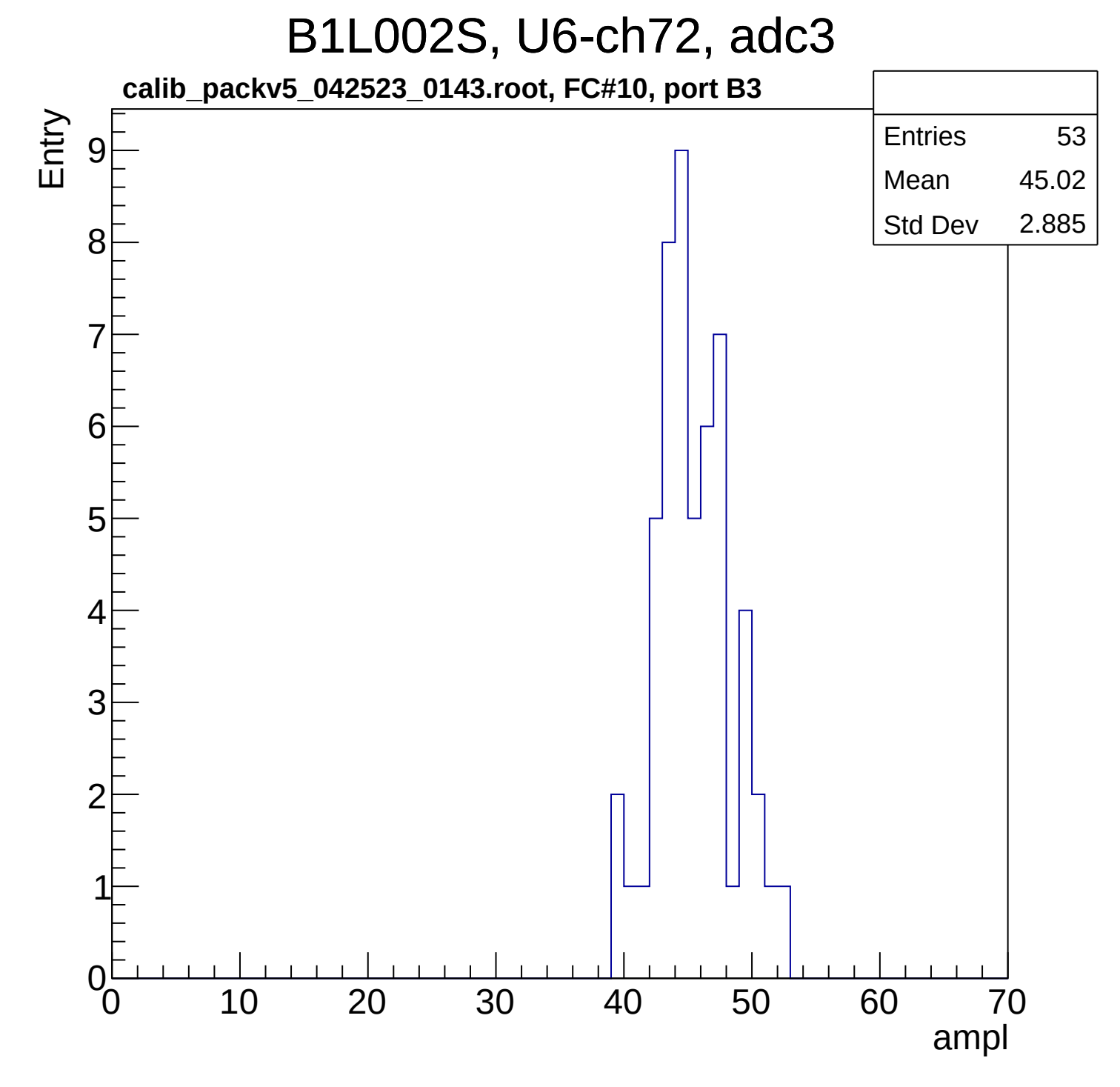

B1L002S, U6-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.92
Std Dev 1.639
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

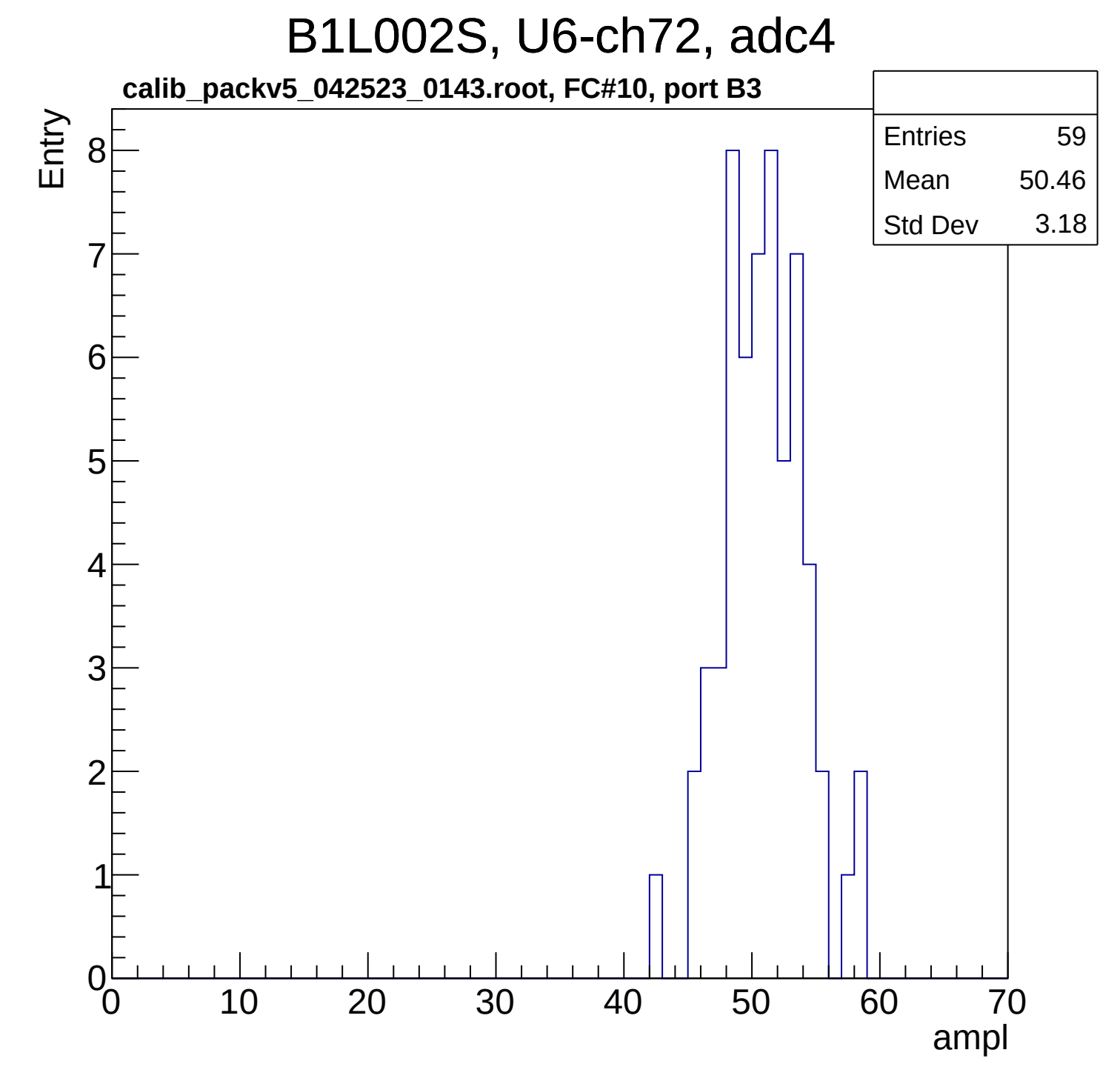

B1L002S, U6-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

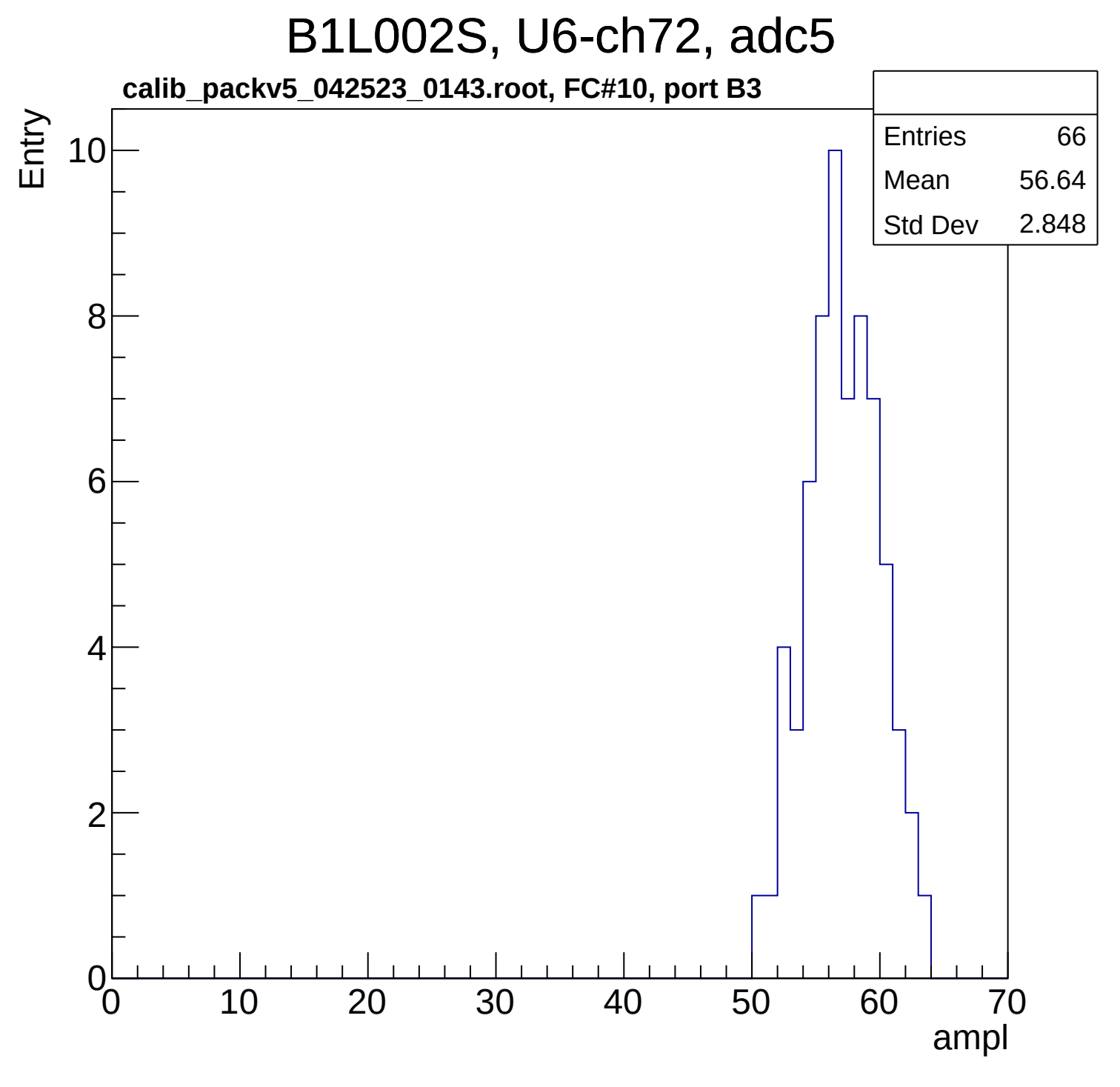

B1L002S, U6-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.57
Std Dev 2.62
Gaus mean : 27.7240
Gaus Width: 2.2212
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

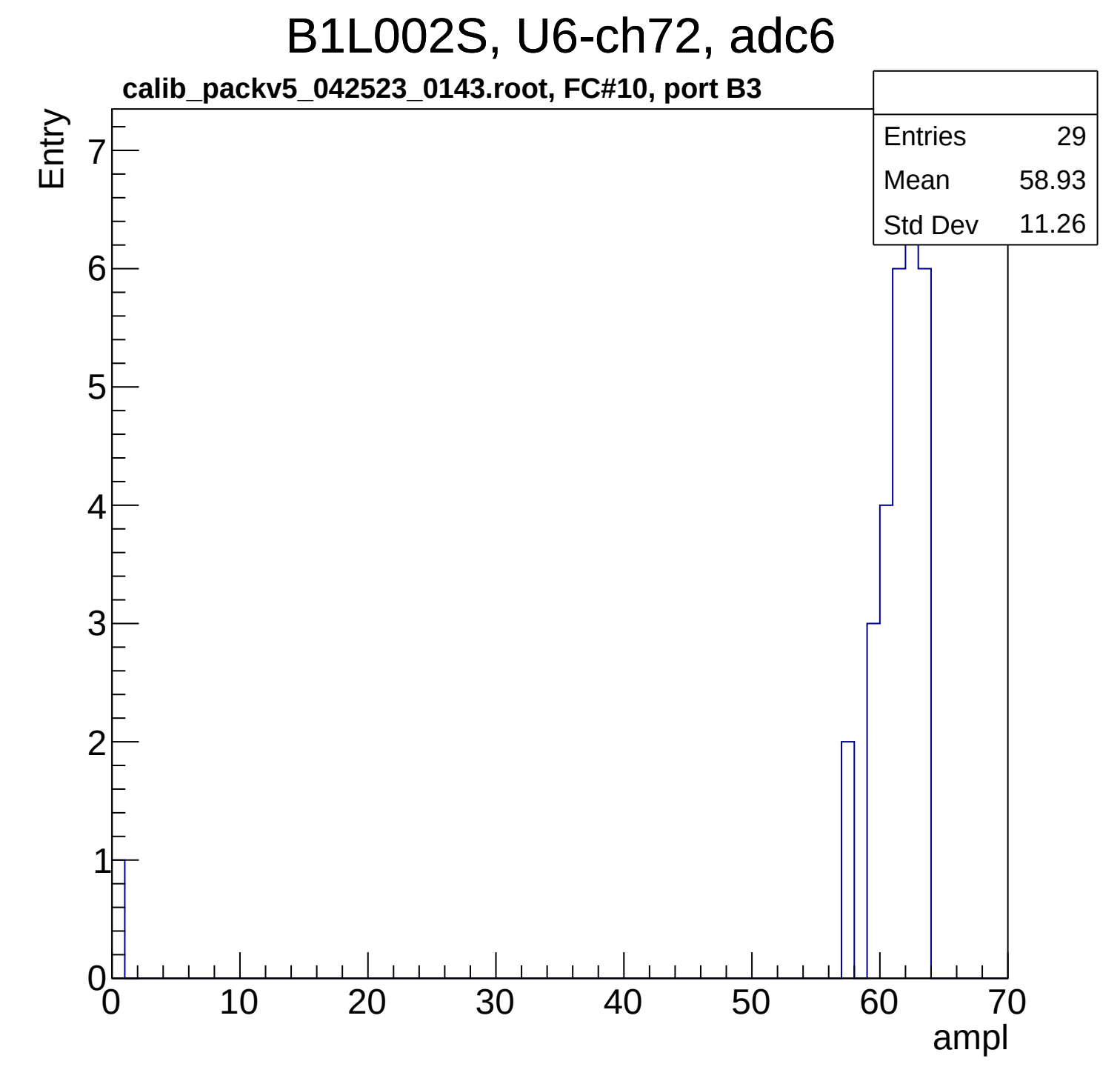

B1L002S, U6-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 32.64
Std Dev 2.855
Gaus mean : 33.2044
Gaus Width: 2.4856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

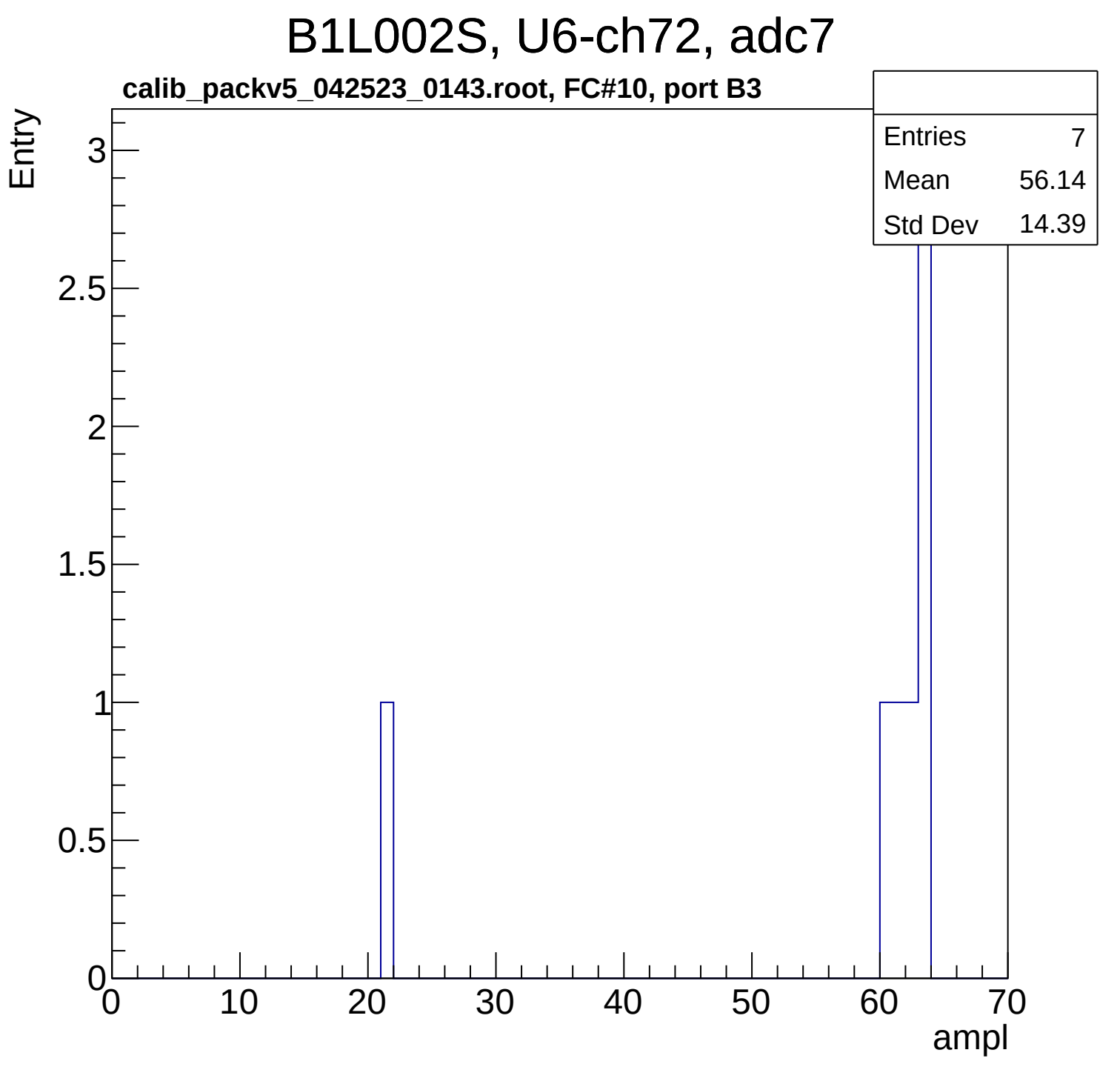

B1L002S, U6-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 38.94
Std Dev 3.211
Gaus mean : 39.2418
Gaus Width: 2.7091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

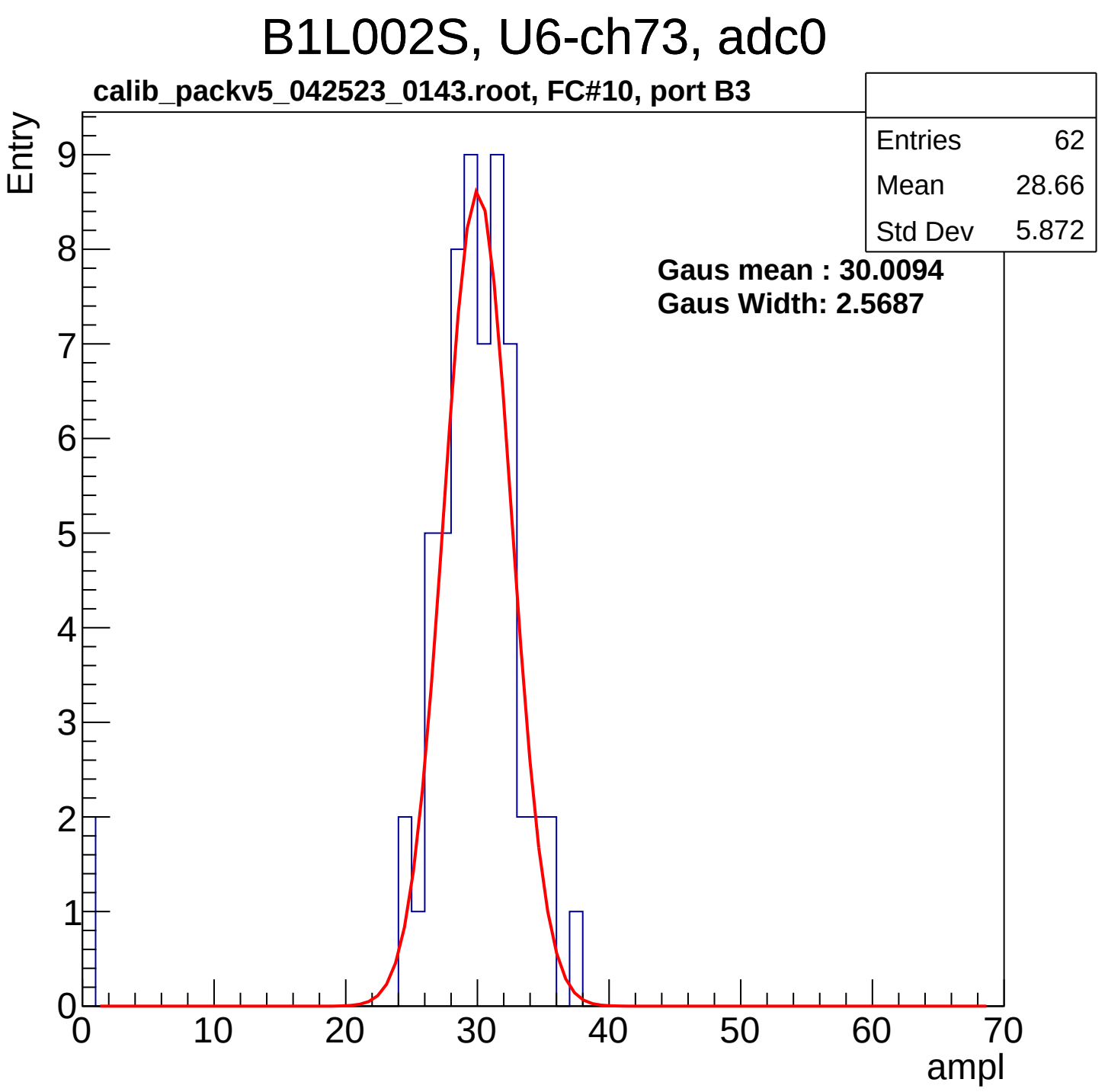

B1L002S, U6-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.02
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

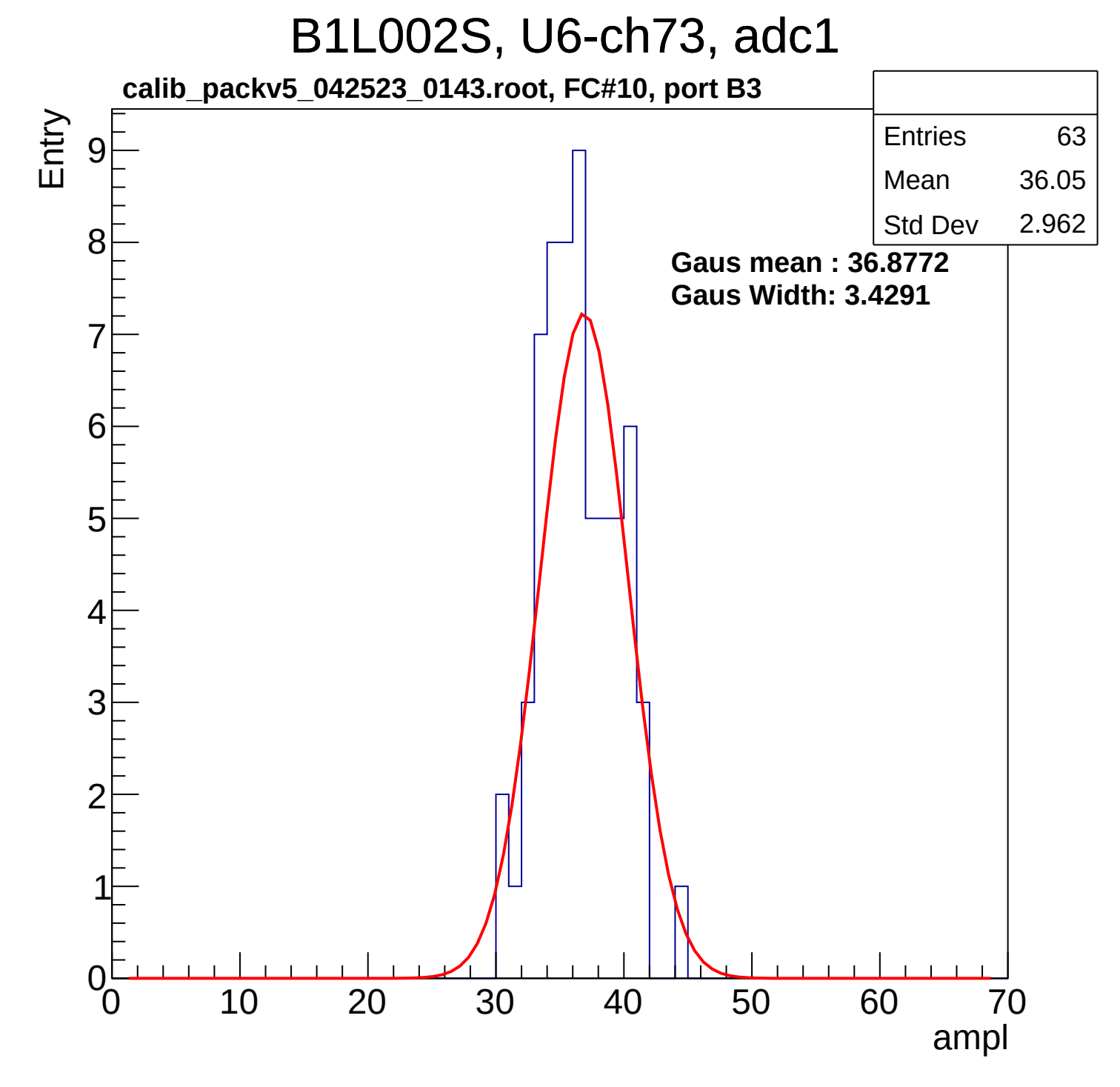

B1L002S, U6-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 50.46
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

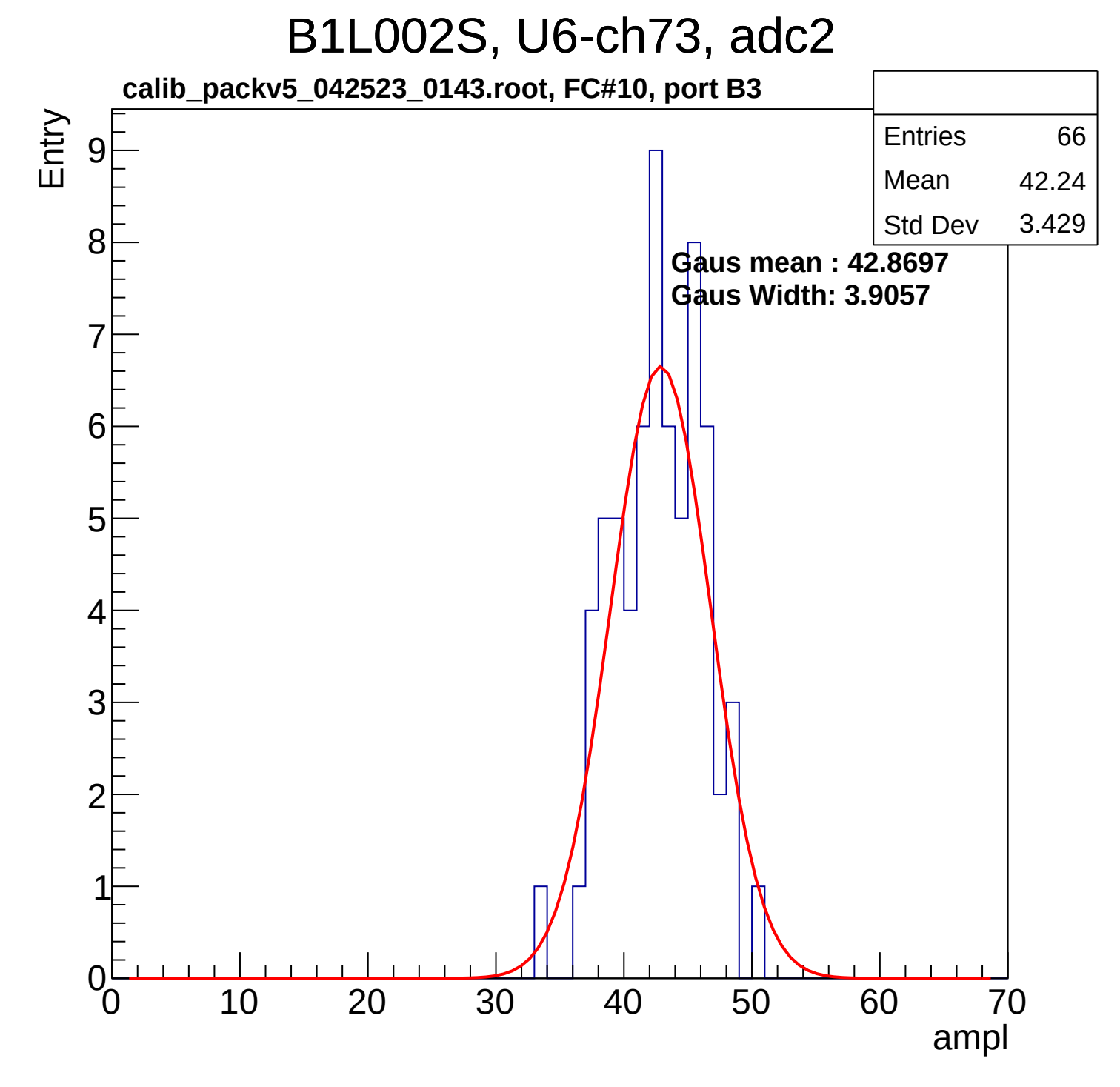

B1L002S, U6-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 56.64
Std Dev 2.848
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

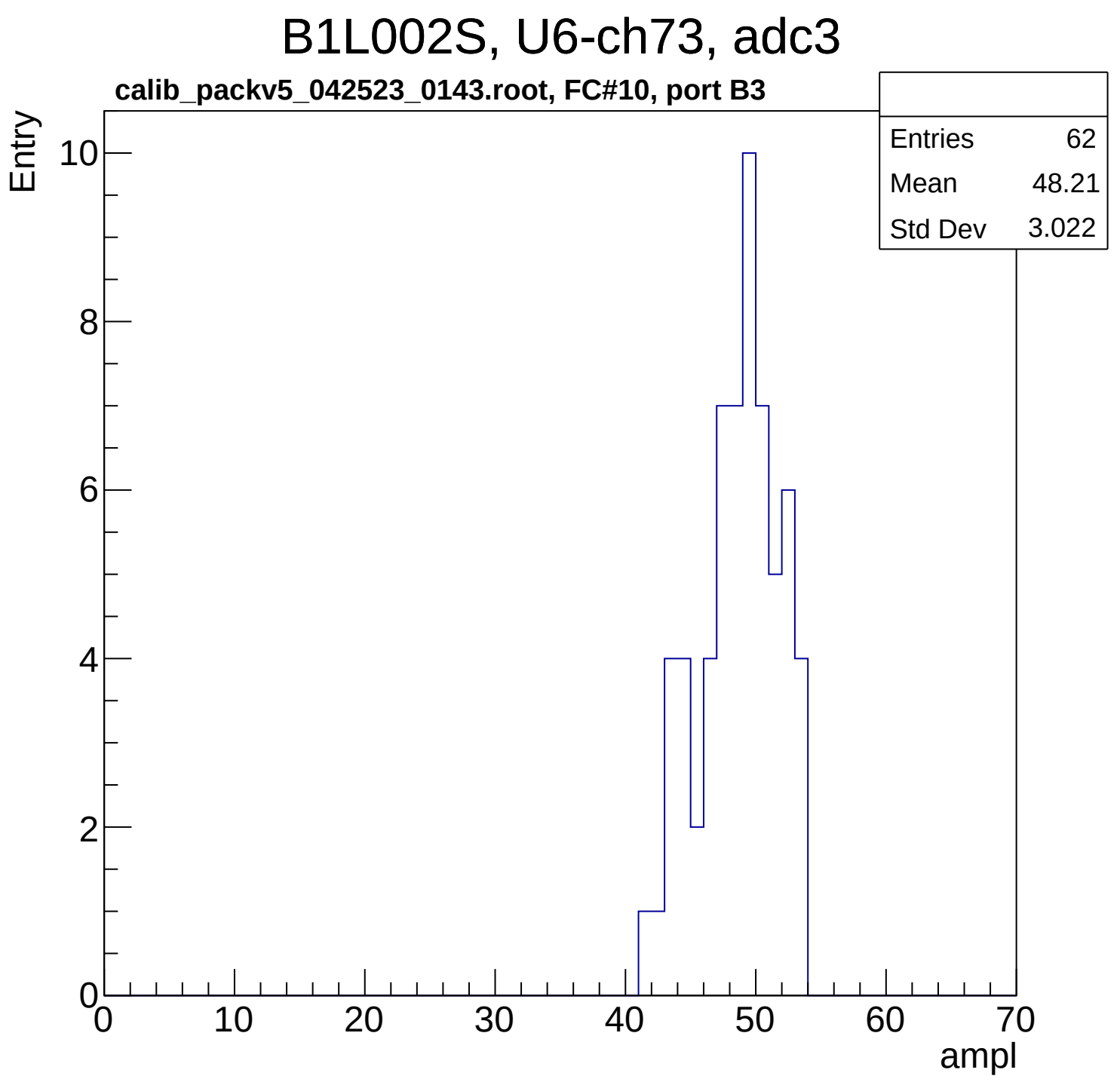

B1L002S, U6-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.93
Std Dev 11.26
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

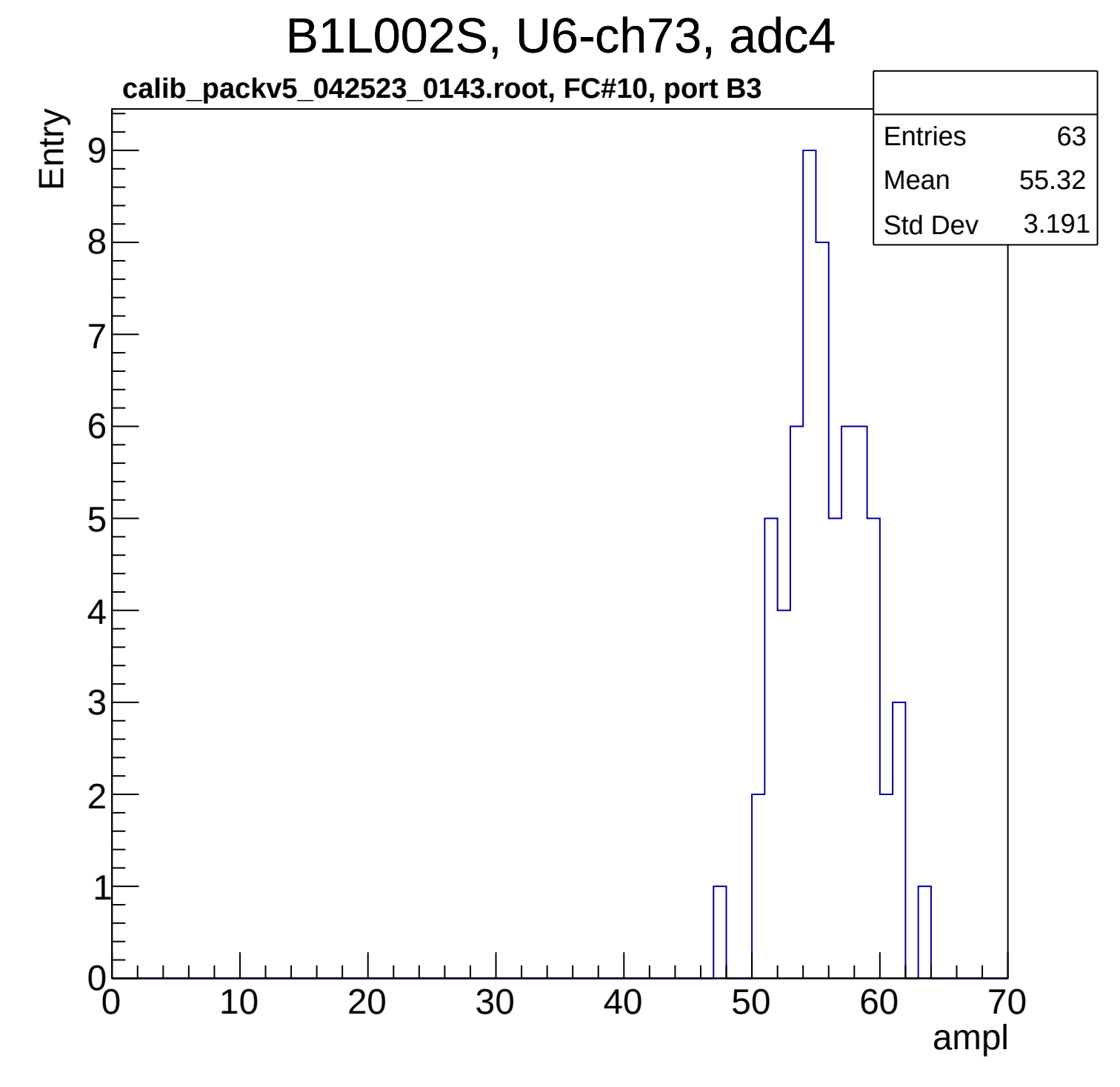

B1L002S, U6-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 56.14
Std Dev 14.39
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

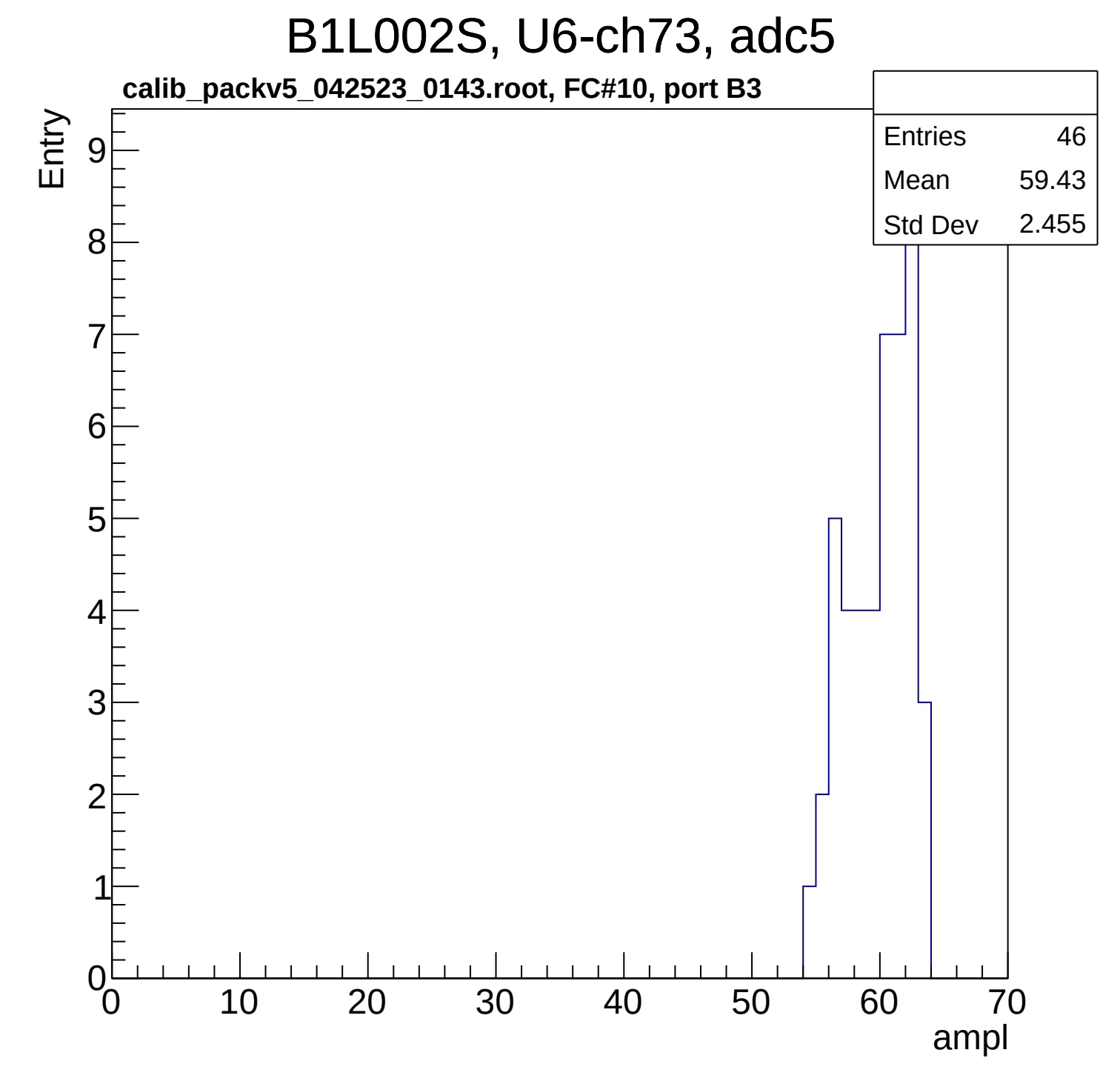

B1L002S, U6-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.66
Std Dev 5.872
Gaus mean : 30.0094
Gaus Width: 2.5687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

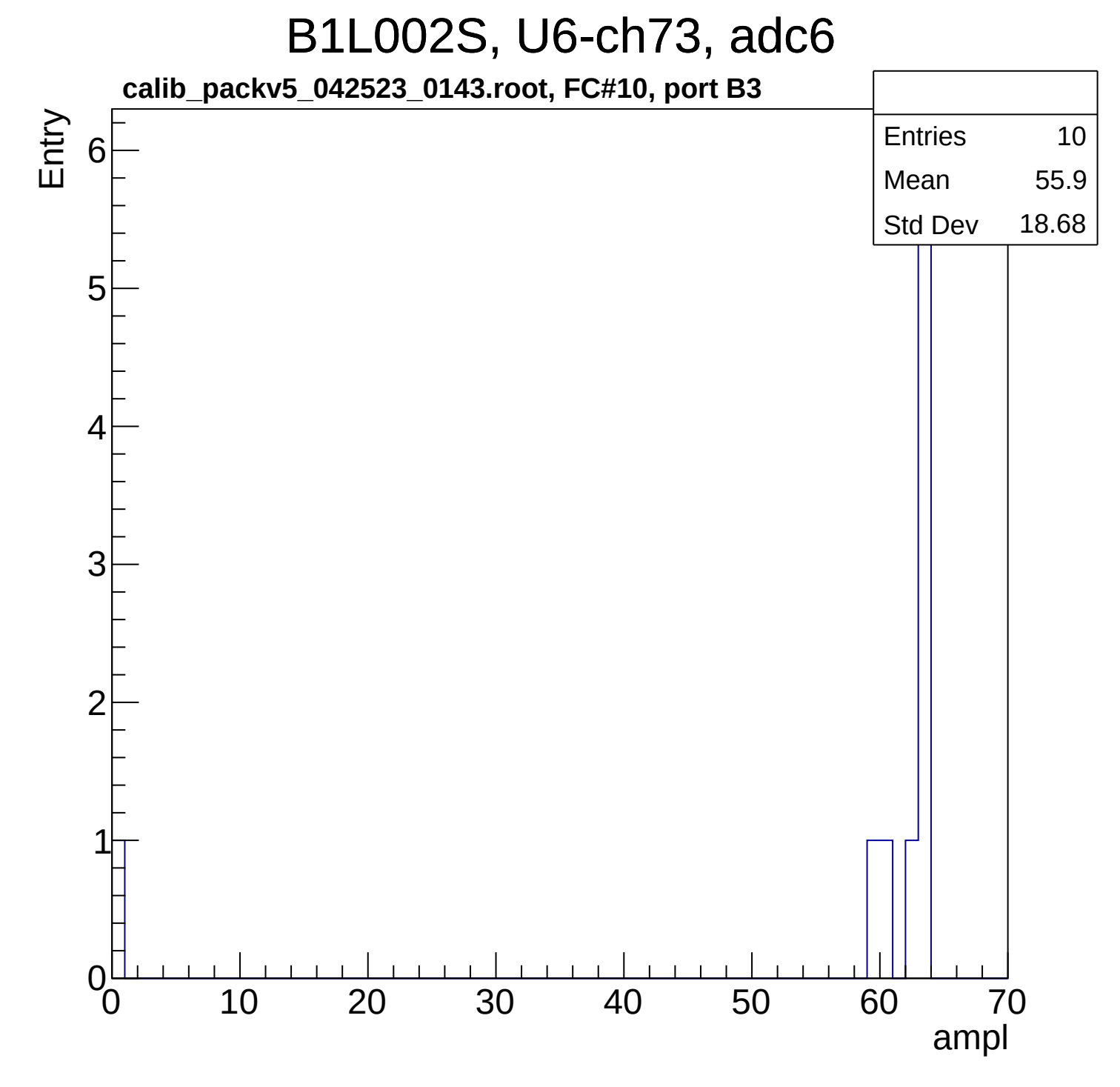

B1L002S, U6-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.05
Std Dev 2.962
Gaus mean : 36.8772
Gaus Width: 3.4291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

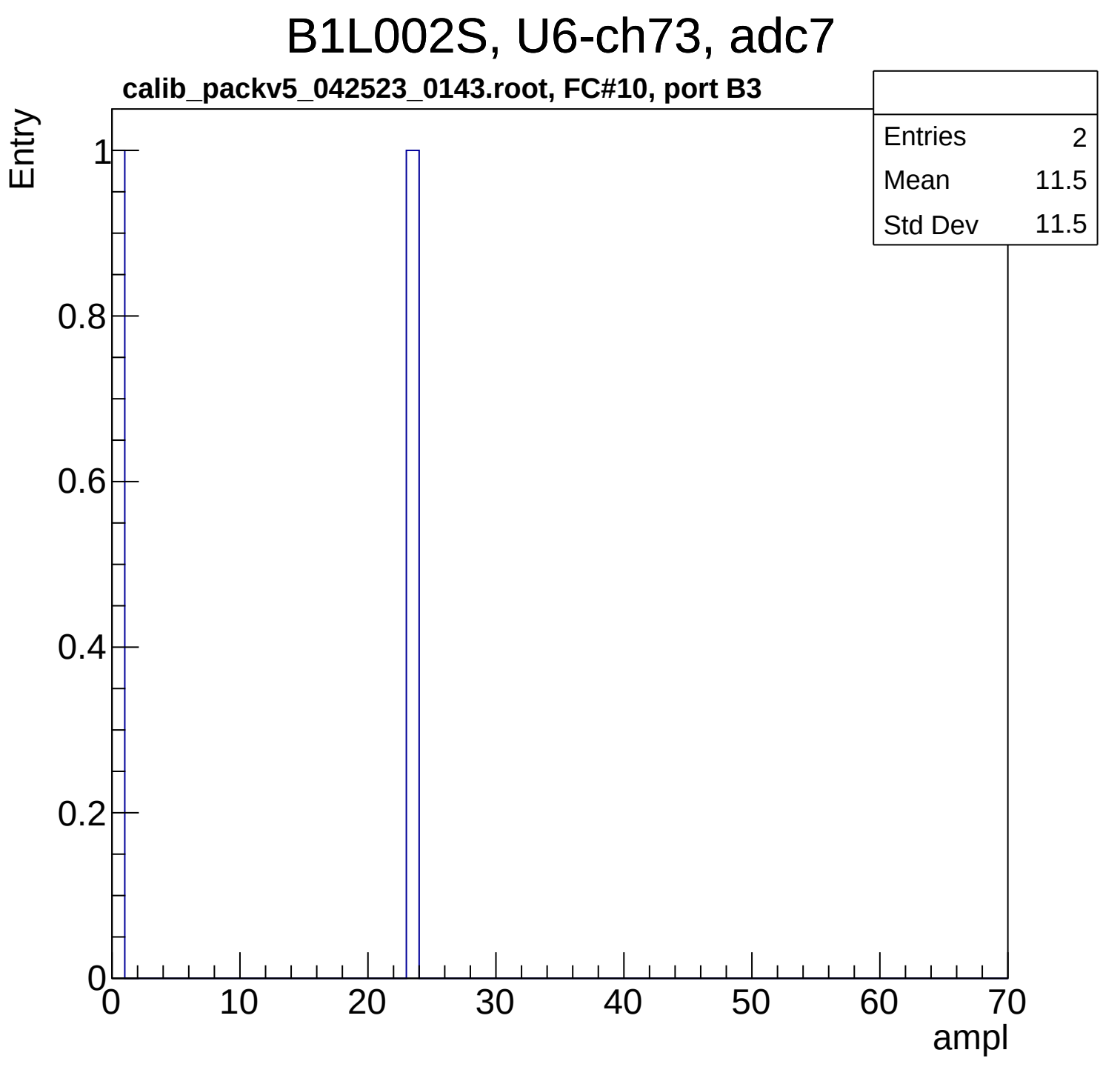

B1L002S, U6-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.24
Std Dev 3.429
Gaus mean : 42.8697
Gaus Width: 3.9057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

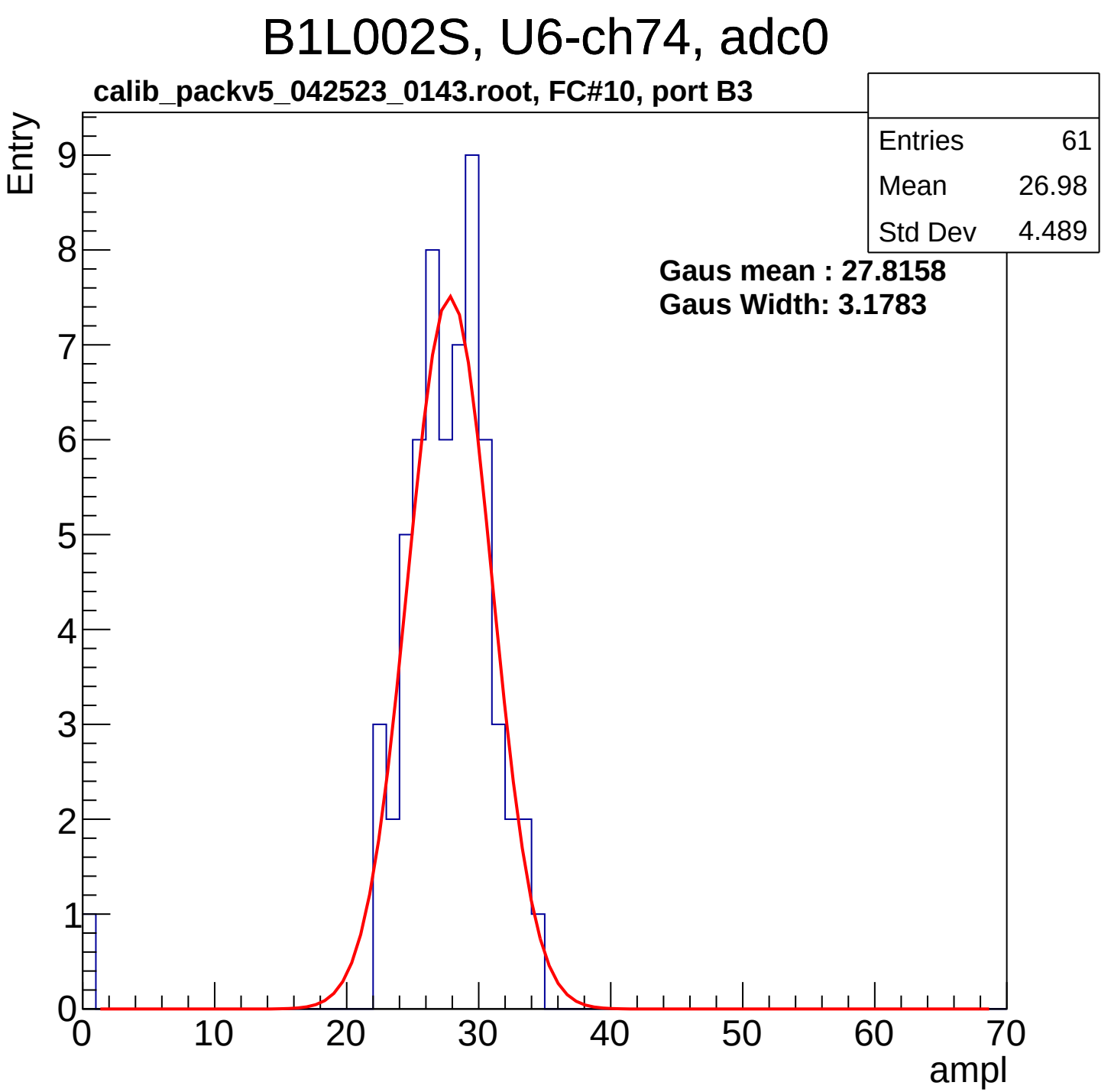

B1L002S, U6-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 48.21
Std Dev 3.022
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

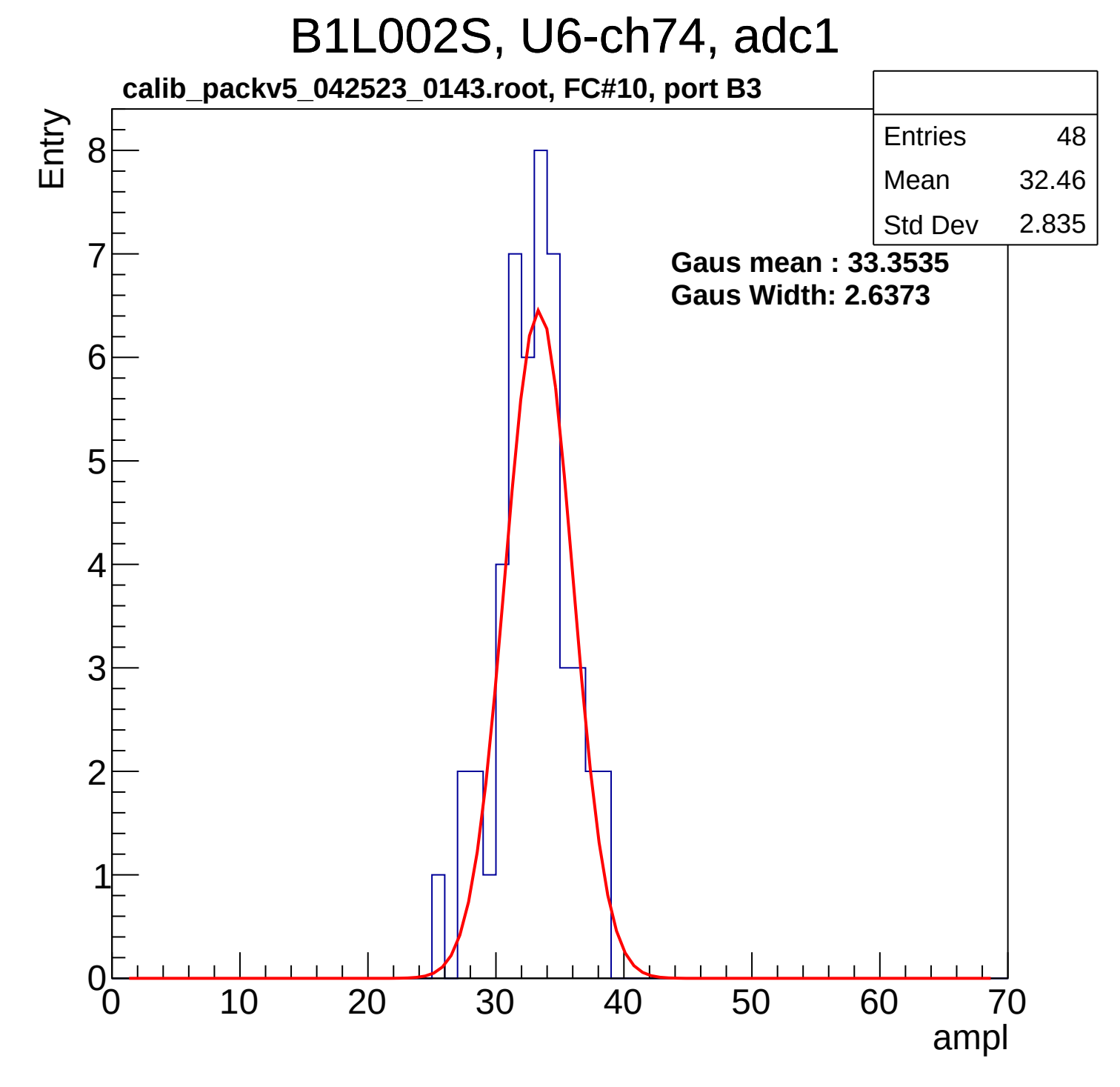

B1L002S, U6-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 55.32
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

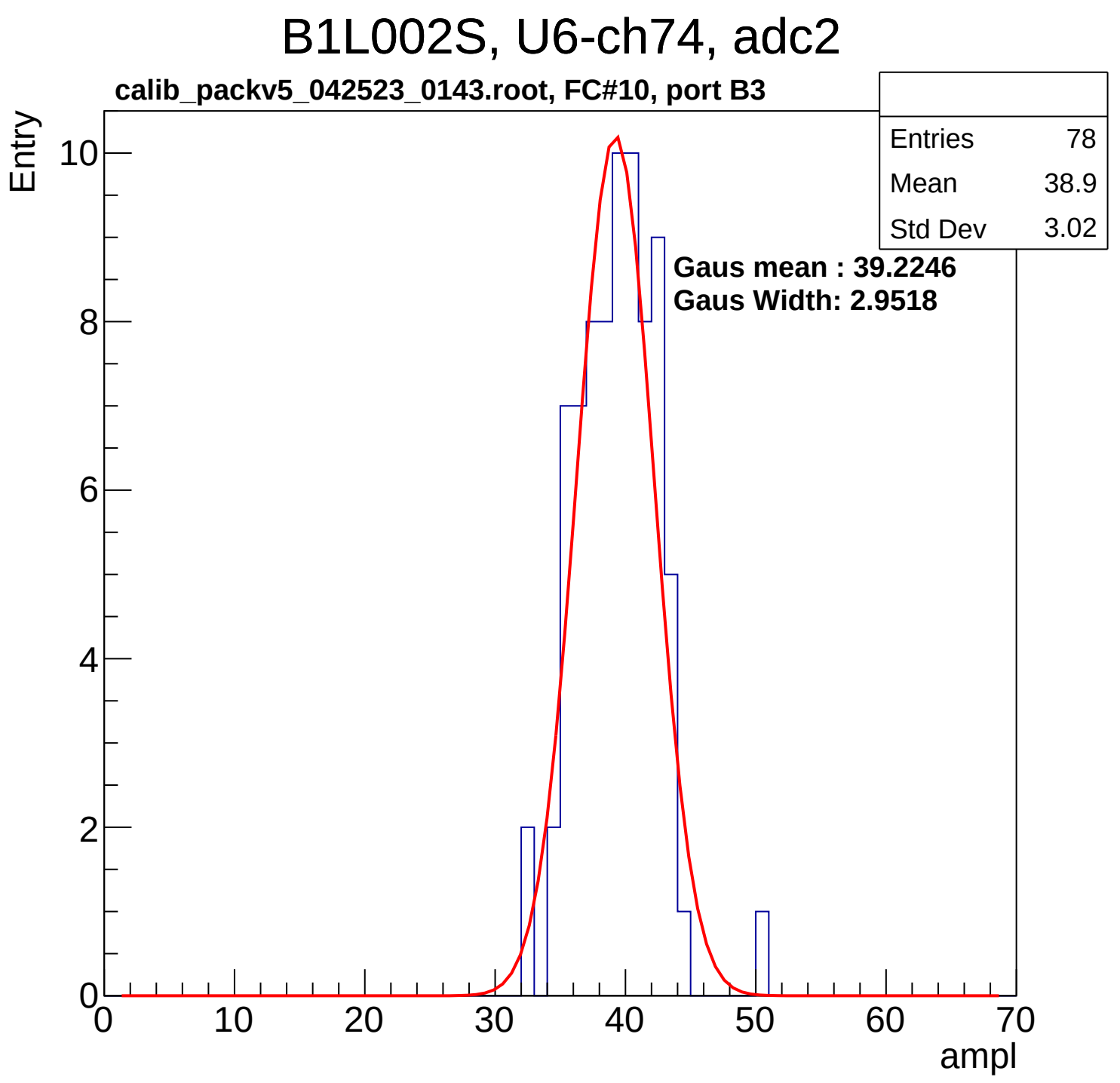

B1L002S, U6-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 59.43
Std Dev 2.455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

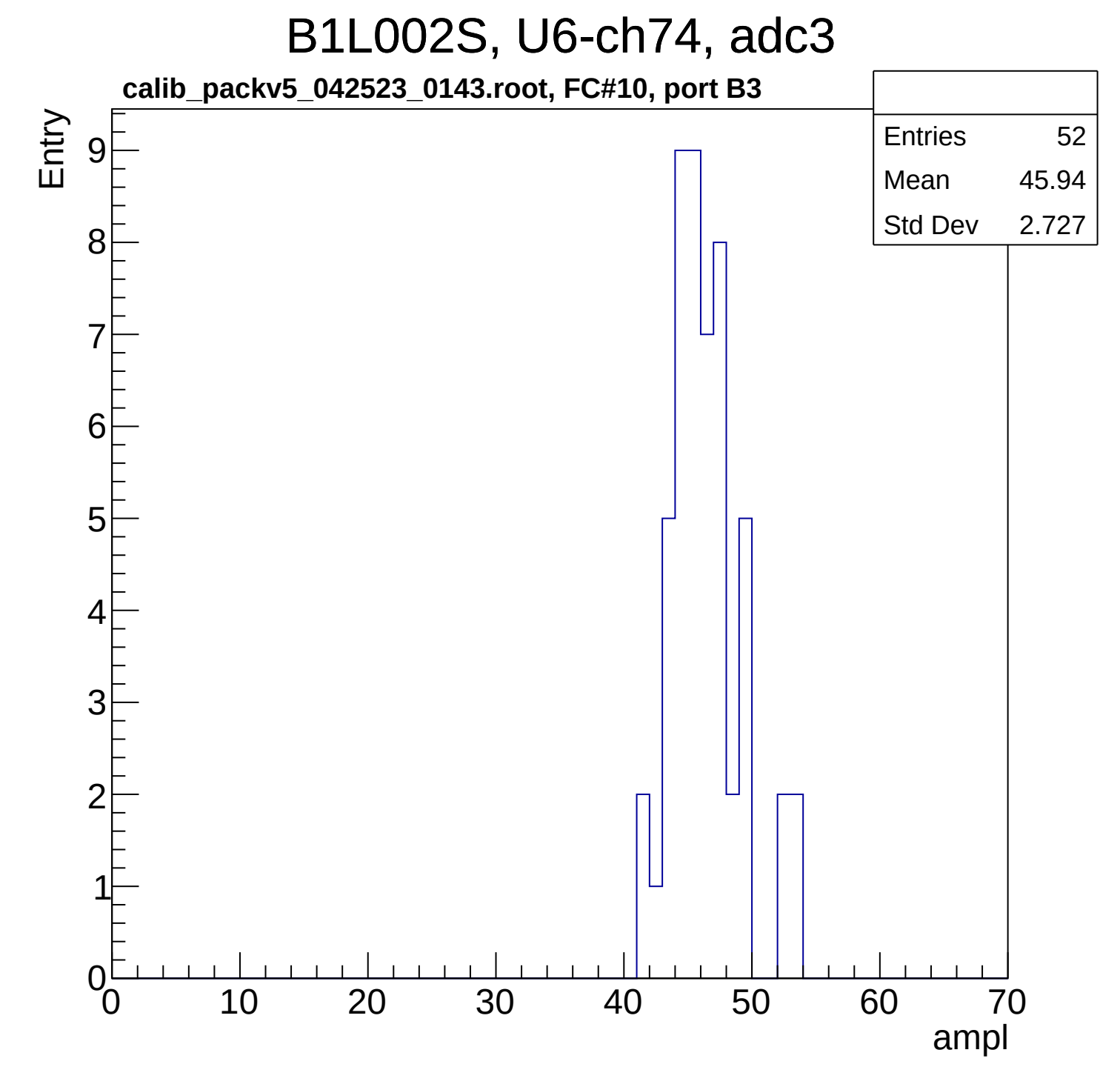

B1L002S, U6-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.9
Std Dev 18.68
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

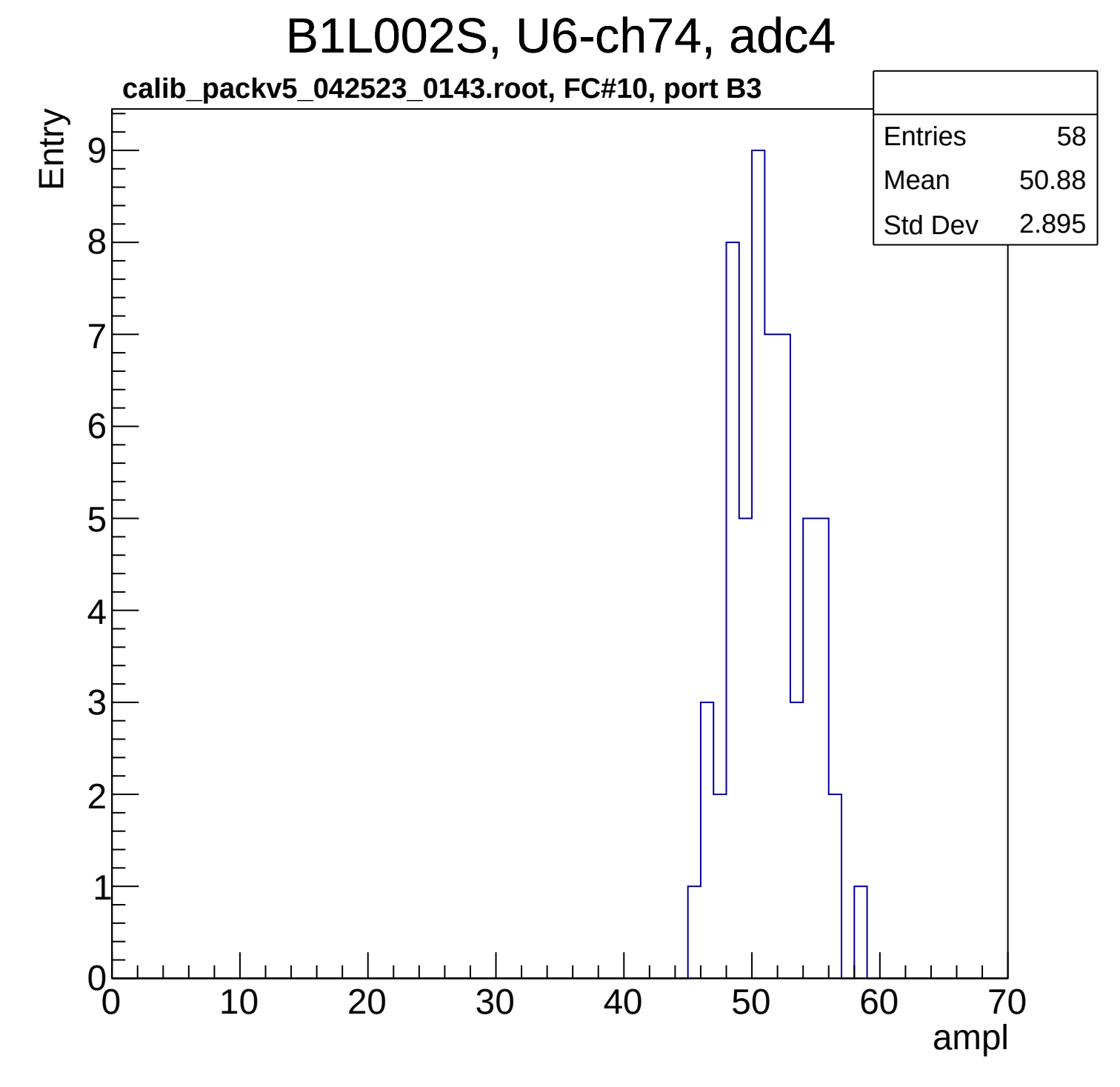

B1L002S, U6-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

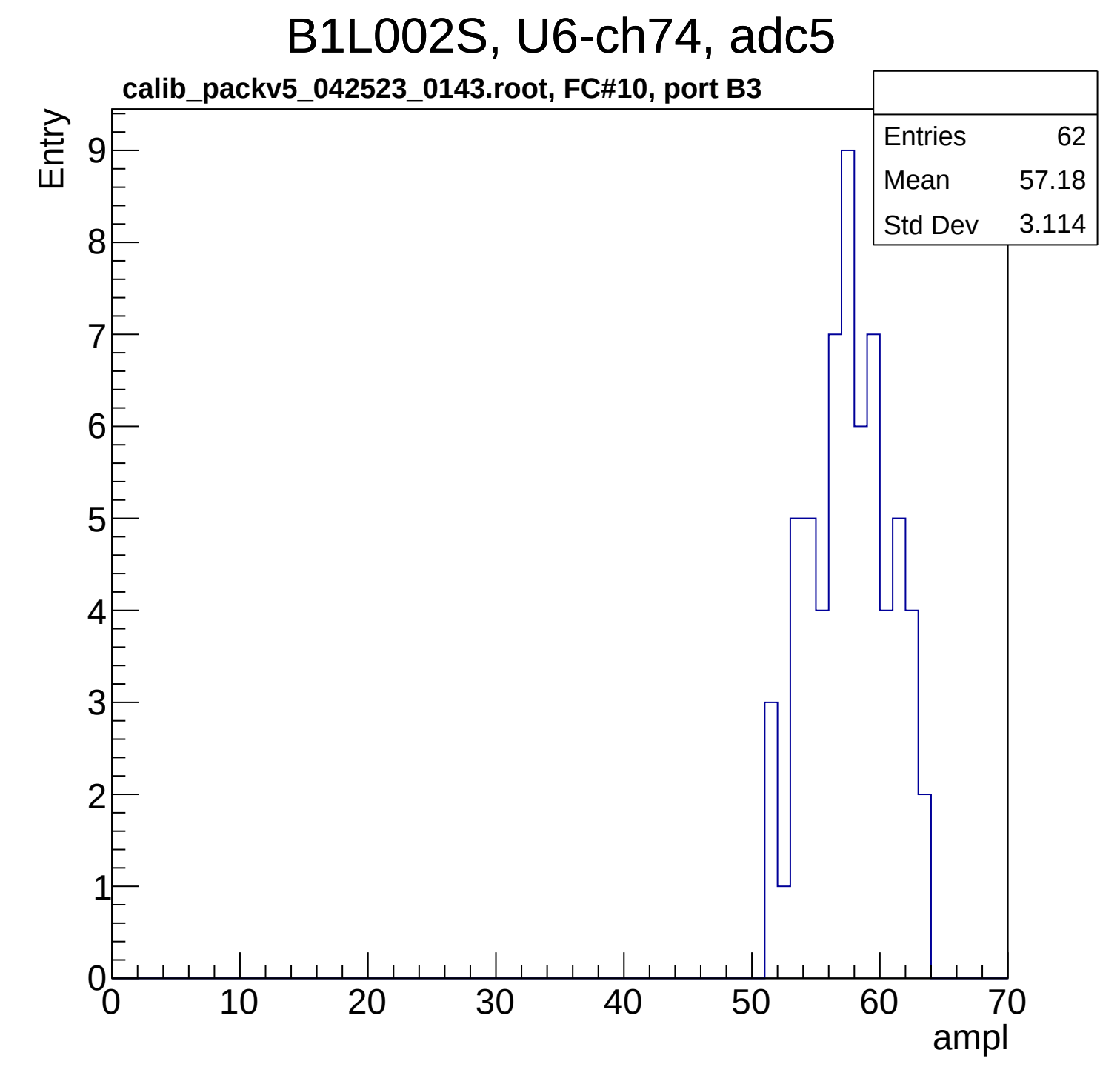

B1L002S, U6-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.98
Std Dev 4.489
Gaus mean : 27.8158
Gaus Width: 3.1783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

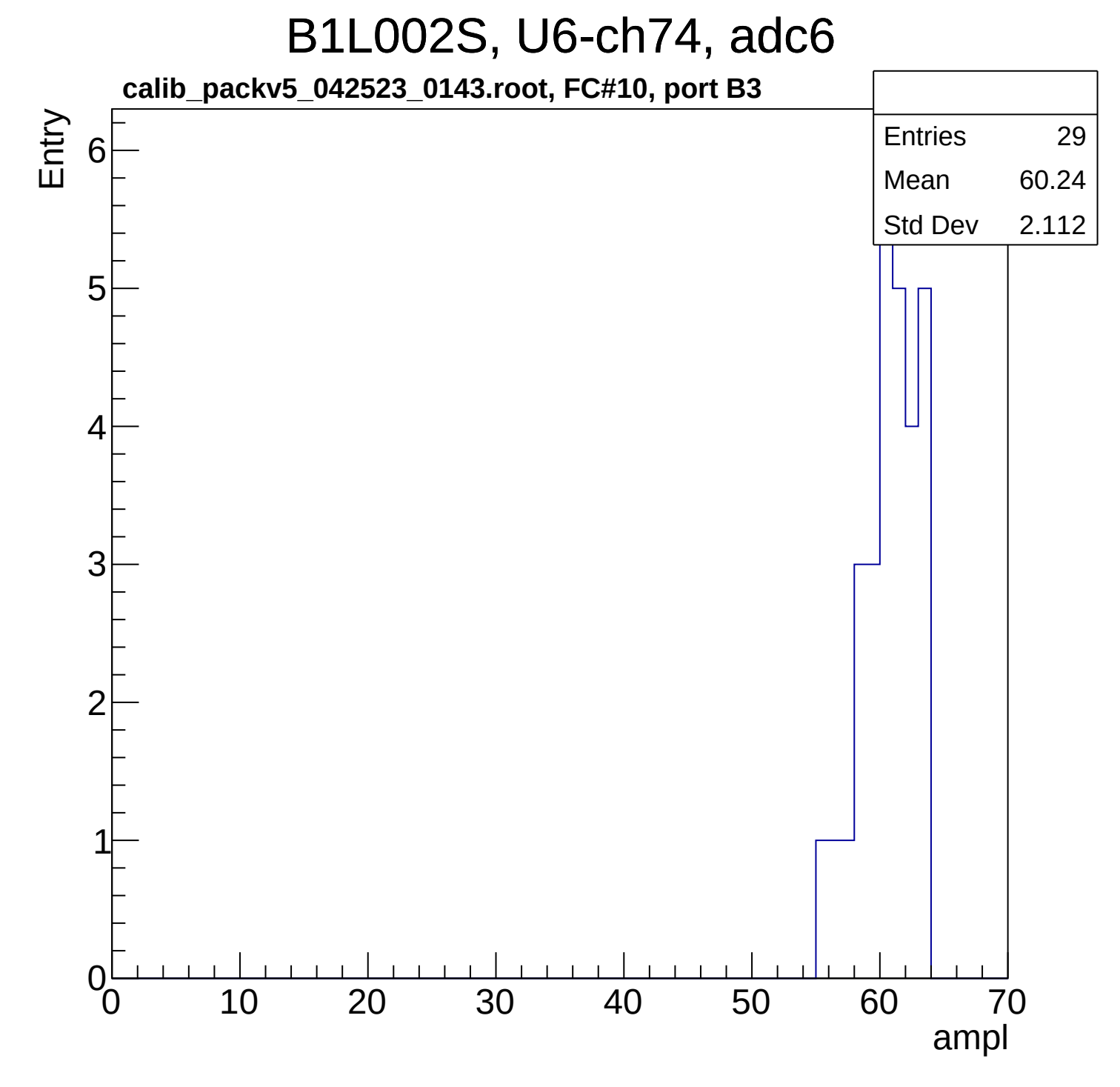

B1L002S, U6-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 32.46
Std Dev 2.835
Gaus mean : 33.3535
Gaus Width: 2.6373
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

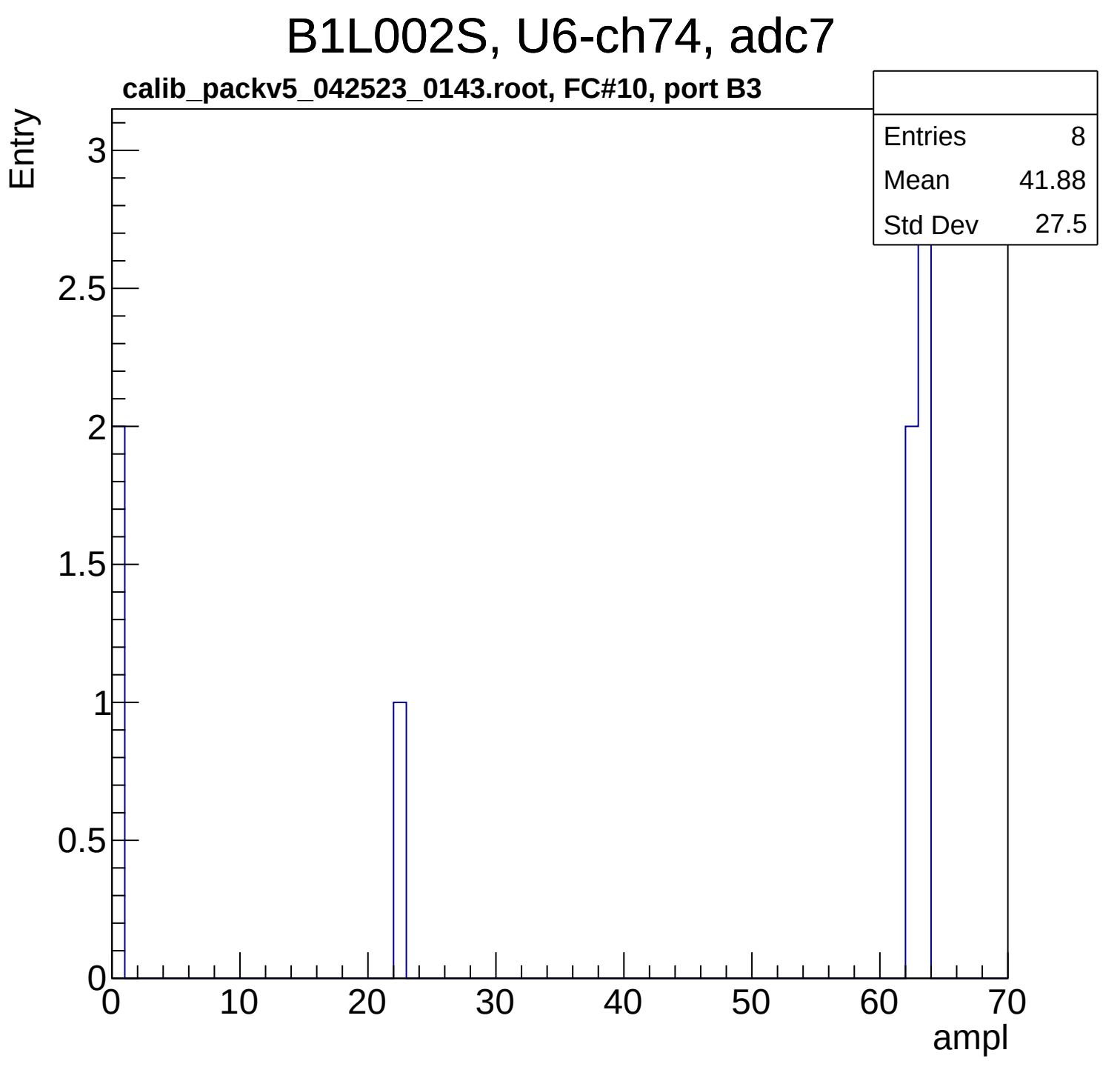

B1L002S, U6-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 38.9
Std Dev 3.02
Gaus mean : 39.2246
Gaus Width: 2.9518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

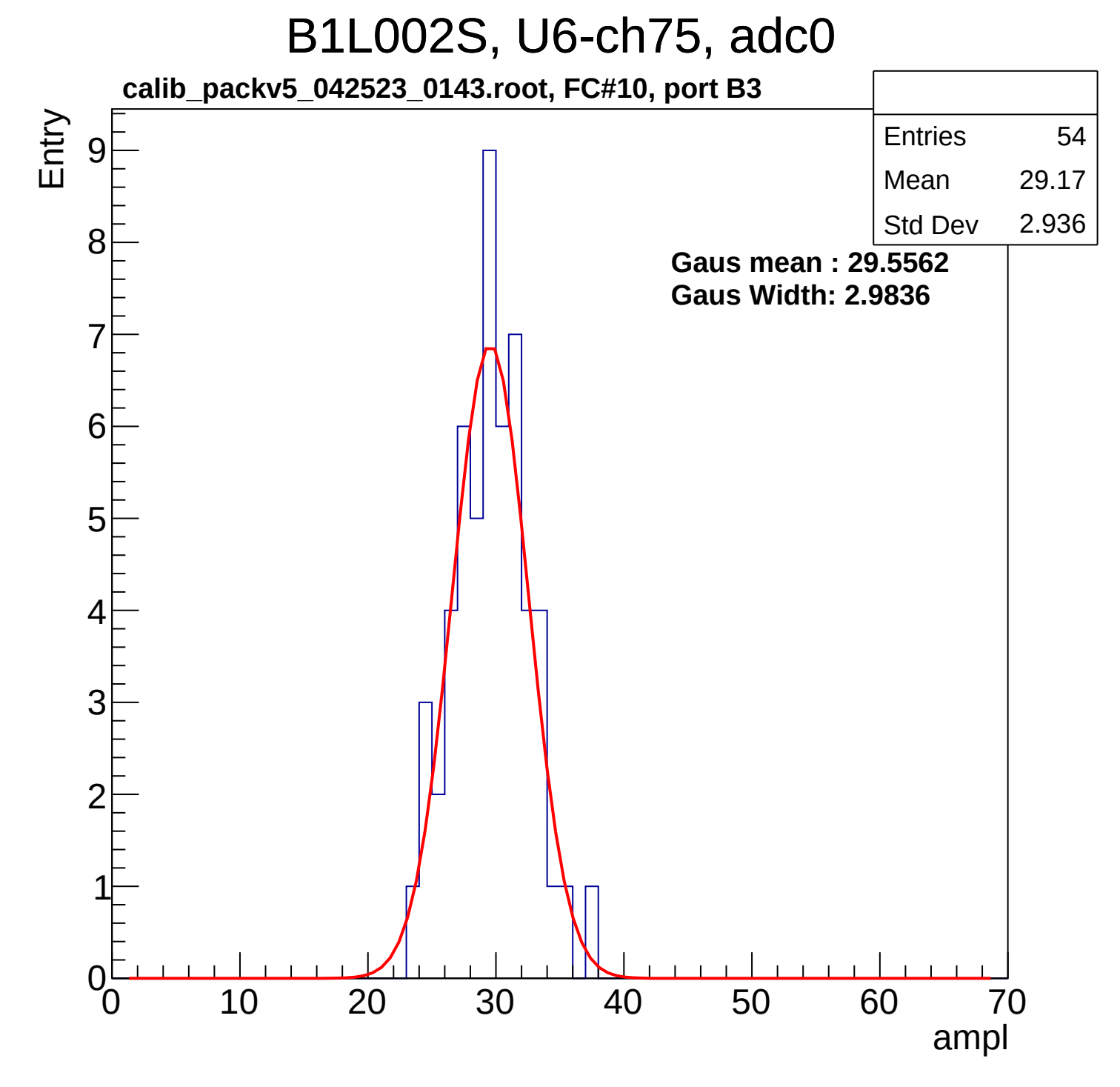

B1L002S, U6-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 45.94
Std Dev 2.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

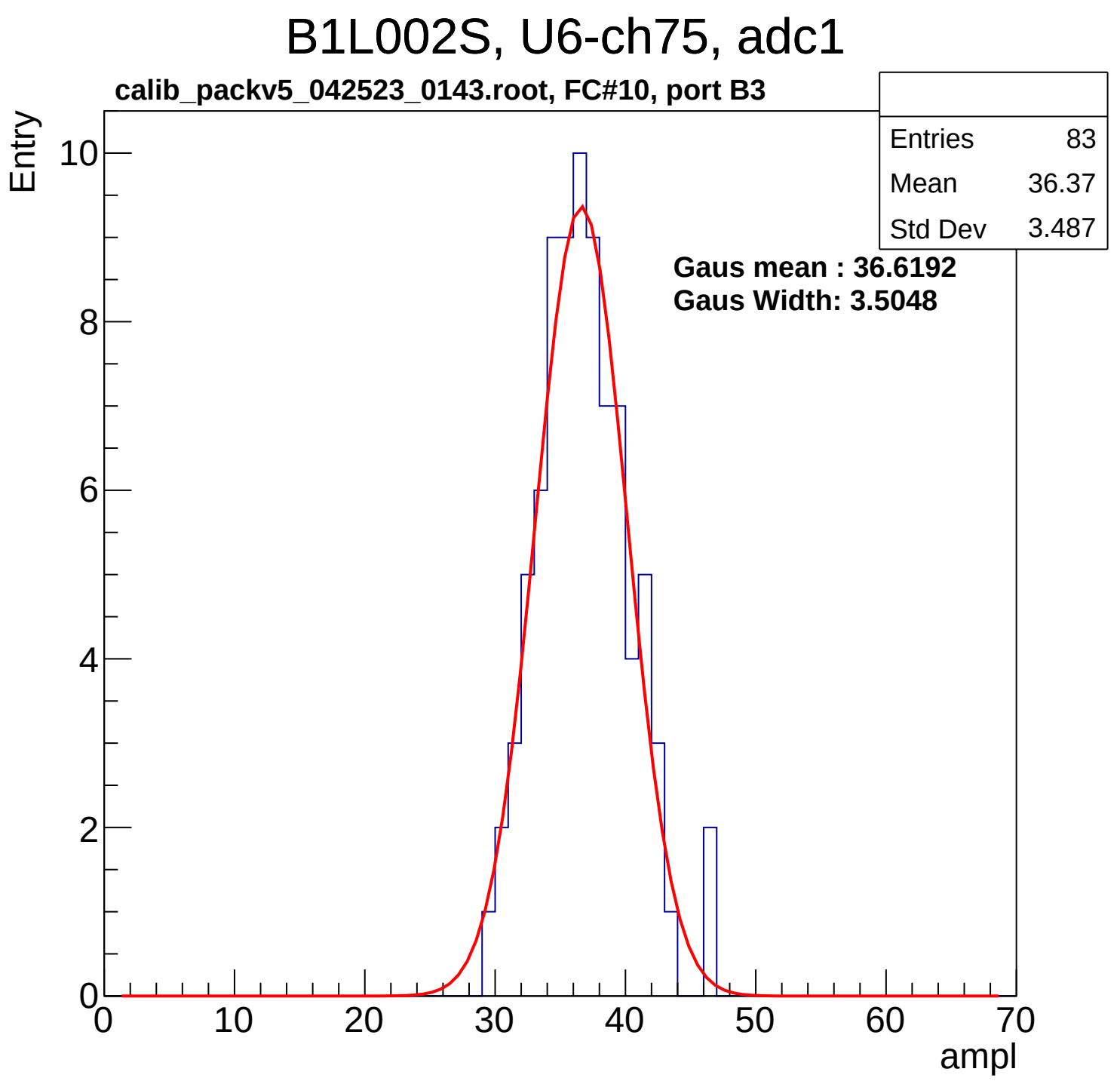

B1L002S, U6-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 50.88
Std Dev 2.895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

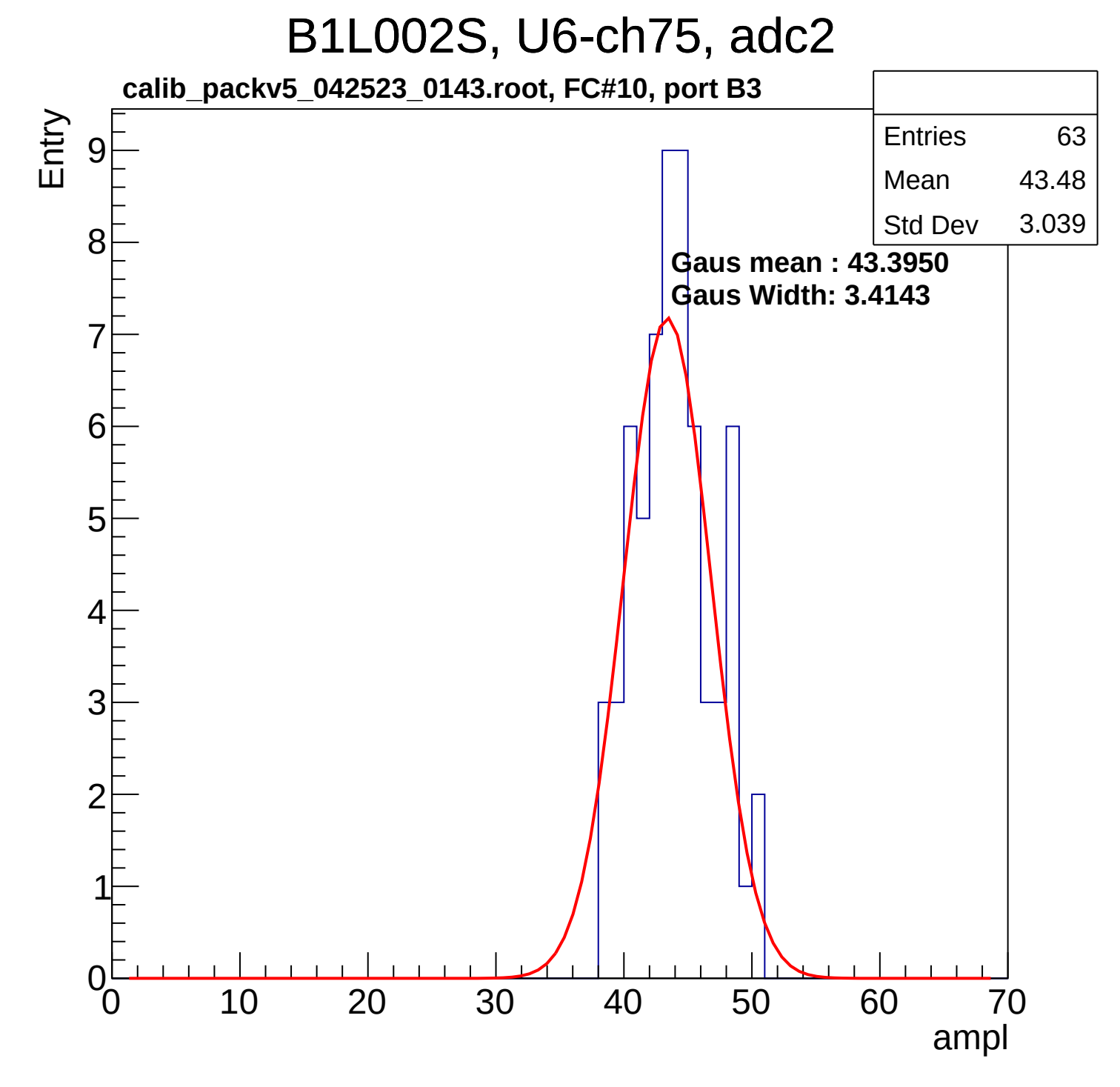

B1L002S, U6-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.18
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

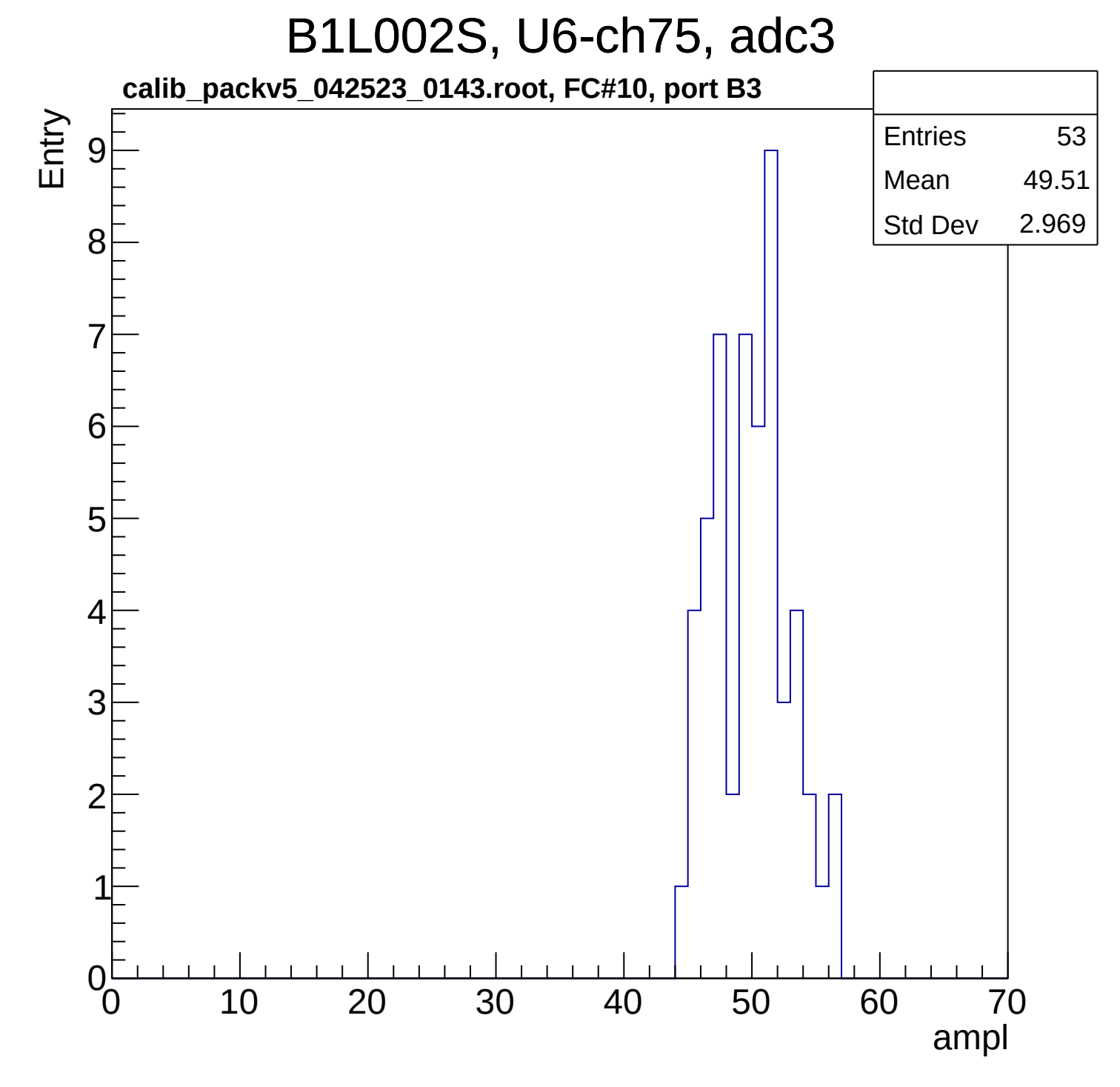

B1L002S, U6-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 60.24
Std Dev 2.112
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

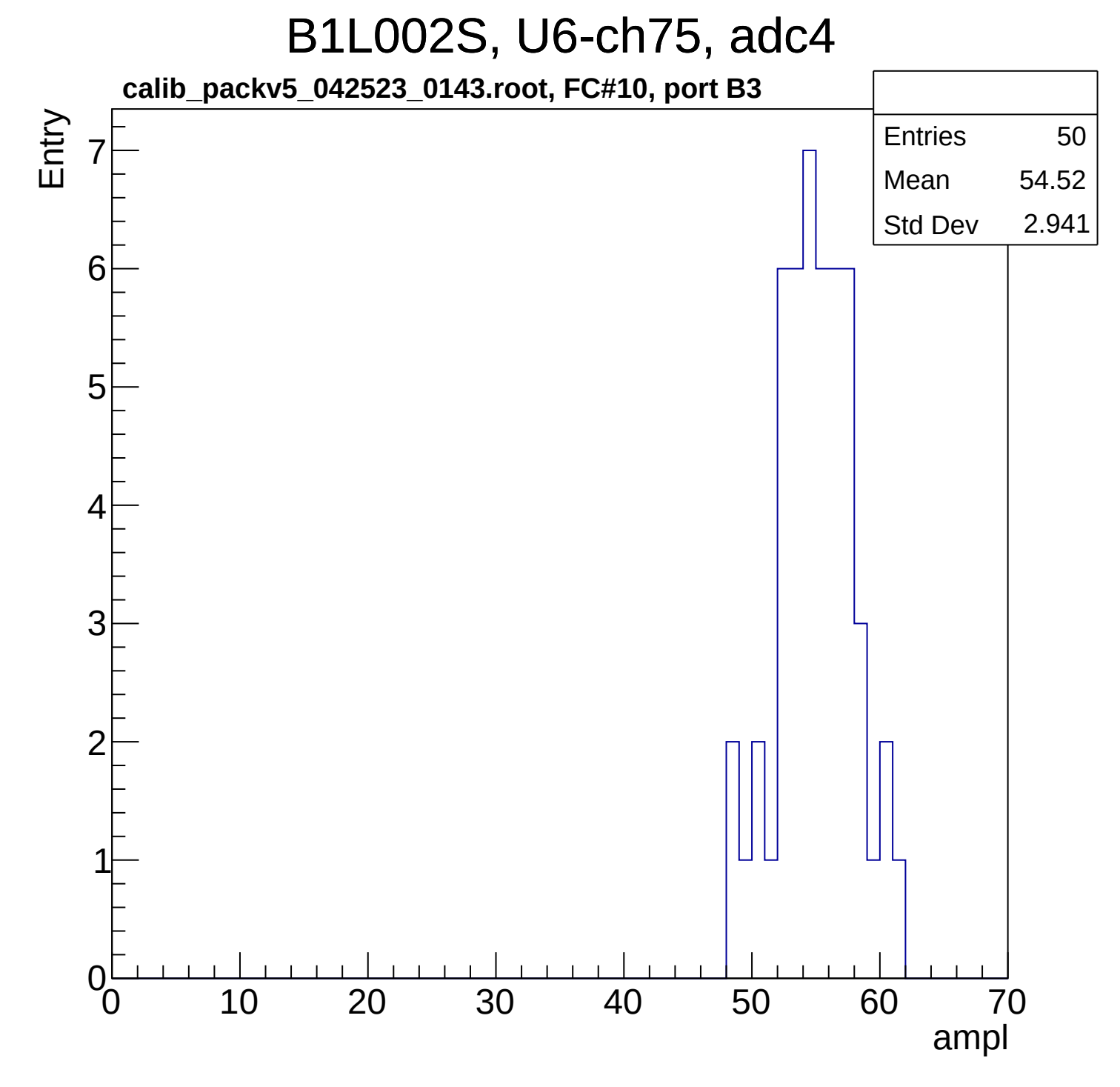

B1L002S, U6-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 41.88
Std Dev 27.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

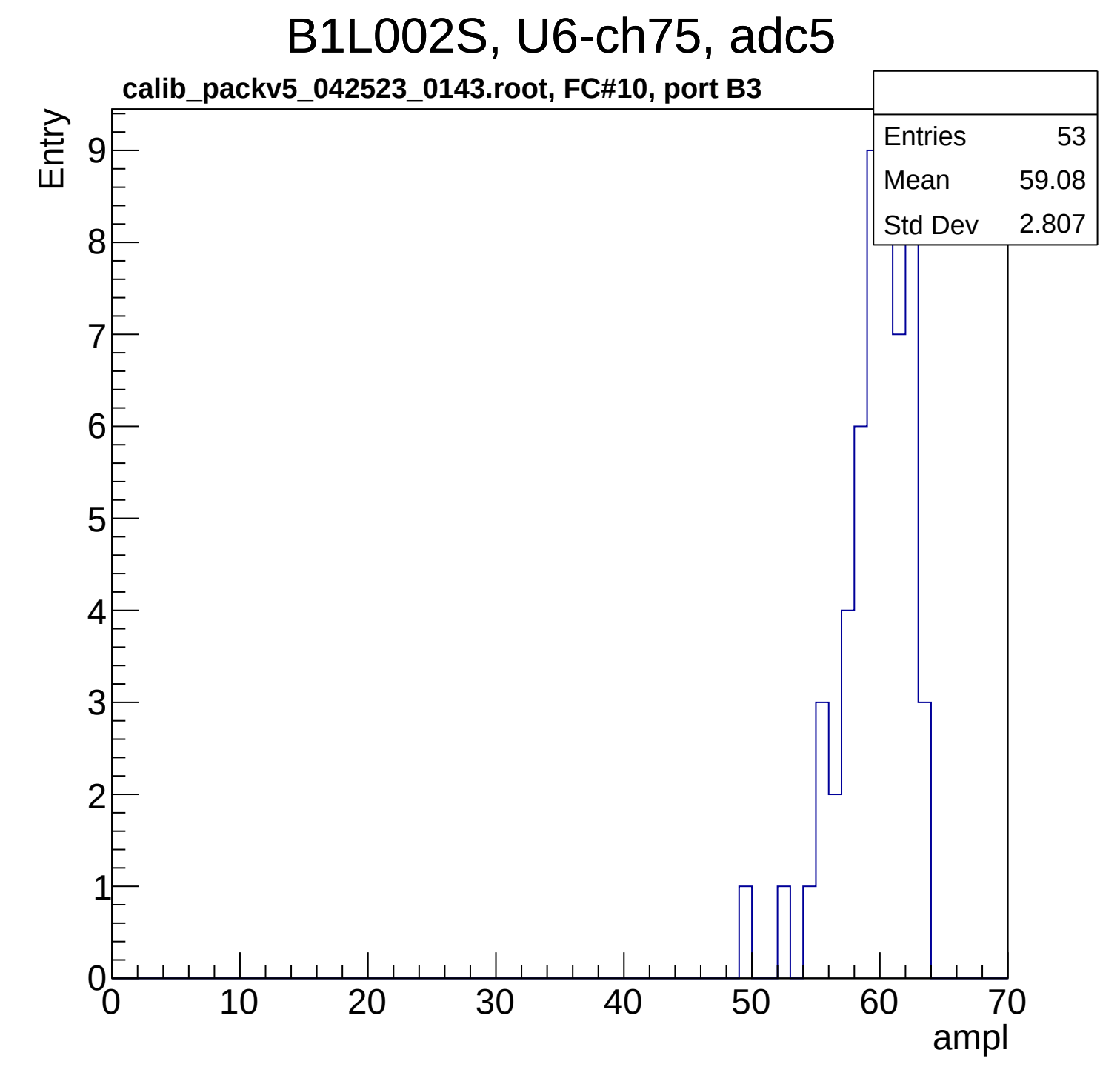

B1L002S, U6-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 29.17
Std Dev 2.936
Gaus mean : 29.5562
Gaus Width: 2.9836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

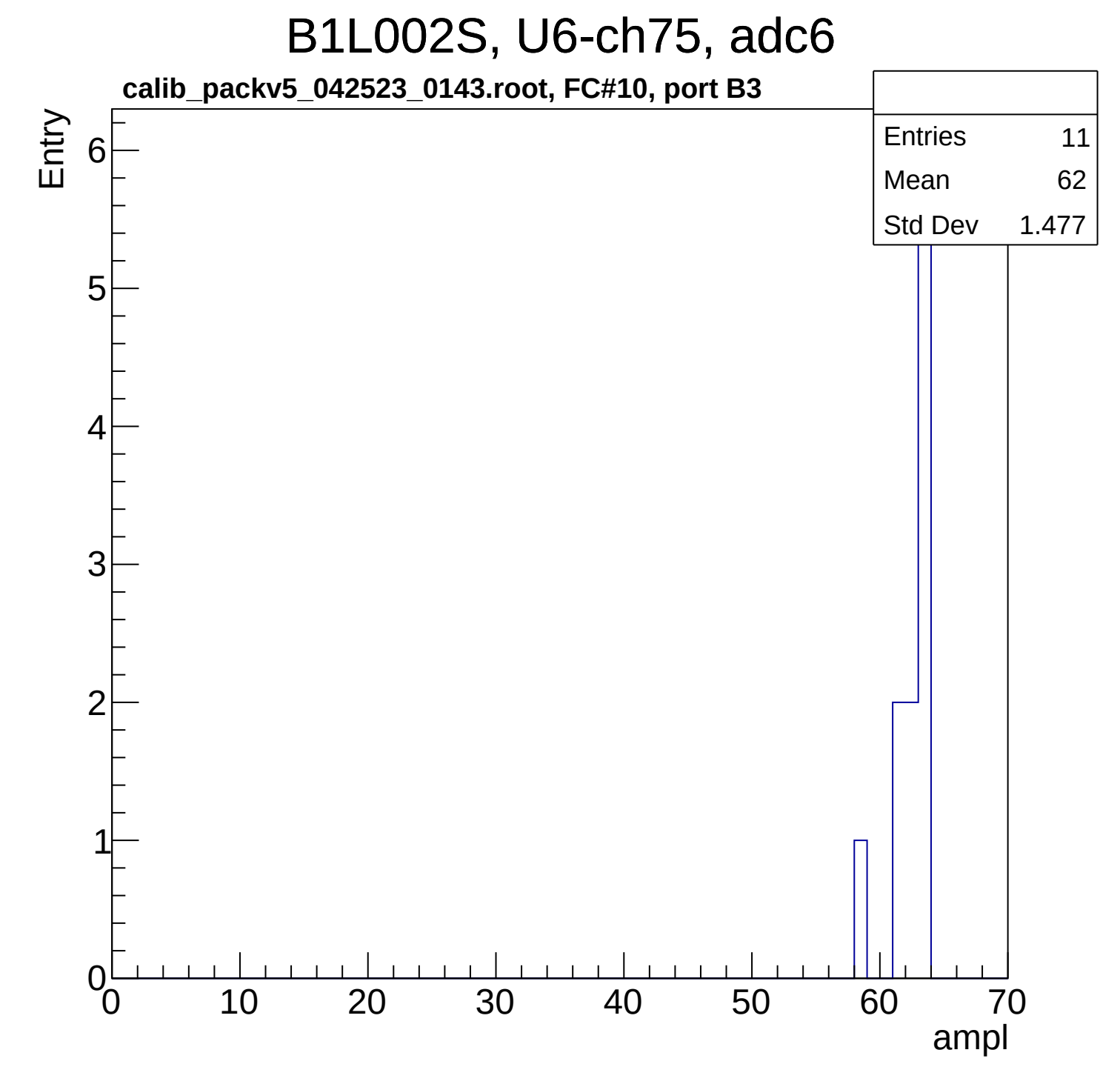

B1L002S, U6-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 36.37
Std Dev 3.487
Gaus mean : 36.6192
Gaus Width: 3.5048
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

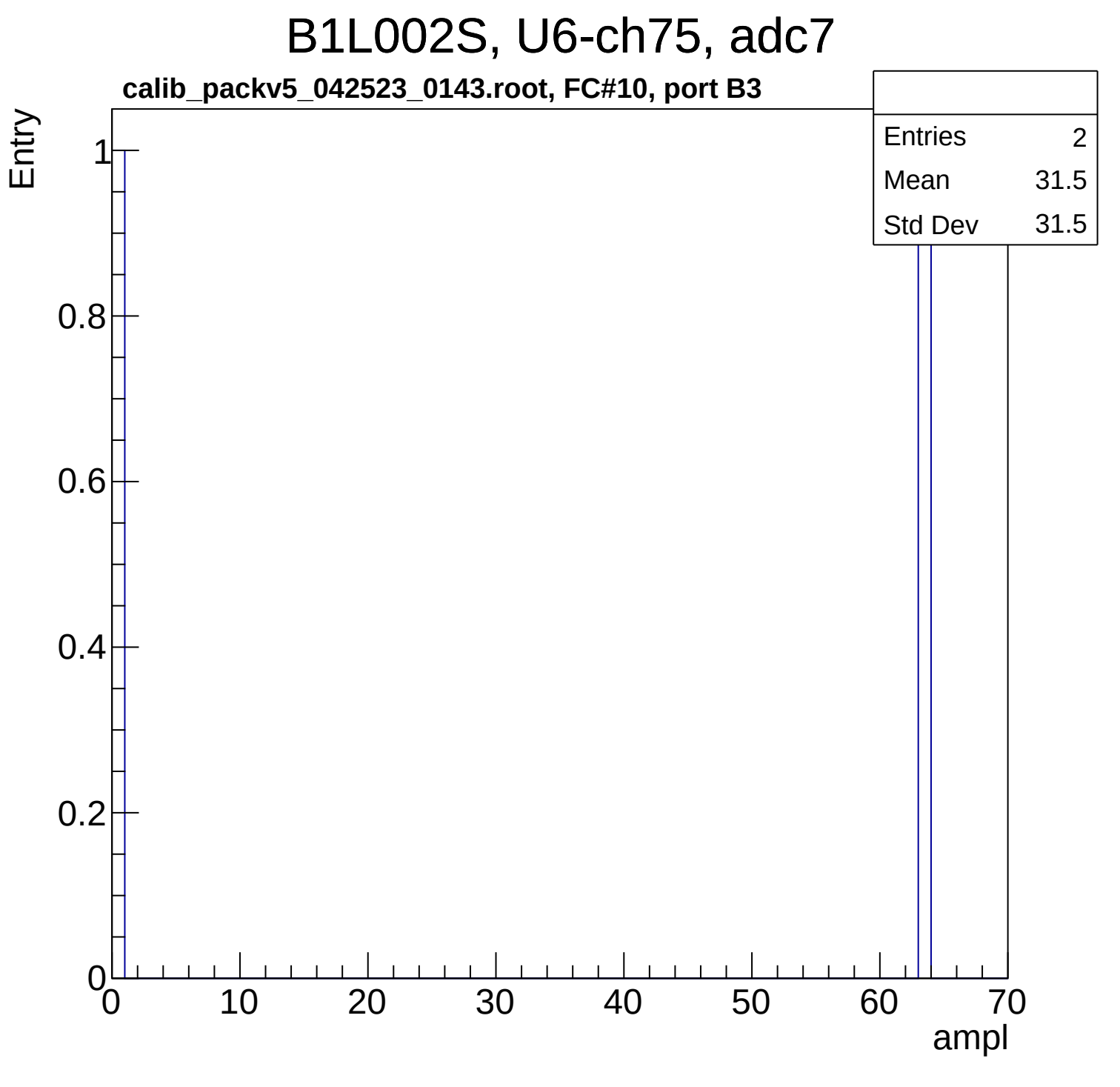

B1L002S, U6-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 43.48
Std Dev 3.039
Gaus mean : 43.3950
Gaus Width: 3.4143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

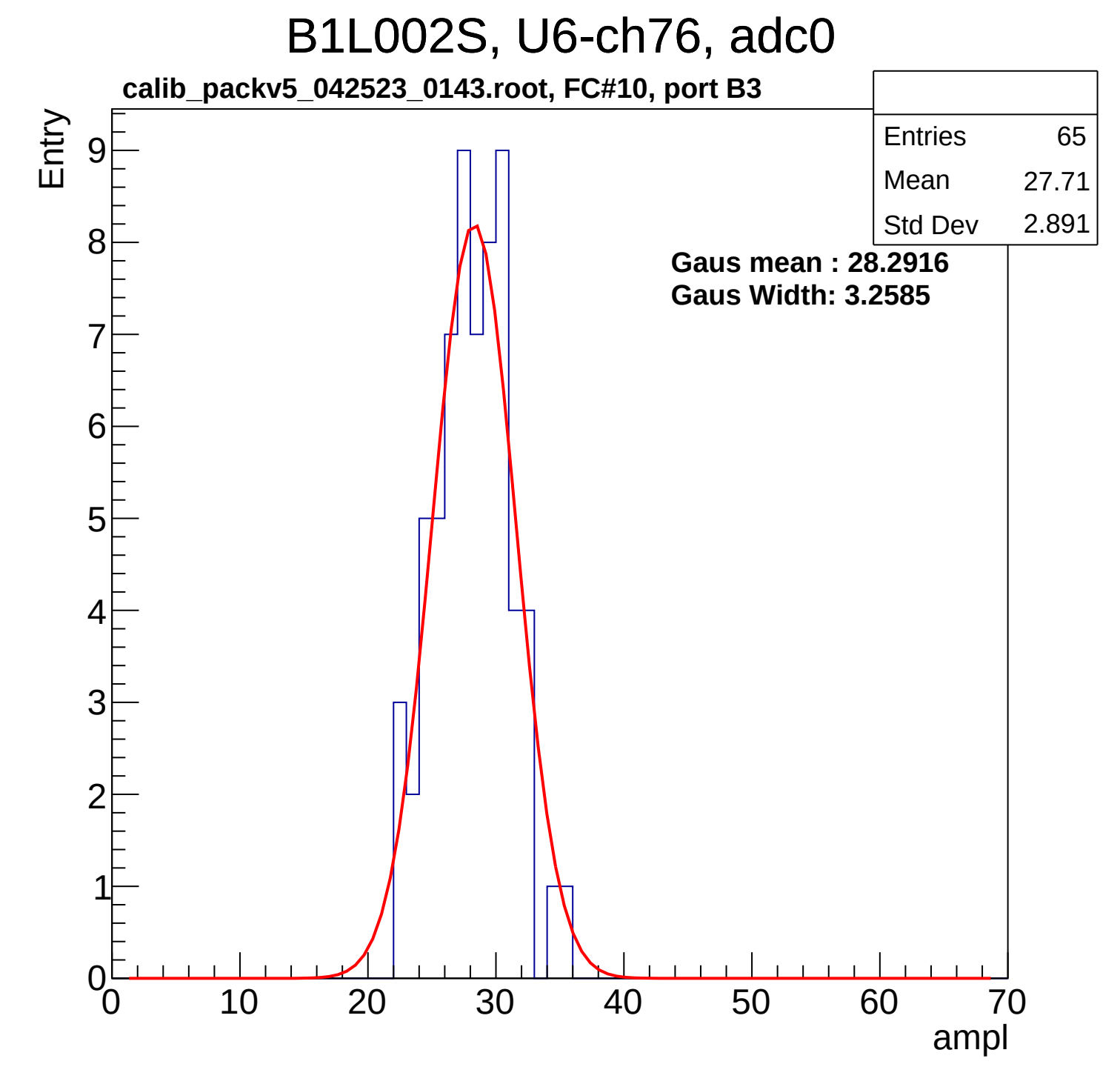

B1L002S, U6-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 49.51
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

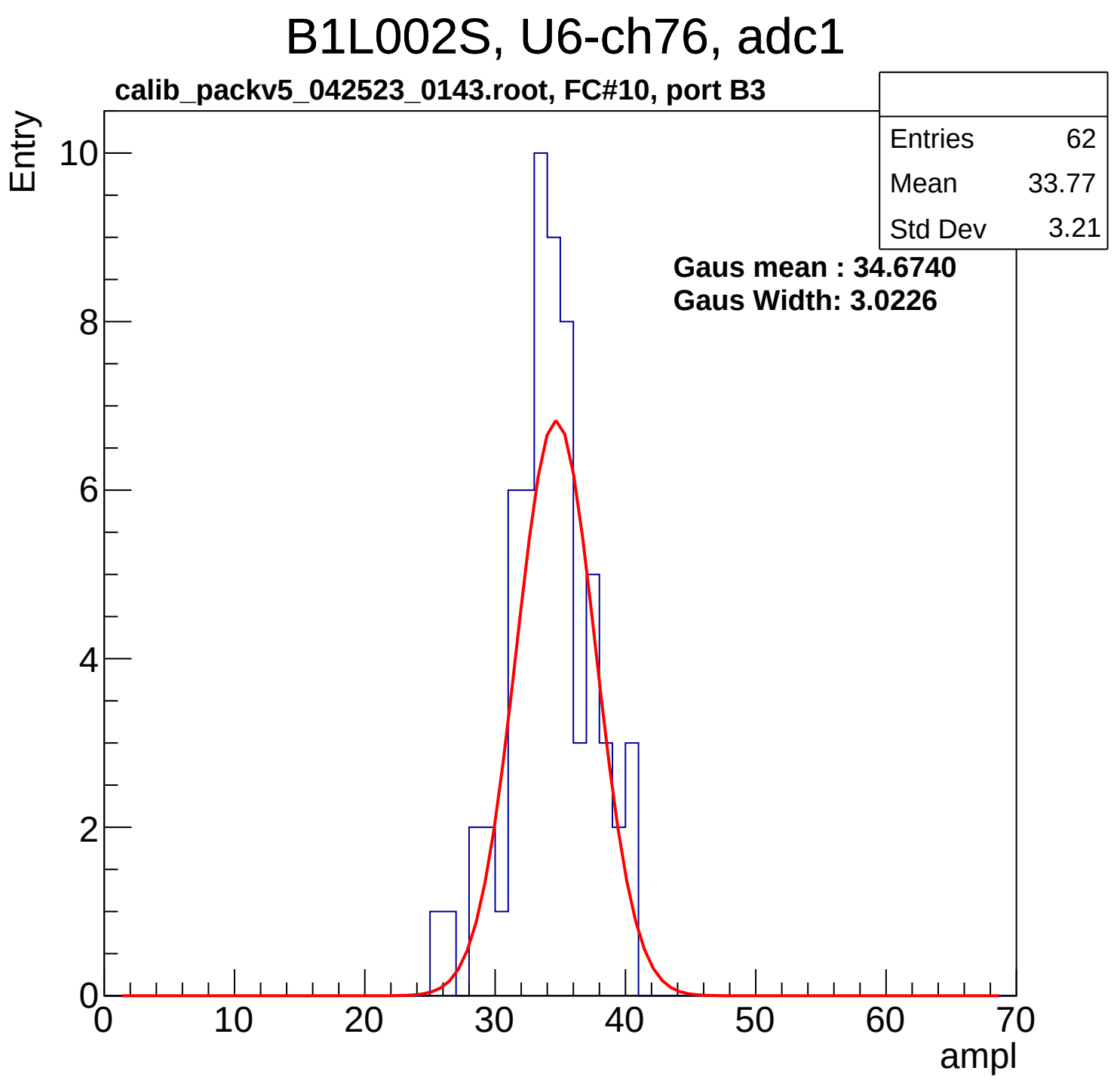

B1L002S, U6-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 54.52
Std Dev 2.941
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

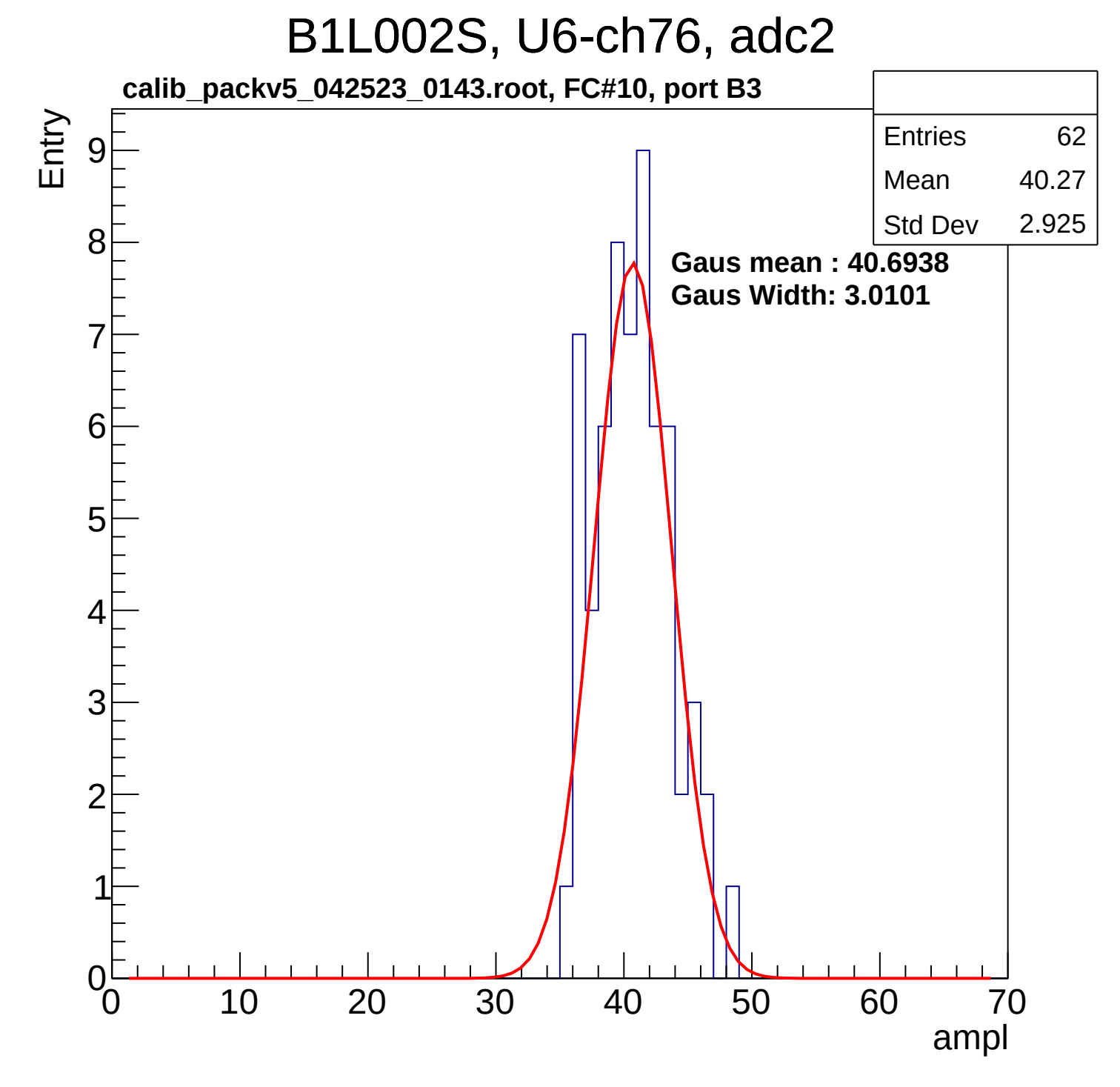

B1L002S, U6-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.08
Std Dev 2.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

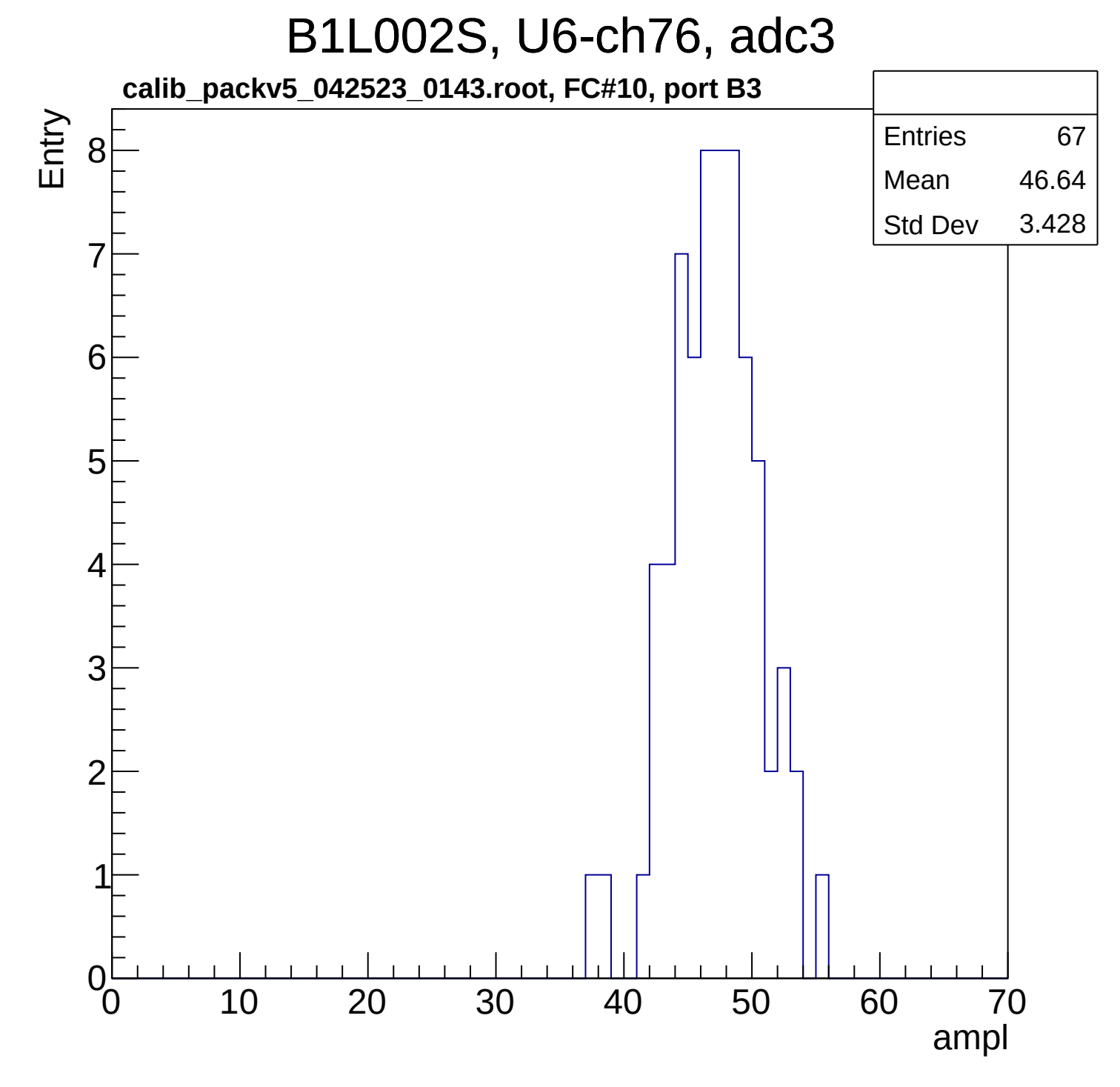

B1L002S, U6-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62
Std Dev 1.477
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

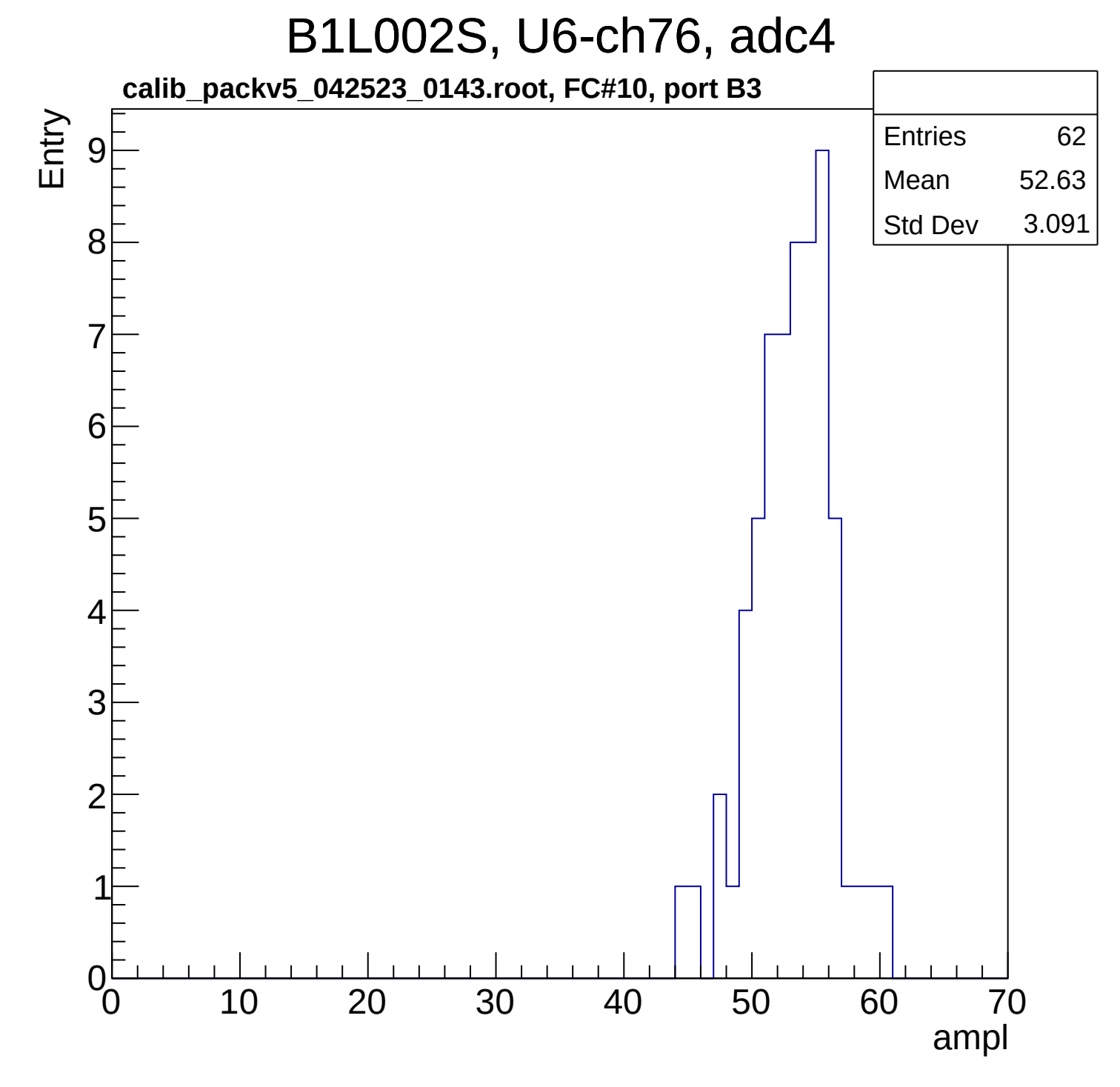

B1L002S, U6-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

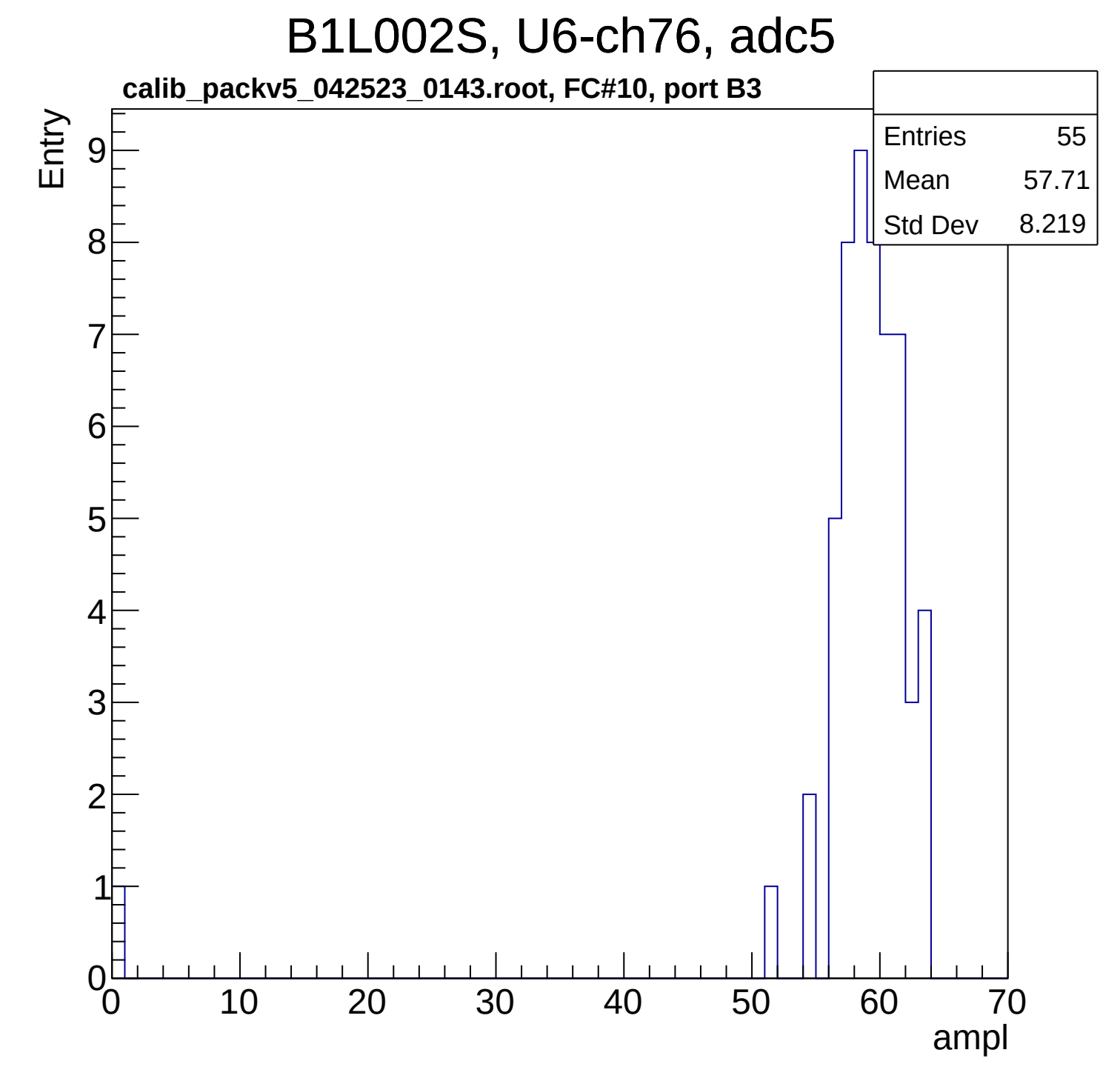

B1L002S, U6-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.71
Std Dev 2.891
Gaus mean : 28.2916
Gaus Width: 3.2585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

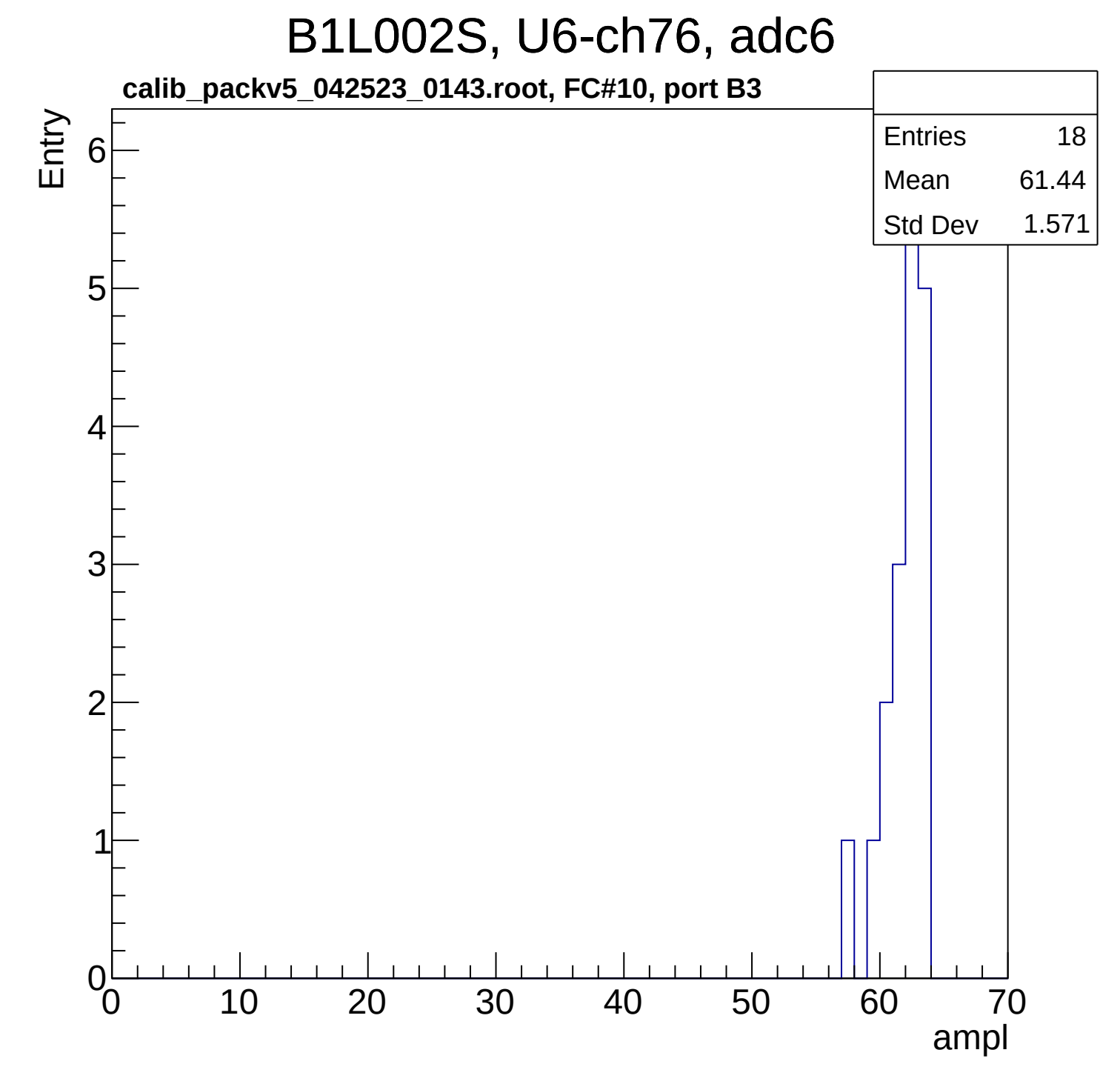

B1L002S, U6-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.77
Std Dev 3.21
Gaus mean : 34.6740
Gaus Width: 3.0226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

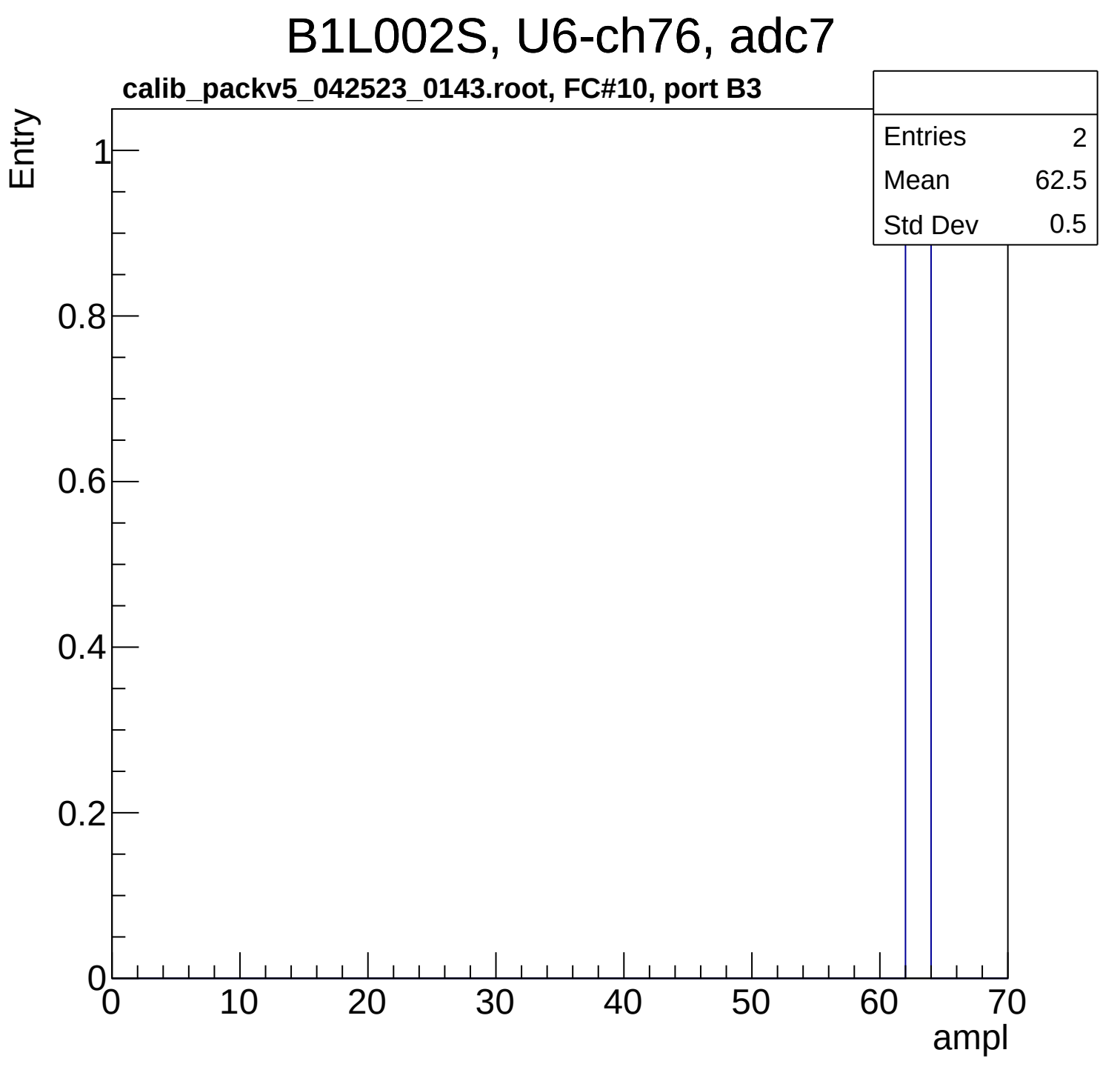

B1L002S, U6-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.27
Std Dev 2.925
Gaus mean : 40.6938
Gaus Width: 3.0101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

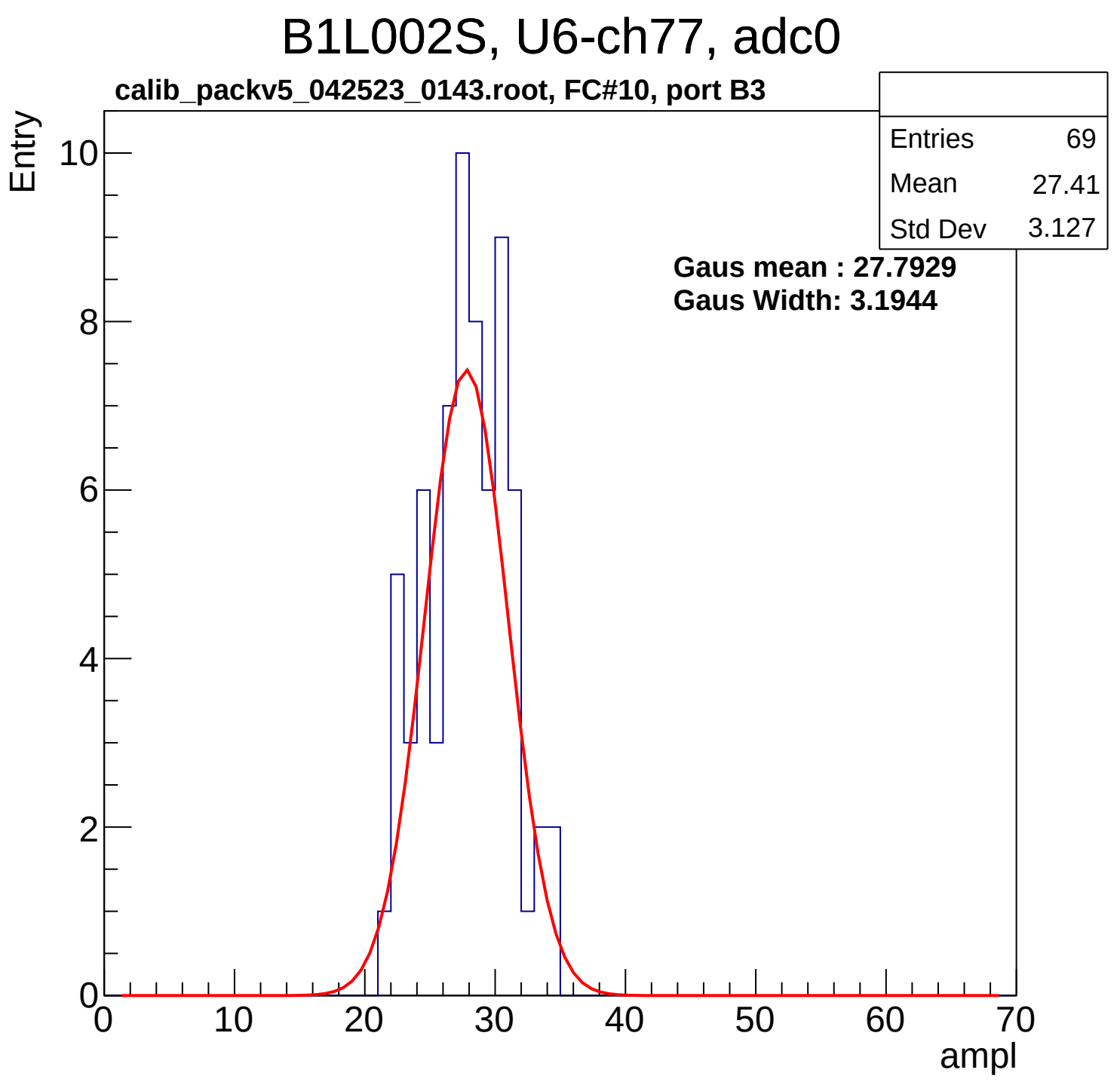

B1L002S, U6-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.64
Std Dev 3.428
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

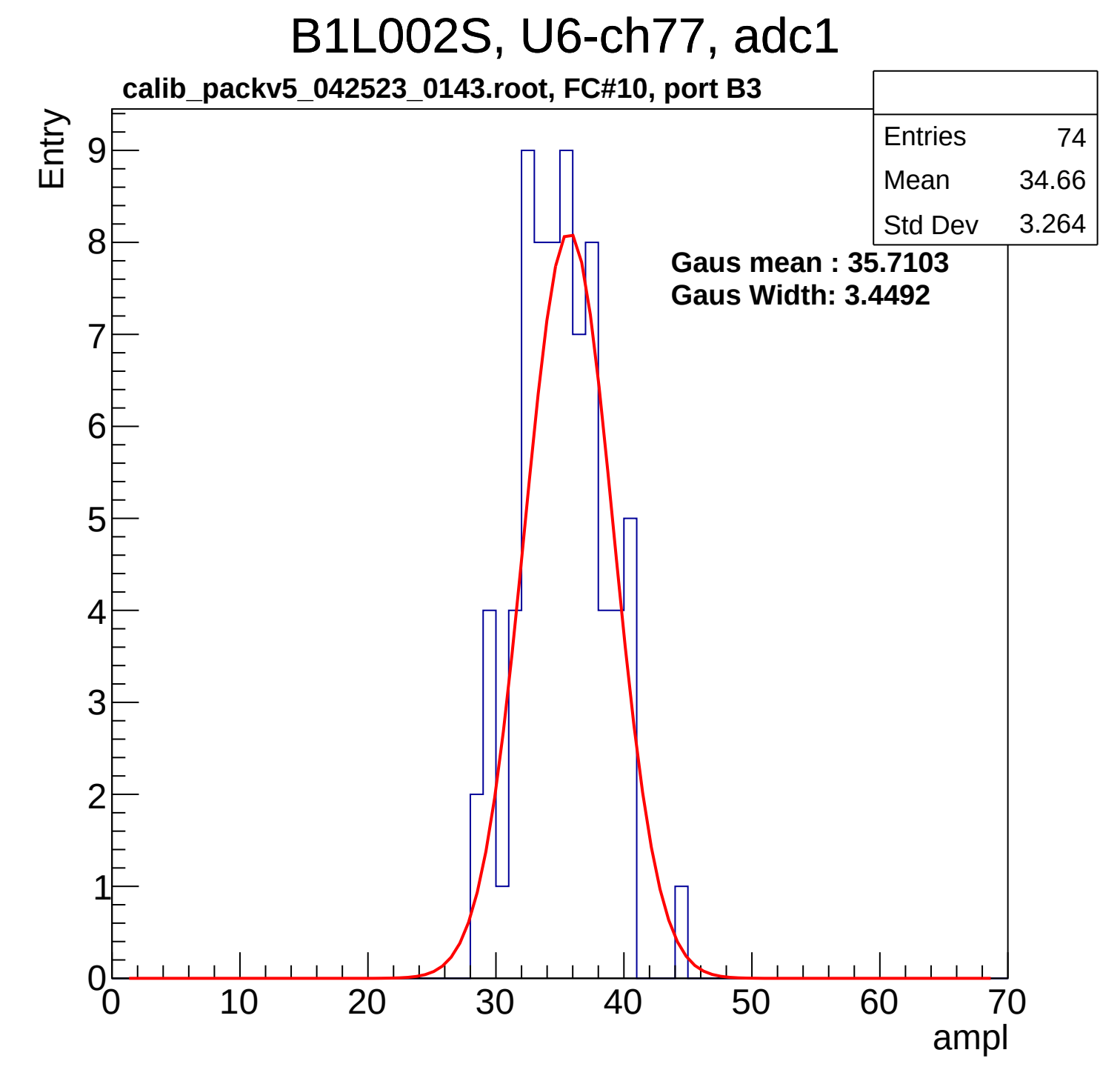

B1L002S, U6-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.63
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

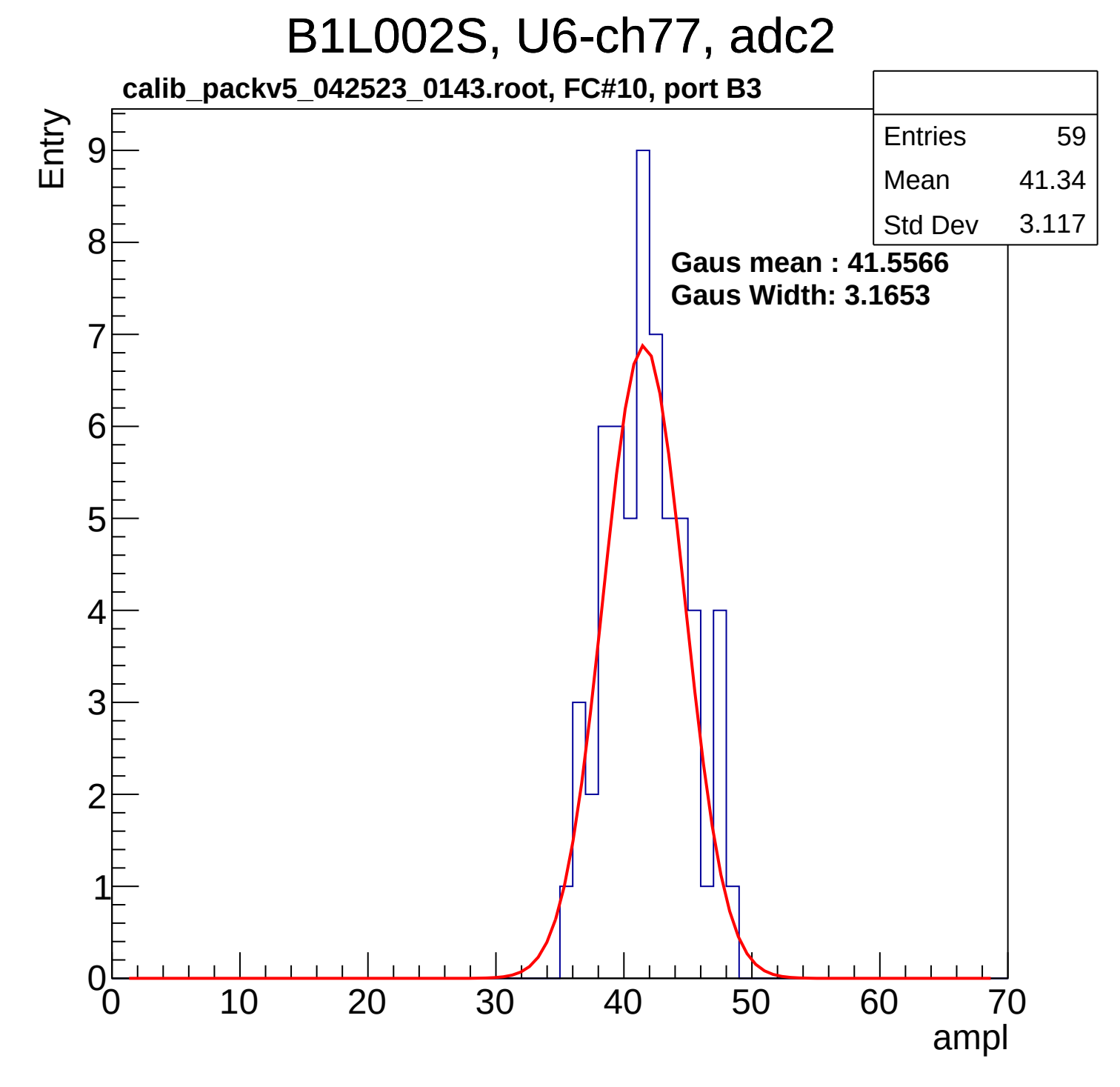

B1L002S, U6-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.71
Std Dev 8.219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

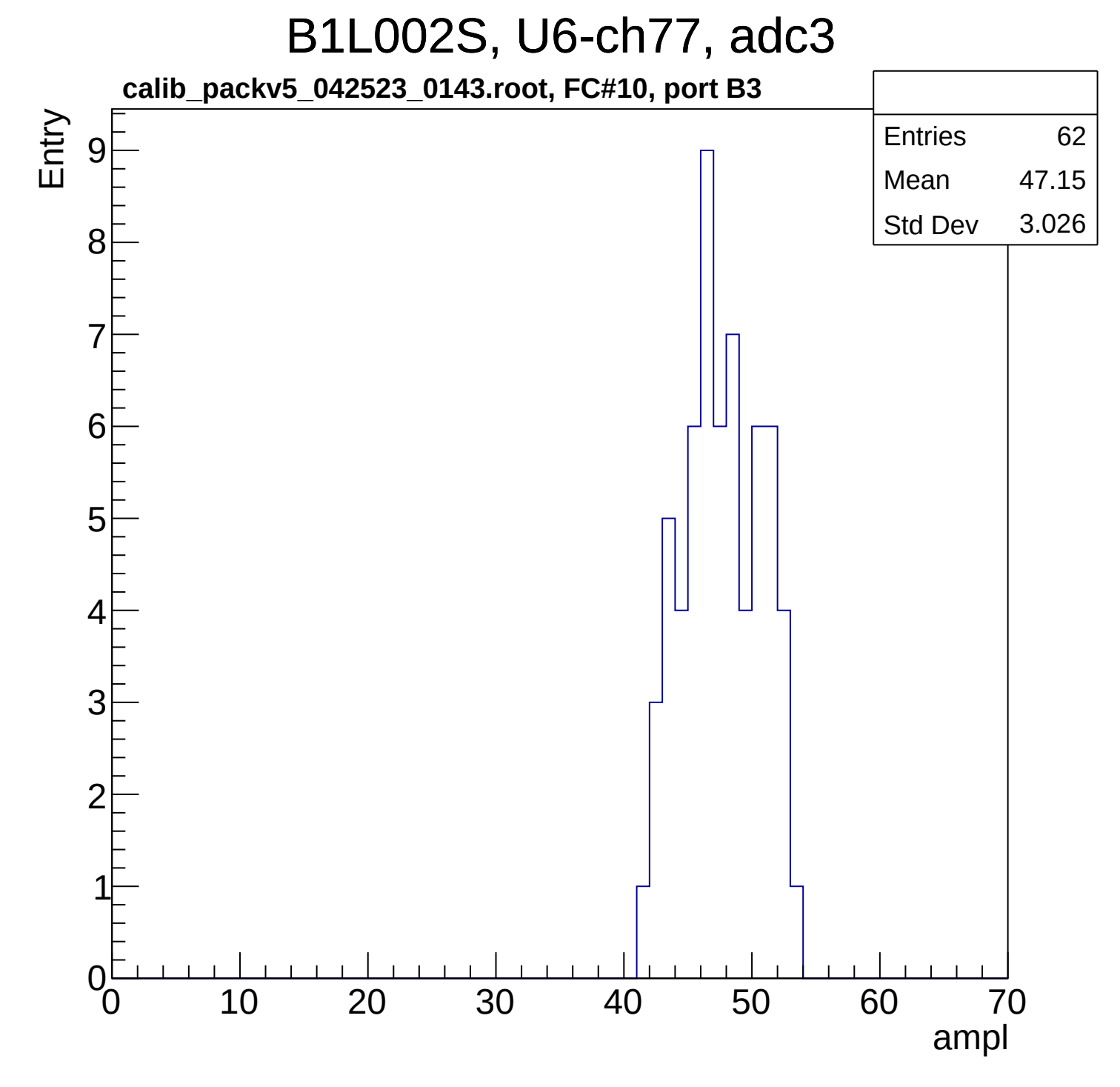

B1L002S, U6-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.44
Std Dev 1.571
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

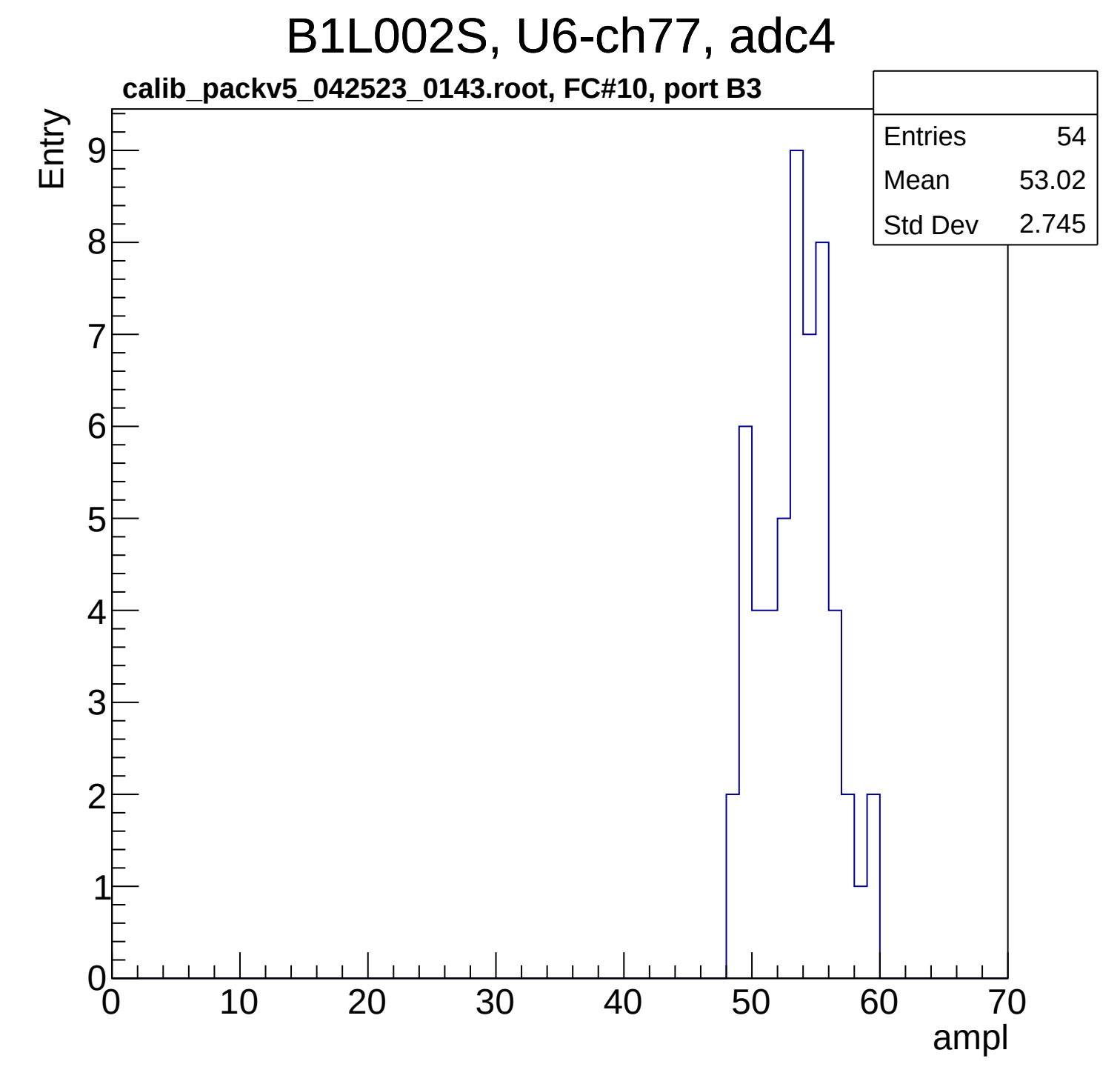

B1L002S, U6-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

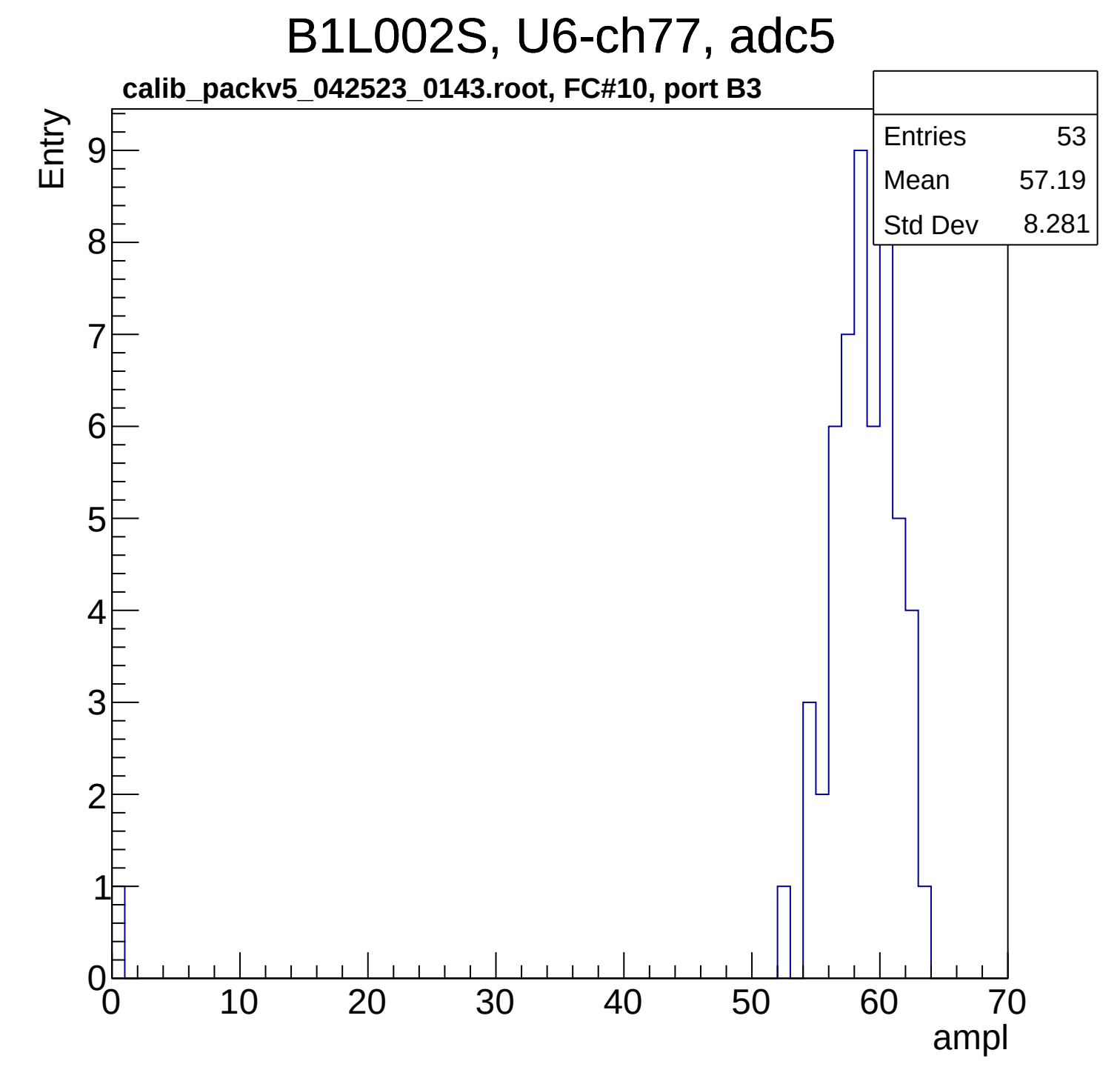

B1L002S, U6-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.41
Std Dev 3.127
Gaus mean : 27.7929
Gaus Width: 3.1944
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

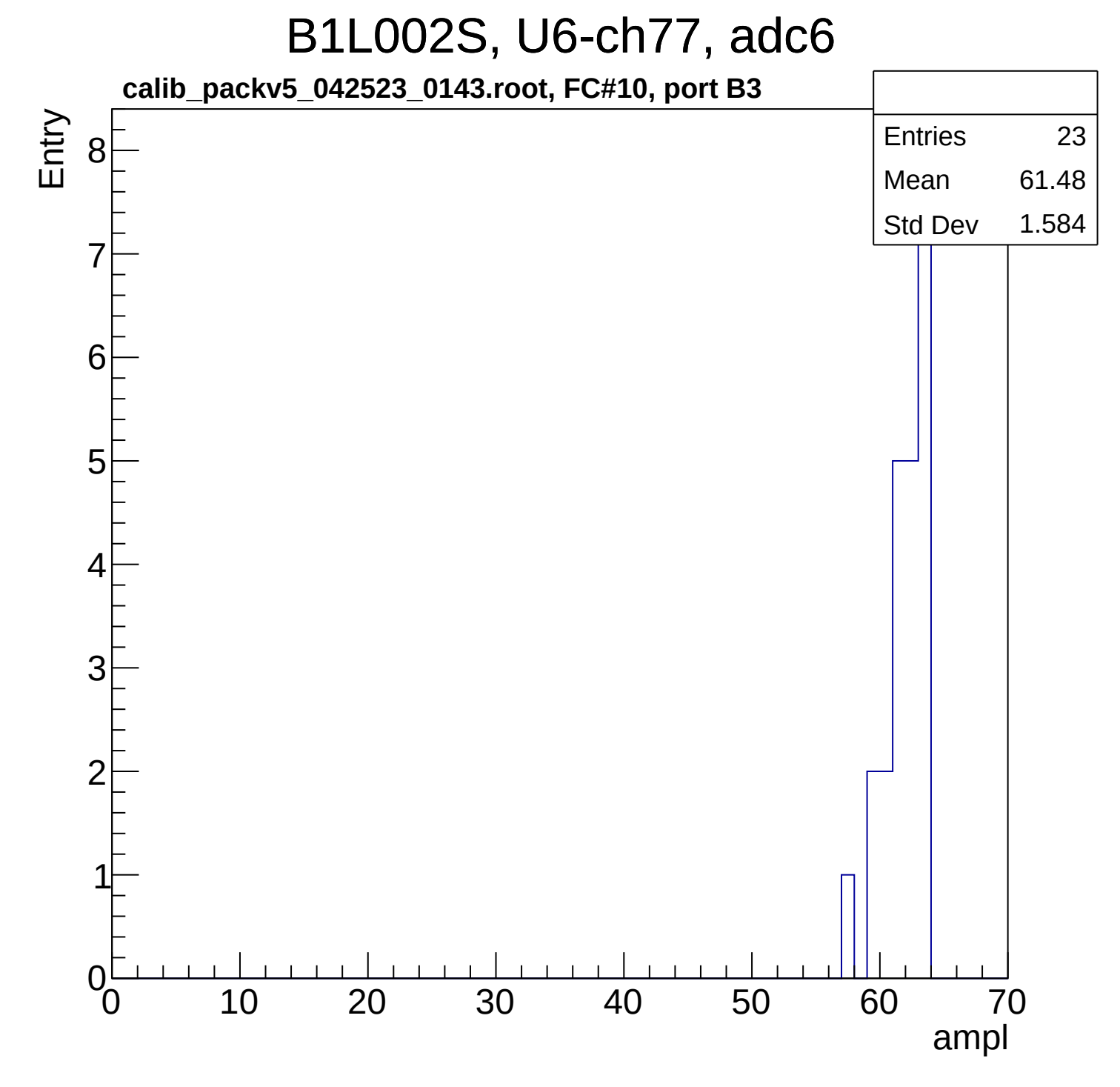

B1L002S, U6-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.66
Std Dev 3.264
Gaus mean : 35.7103
Gaus Width: 3.4492
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

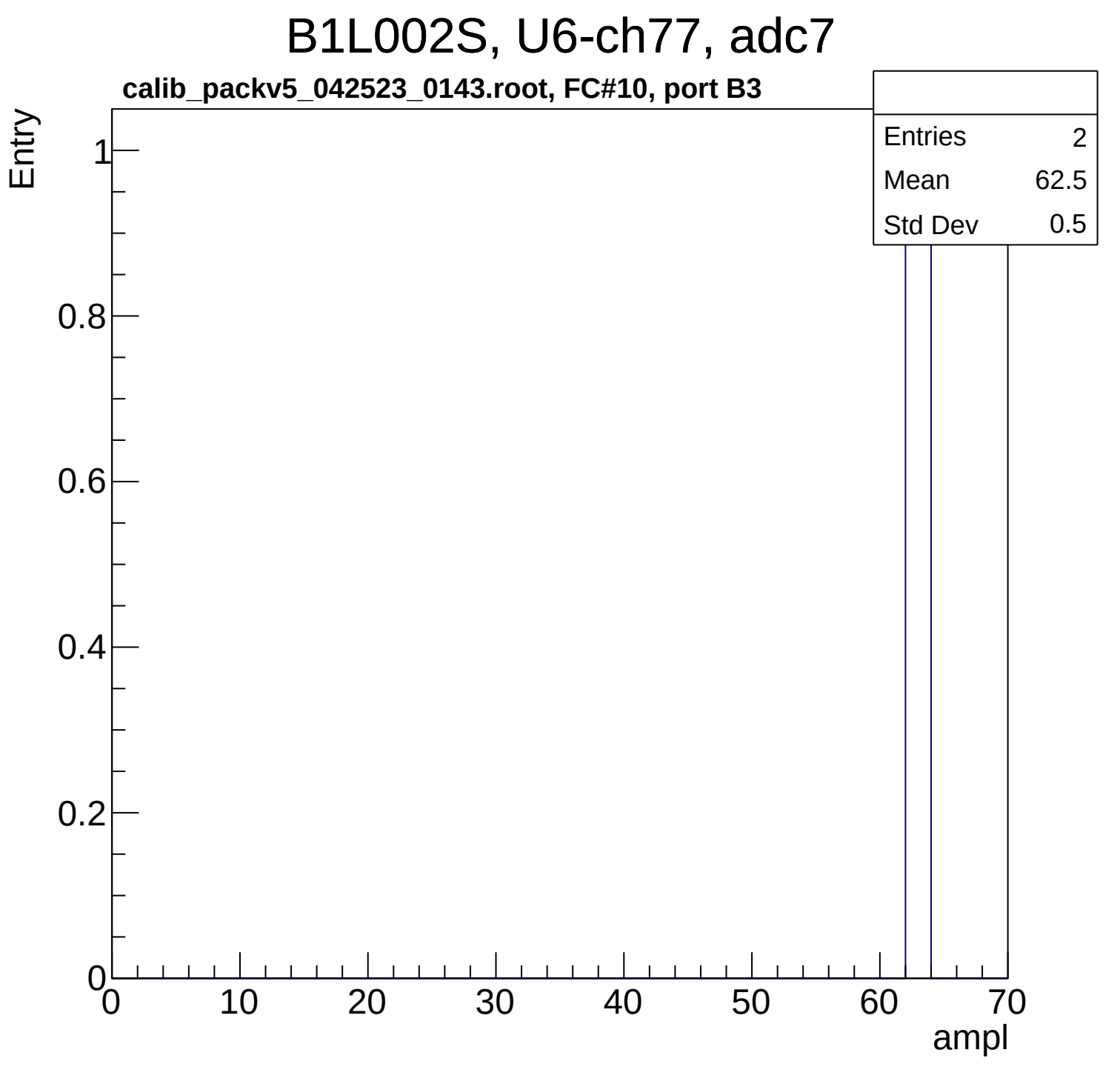

B1L002S, U6-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.34
Std Dev 3.117
Gaus mean : 41.5566
Gaus Width: 3.1653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

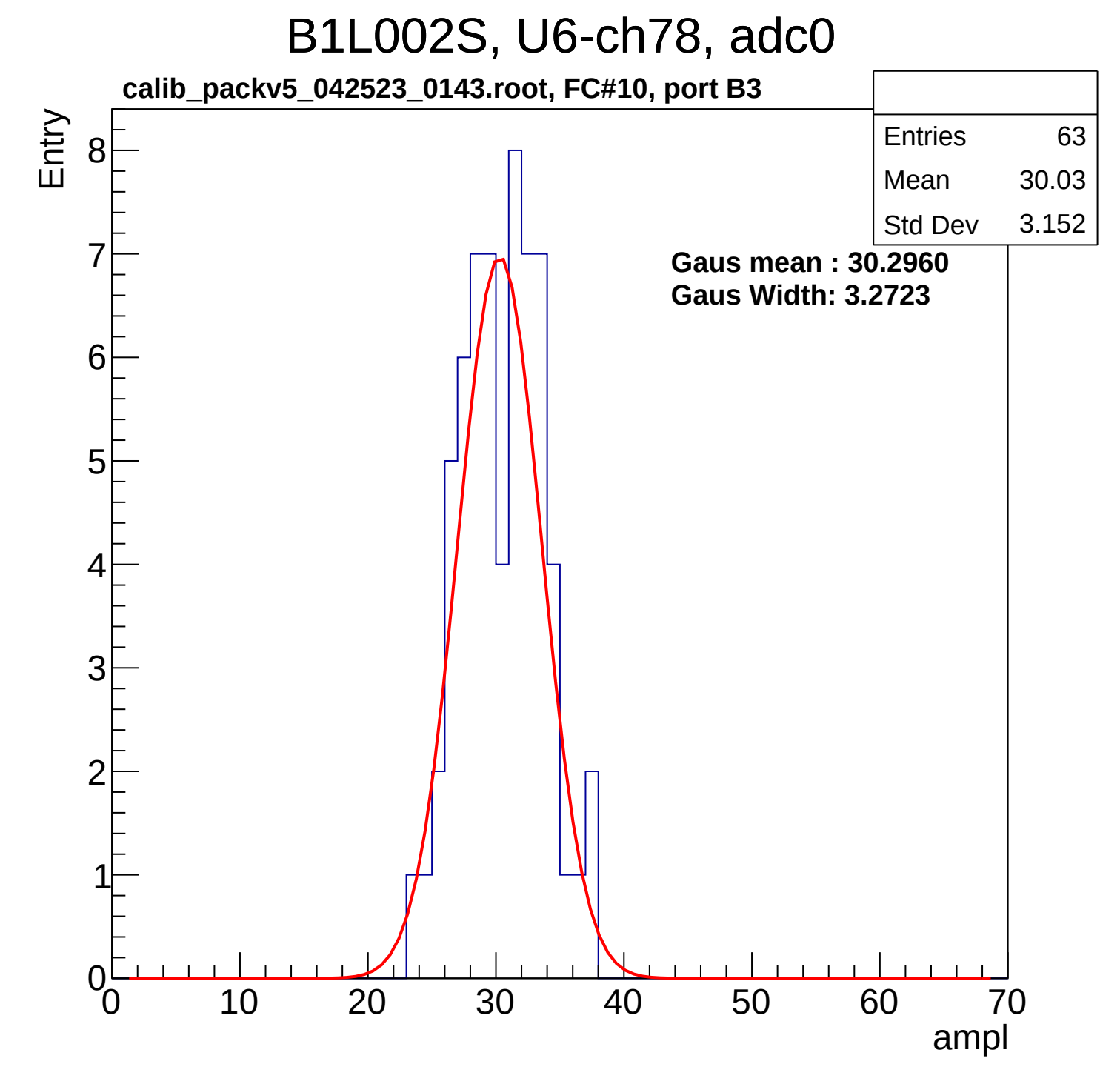

B1L002S, U6-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.15
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

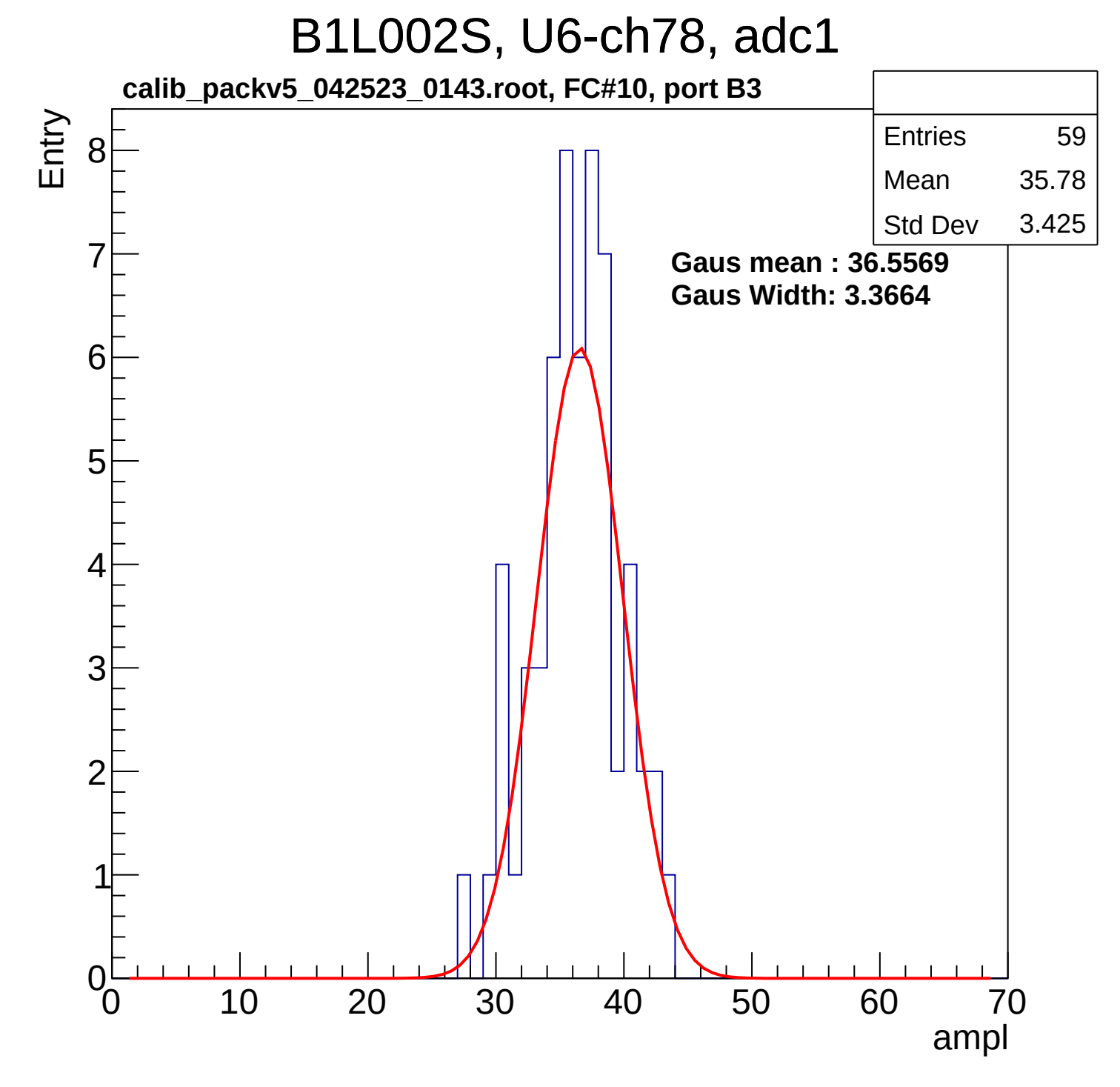

B1L002S, U6-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.02
Std Dev 2.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

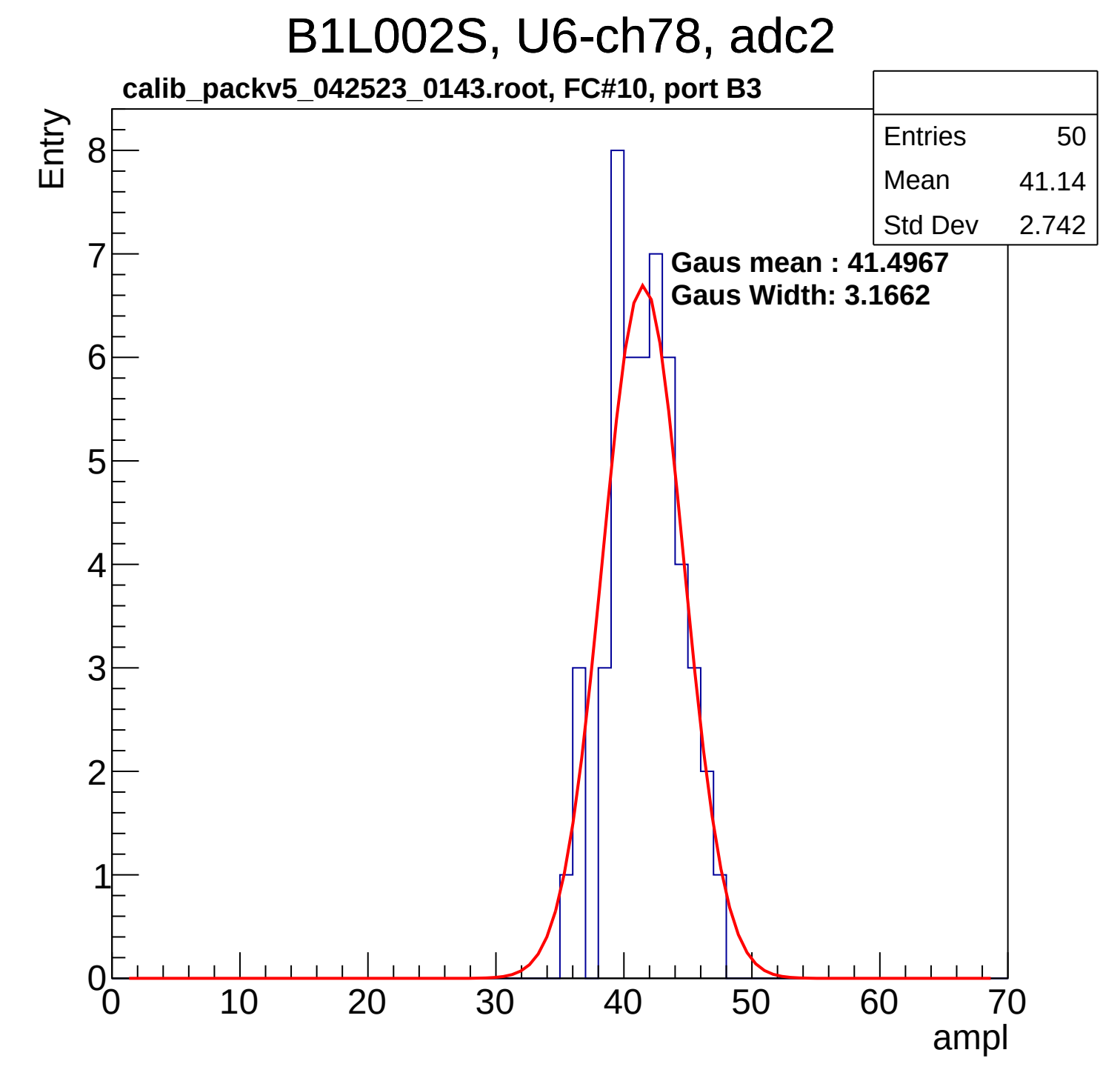

B1L002S, U6-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.19
Std Dev 8.281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

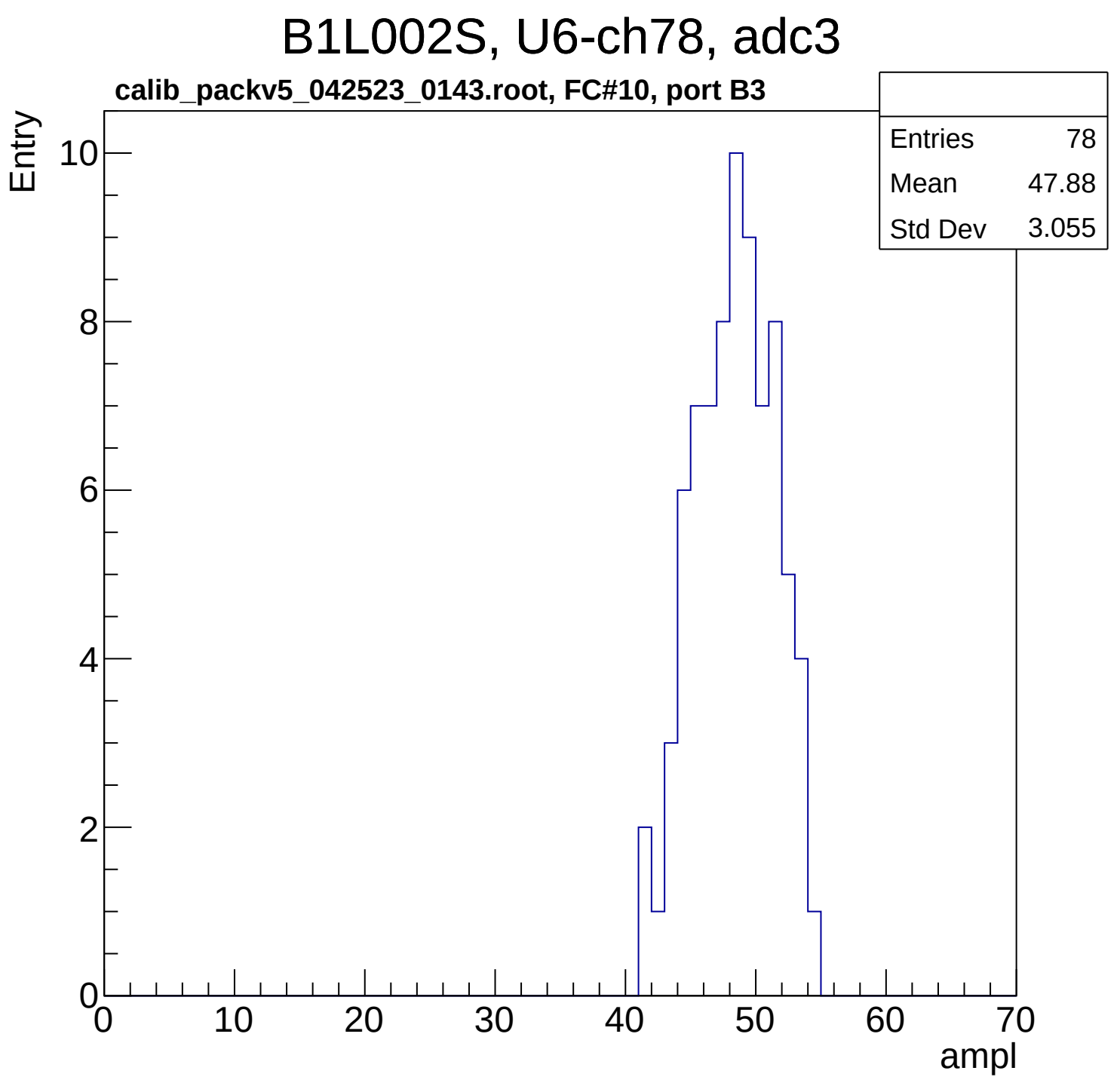

B1L002S, U6-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.48
Std Dev 1.584
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

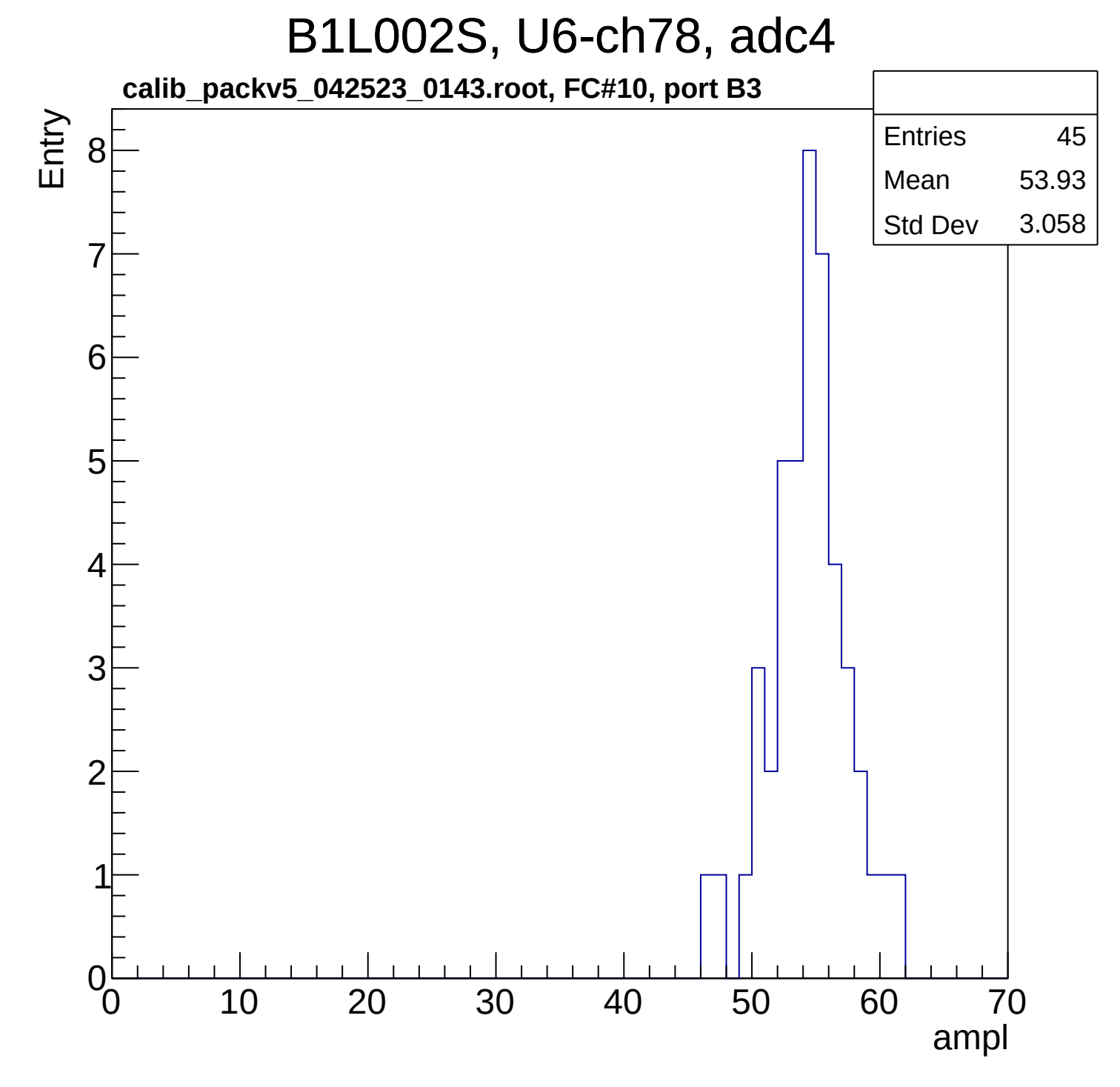

B1L002S, U6-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

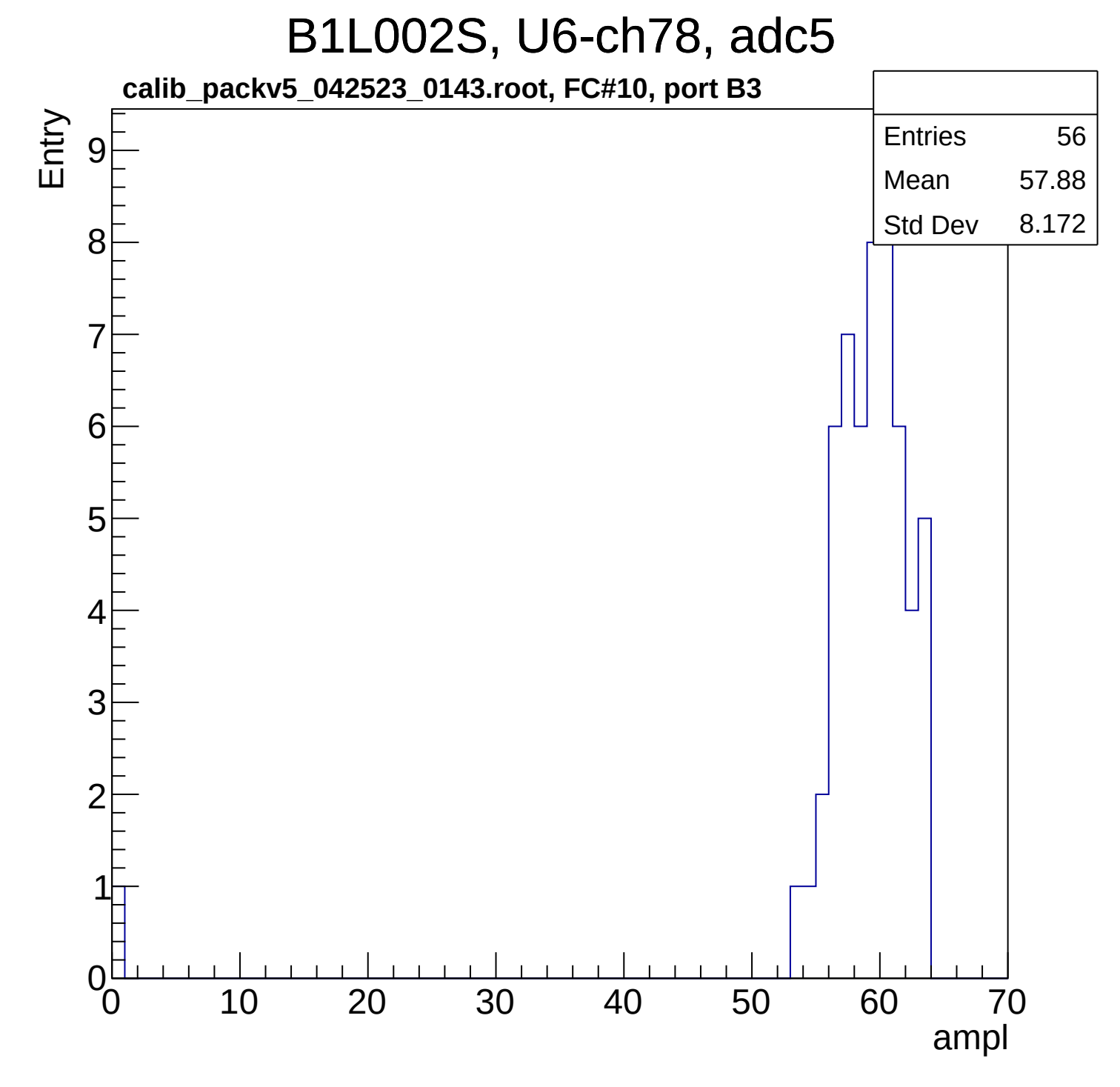

B1L002S, U6-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 30.03
Std Dev 3.152
Gaus mean : 30.2960
Gaus Width: 3.2723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

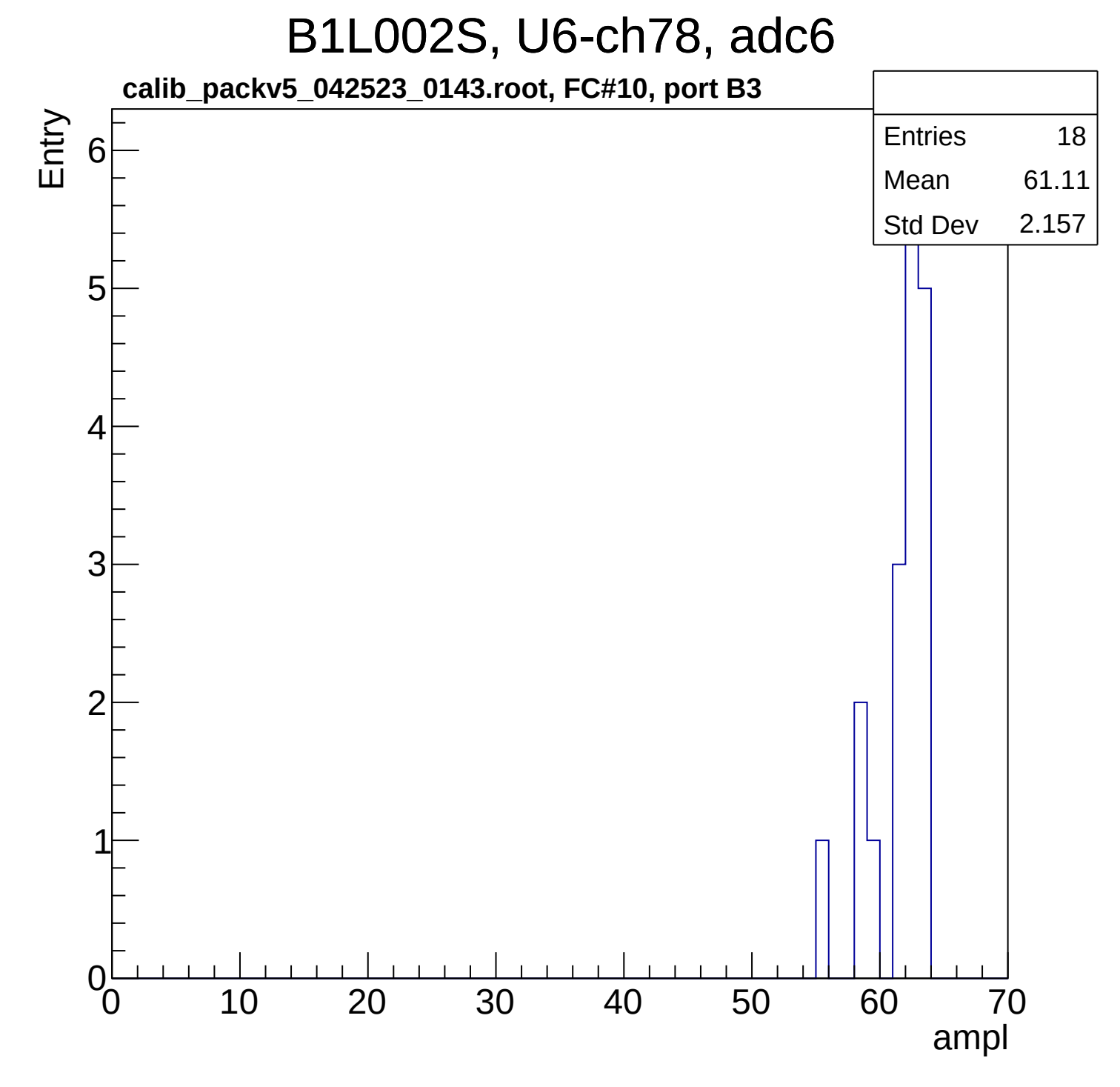

B1L002S, U6-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.78
Std Dev 3.425
Gaus mean : 36.5569
Gaus Width: 3.3664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

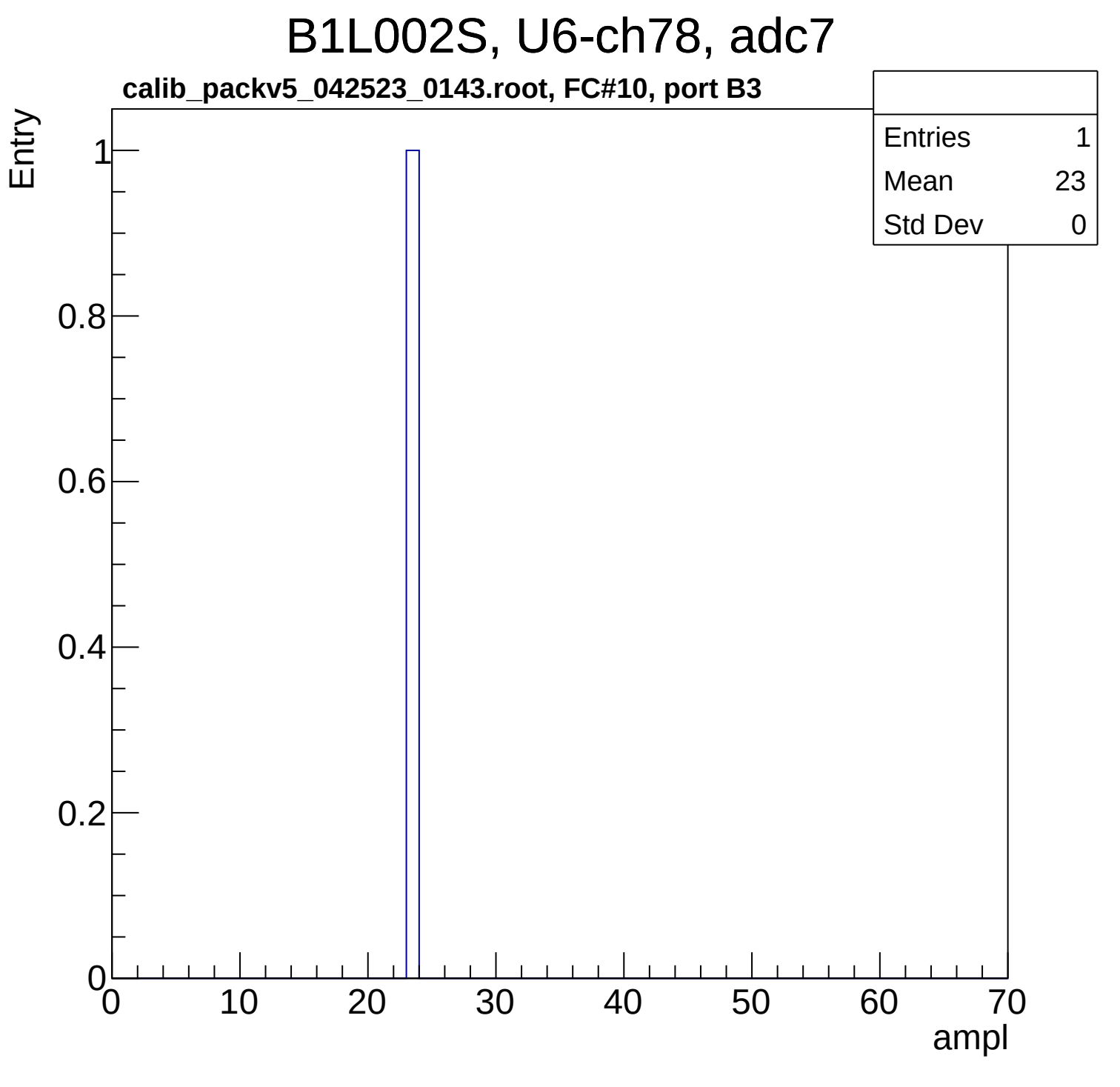

B1L002S, U6-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.14
Std Dev 2.742
Gaus mean : 41.4967
Gaus Width: 3.1662
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

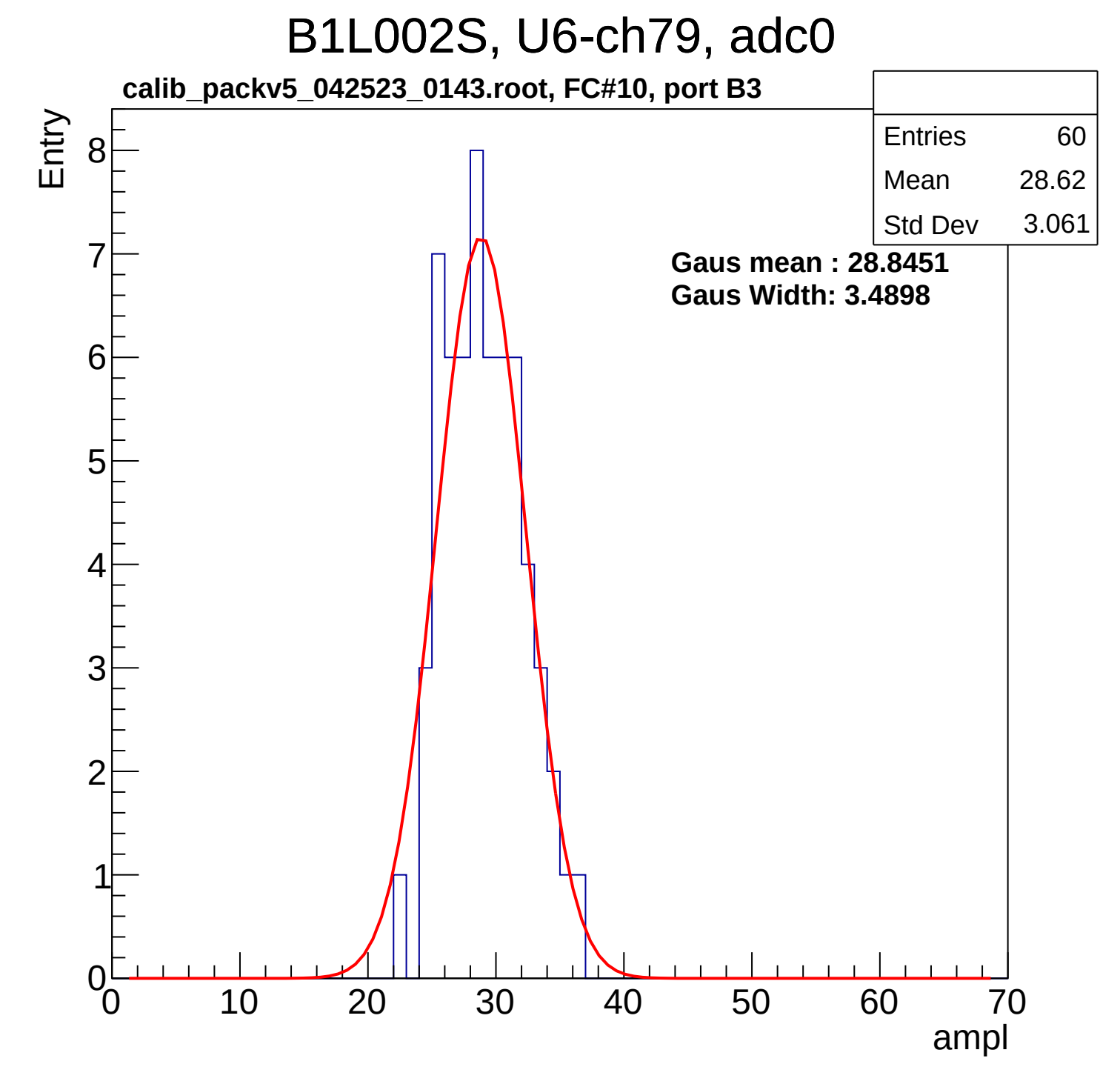

B1L002S, U6-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 47.88
Std Dev 3.055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

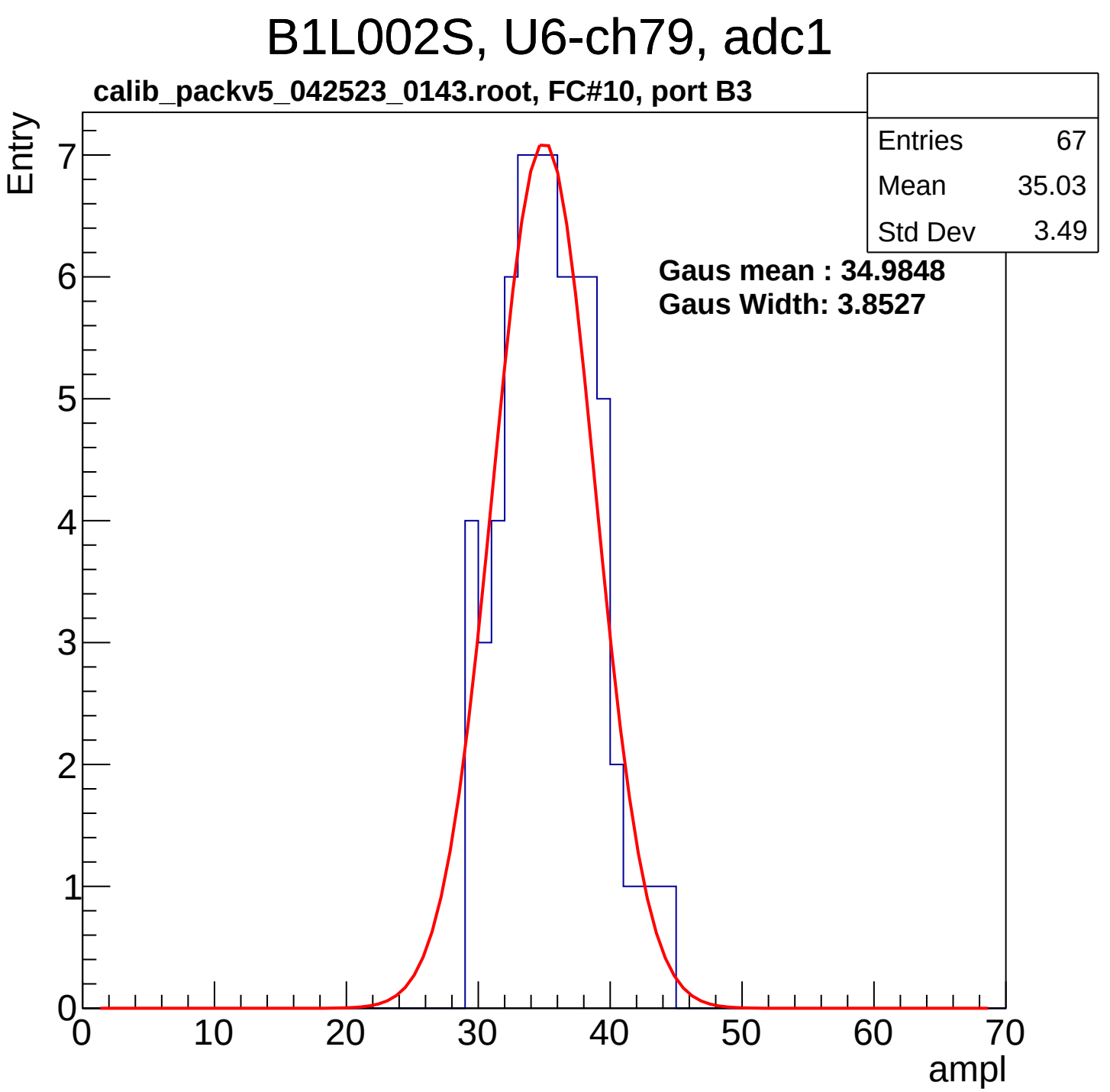

B1L002S, U6-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53.93
Std Dev 3.058
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

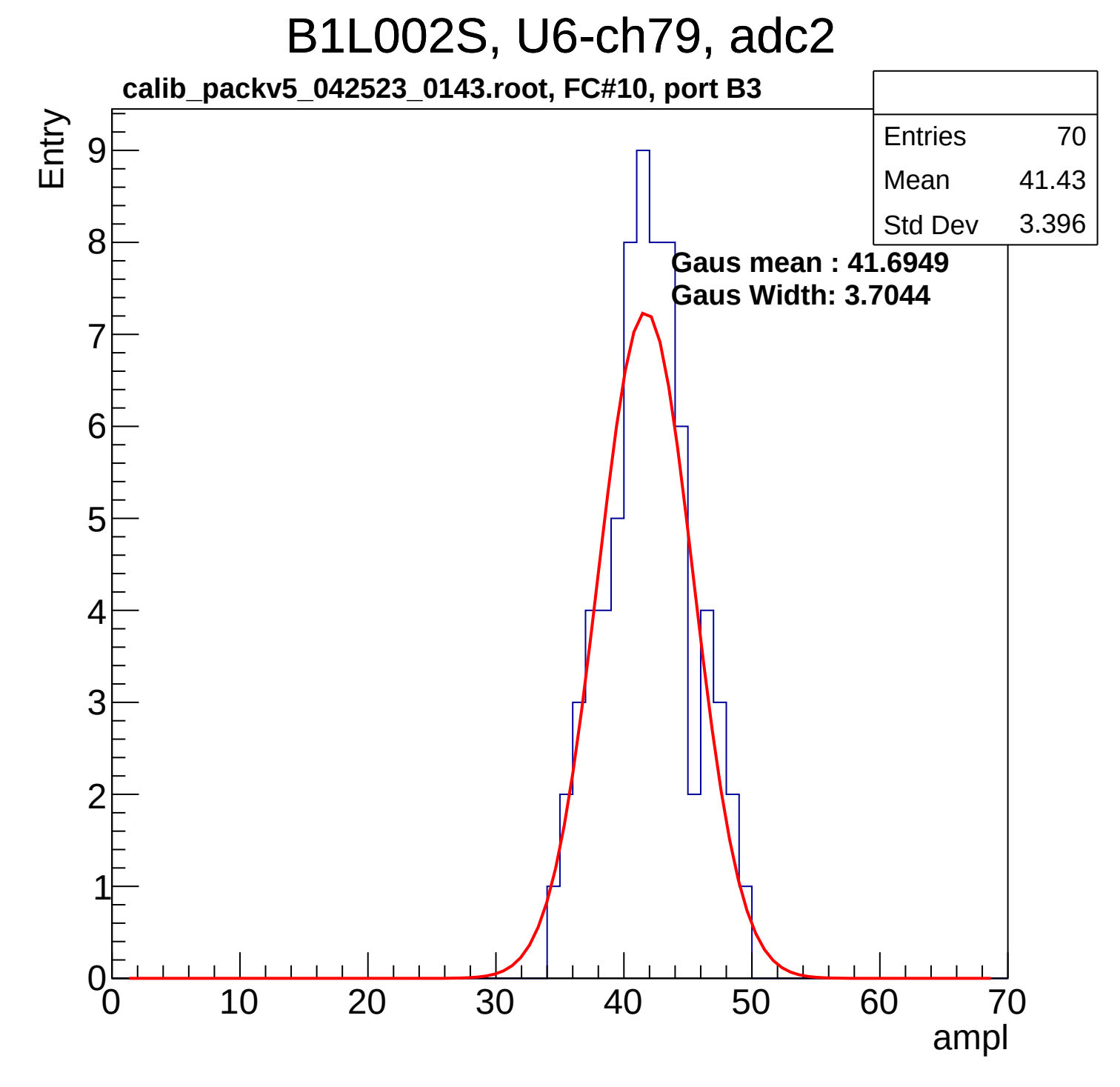

B1L002S, U6-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.88
Std Dev 8.172
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

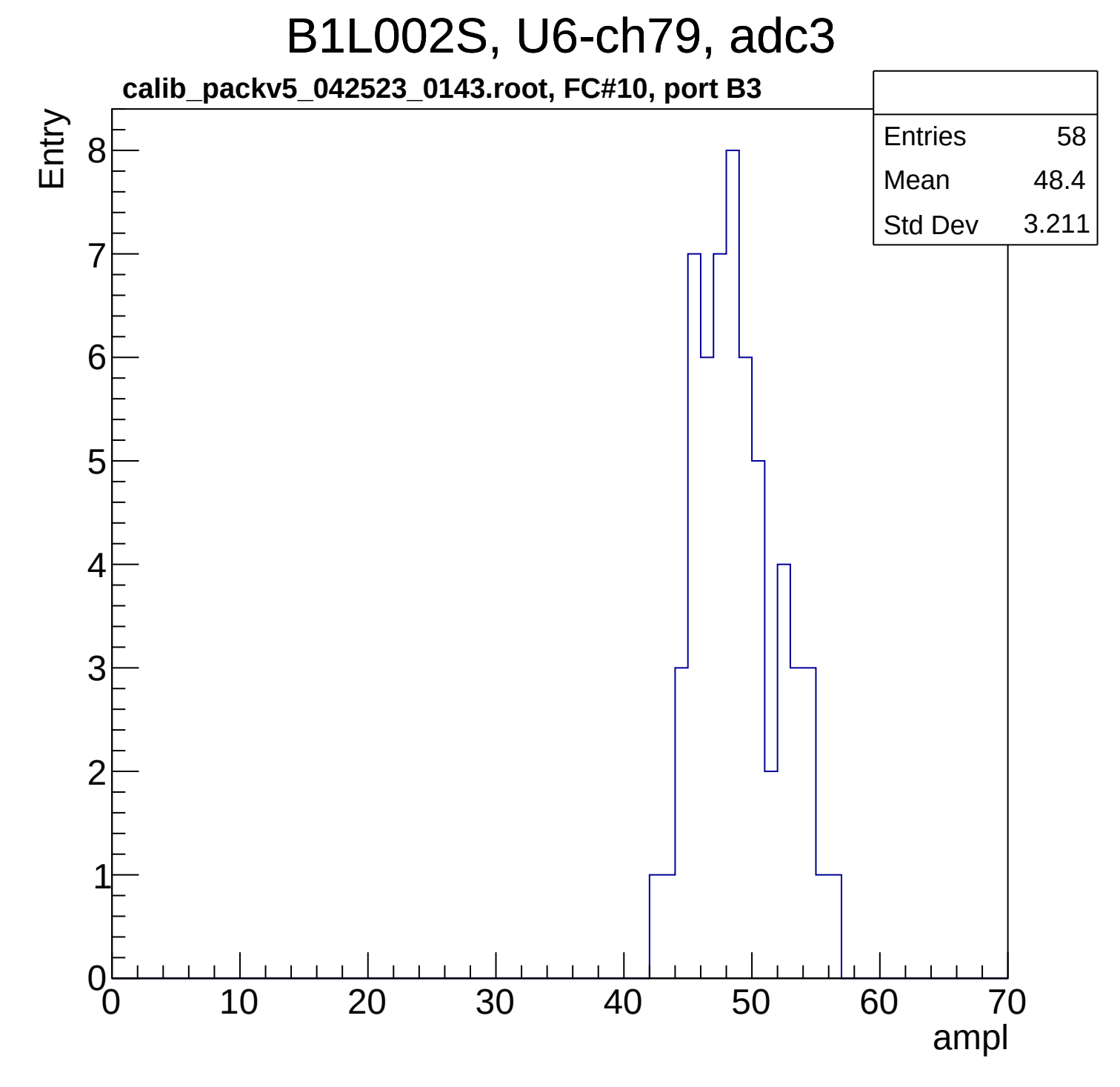

B1L002S, U6-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.11
Std Dev 2.157
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

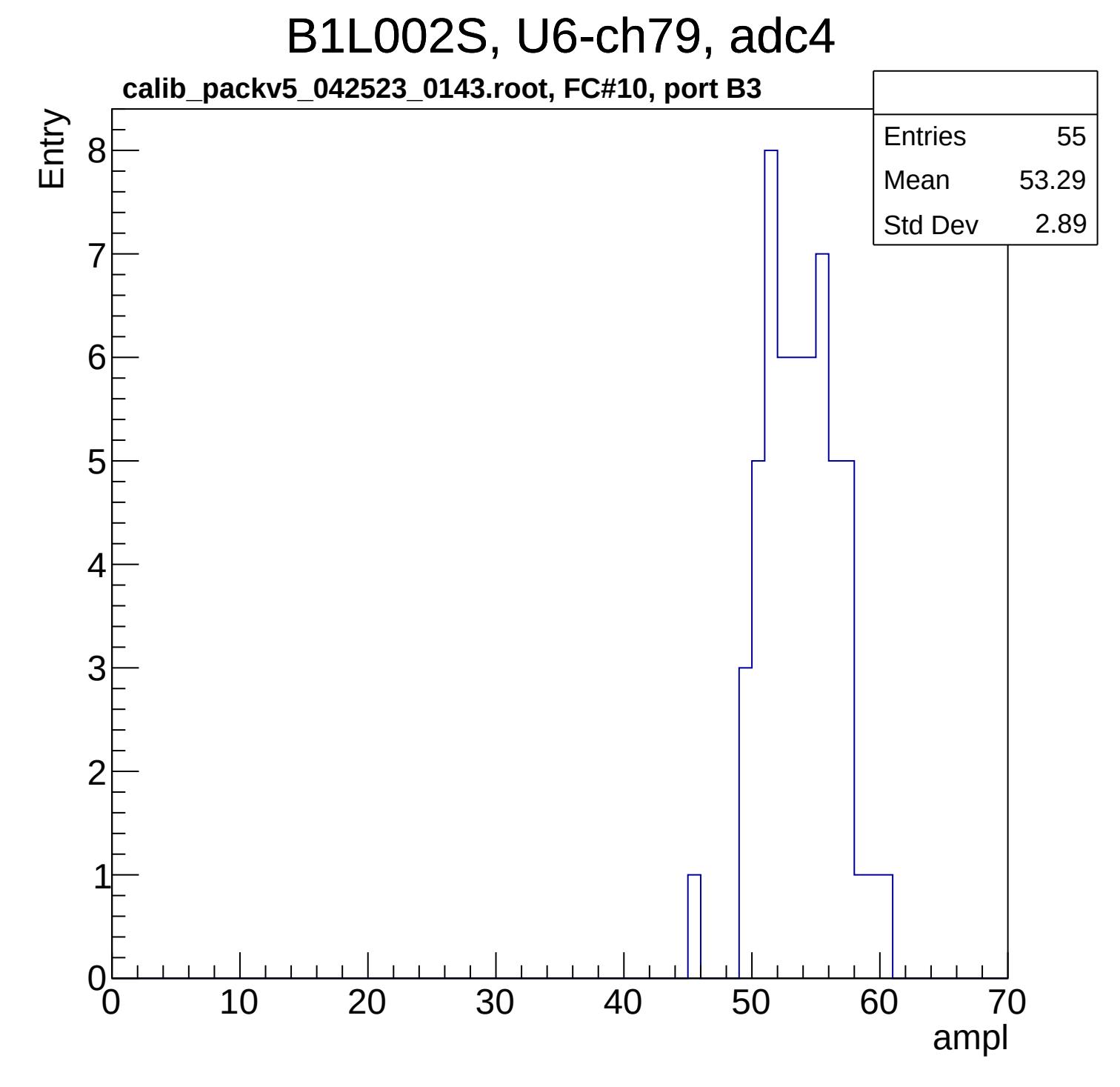

B1L002S, U6-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

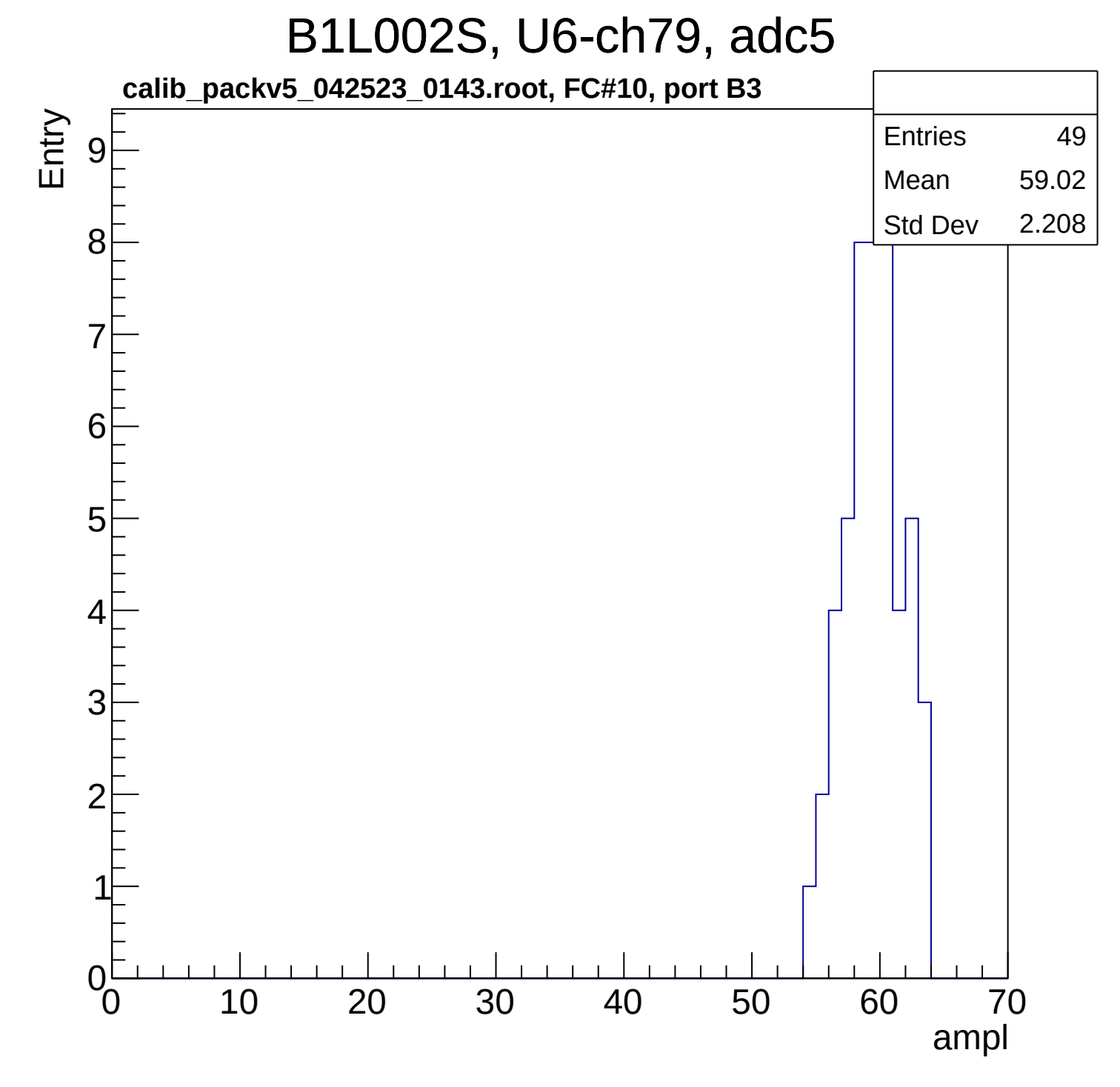

B1L002S, U6-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.62
Std Dev 3.061
Gaus mean : 28.8451
Gaus Width: 3.4898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

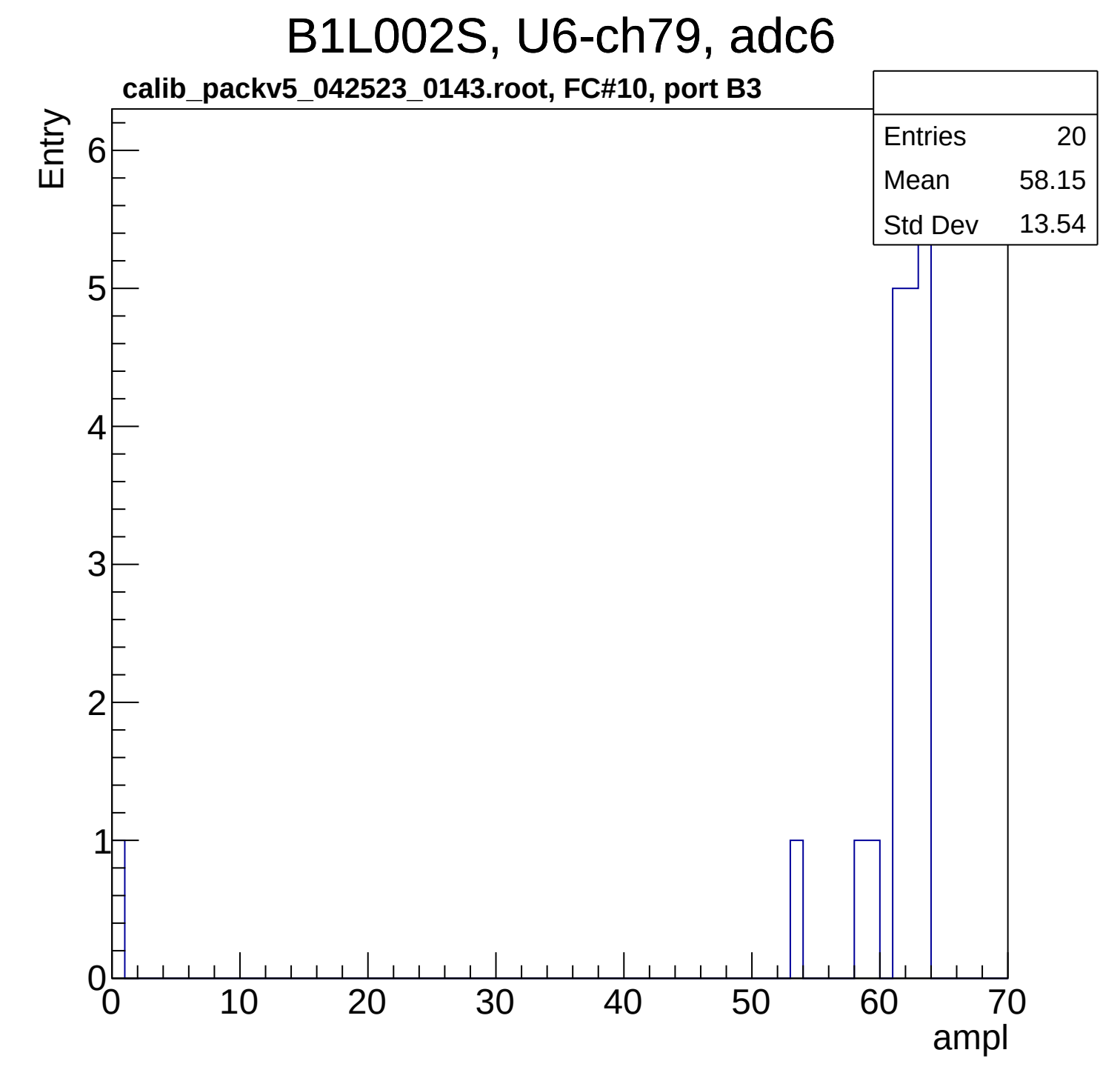

B1L002S, U6-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.03
Std Dev 3.49
Gaus mean : 34.9848
Gaus Width: 3.8527
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

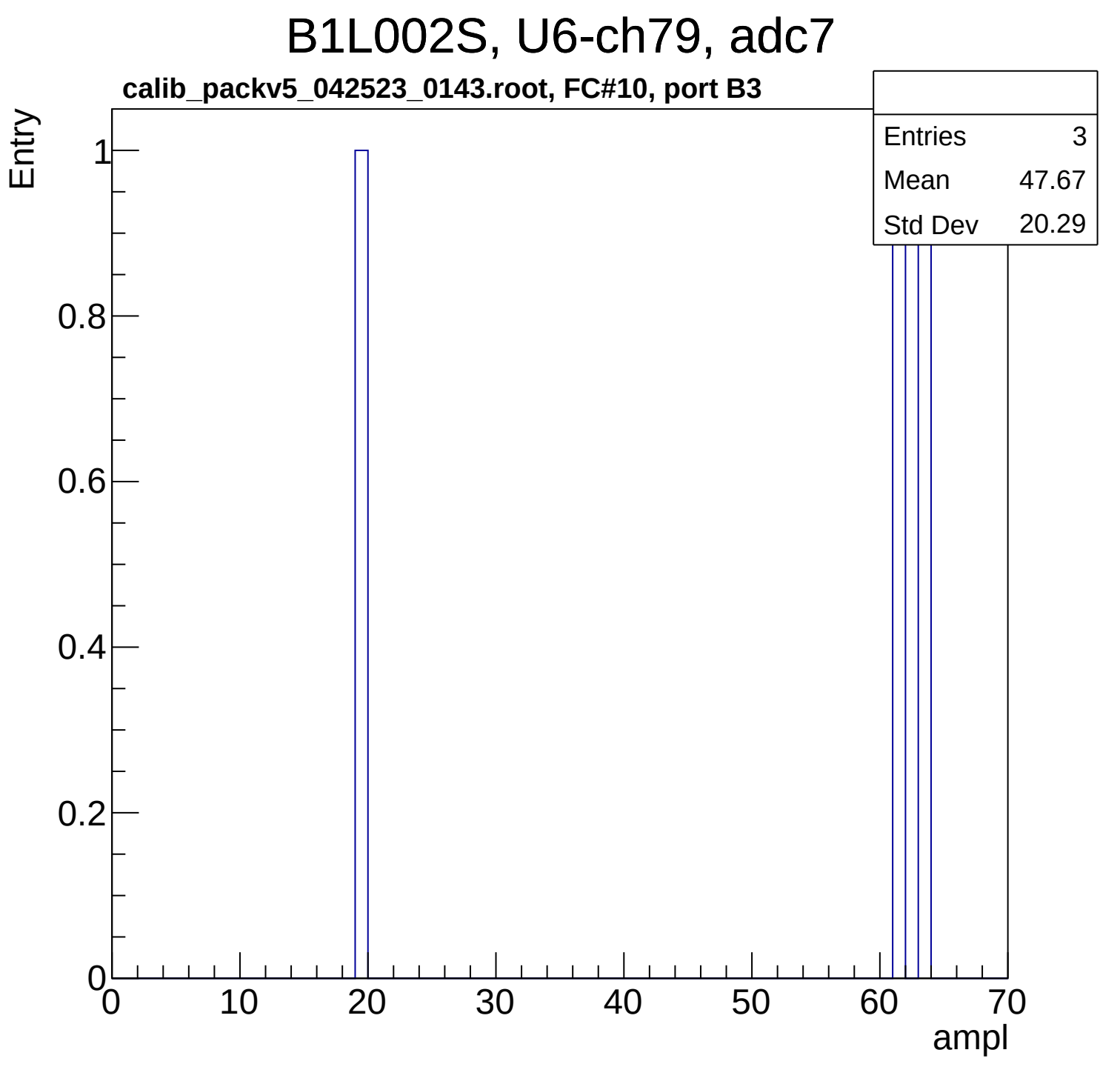

B1L002S, U6-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.43
Std Dev 3.396
Gaus mean : 41.6949
Gaus Width: 3.7044
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

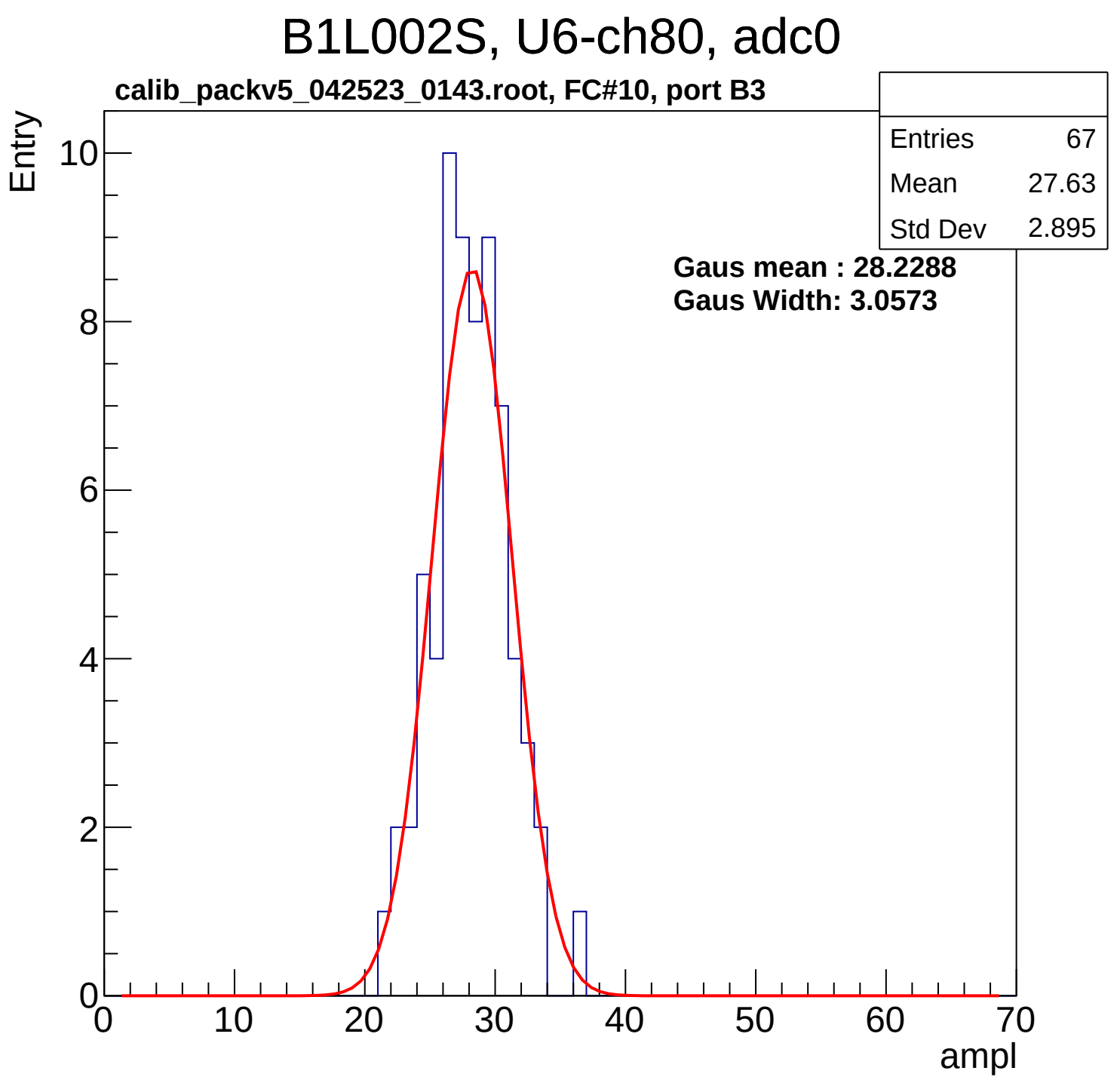

B1L002S, U6-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 48.4
Std Dev 3.211
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

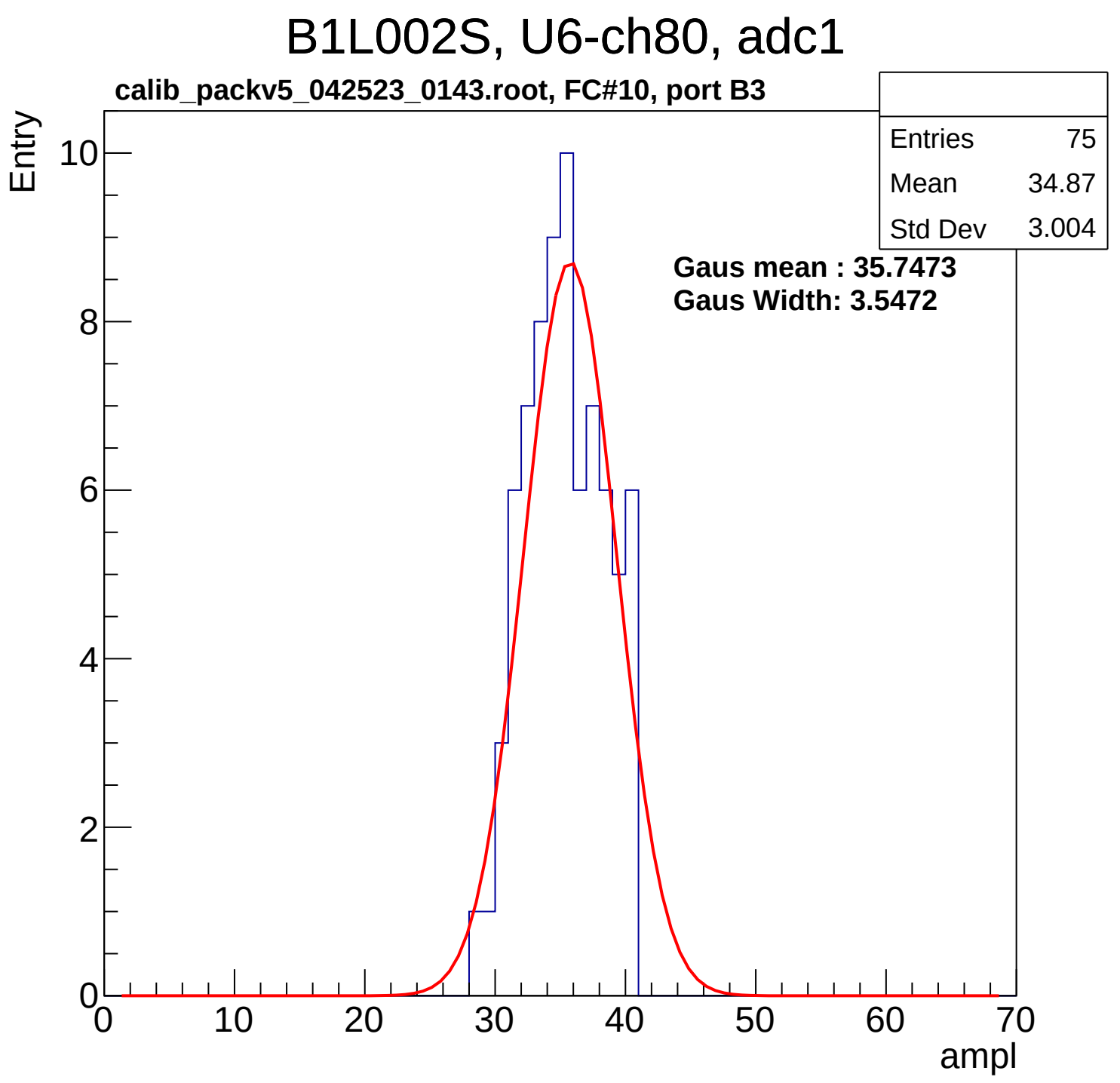

B1L002S, U6-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.29
Std Dev 2.89
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

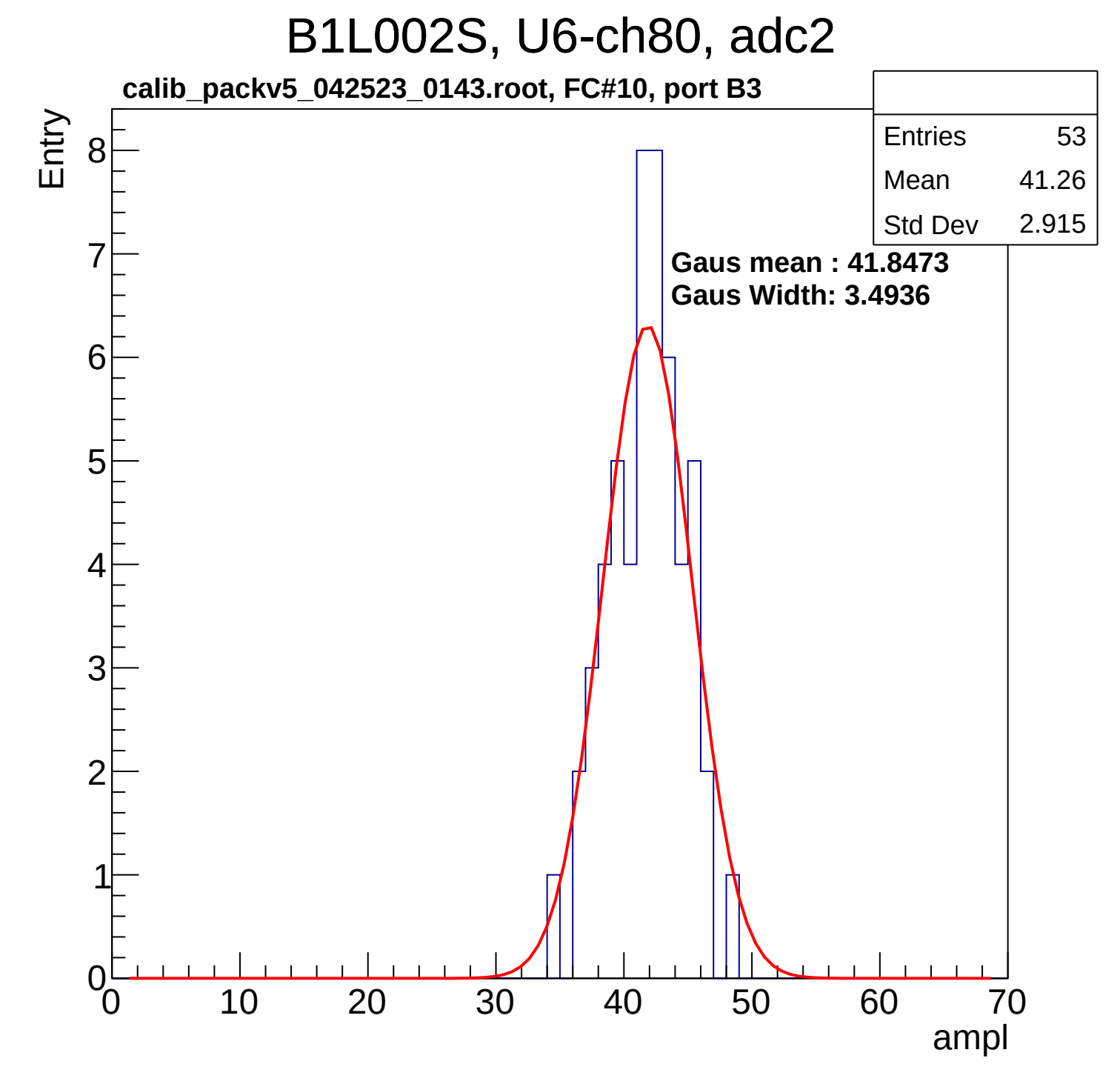

B1L002S, U6-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.02
Std Dev 2.208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

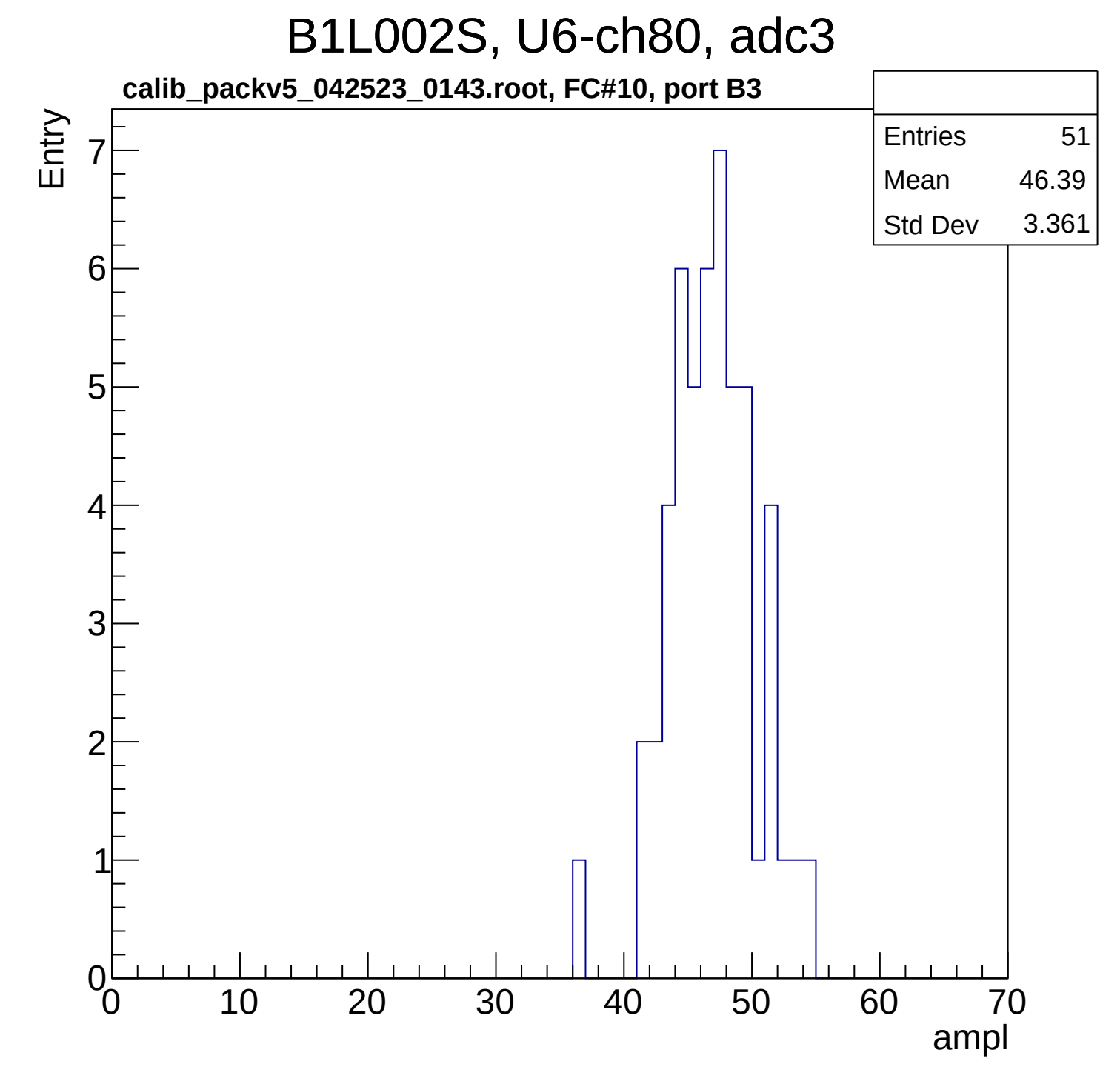

B1L002S, U6-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.15
Std Dev 13.54
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

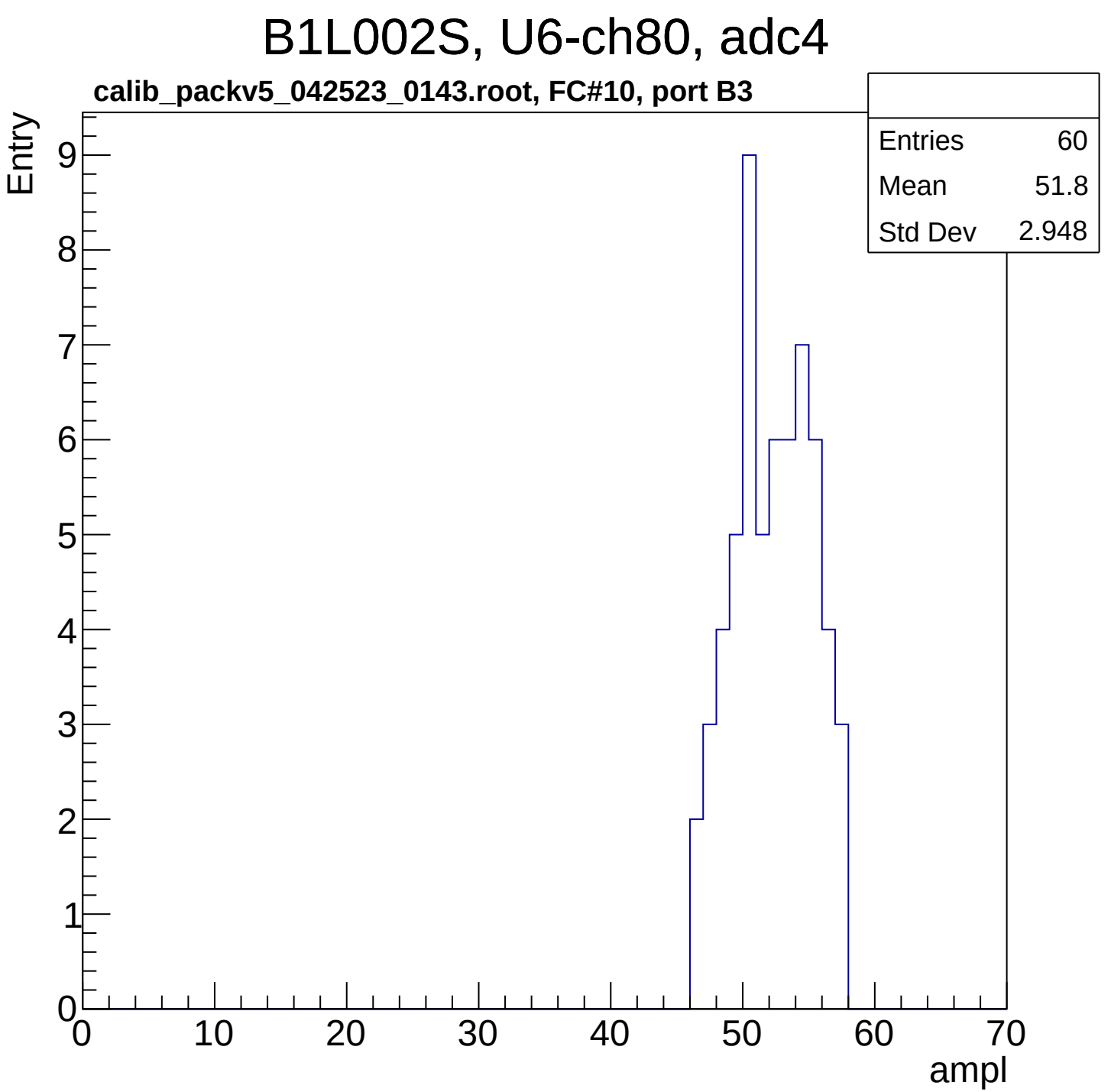

B1L002S, U6-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 47.67
Std Dev 20.29
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

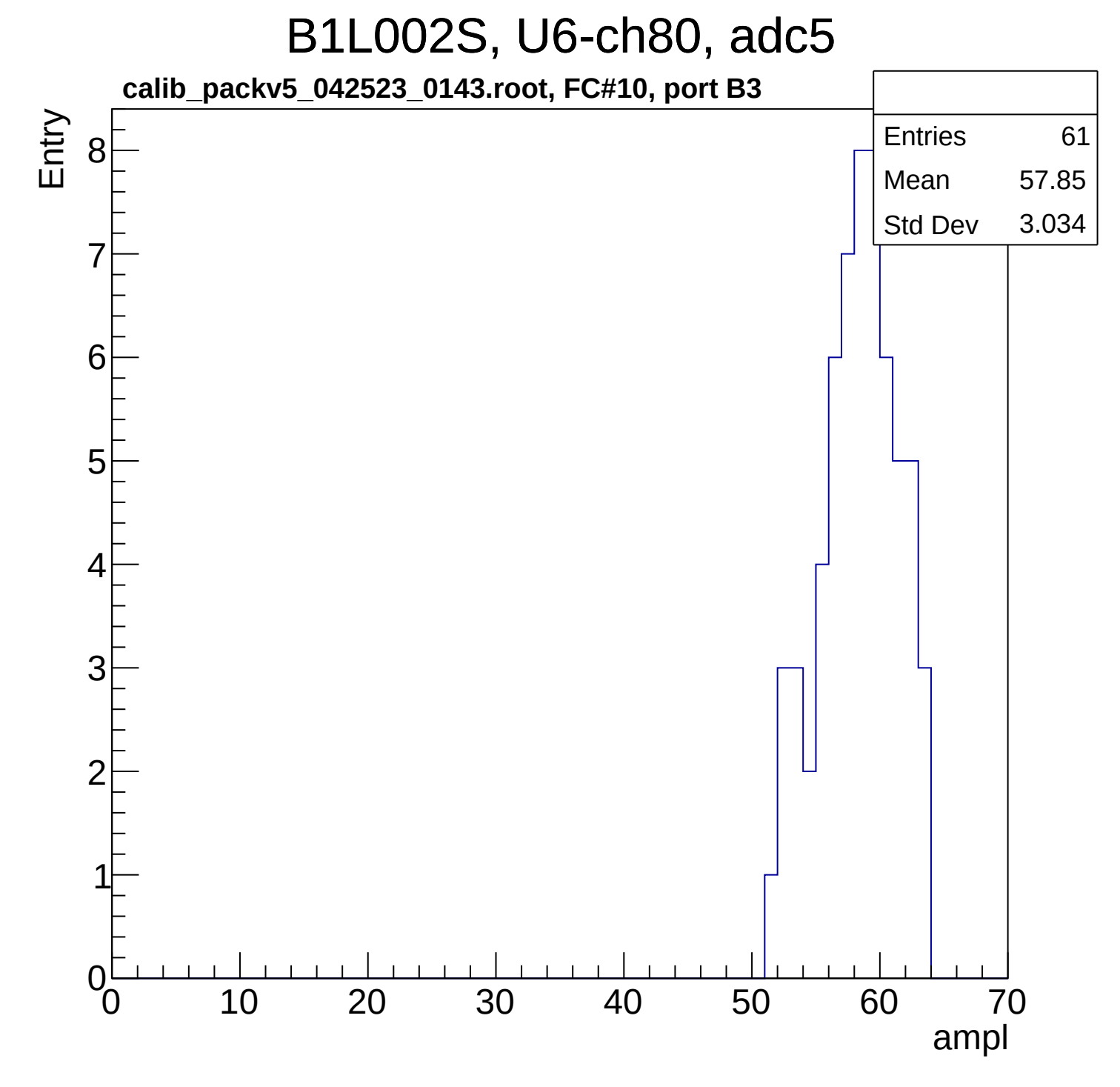

B1L002S, U6-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.63
Std Dev 2.895
Gaus mean : 28.2288
Gaus Width: 3.0573
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

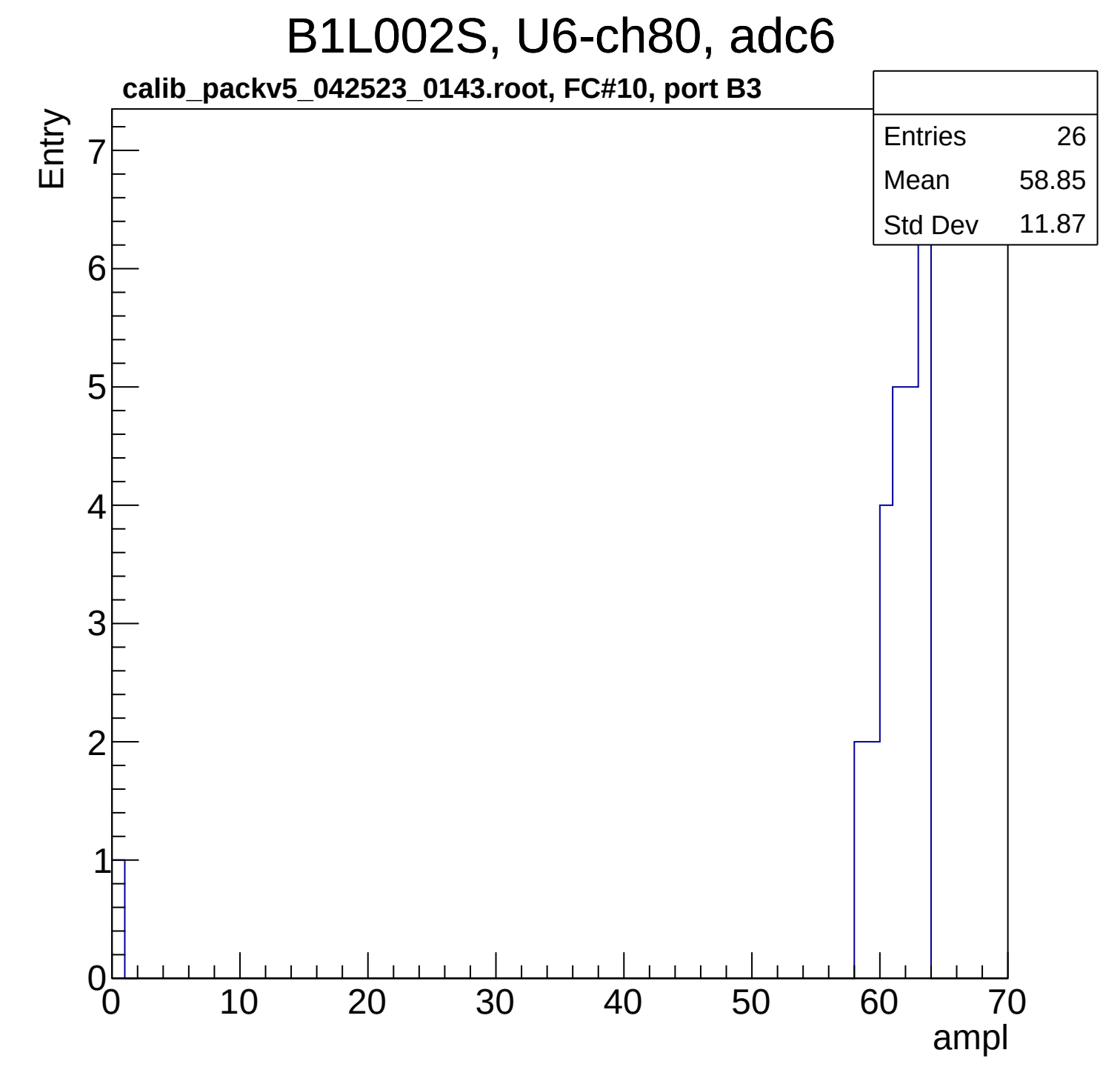

B1L002S, U6-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.87
Std Dev 3.004
Gaus mean : 35.7473
Gaus Width: 3.5472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

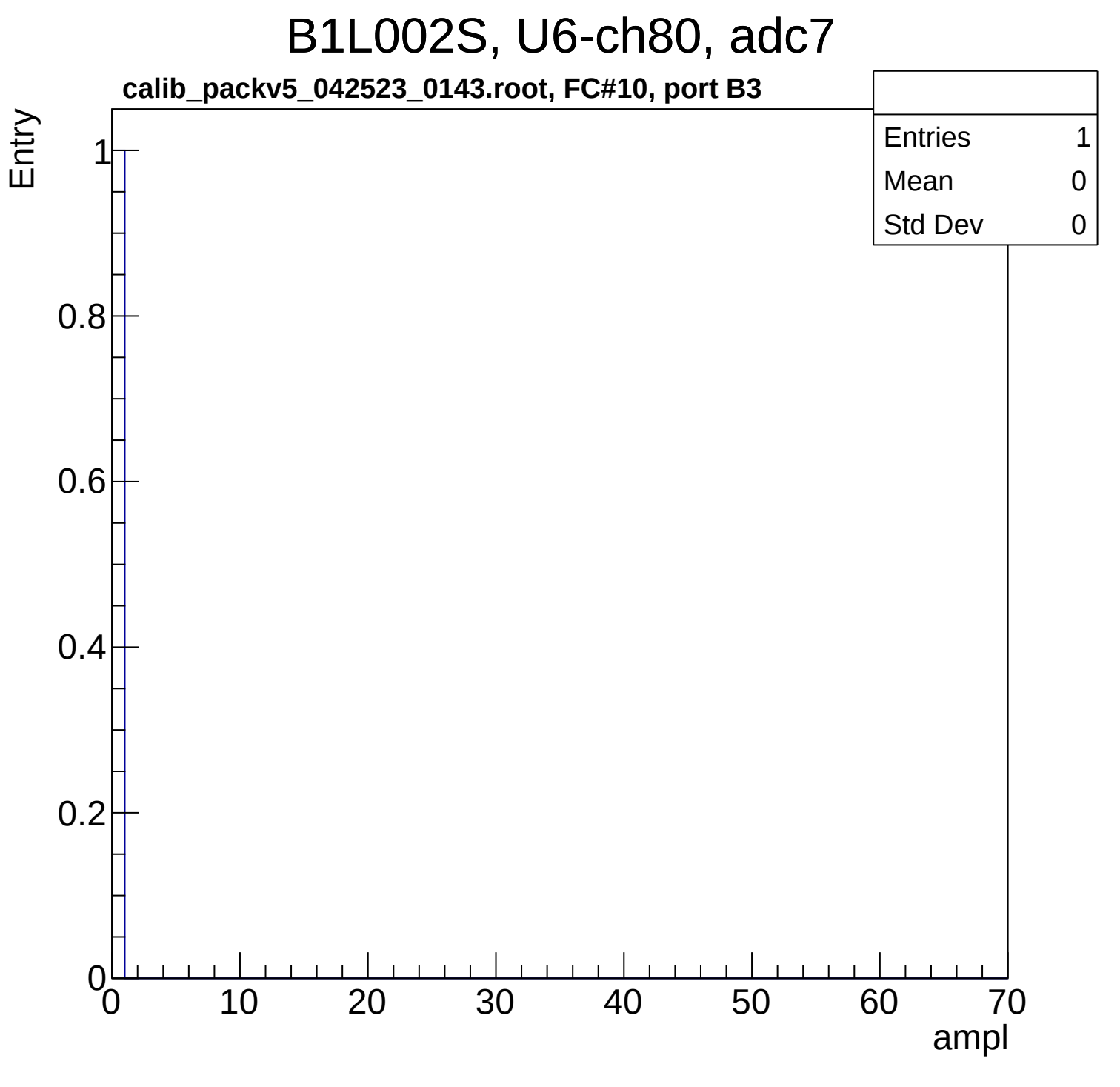

B1L002S, U6-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.26
Std Dev 2.915
Gaus mean : 41.8473
Gaus Width: 3.4936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

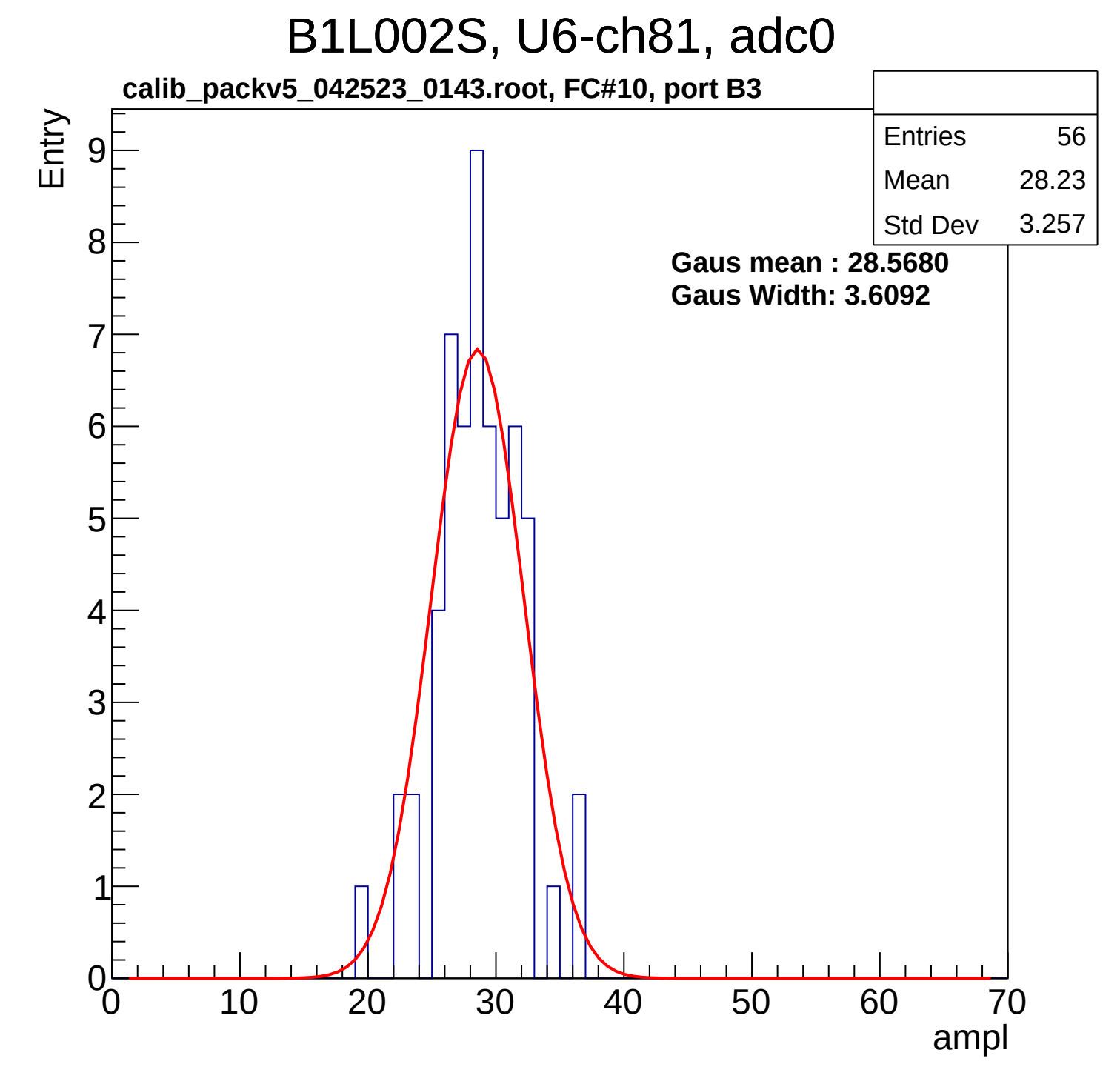

B1L002S, U6-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.39
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

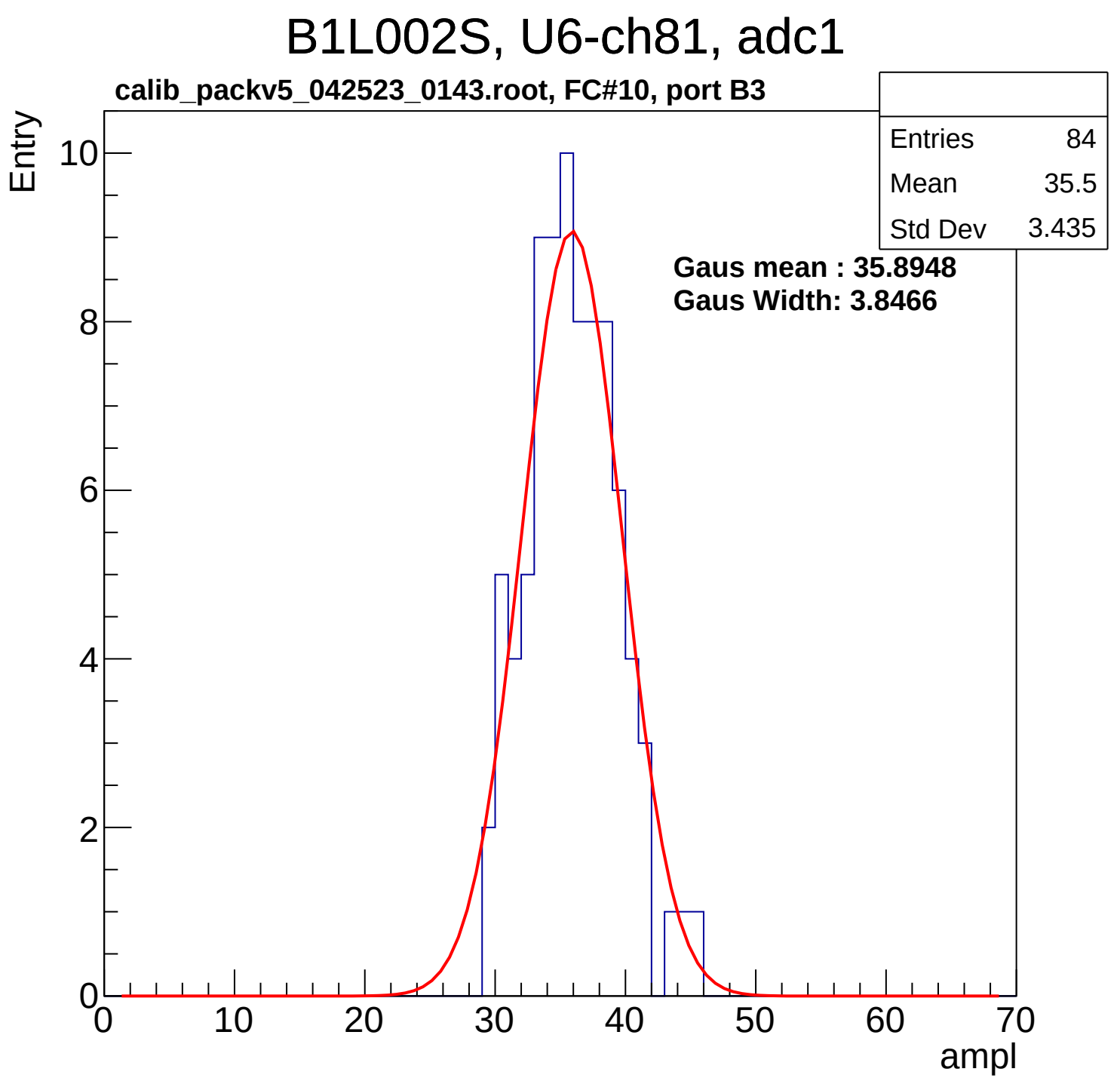

B1L002S, U6-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.8
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

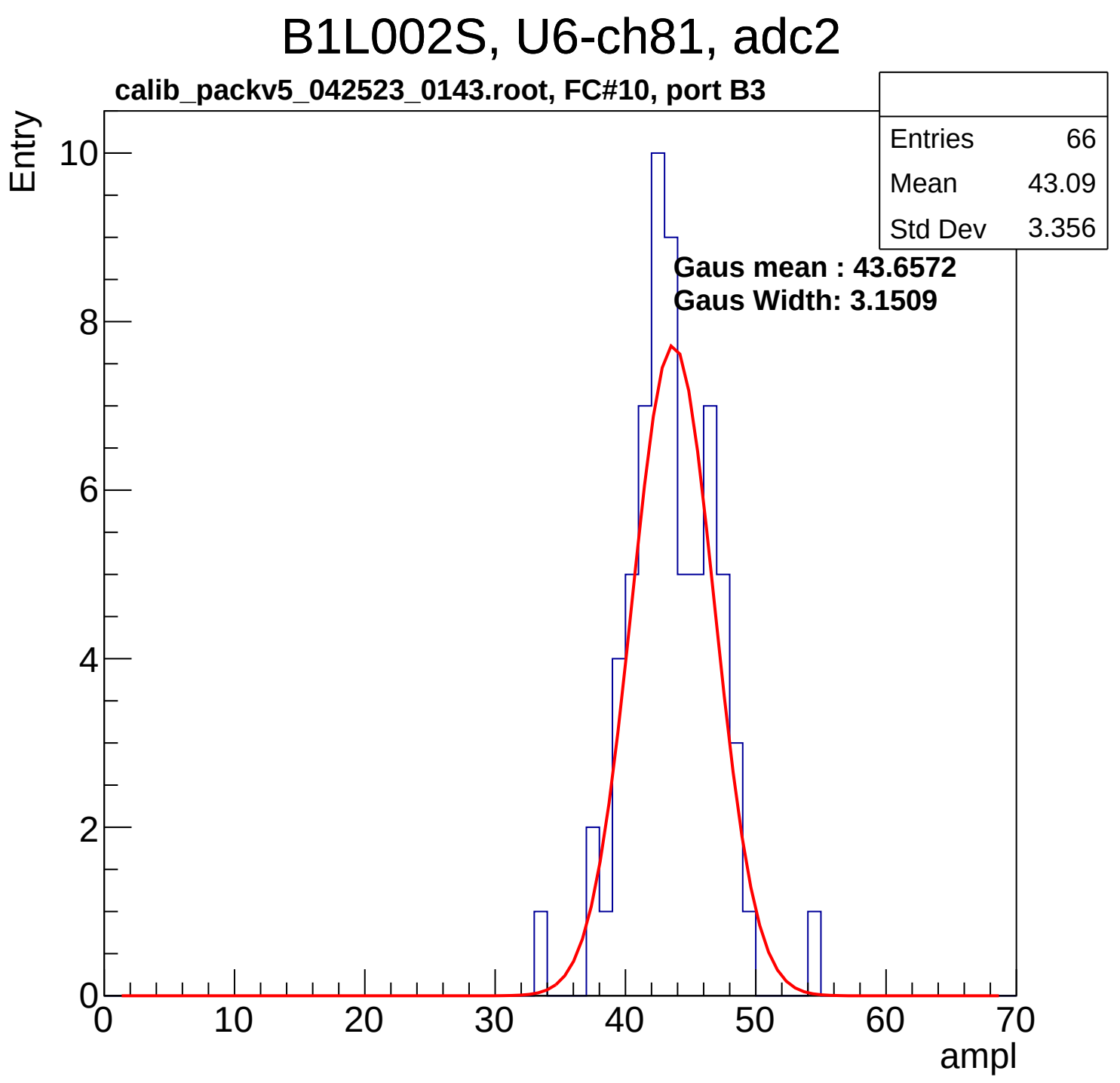

B1L002S, U6-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.85
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

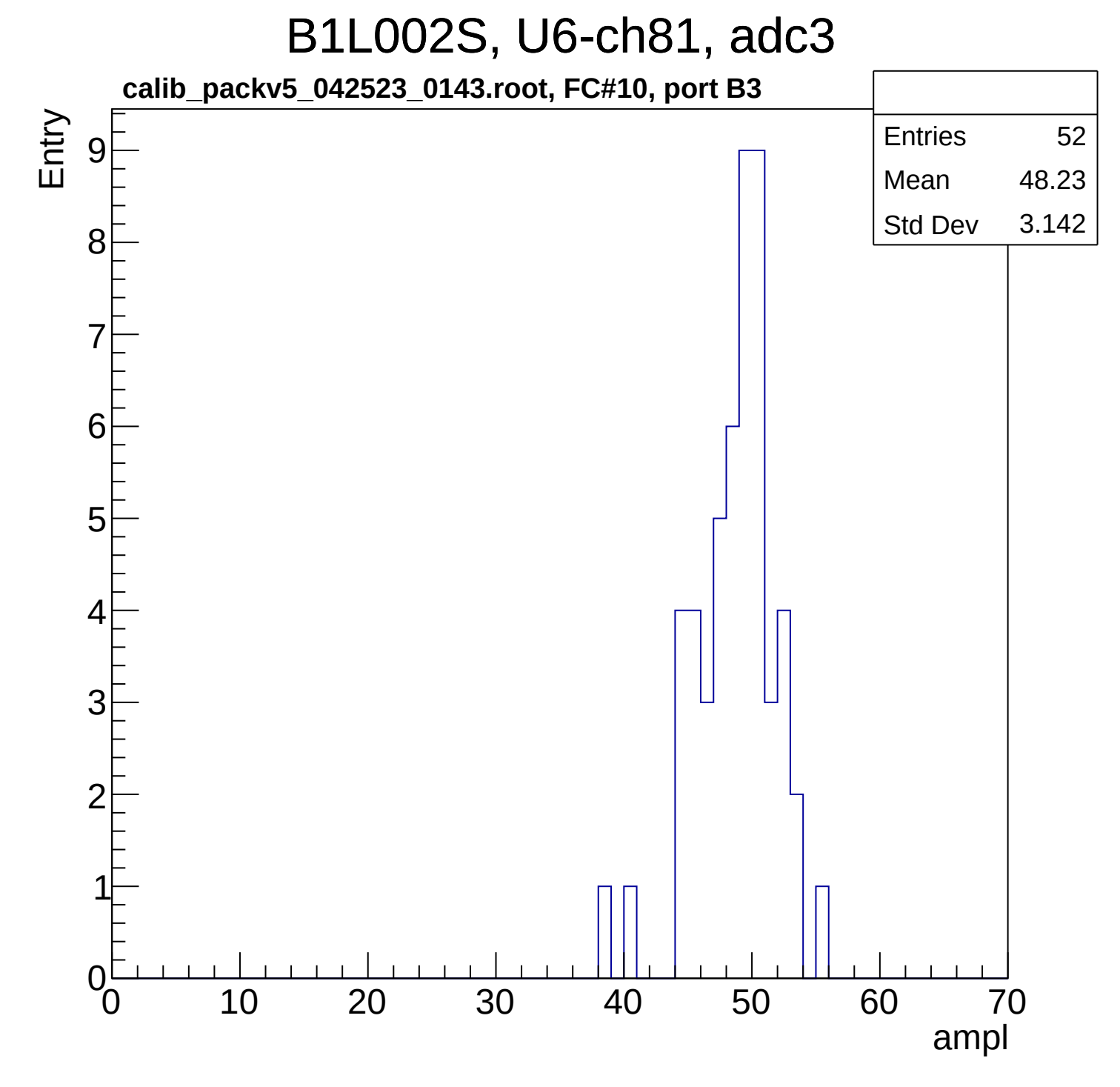

B1L002S, U6-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.85
Std Dev 11.87
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

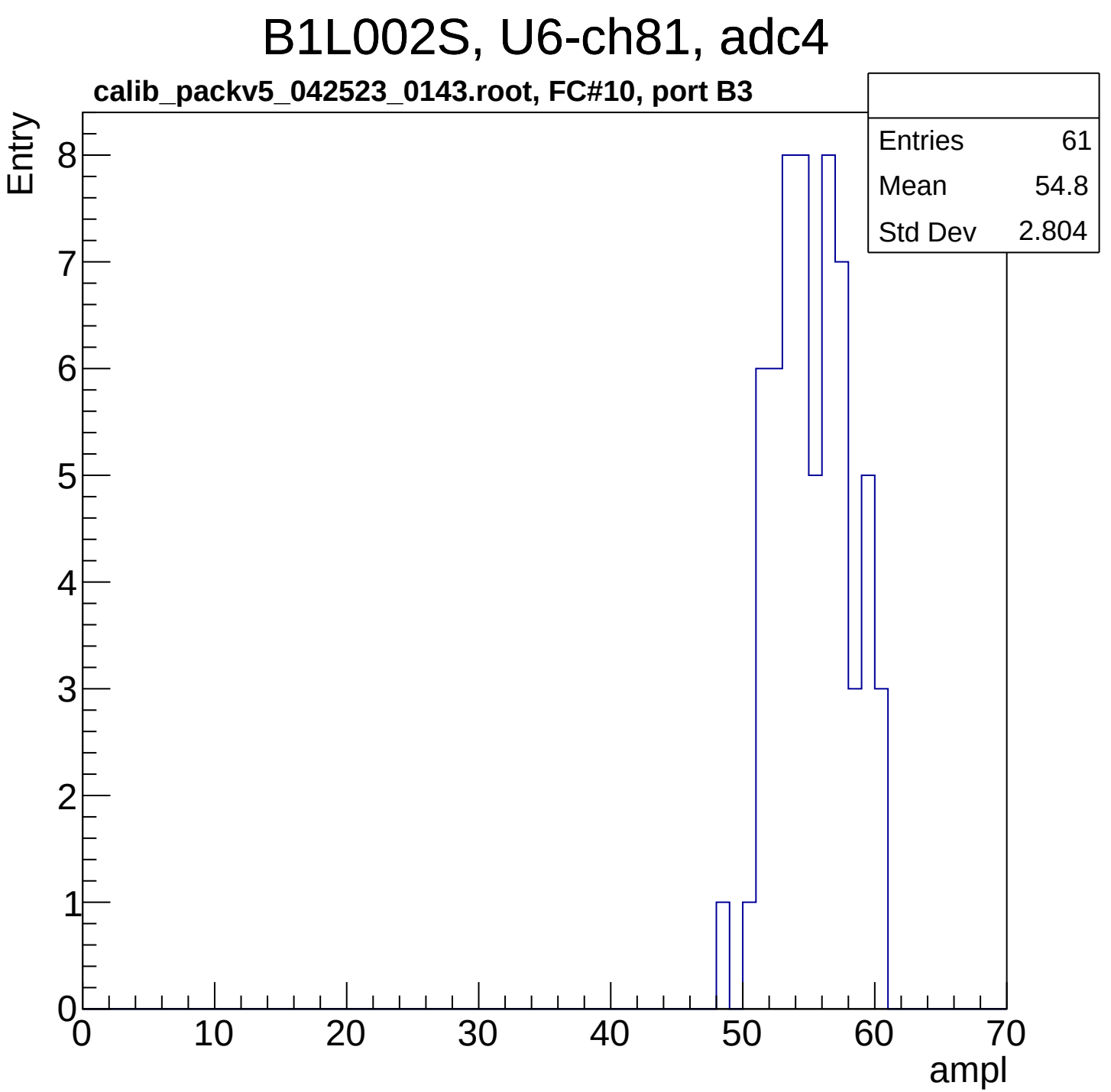

B1L002S, U6-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

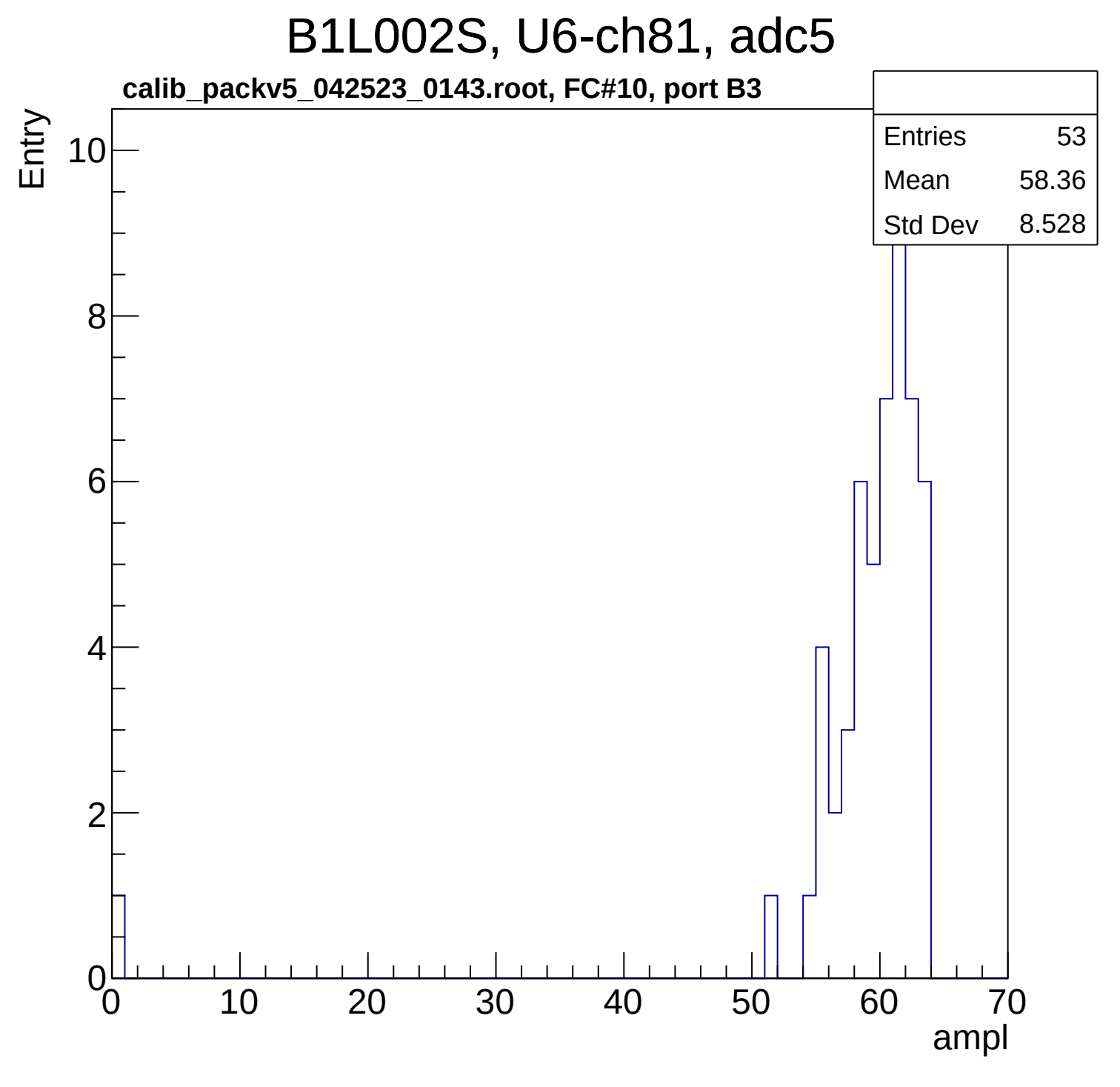

B1L002S, U6-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.23
Std Dev 3.257
Gaus mean : 28.5680
Gaus Width: 3.6092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

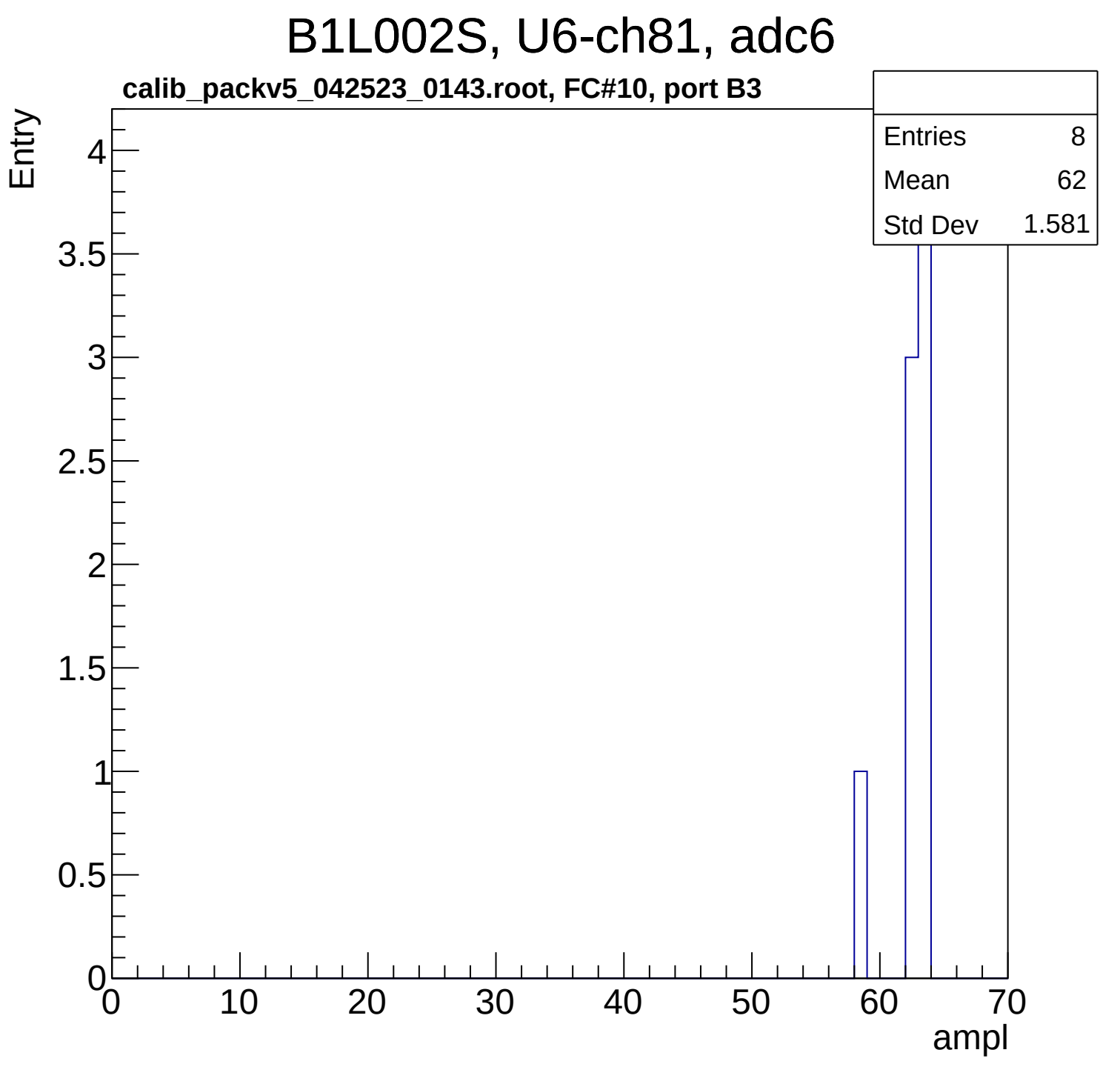

B1L002S, U6-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 35.5
Std Dev 3.435
Gaus mean : 35.8948
Gaus Width: 3.8466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

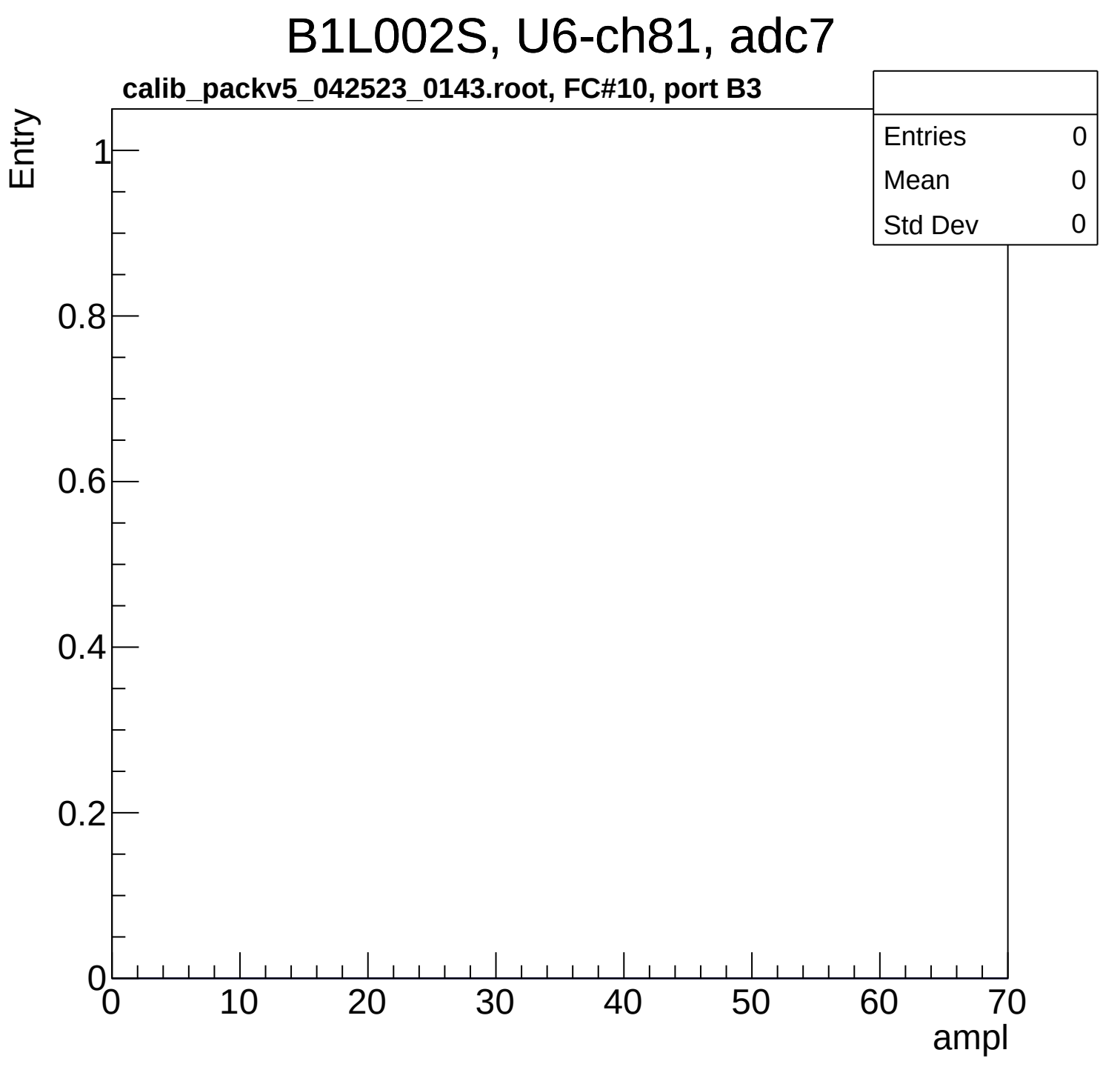

B1L002S, U6-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 43.09
Std Dev 3.356
Gaus mean : 43.6572
Gaus Width: 3.1509
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

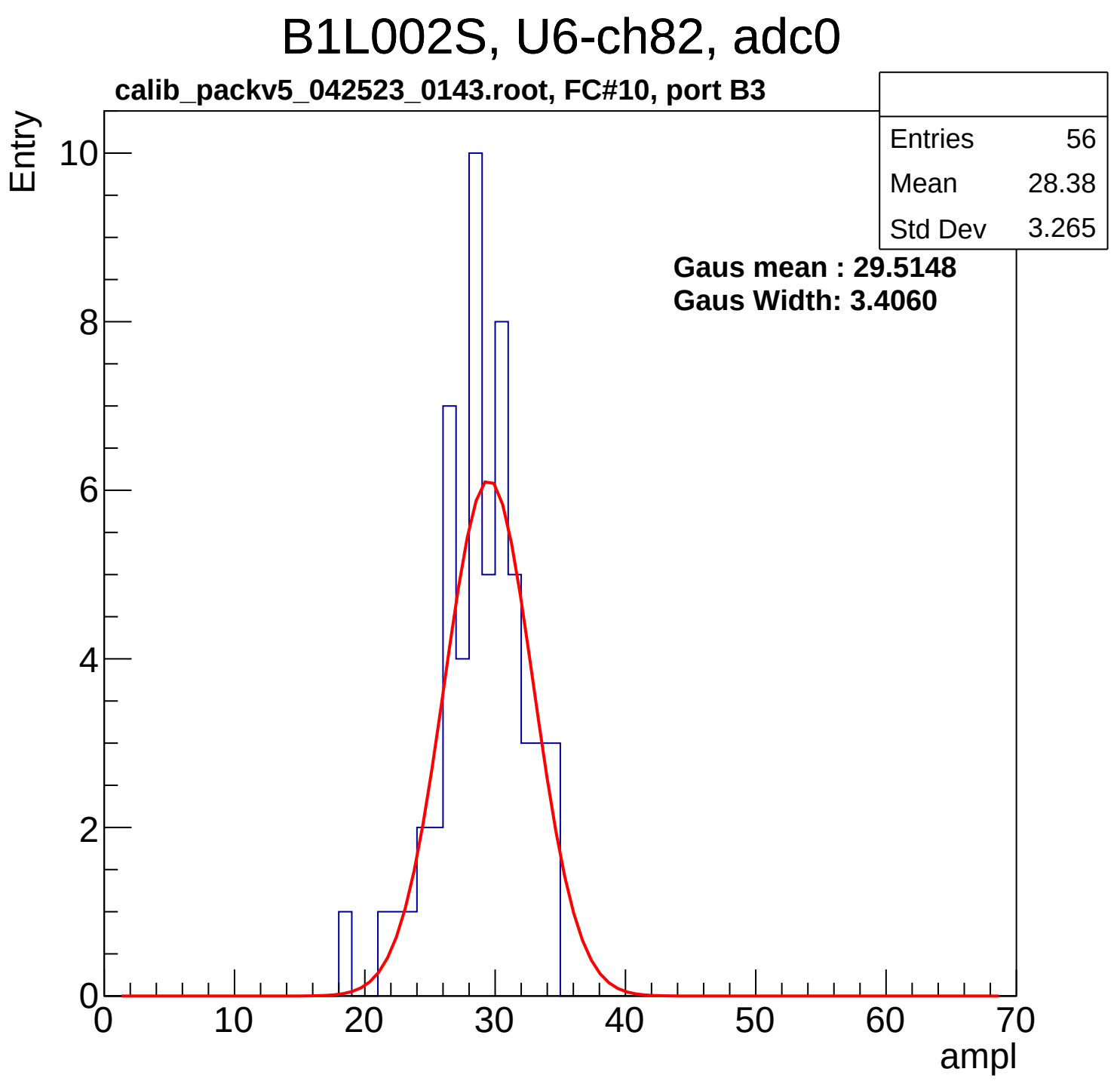

B1L002S, U6-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 48.23
Std Dev 3.142
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

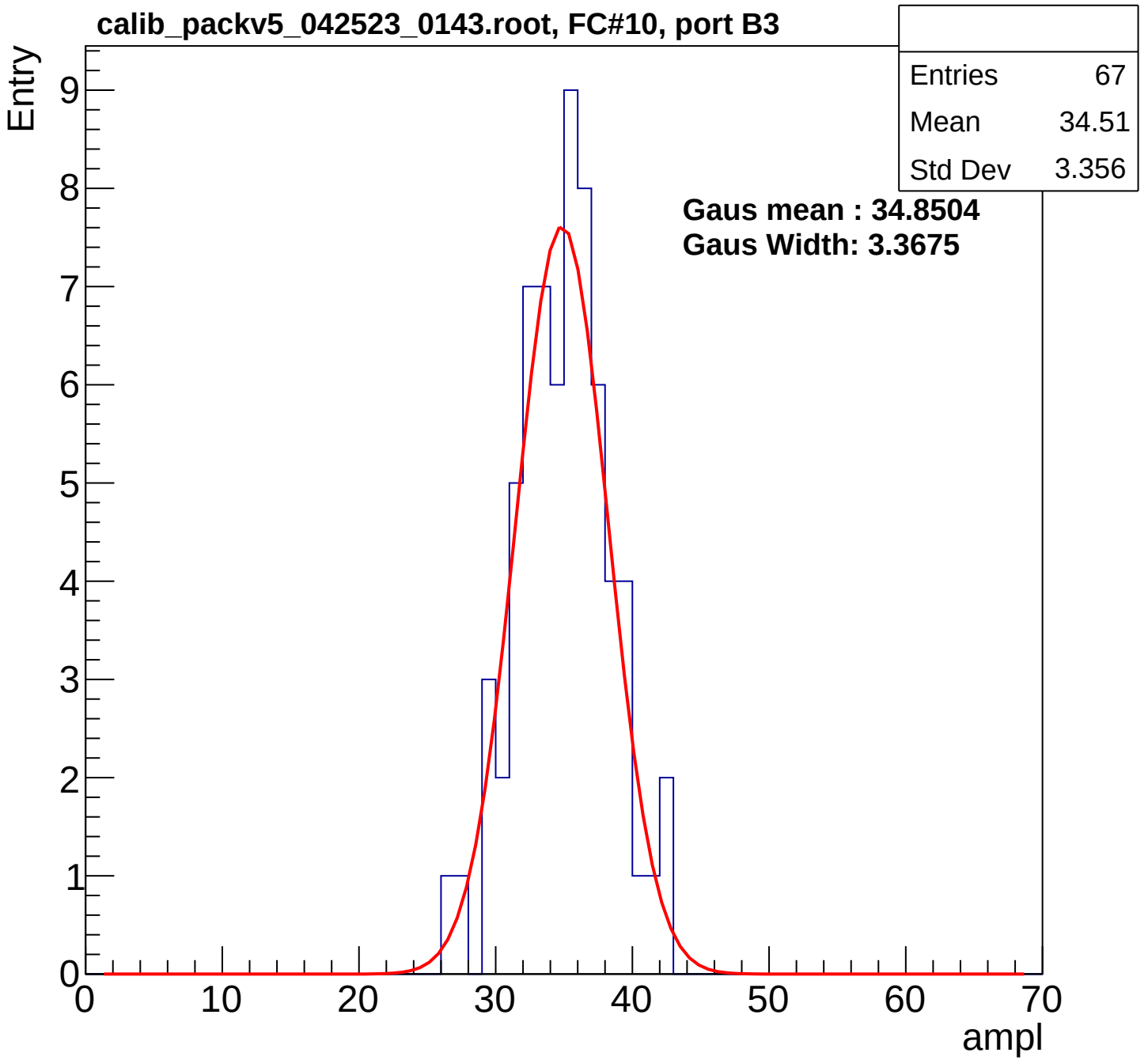

B1L002S, U6-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.8
Std Dev 2.804
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

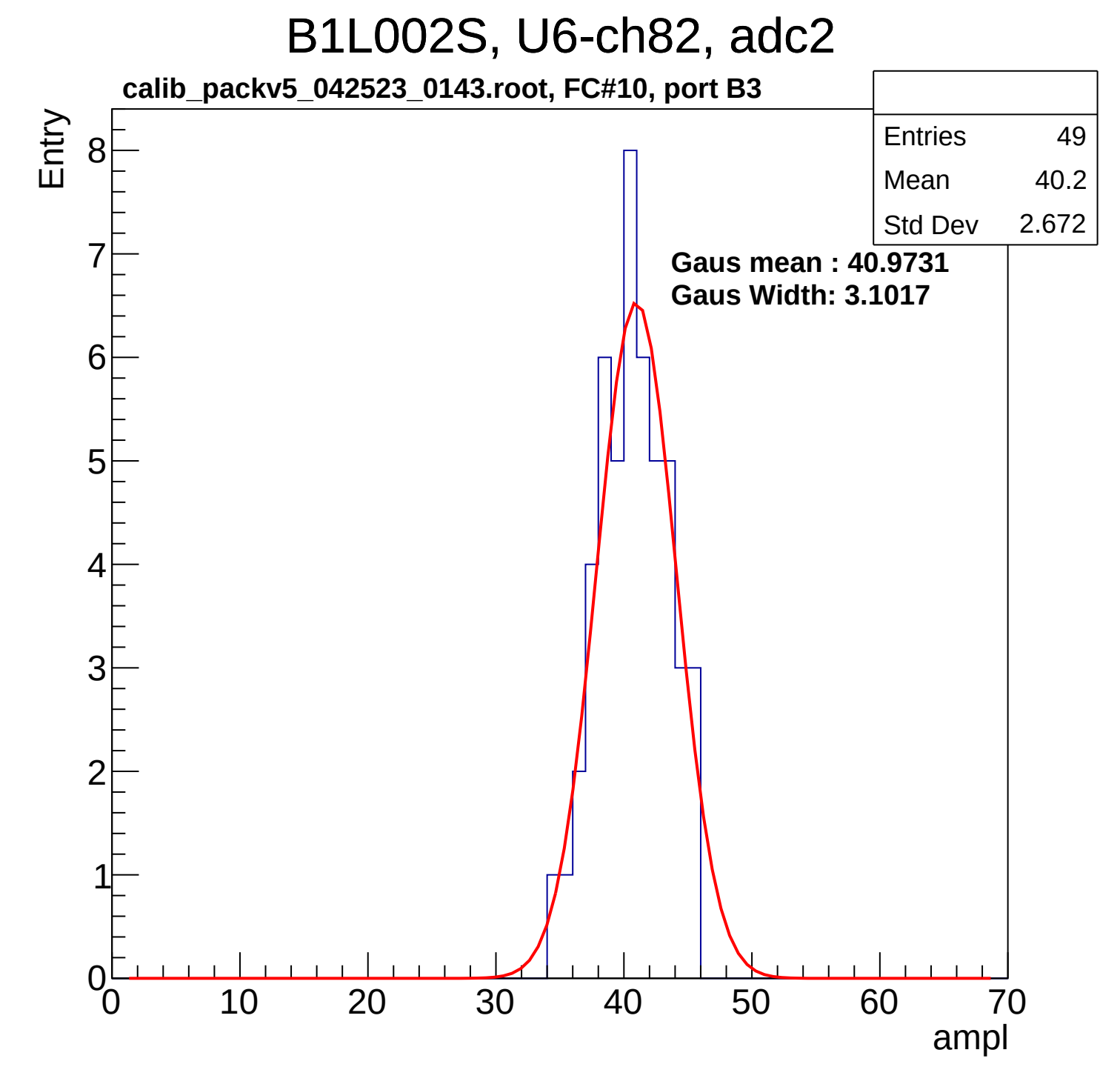

B1L002S, U6-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.36
Std Dev 8.528
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

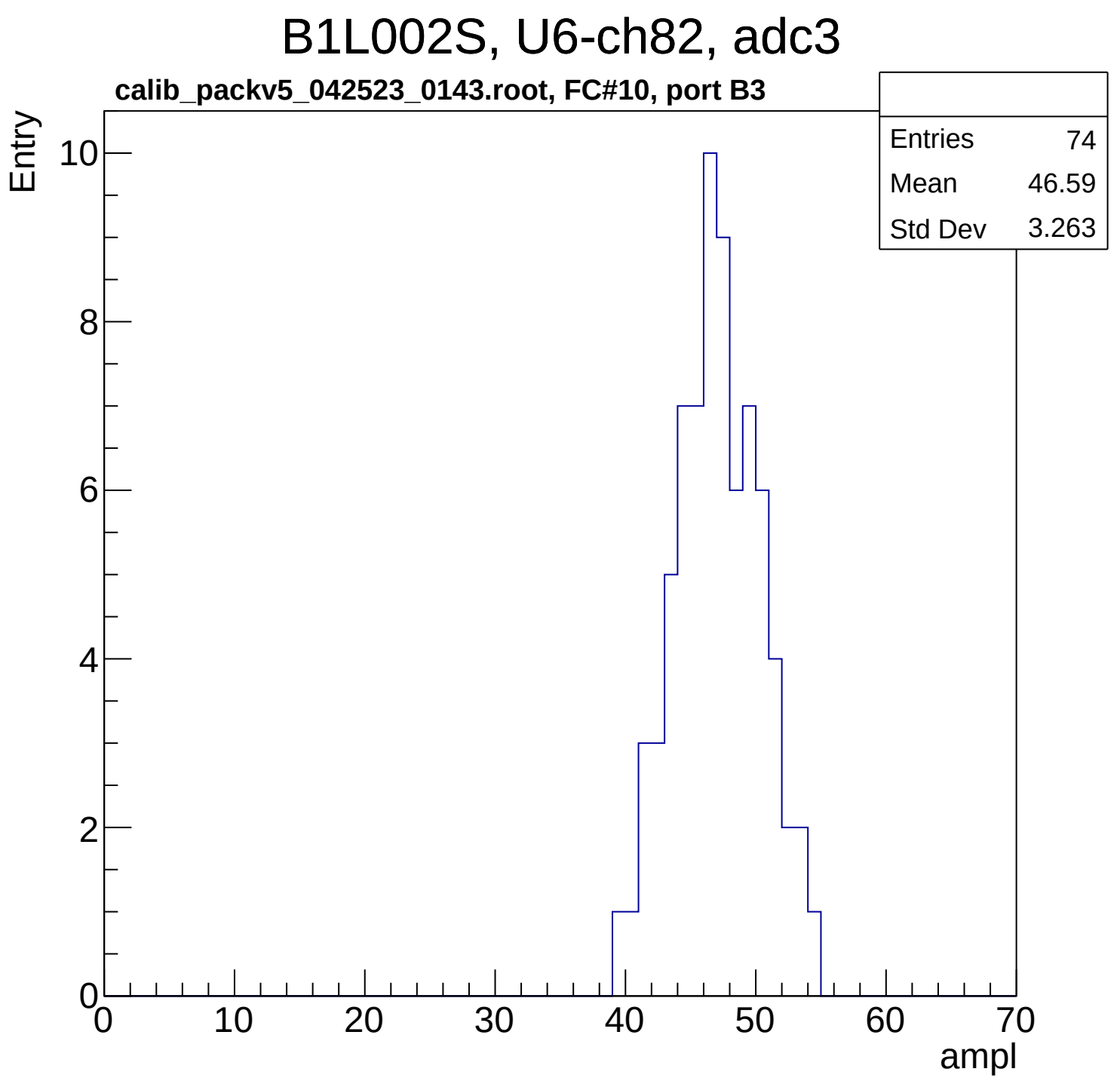

B1L002S, U6-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62
Std Dev 1.581
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

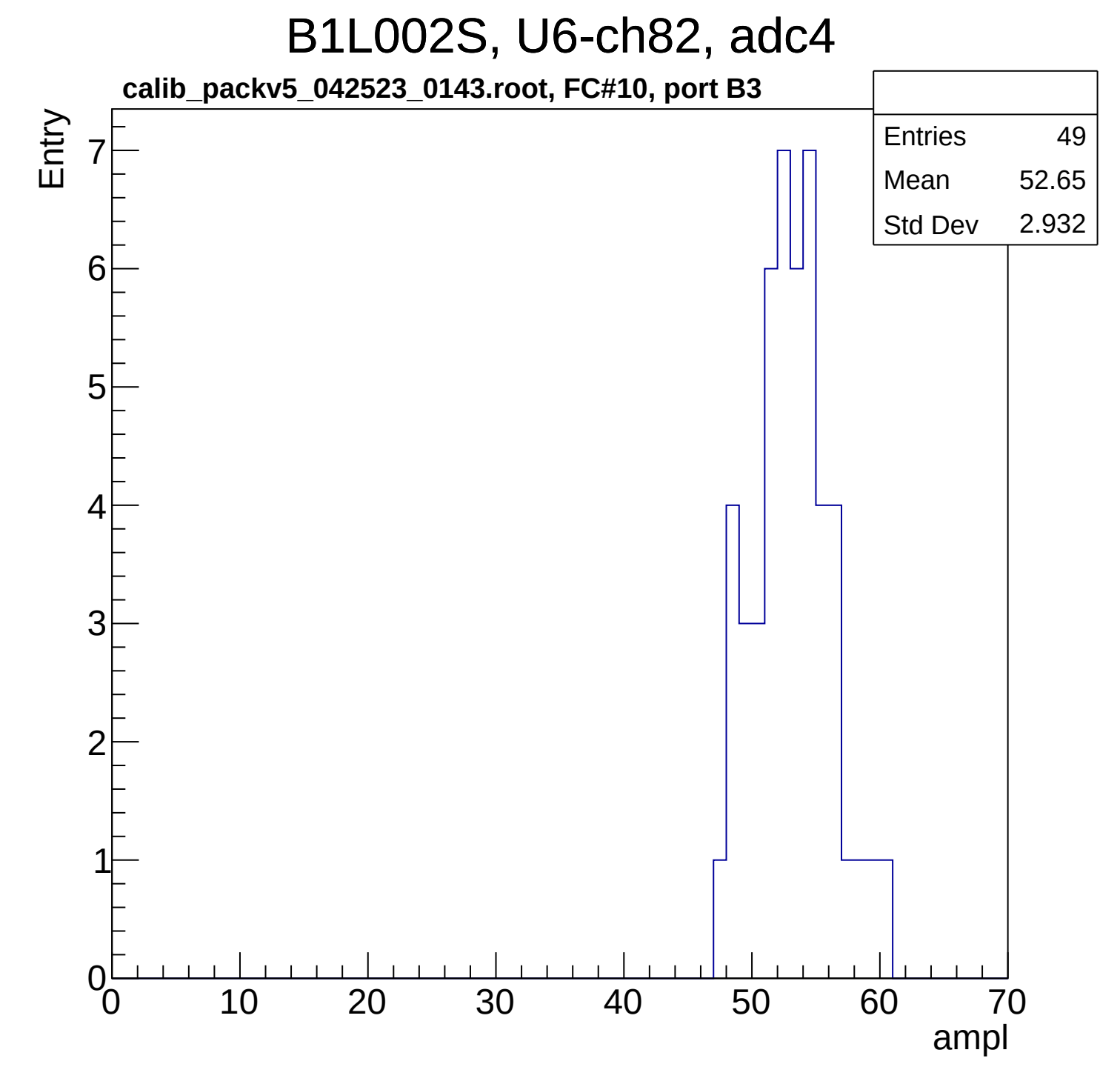

B1L002S, U6-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

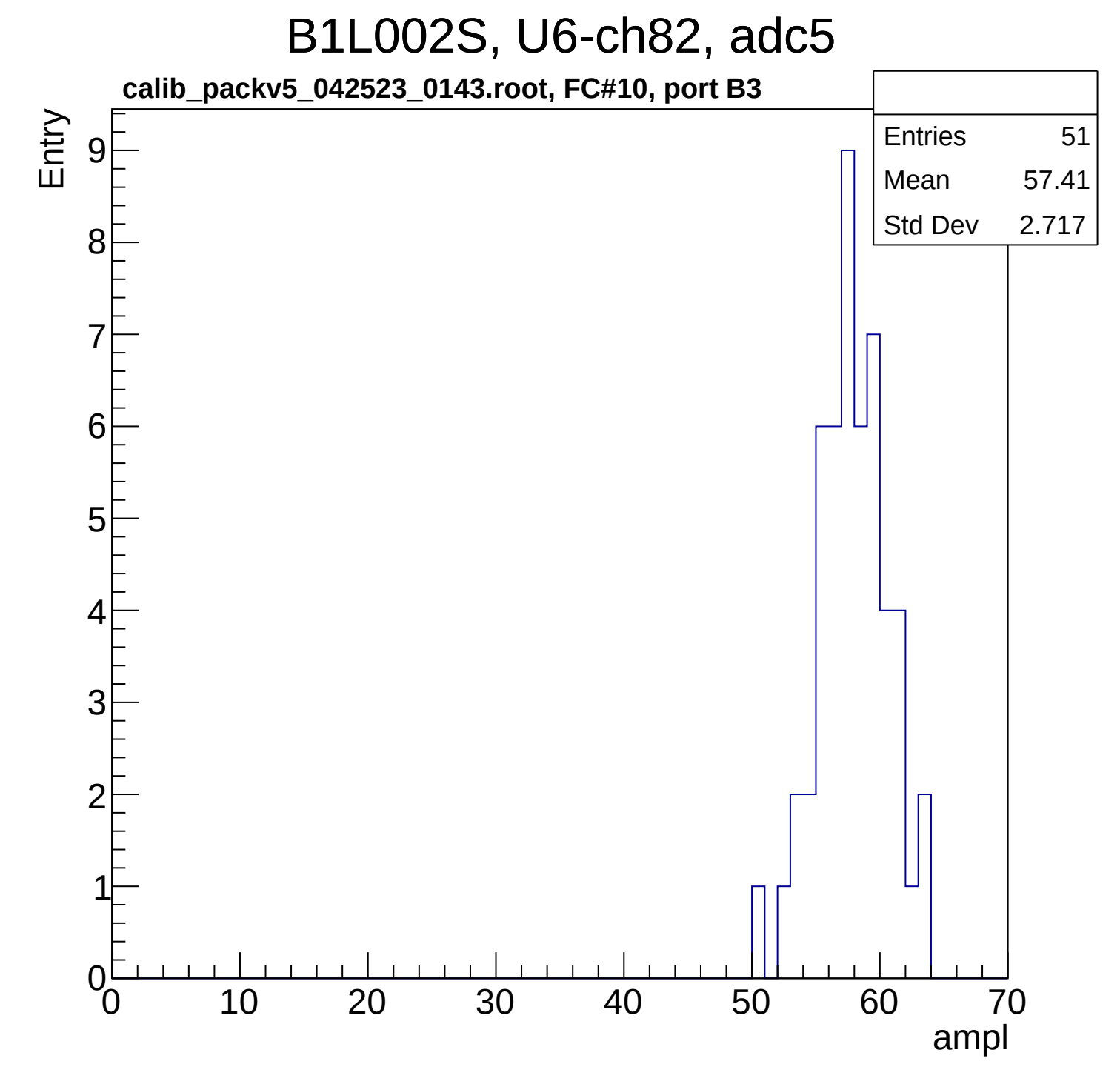

B1L002S, U6-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.38
Std Dev 3.265
Gaus mean : 29.5148
Gaus Width: 3.4060
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

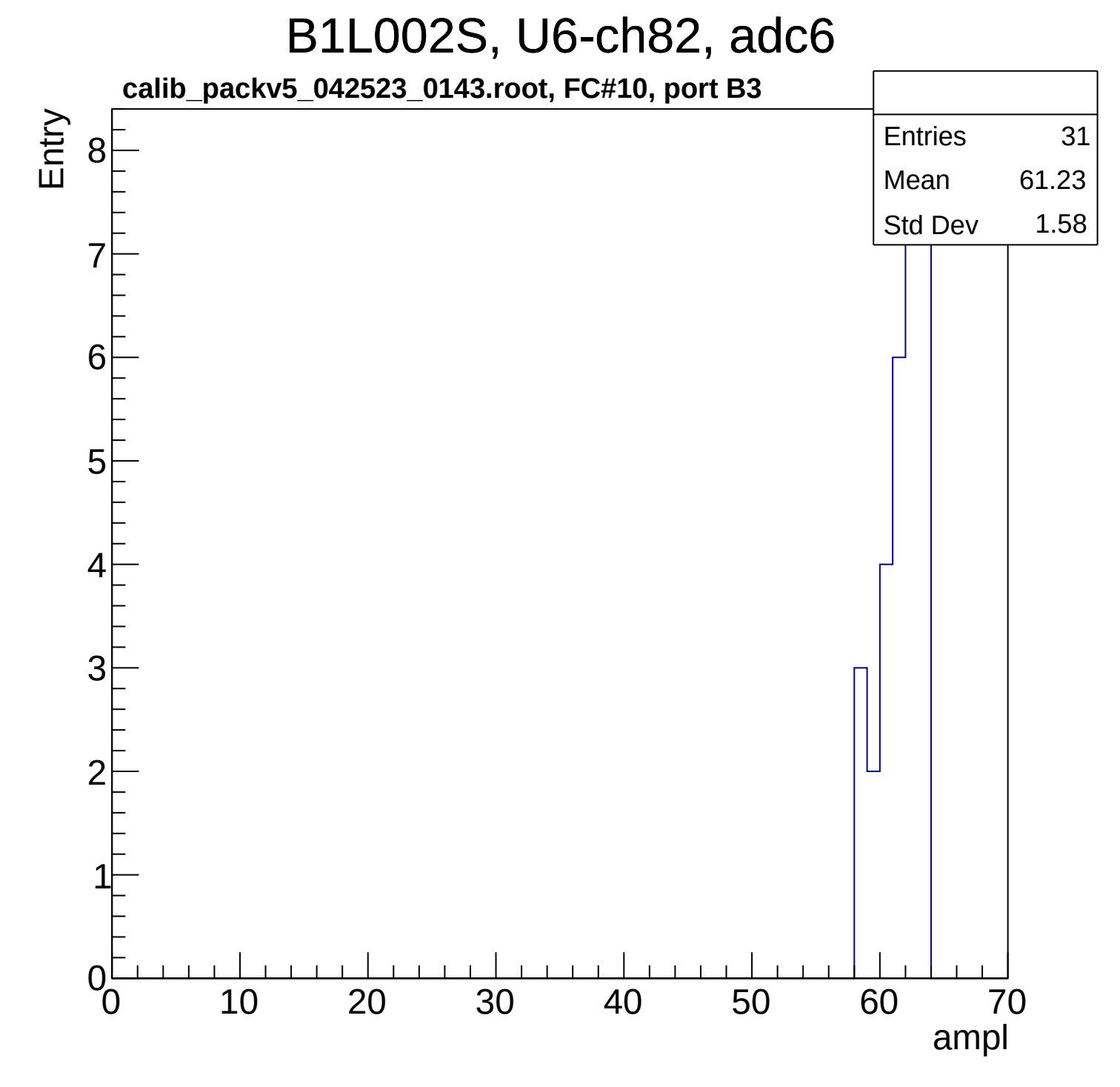

B1L002S, U6-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.51
Std Dev 3.356
Gaus mean : 34.8504
Gaus Width: 3.3675
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

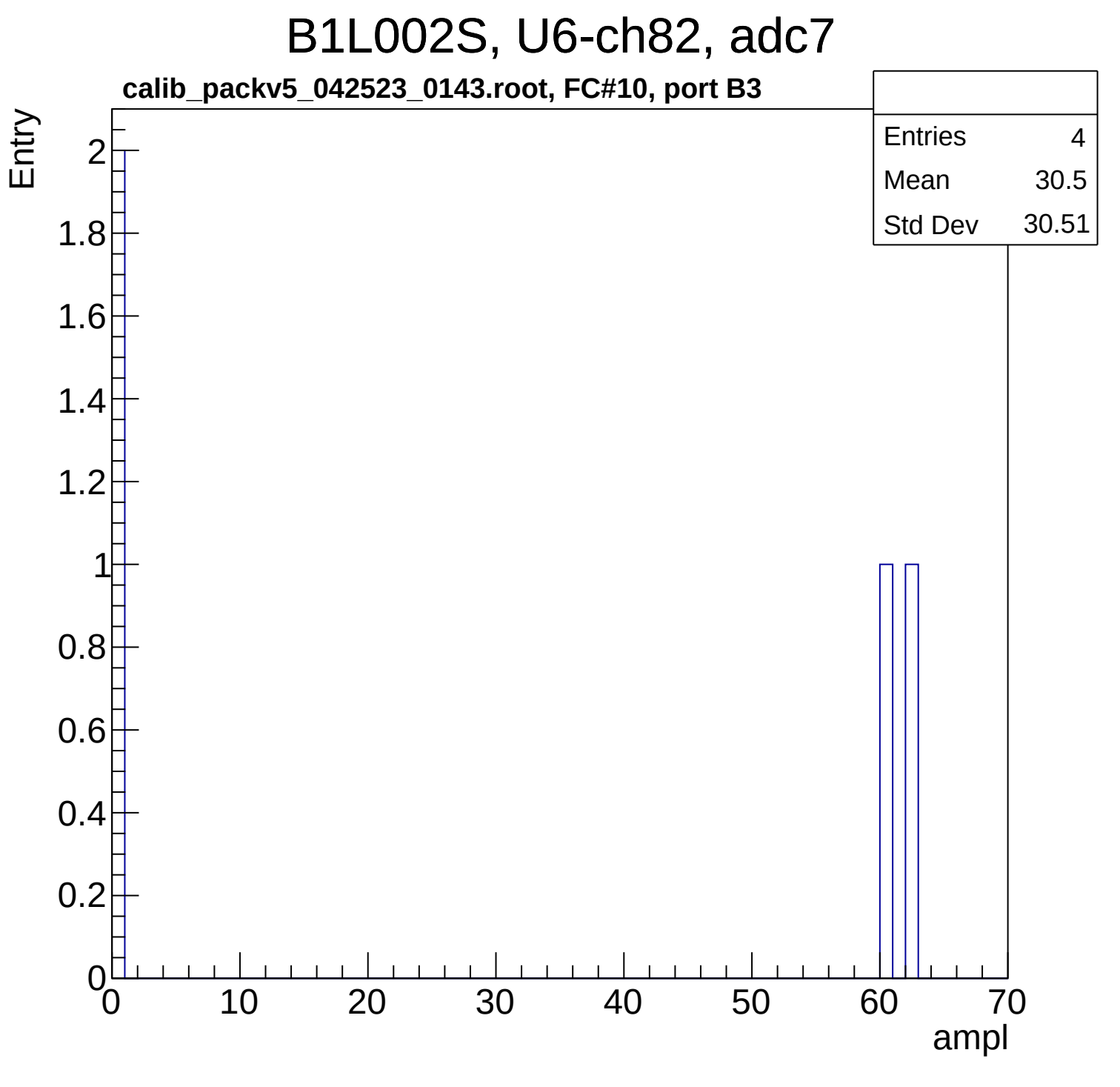

B1L002S, U6-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.2
Std Dev 2.672
Gaus mean : 40.9731
Gaus Width: 3.1017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

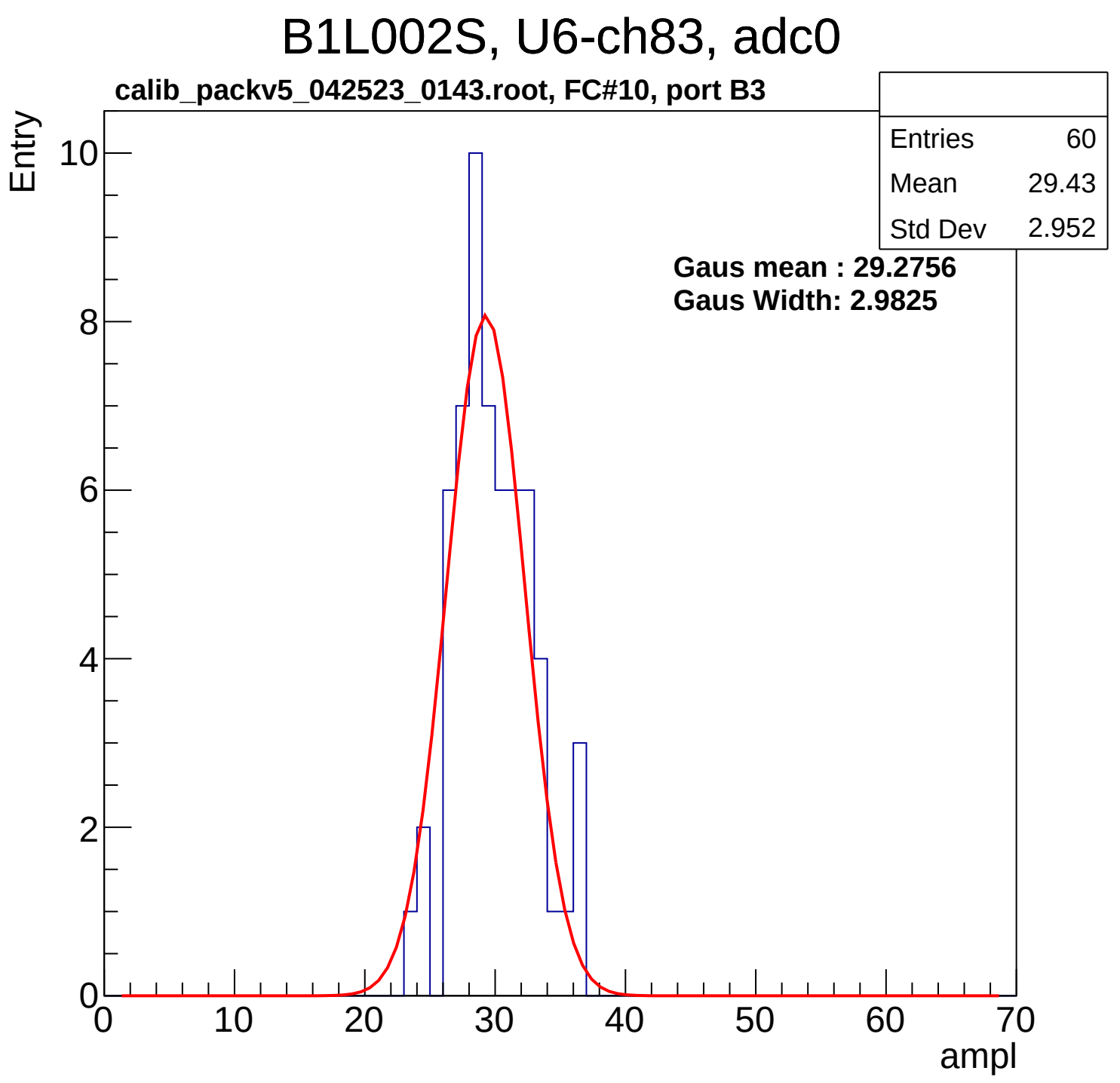

B1L002S, U6-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.59
Std Dev 3.263
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

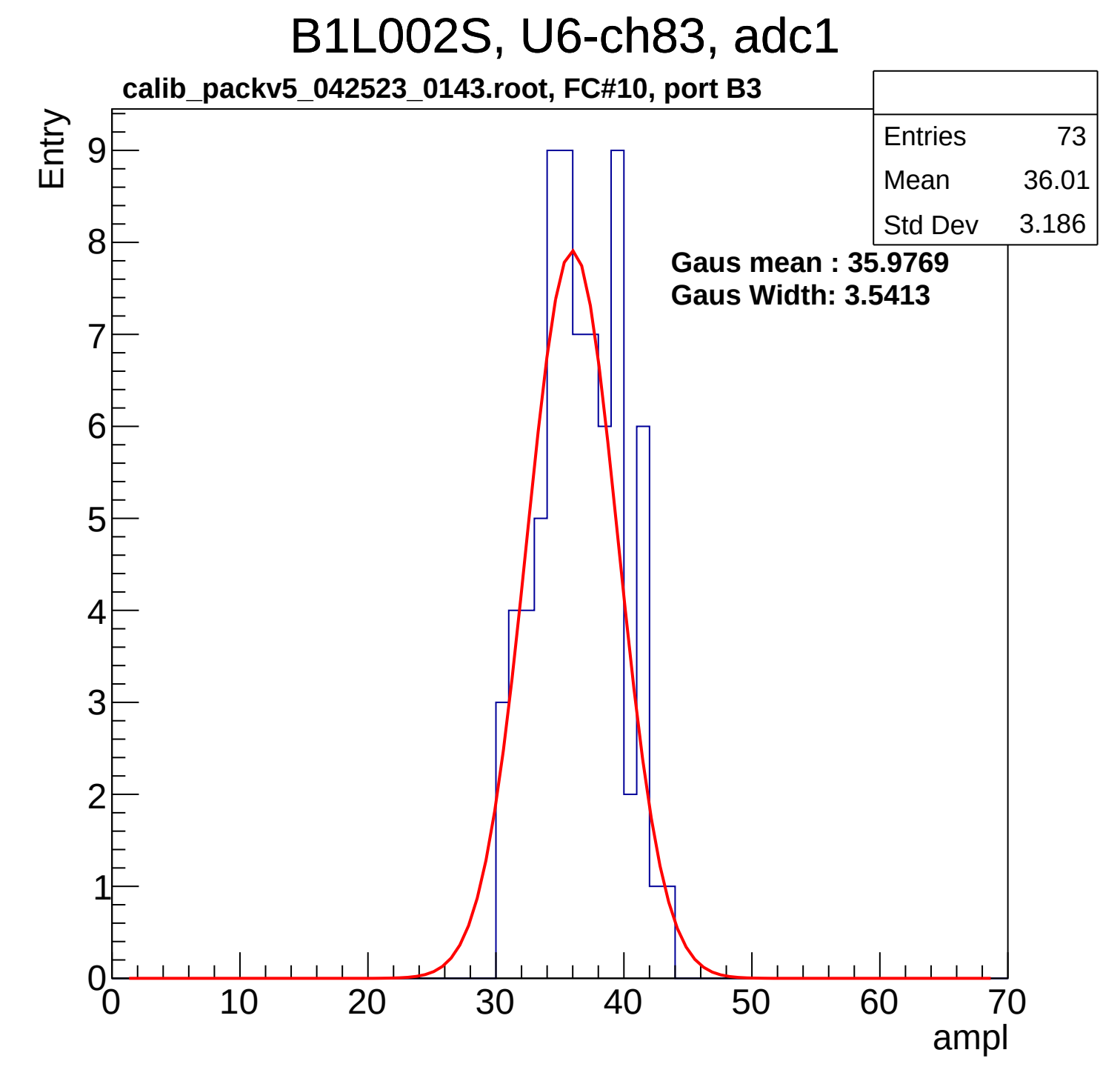

B1L002S, U6-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.65
Std Dev 2.932
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

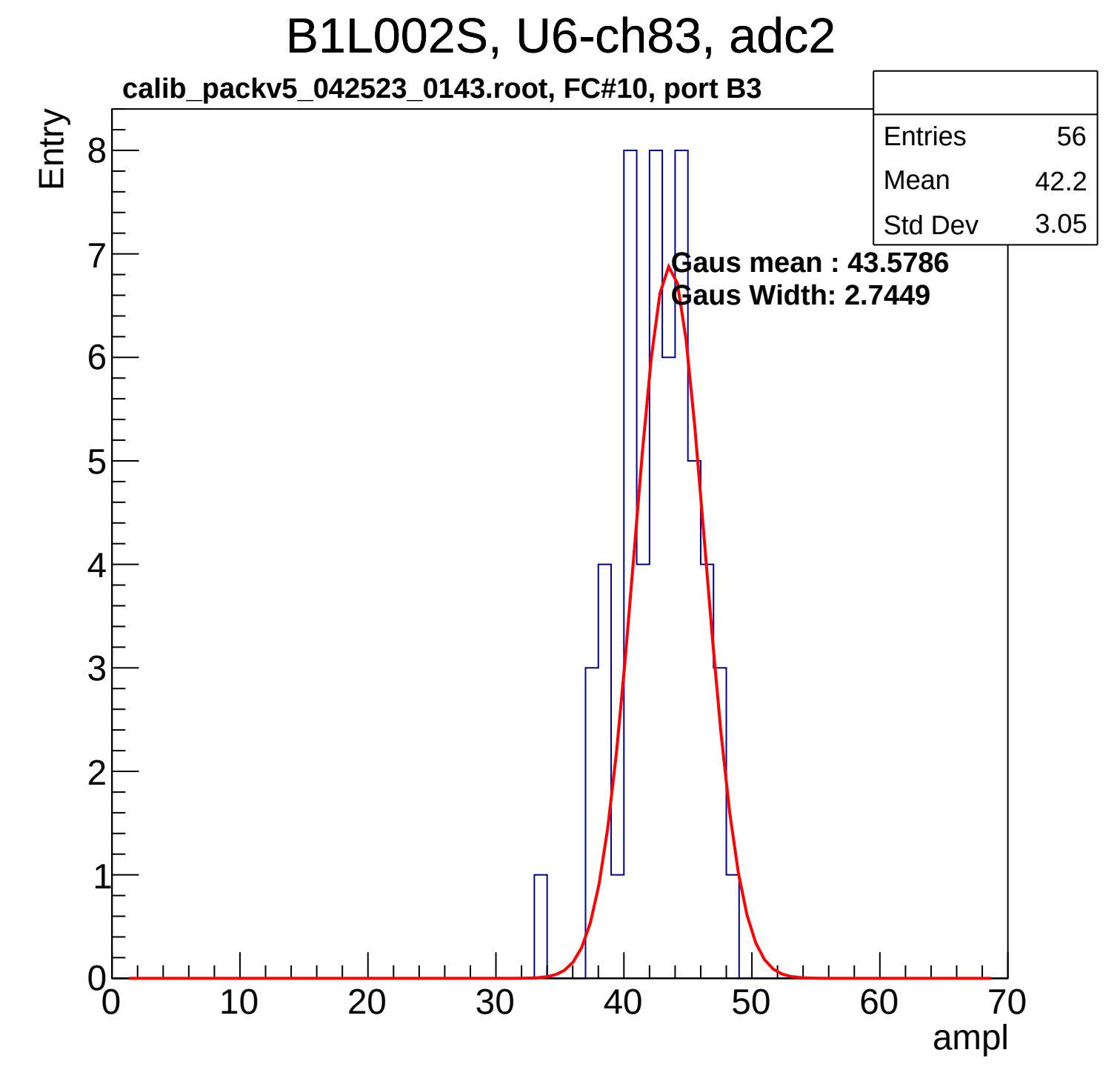

B1L002S, U6-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.41
Std Dev 2.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

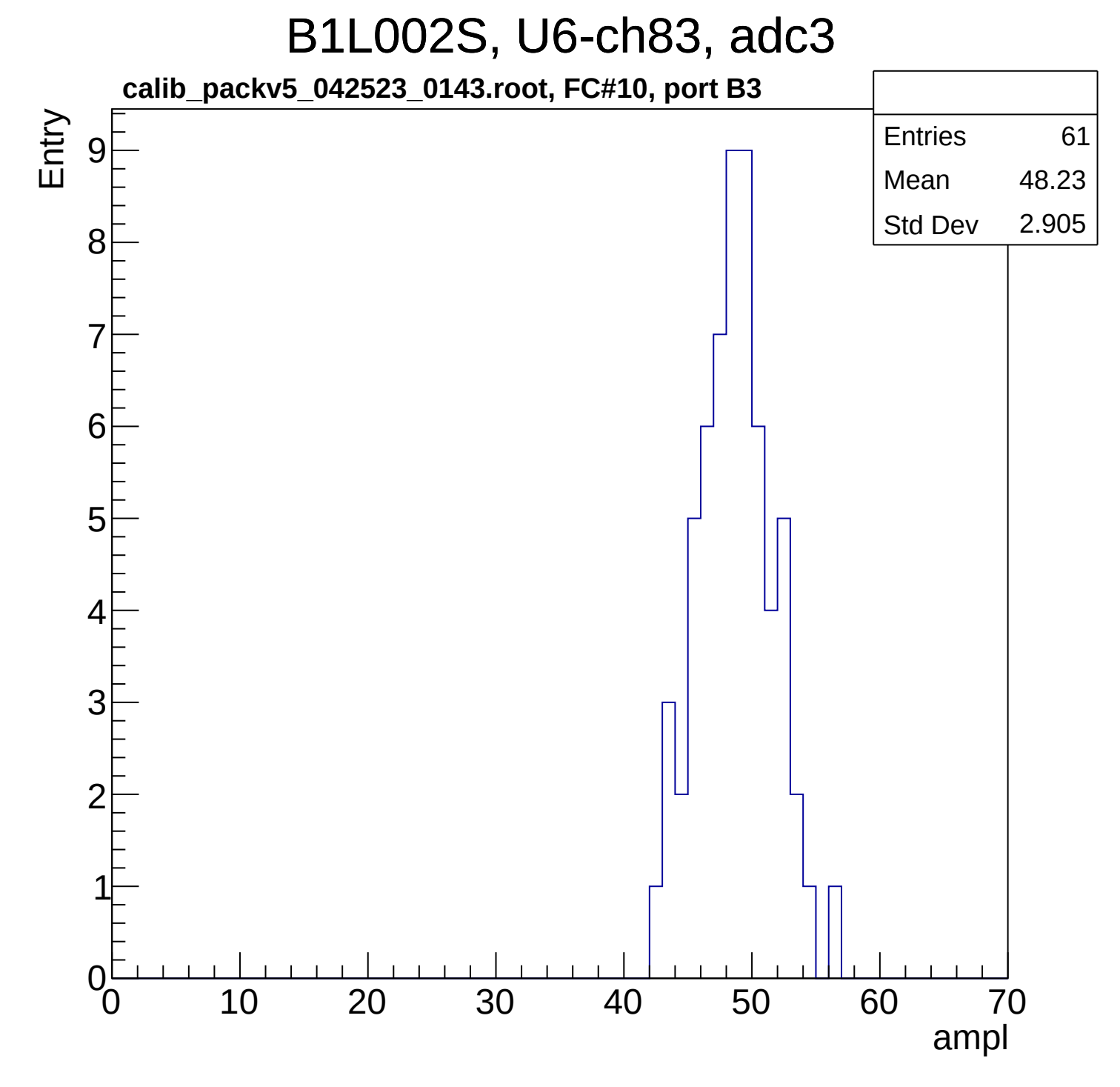

B1L002S, U6-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 61.23
Std Dev 1.58
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

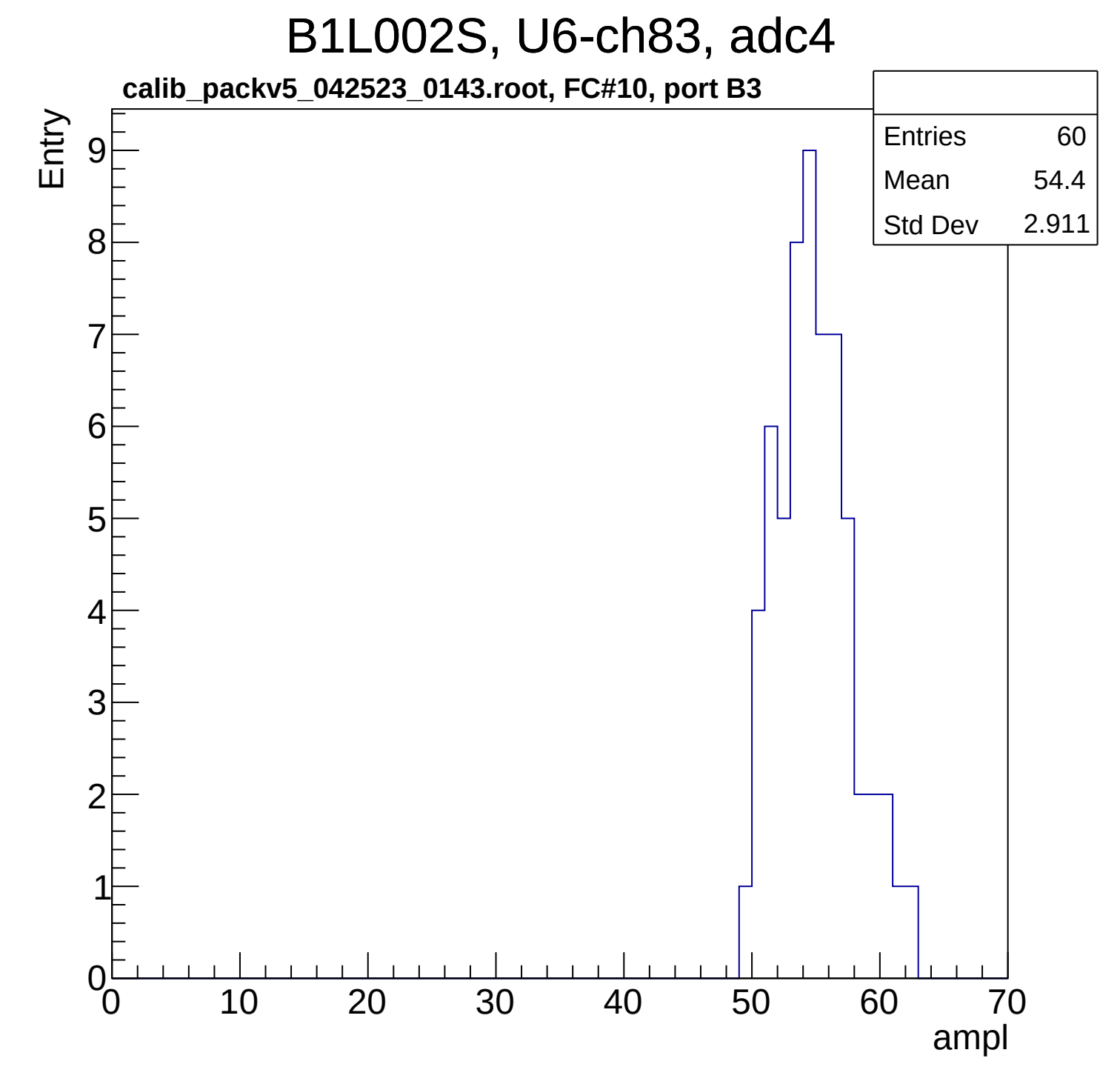

B1L002S, U6-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.5
Std Dev 30.51
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

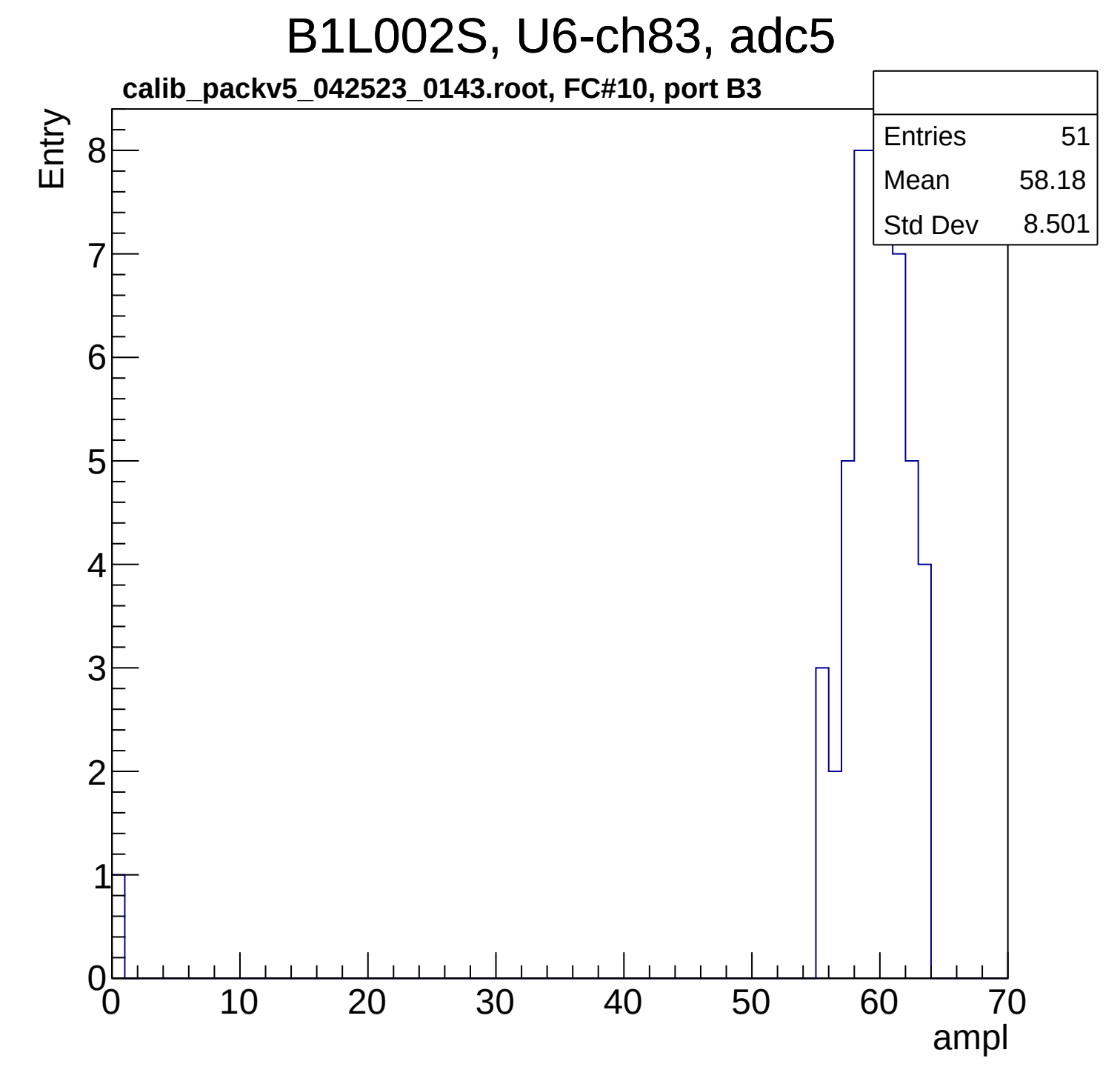

B1L002S, U6-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.43
Std Dev 2.952
Gaus mean : 29.2756
Gaus Width: 2.9825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

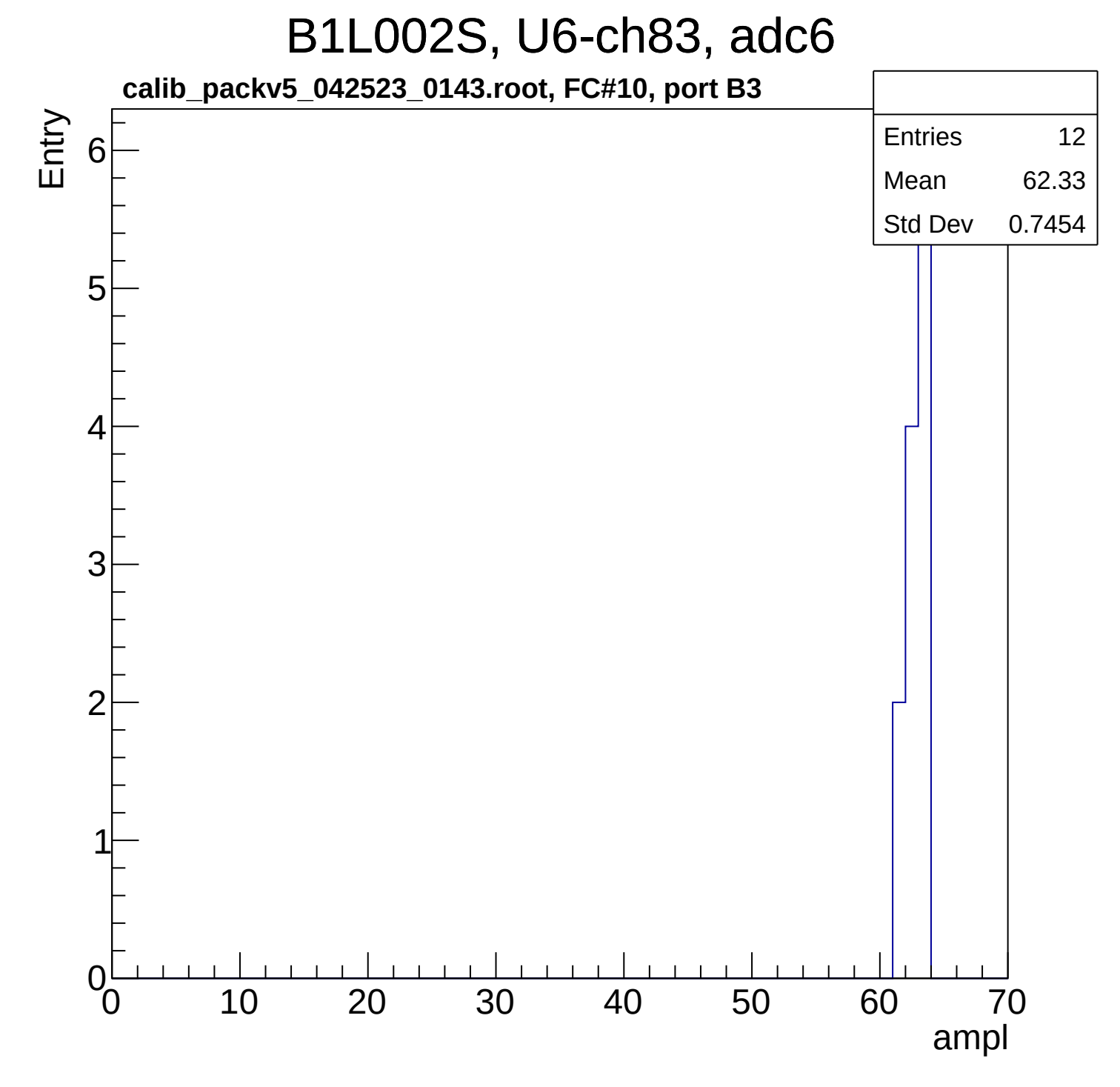

B1L002S, U6-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.01
Std Dev 3.186
Gaus mean : 35.9769
Gaus Width: 3.5413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

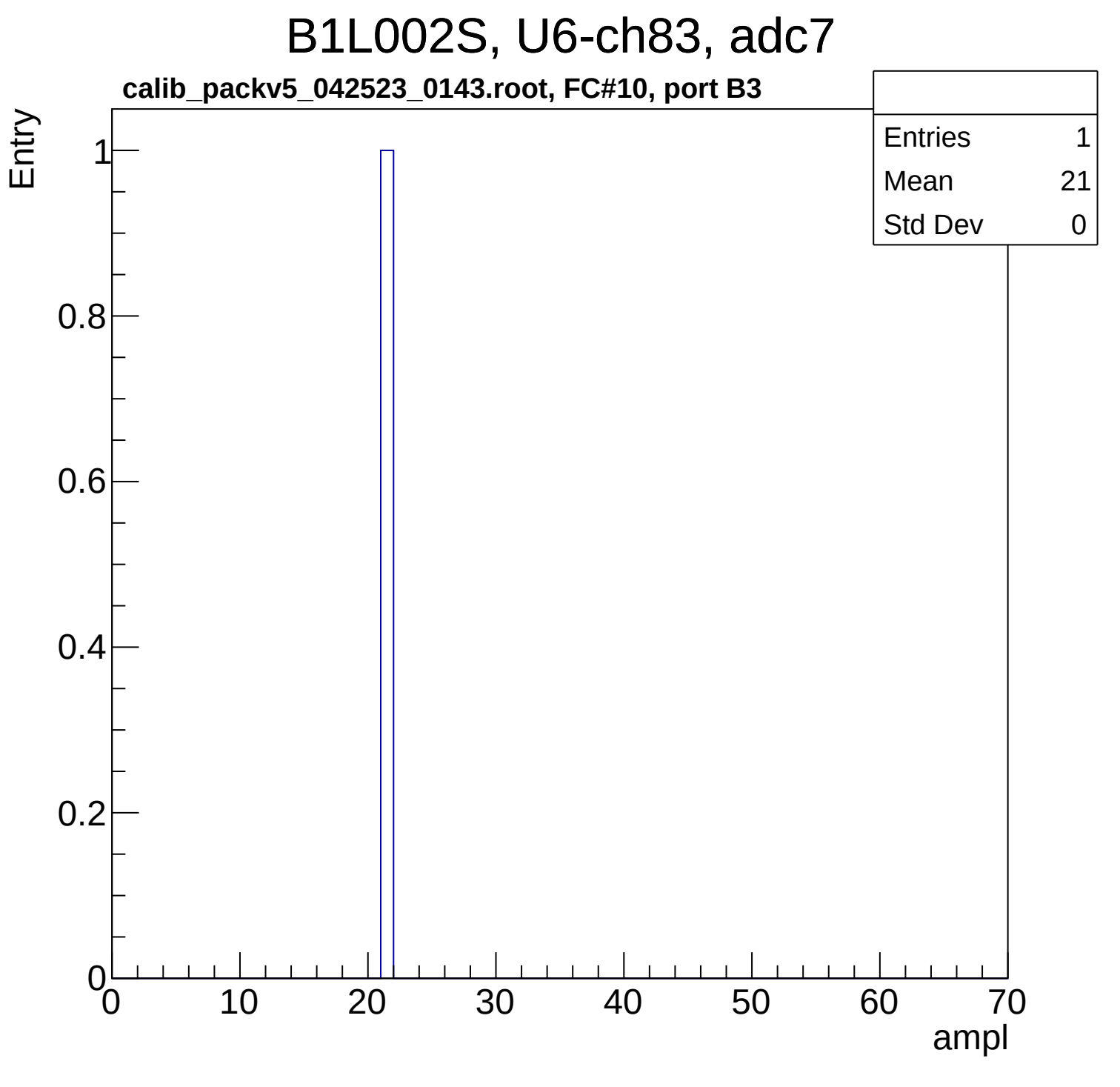

B1L002S, U6-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.2
Std Dev 3.05
Gaus mean : 43.5786
Gaus Width: 2.7449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

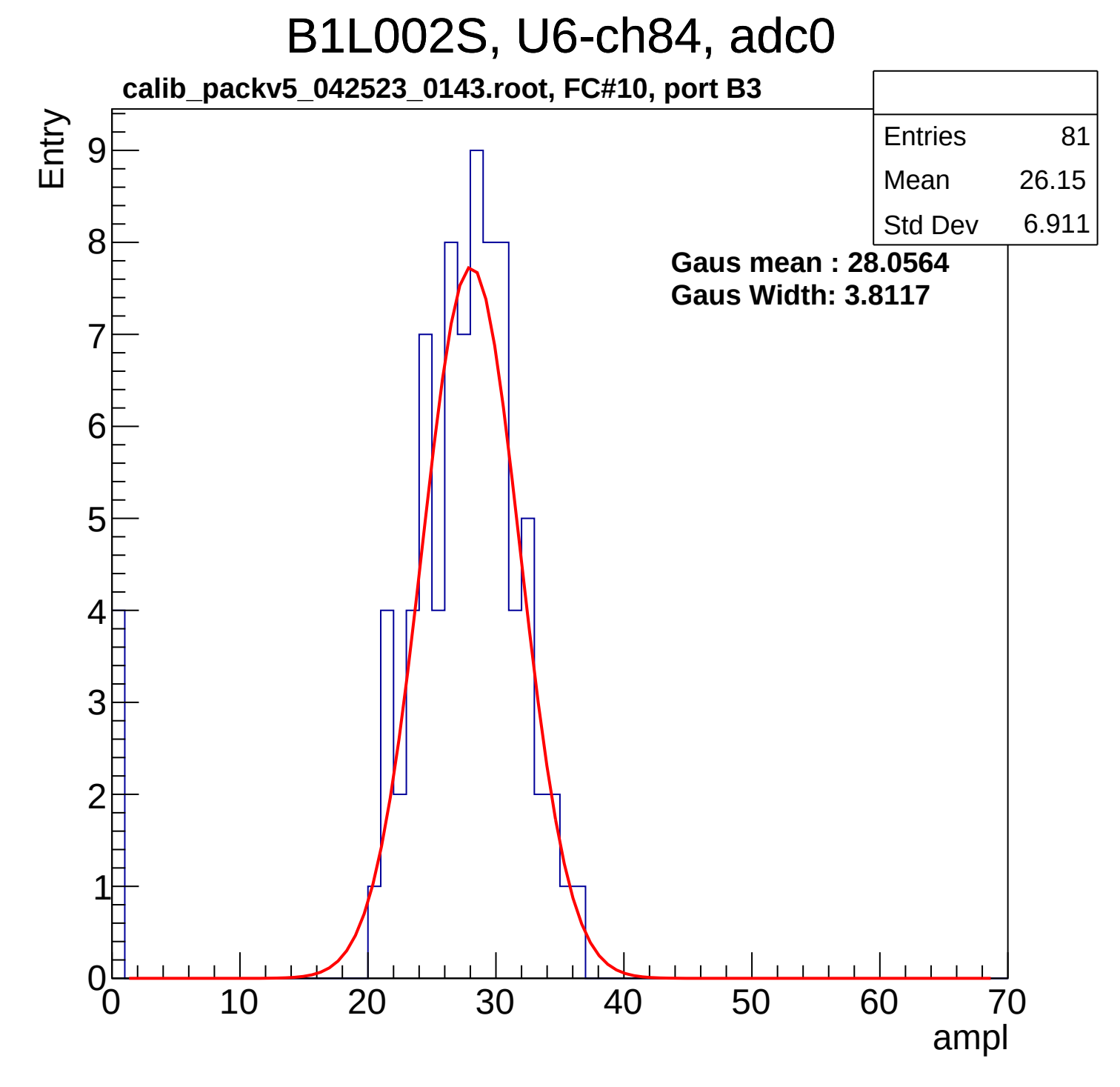

B1L002S, U6-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.23
Std Dev 2.905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

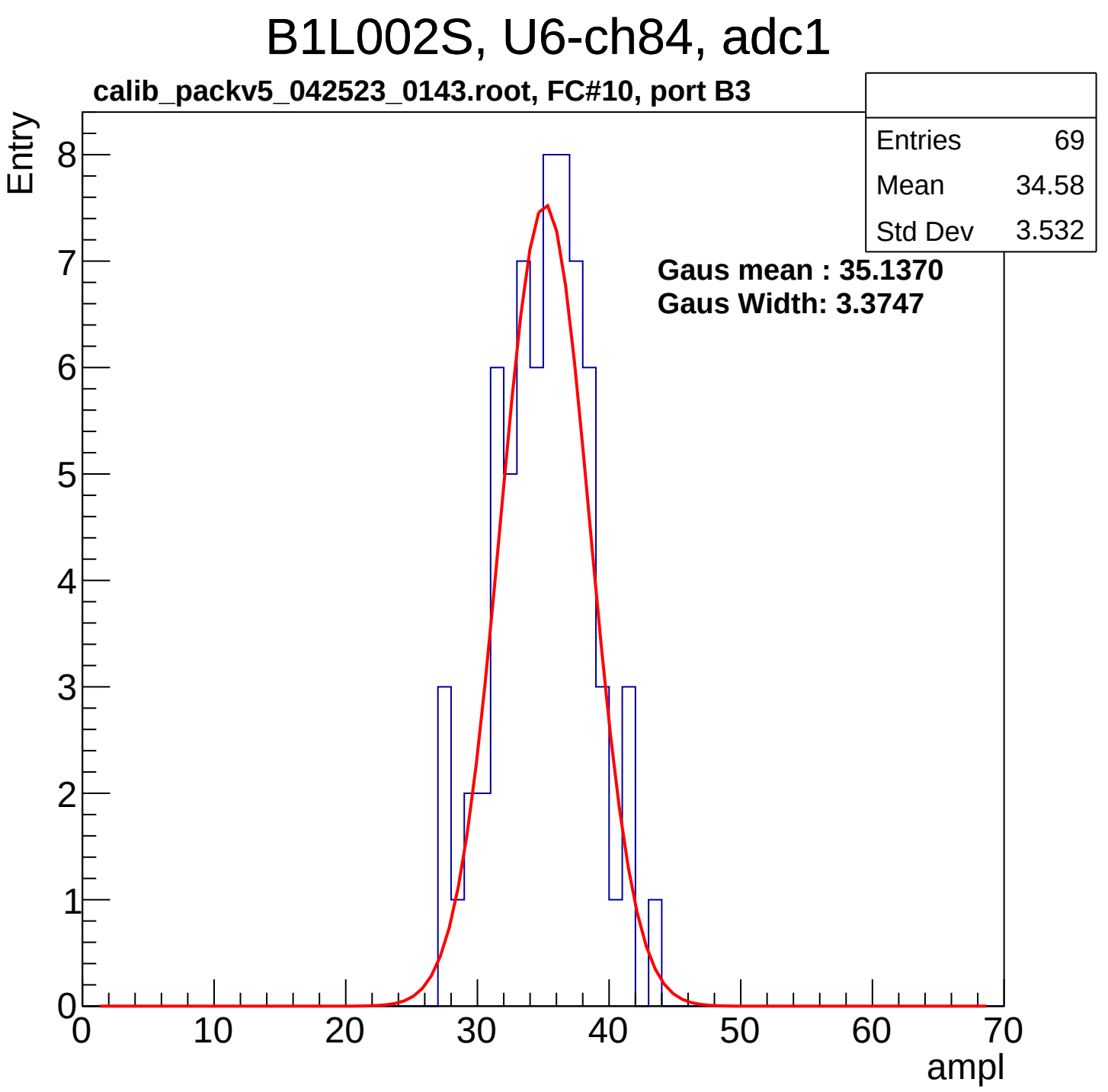

B1L002S, U6-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.4
Std Dev 2.911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

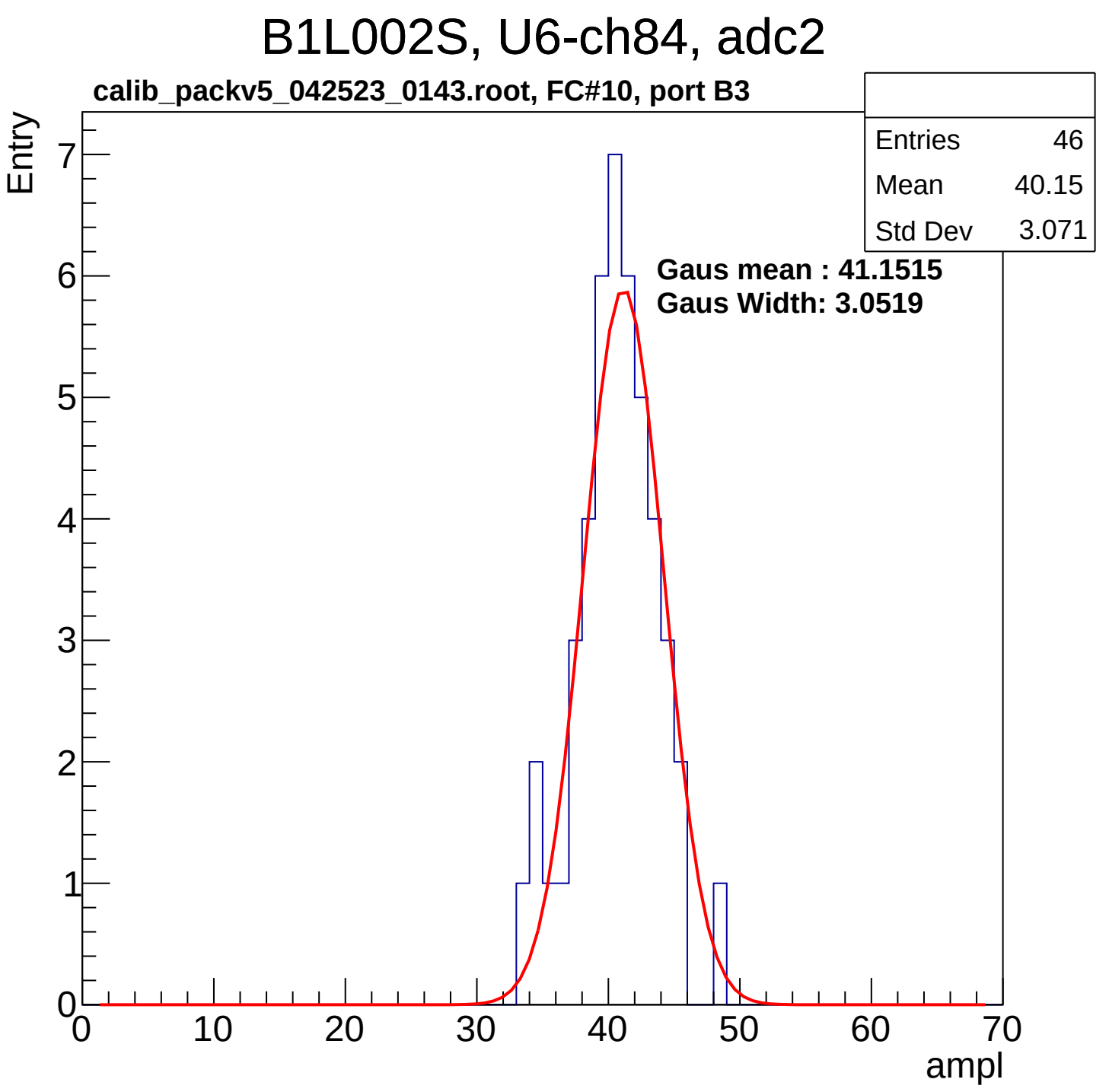

B1L002S, U6-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.18
Std Dev 8.501
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

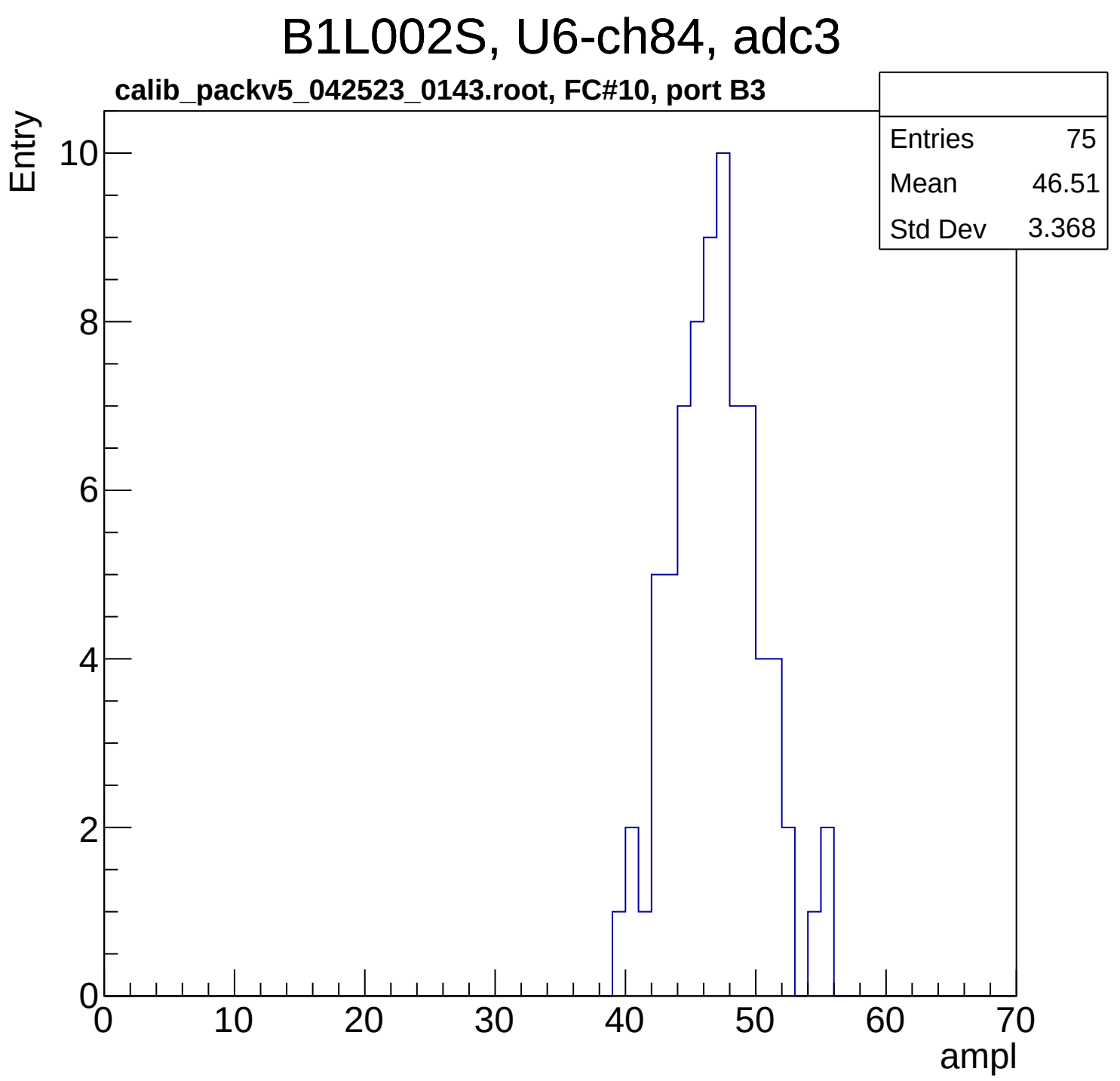

B1L002S, U6-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62.33
Std Dev 0.7454
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

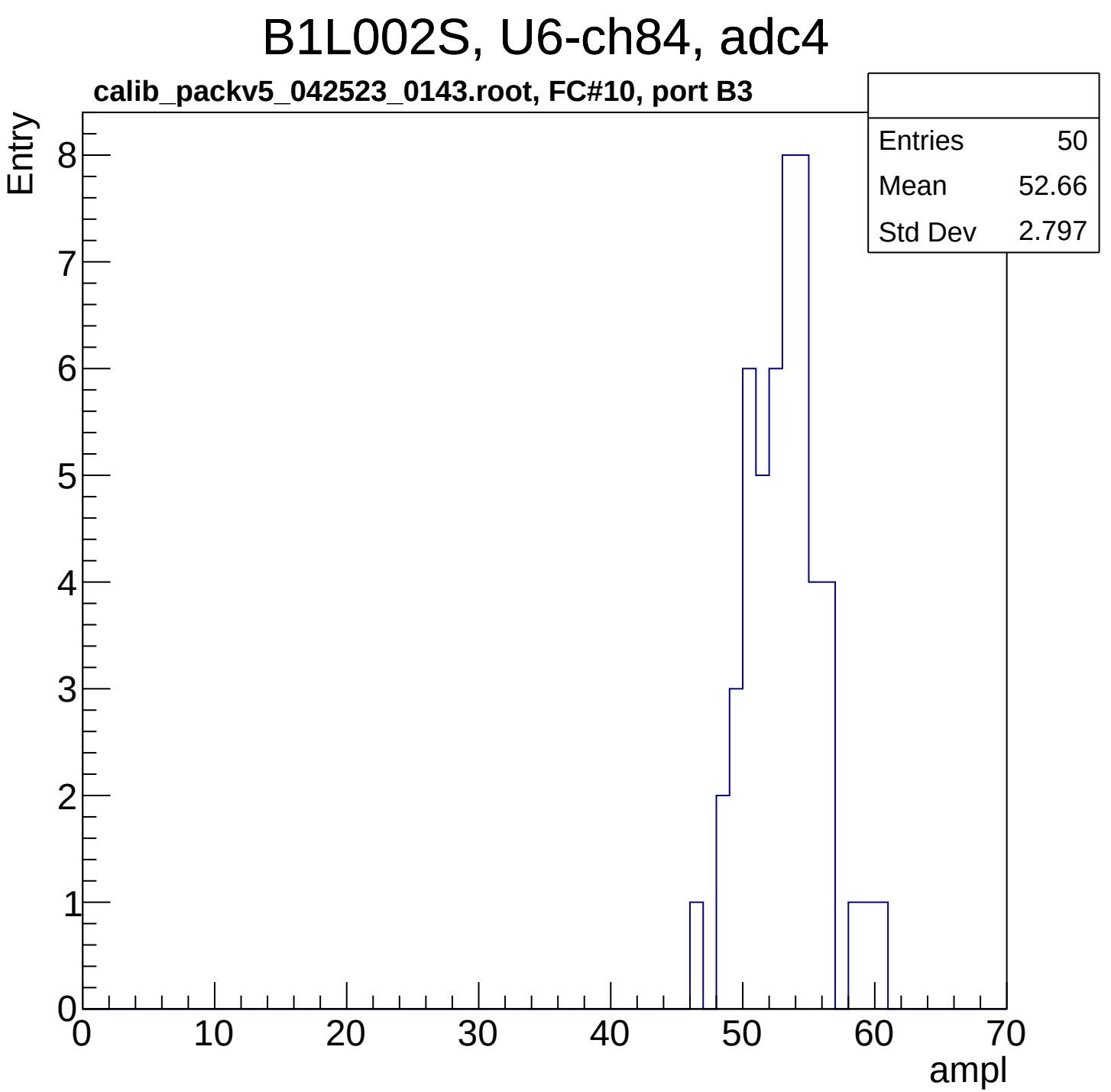

B1L002S, U6-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

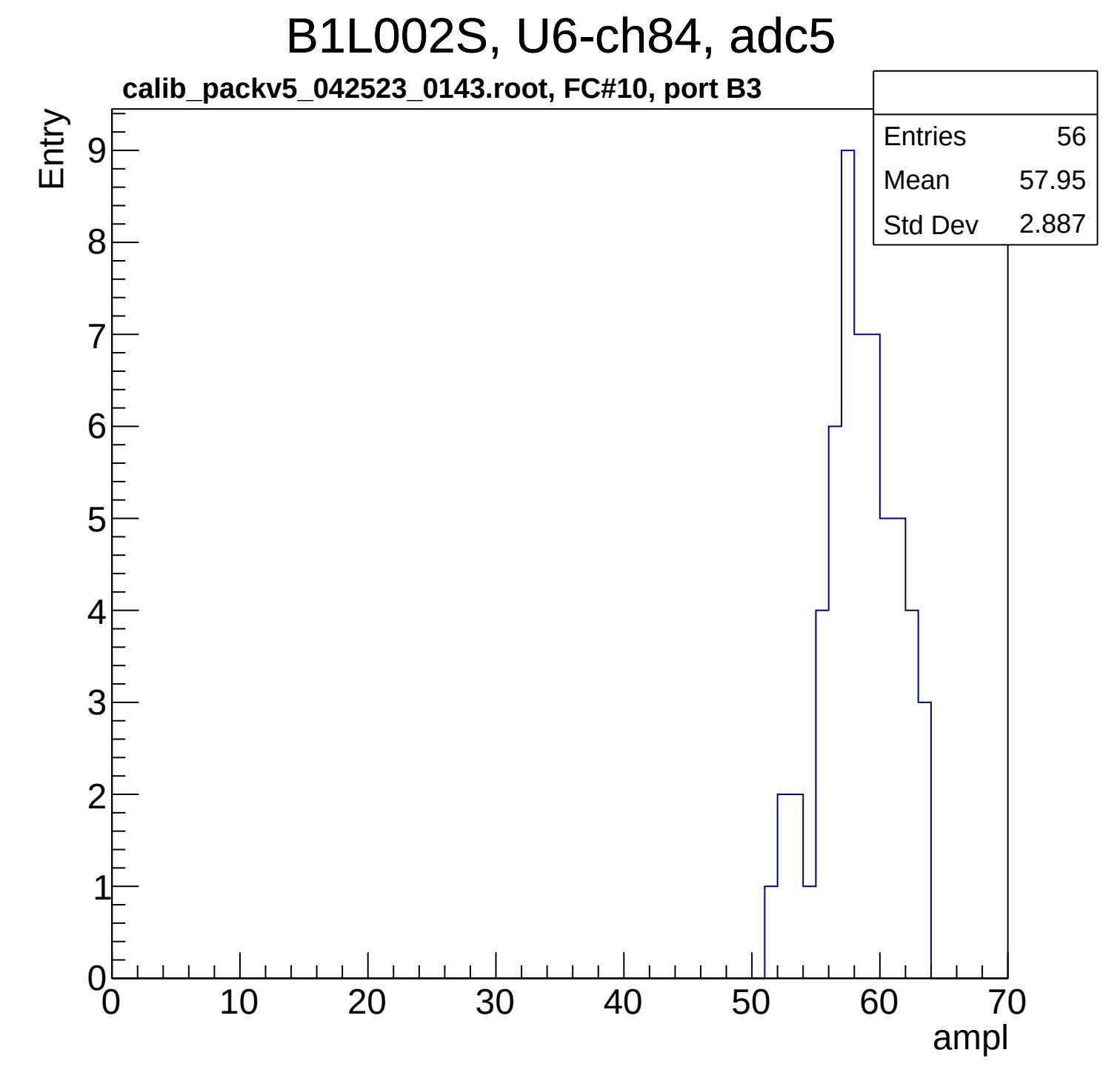

B1L002S, U6-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 26.15
Std Dev 6.911
Gaus mean : 28.0564
Gaus Width: 3.8117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

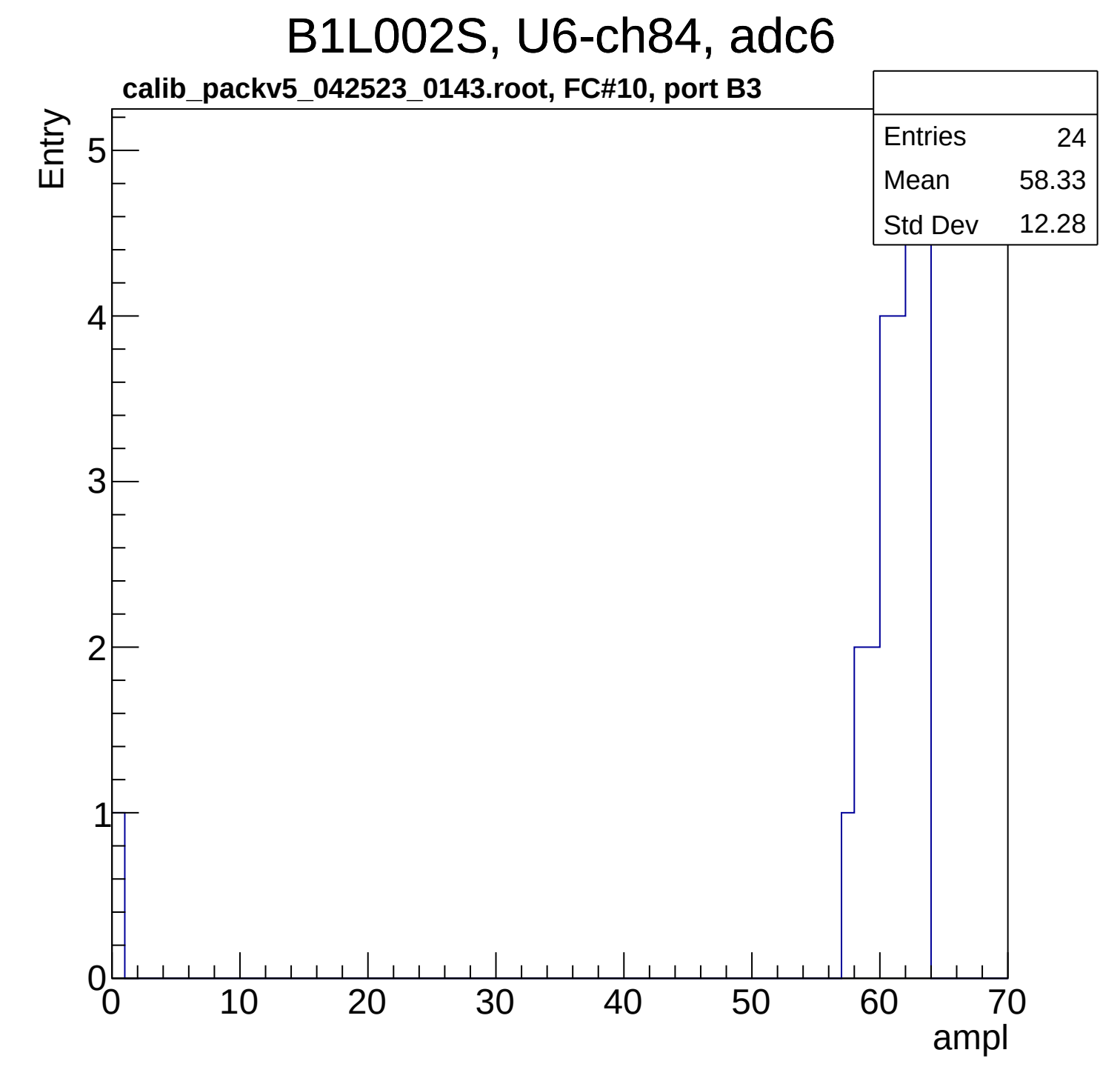

B1L002S, U6-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.58
Std Dev 3.532
Gaus mean : 35.1370
Gaus Width: 3.3747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

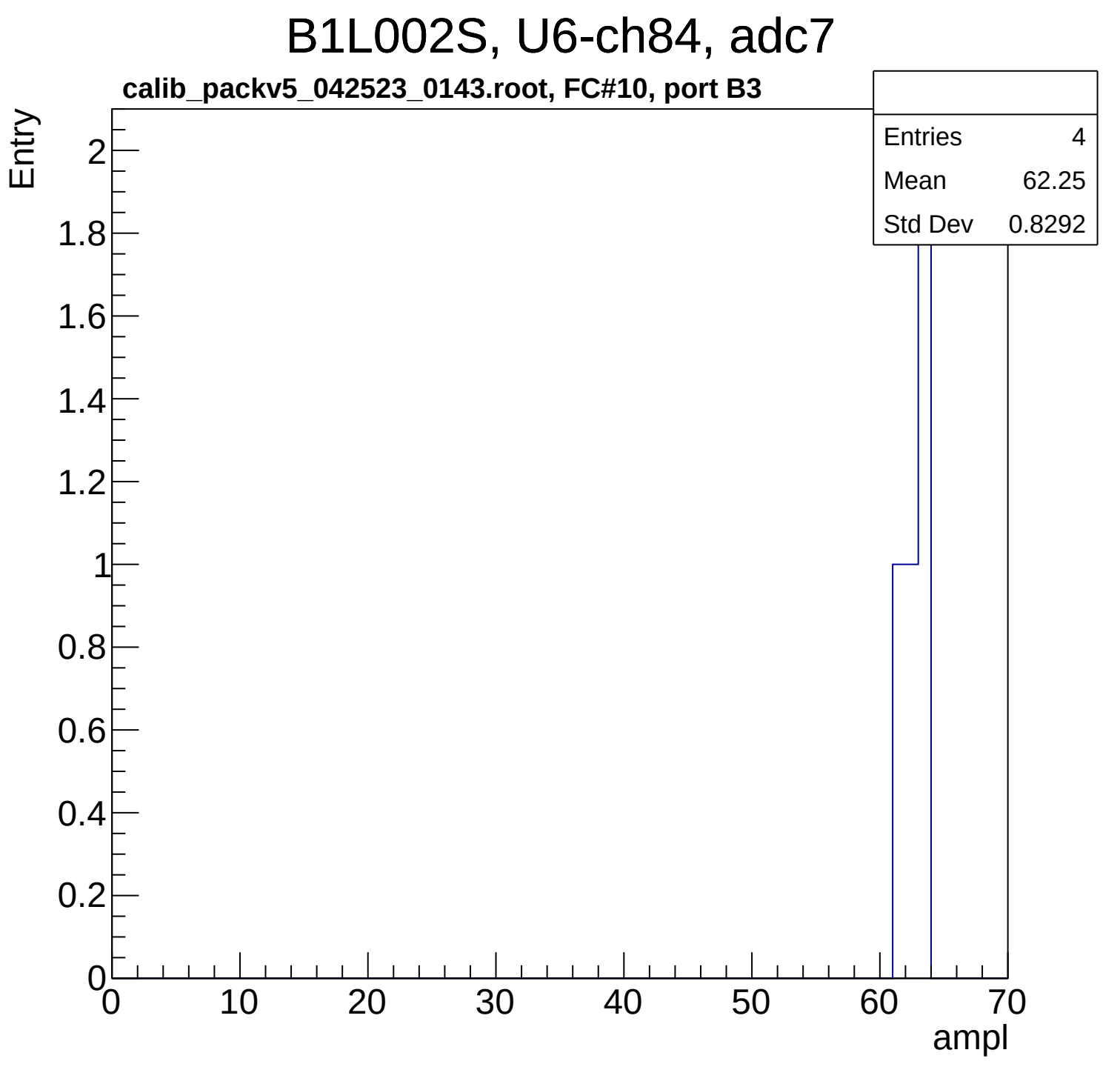

B1L002S, U6-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 40.15
Std Dev 3.071
Gaus mean : 41.1515
Gaus Width: 3.0519
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

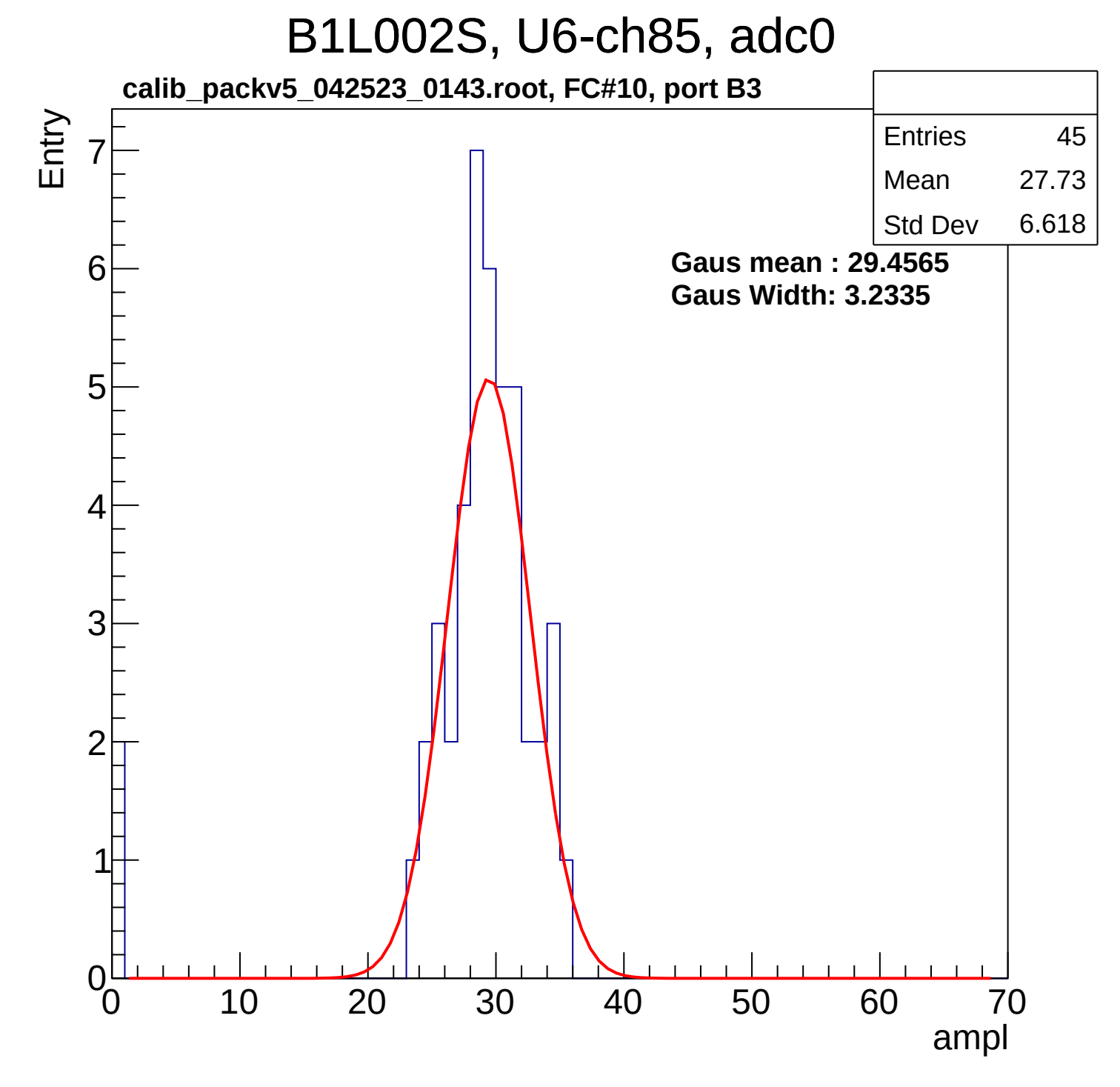

B1L002S, U6-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 46.51
Std Dev 3.368
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

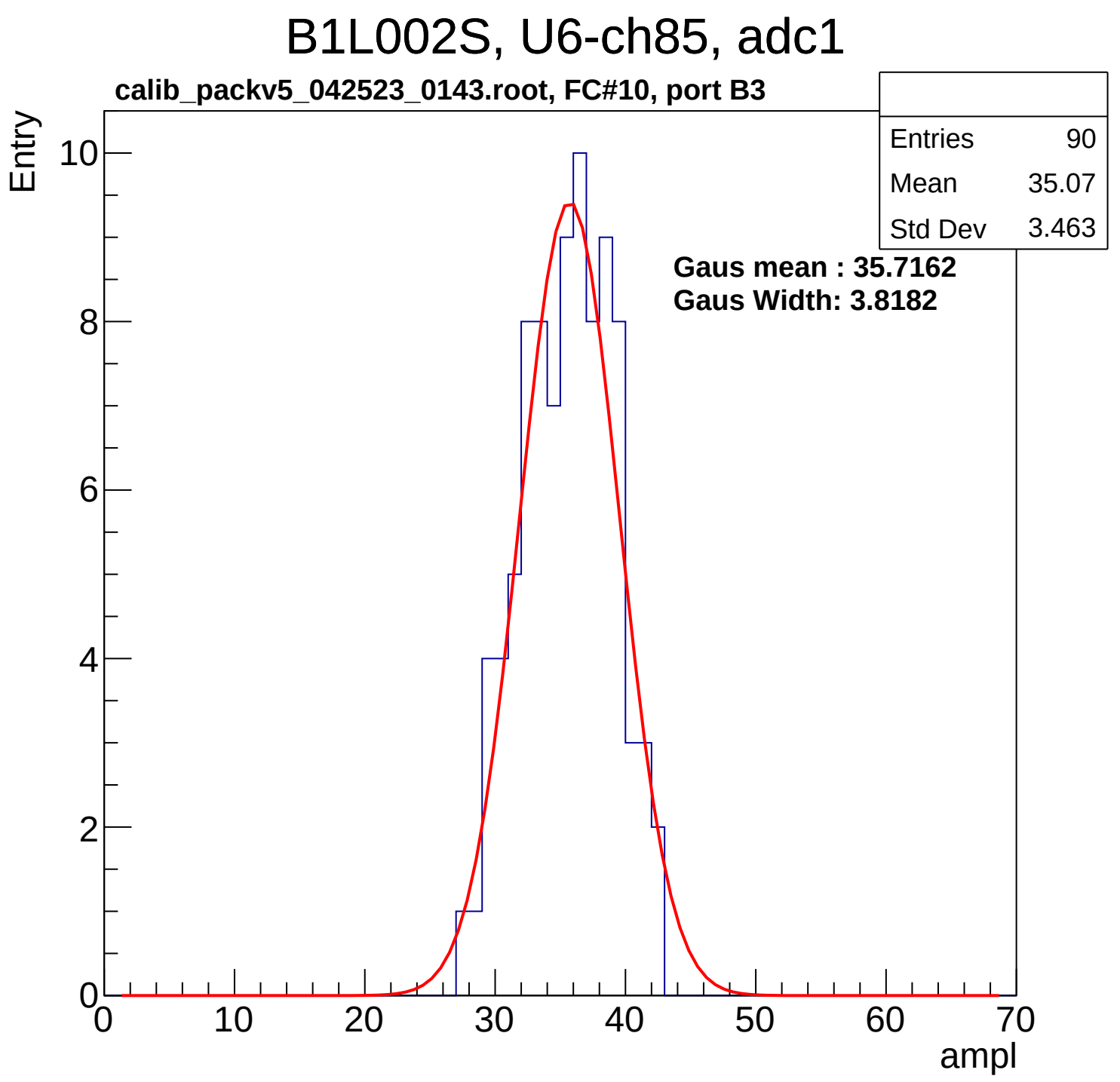

B1L002S, U6-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.66
Std Dev 2.797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

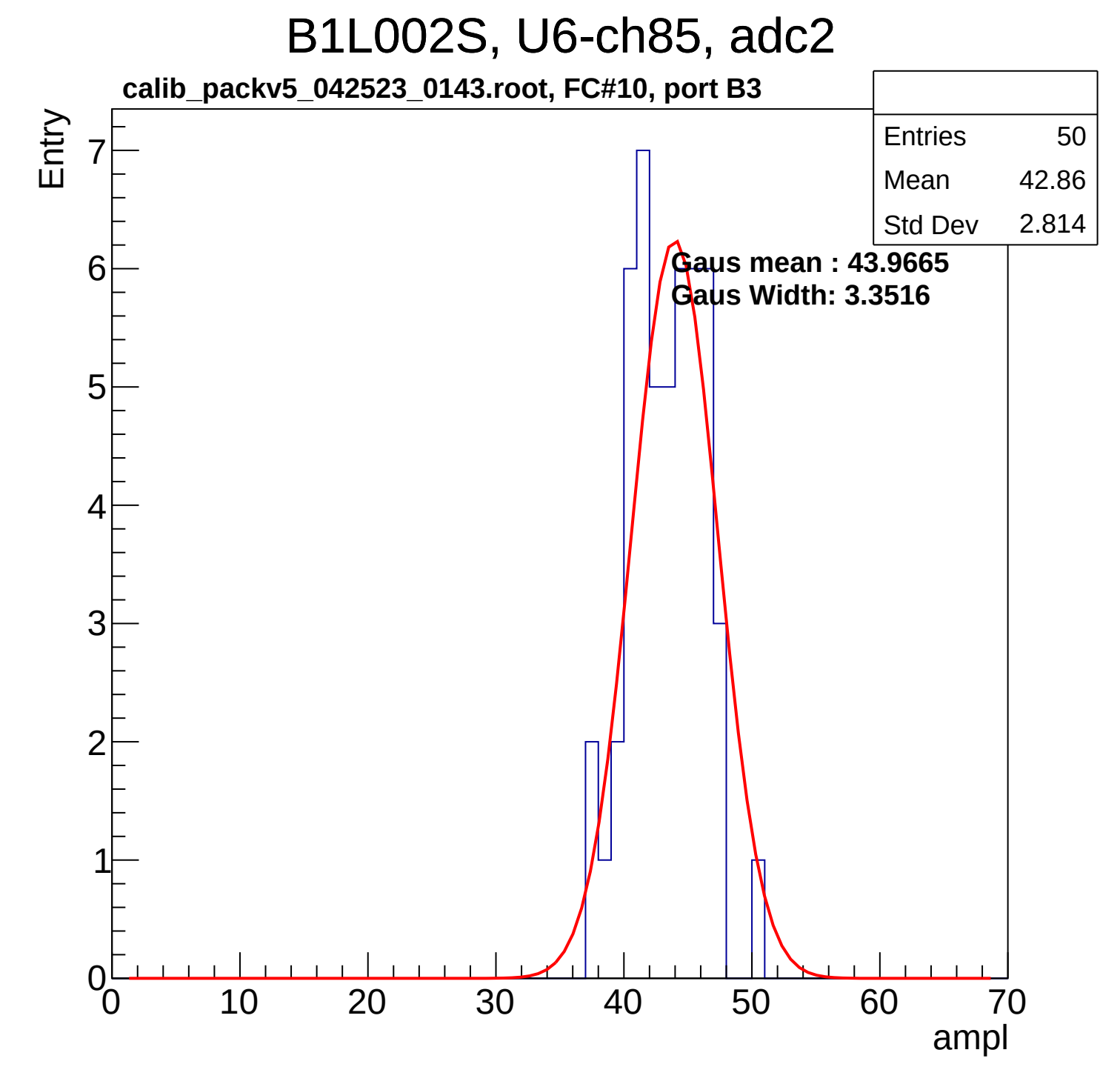

B1L002S, U6-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.95
Std Dev 2.887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

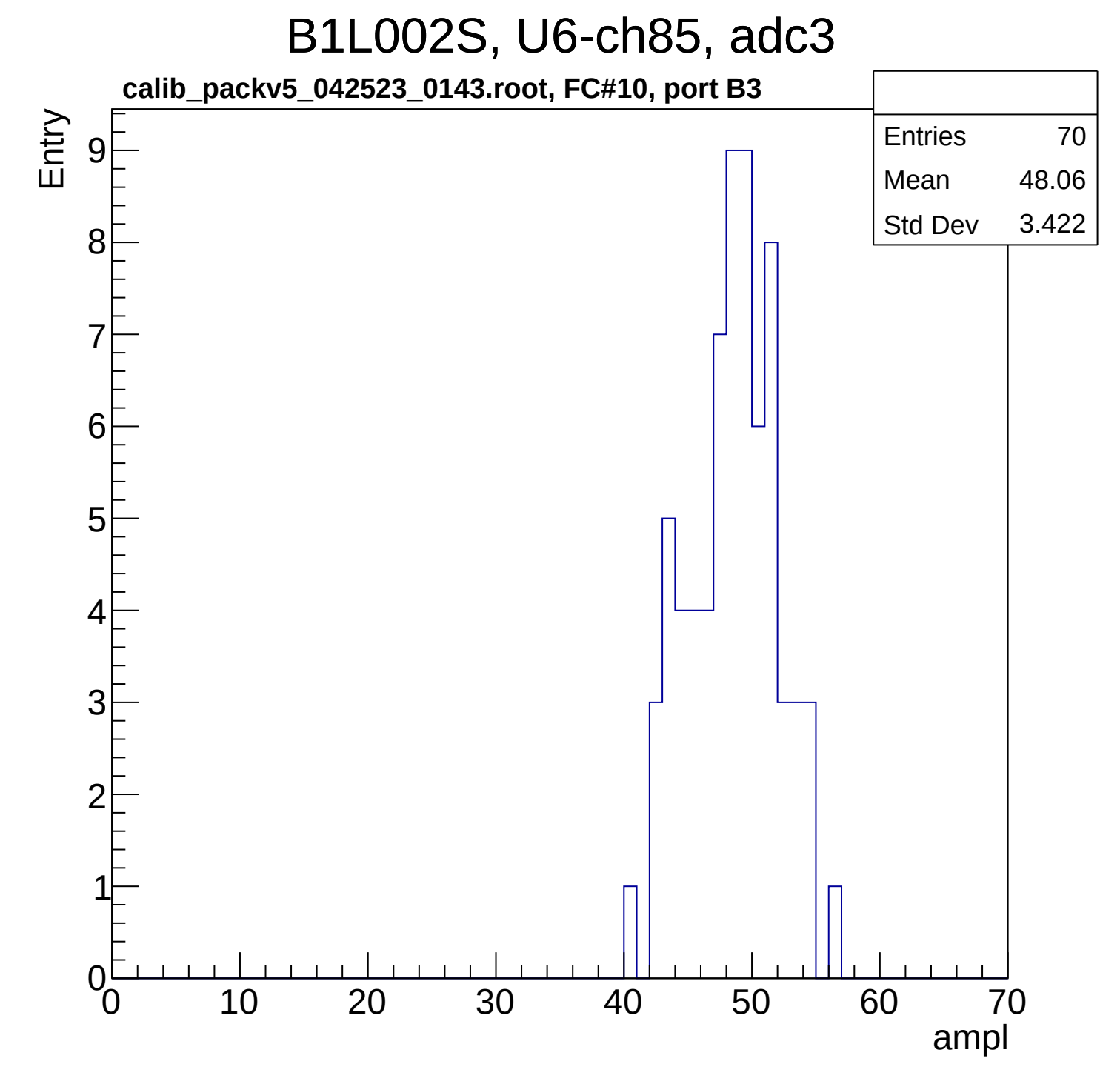

B1L002S, U6-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.33
Std Dev 12.28
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

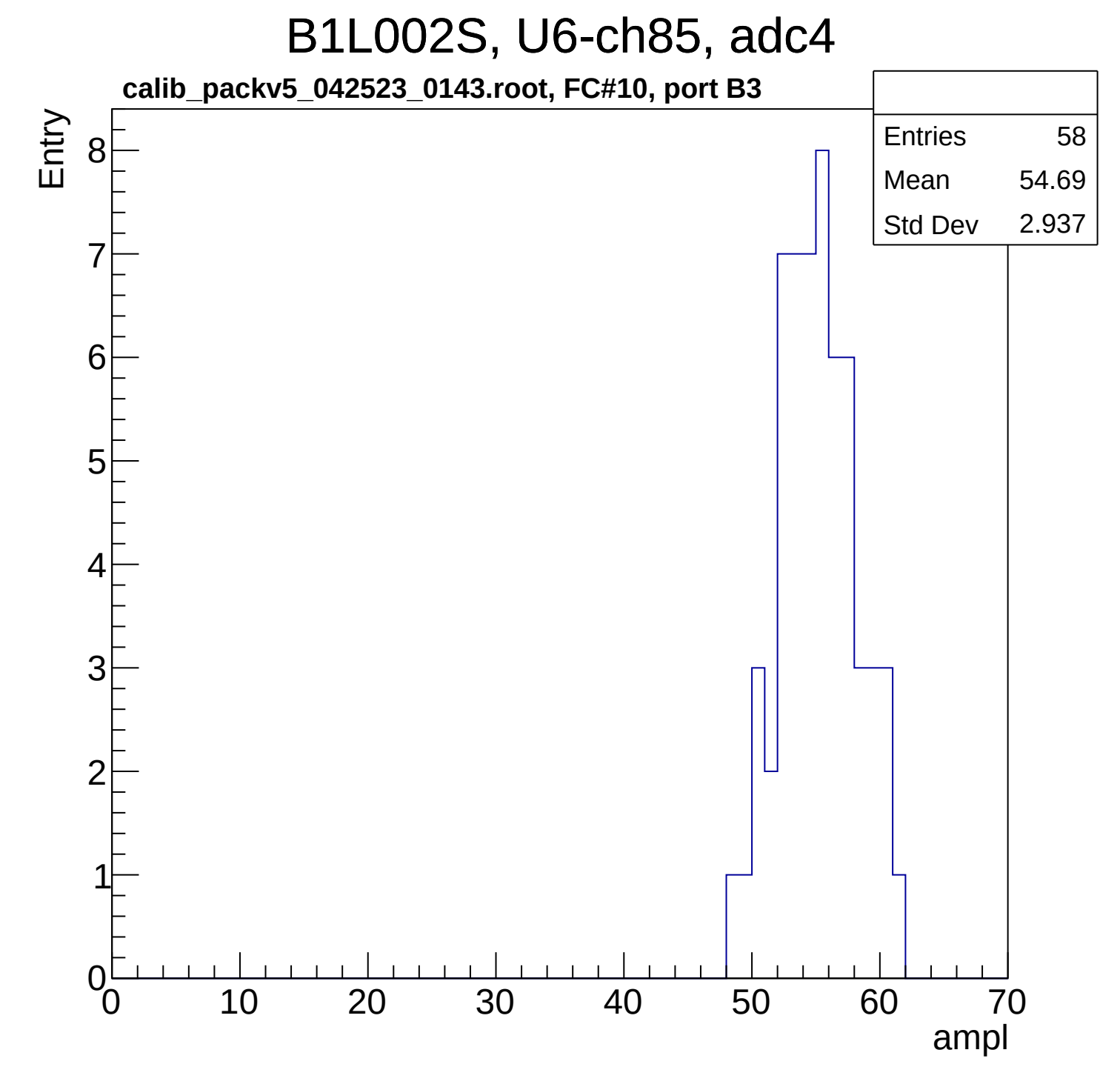

B1L002S, U6-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

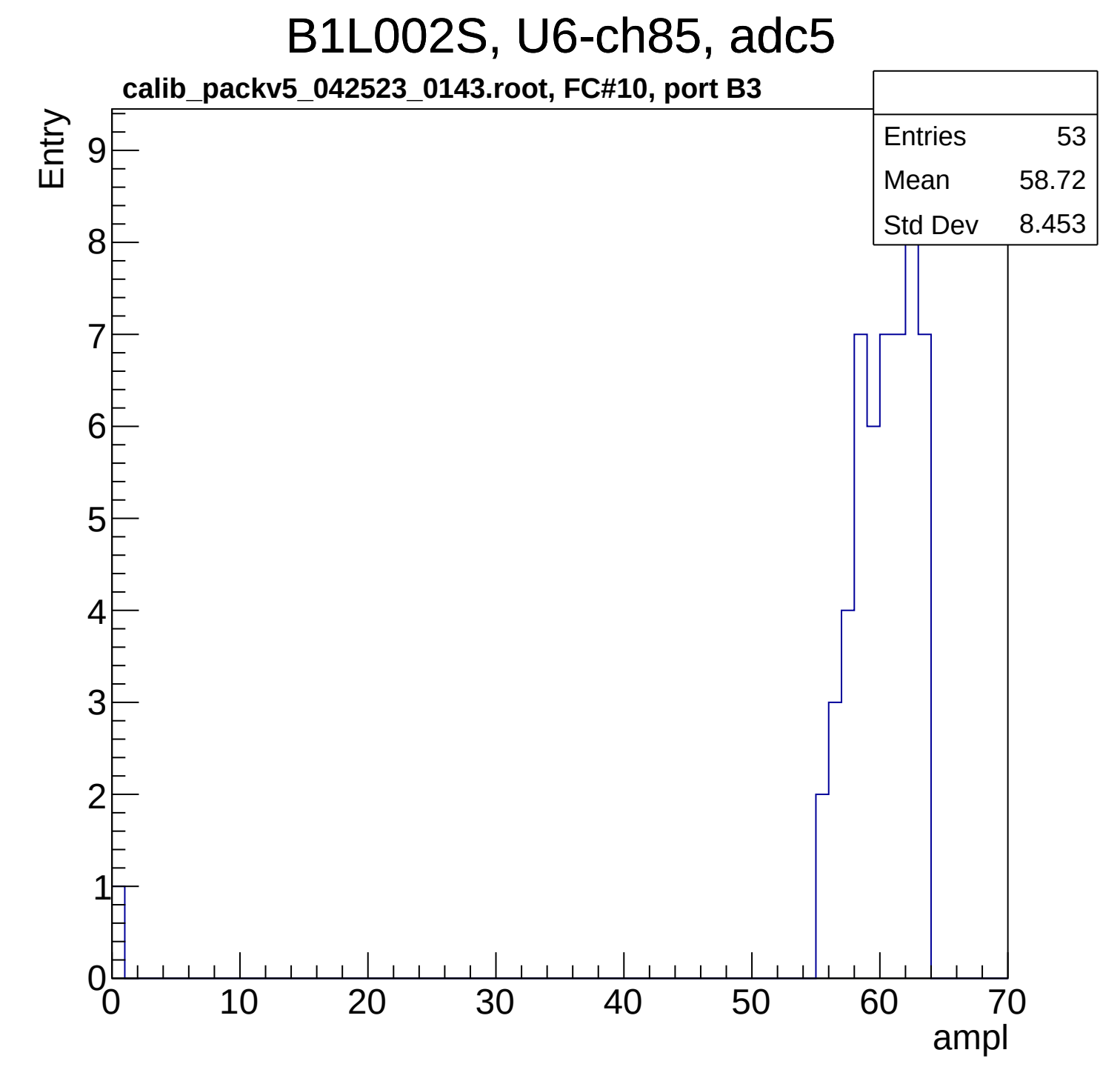

B1L002S, U6-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 27.73
Std Dev 6.618
Gaus mean : 29.4565
Gaus Width: 3.2335
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

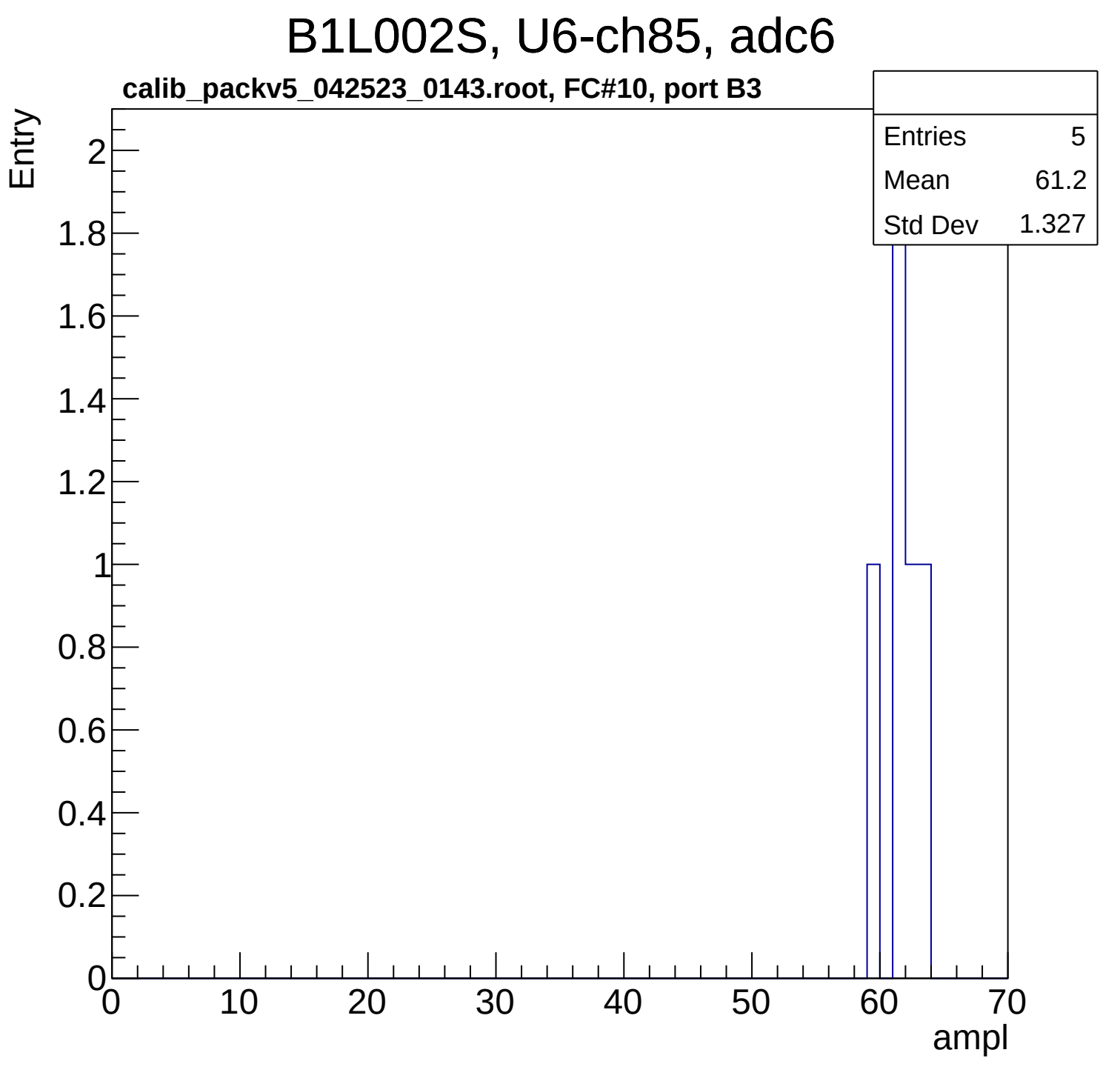

B1L002S, U6-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 35.07
Std Dev 3.463
Gaus mean : 35.7162
Gaus Width: 3.8182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

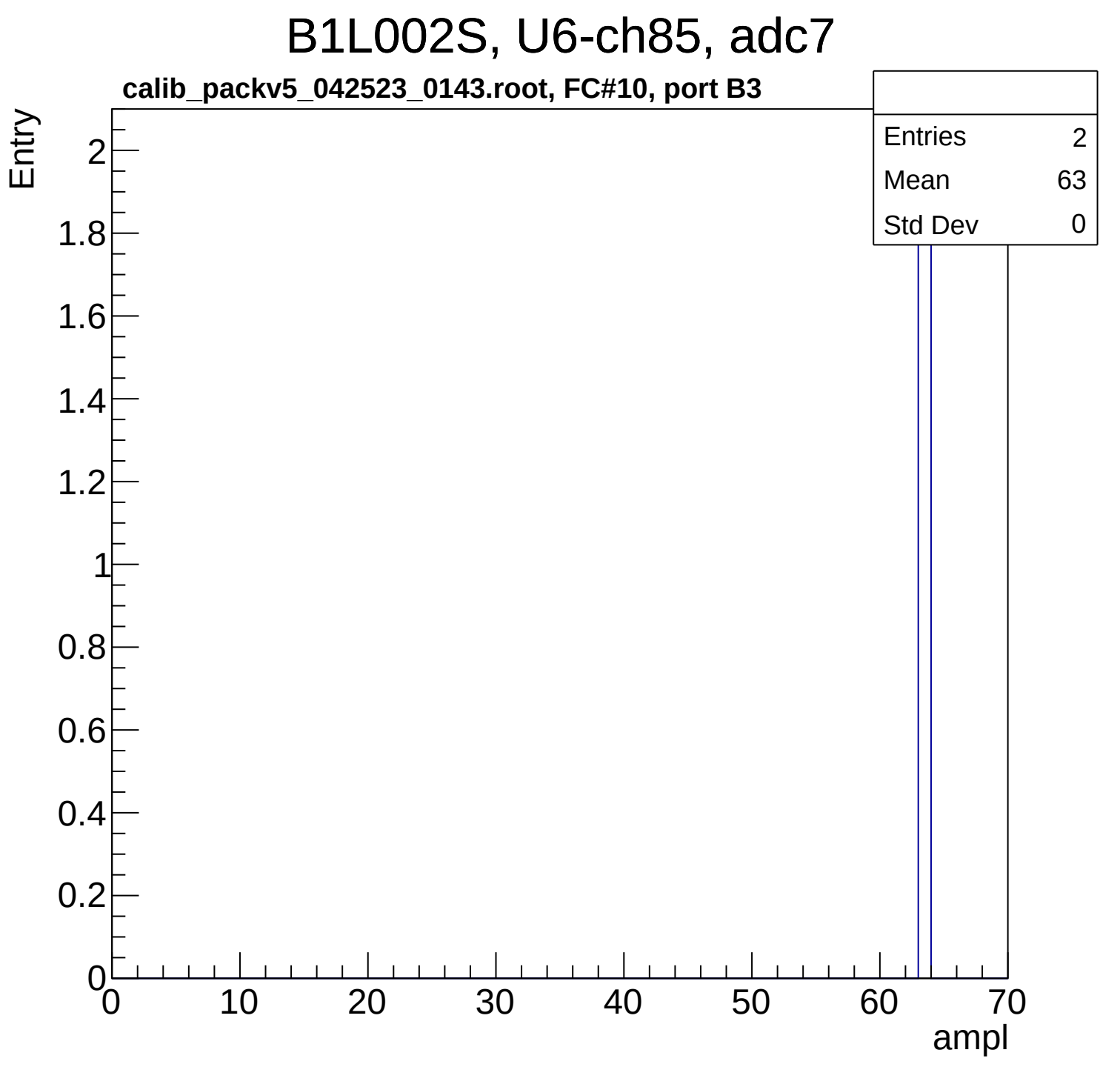

B1L002S, U6-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.86
Std Dev 2.814
Gaus mean : 43.9665
Gaus Width: 3.3516
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

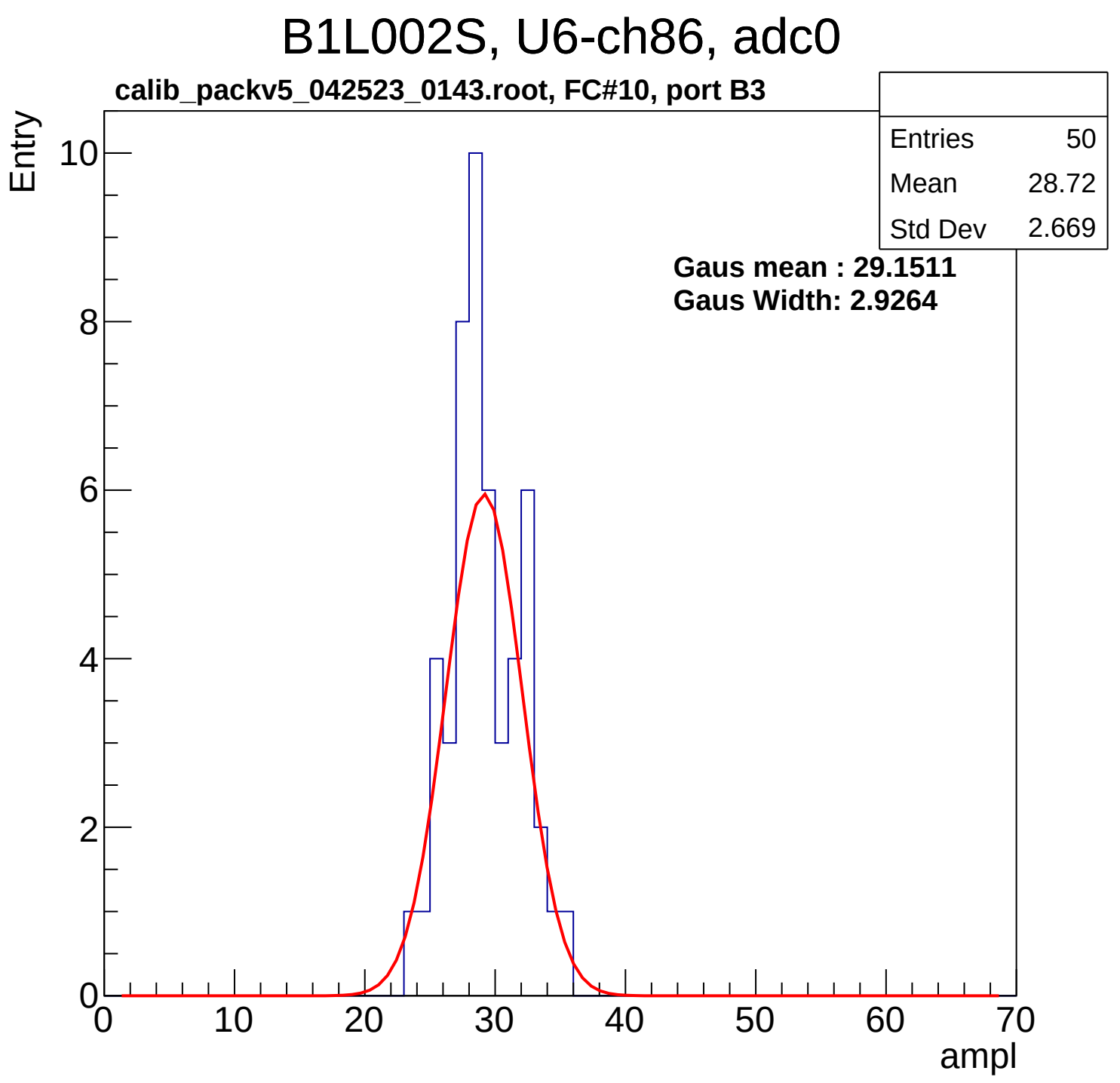

B1L002S, U6-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.06
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

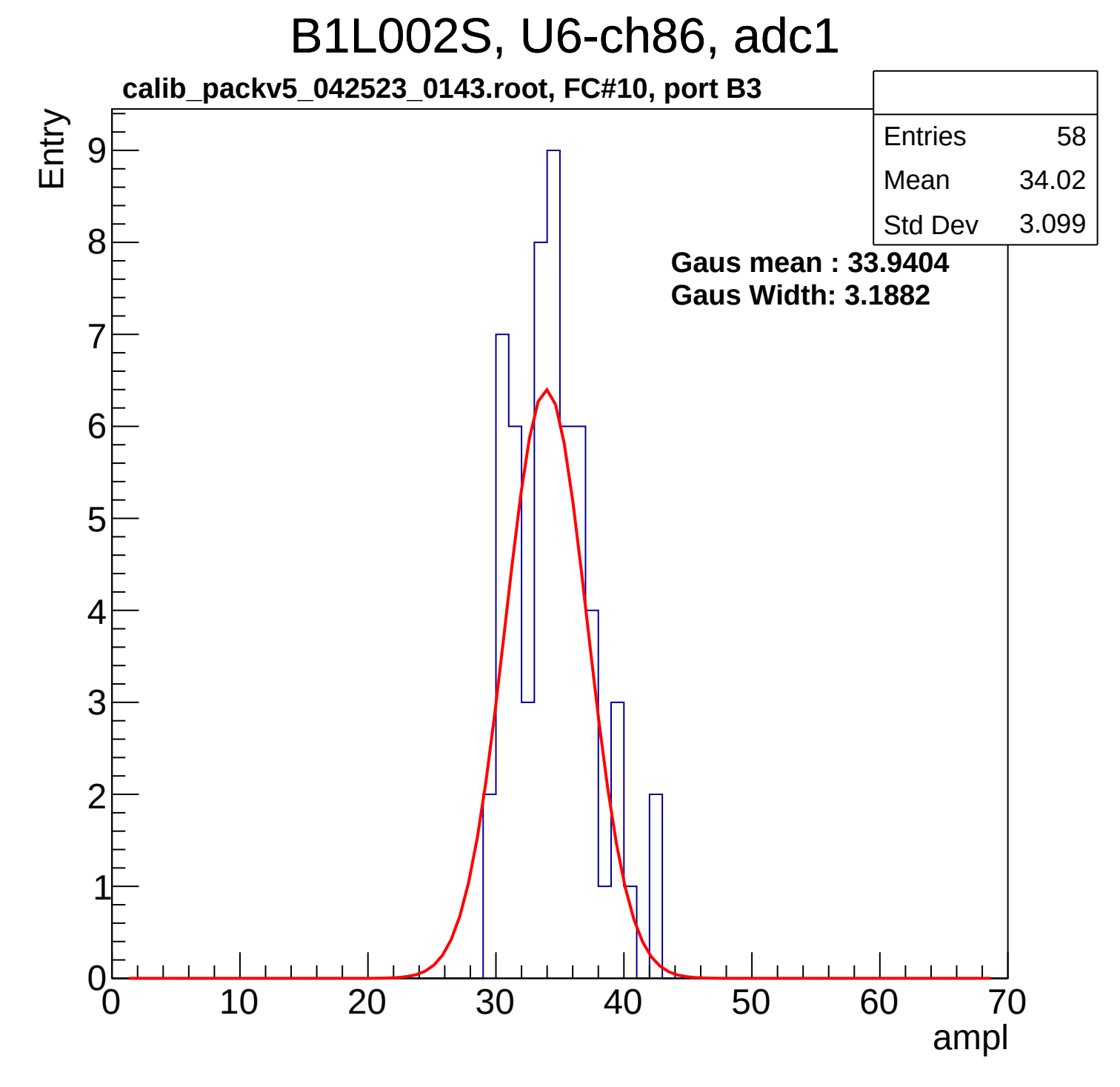

B1L002S, U6-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.69
Std Dev 2.937
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

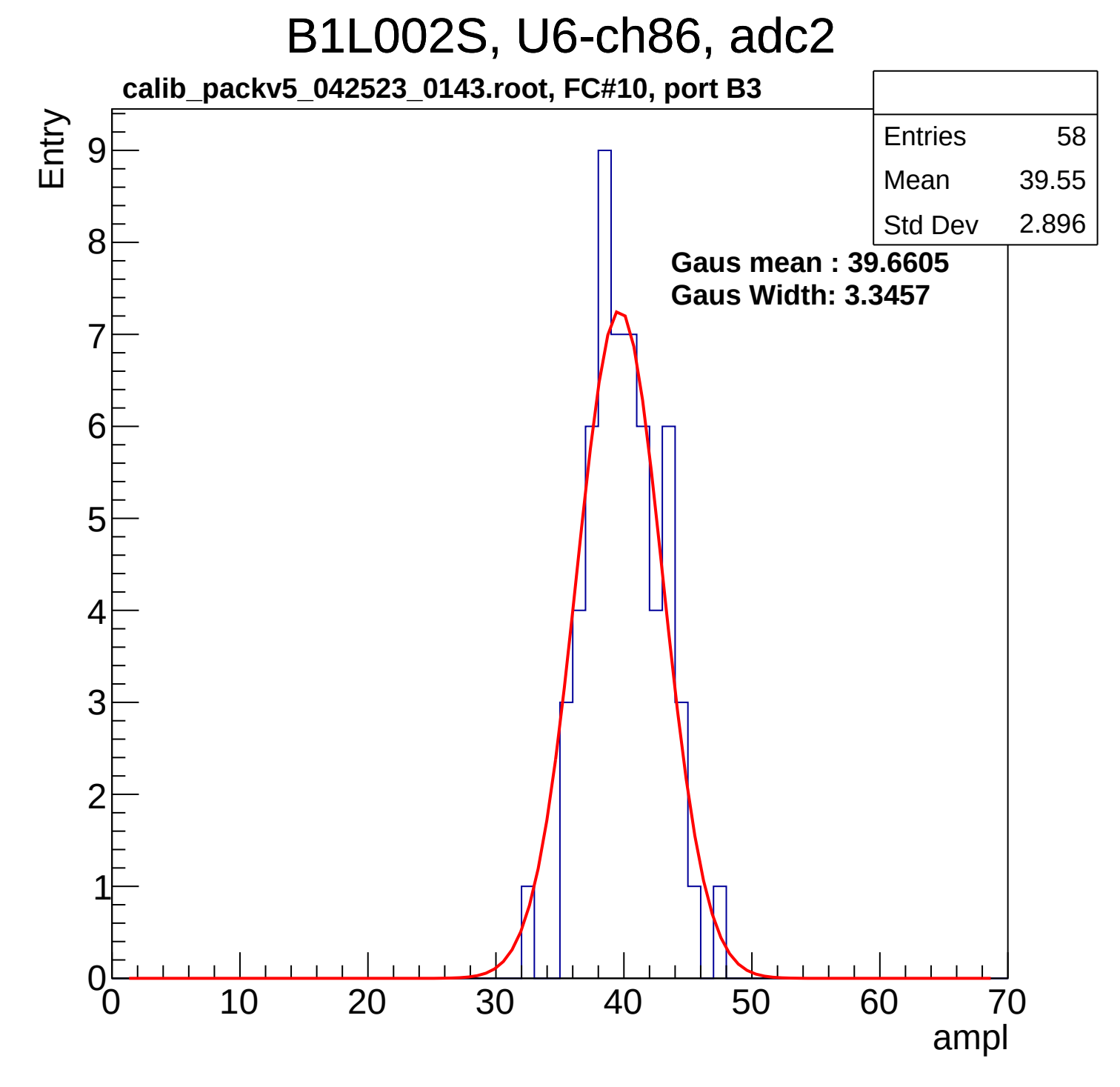

B1L002S, U6-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.72
Std Dev 8.453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

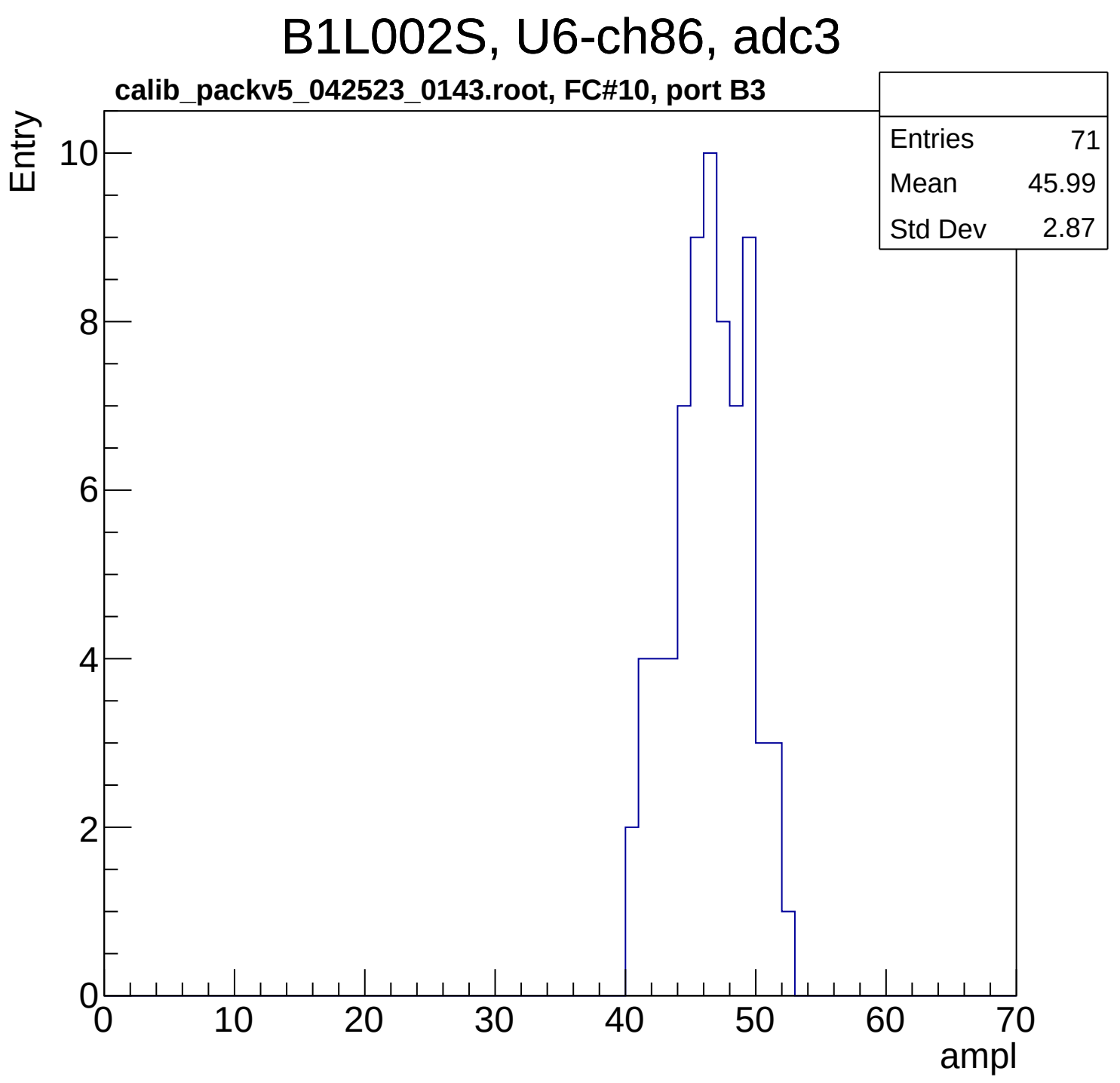

B1L002S, U6-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

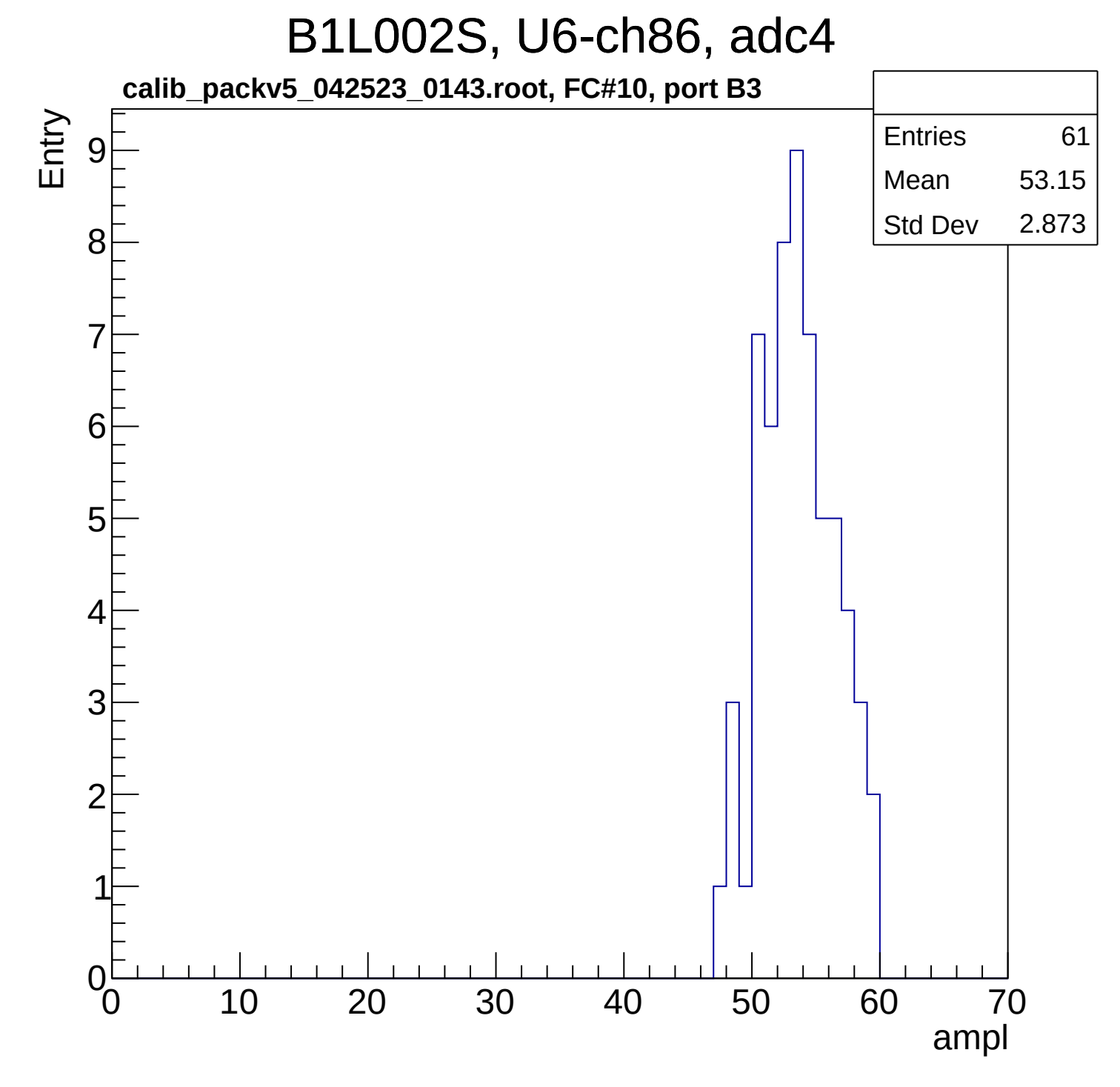

B1L002S, U6-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

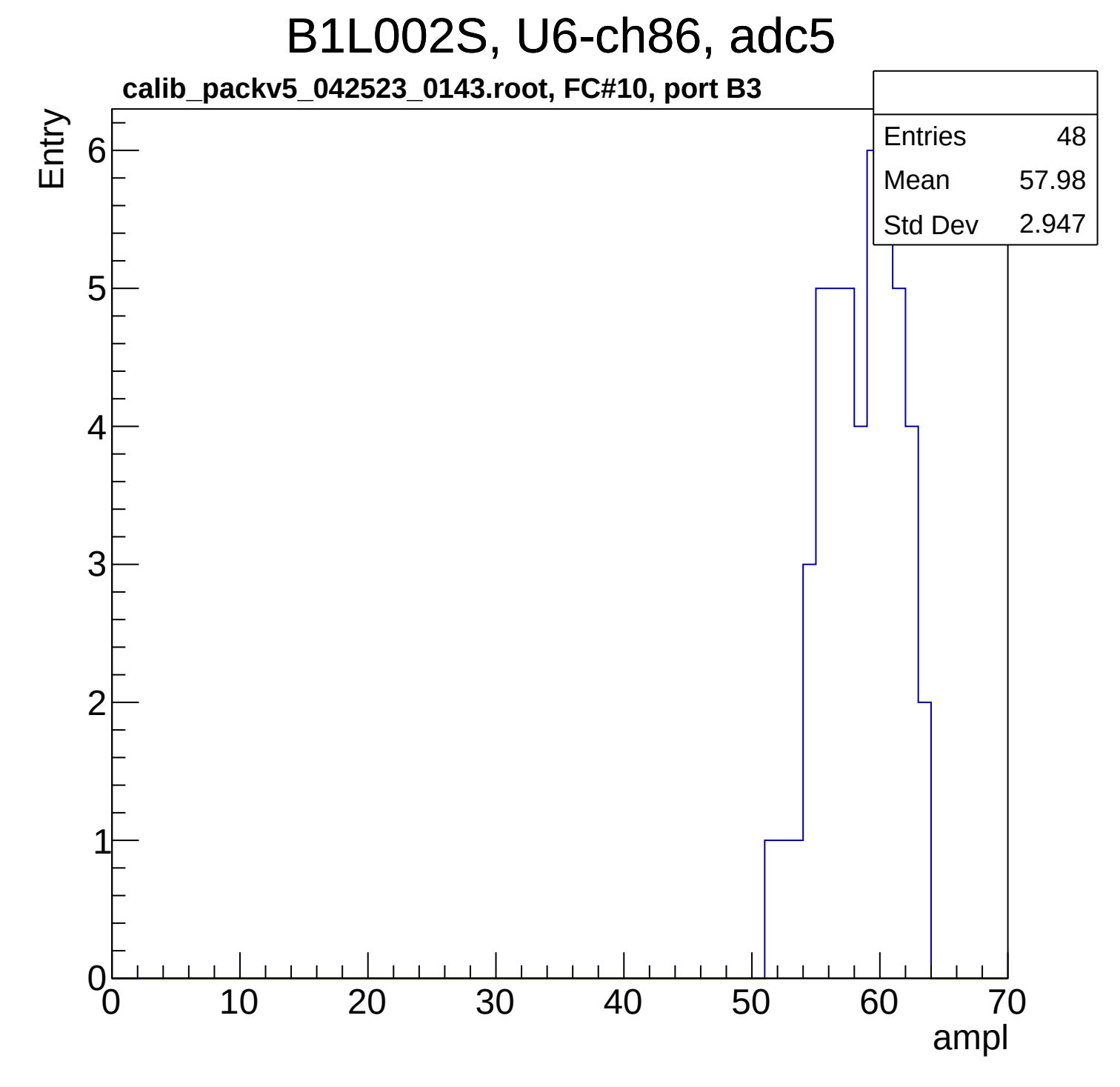

B1L002S, U6-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 28.72
Std Dev 2.669
Gaus mean : 29.1511
Gaus Width: 2.9264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

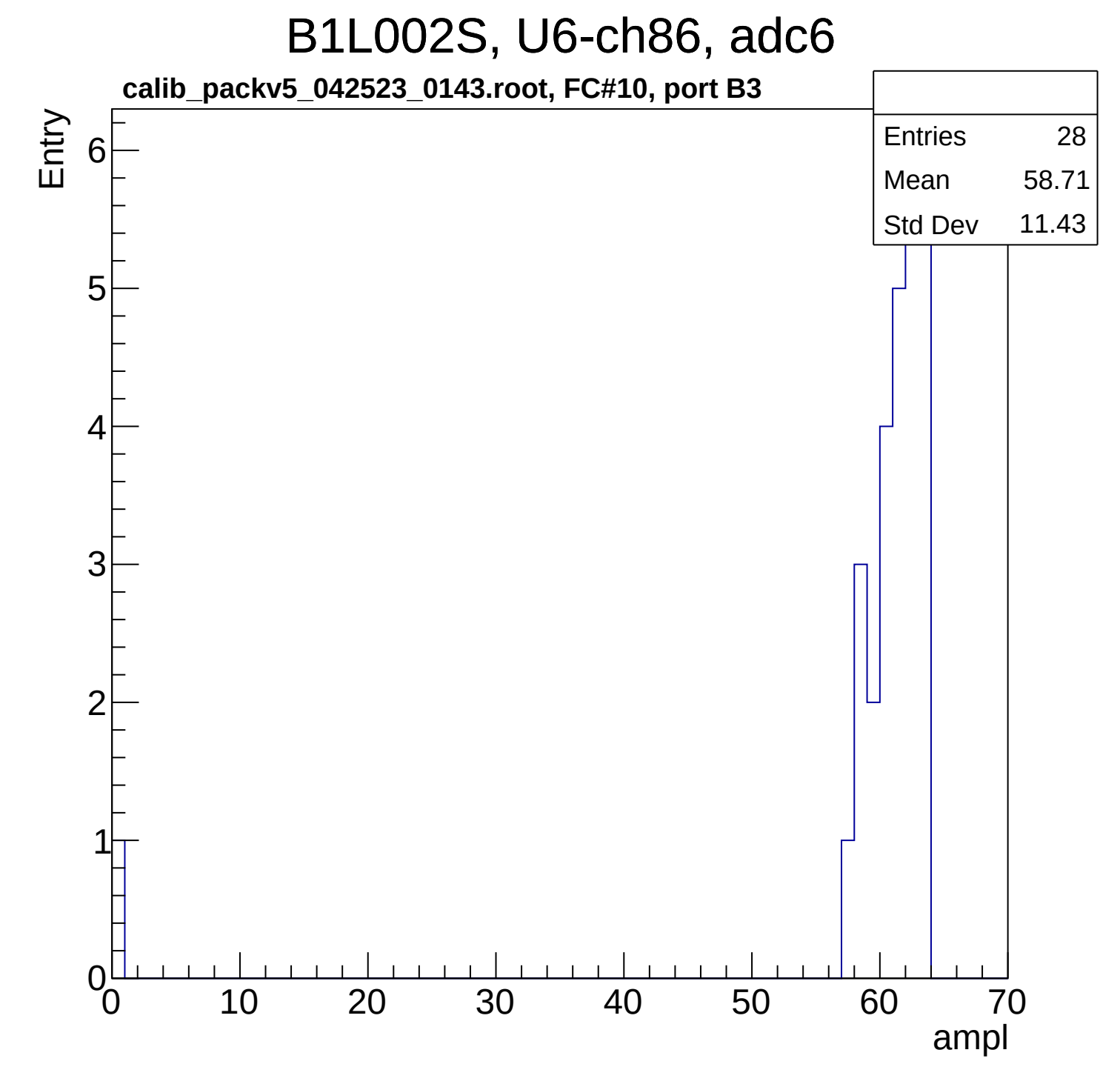

B1L002S, U6-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.02
Std Dev 3.099
Gaus mean : 33.9404
Gaus Width: 3.1882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

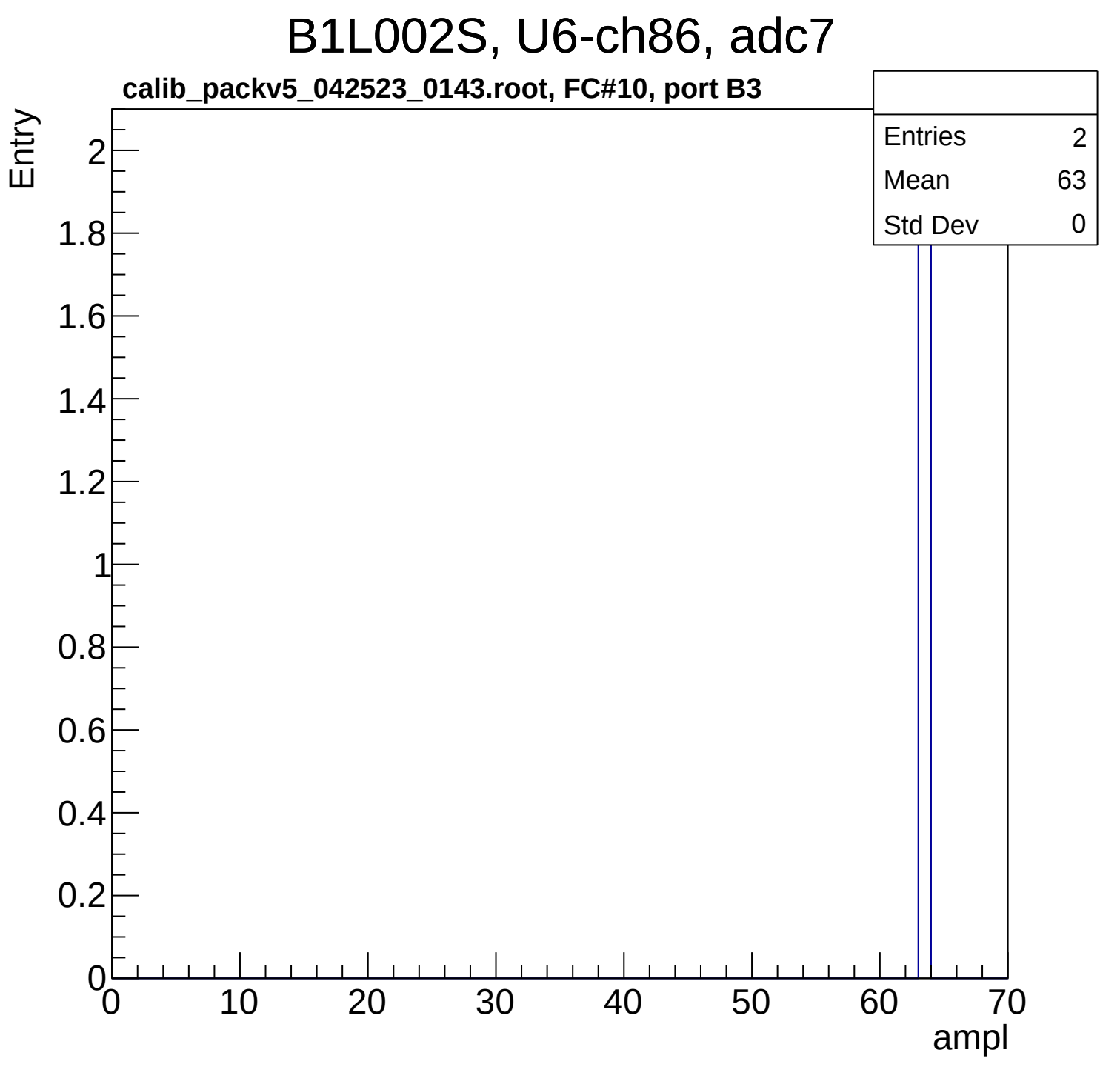

B1L002S, U6-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 39.55
Std Dev 2.896
Gaus mean : 39.6605
Gaus Width: 3.3457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

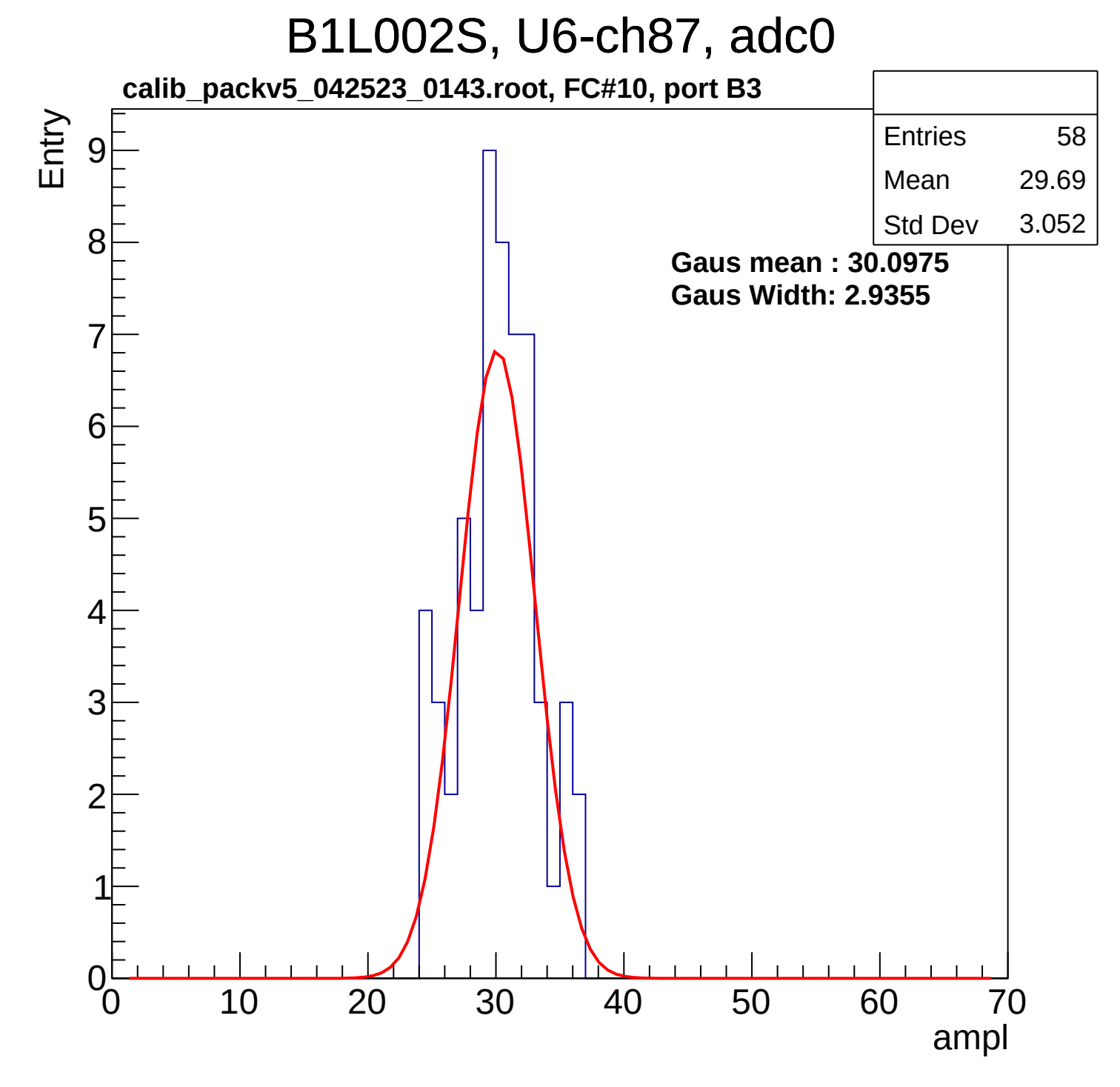

B1L002S, U6-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 45.99
Std Dev 2.87
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

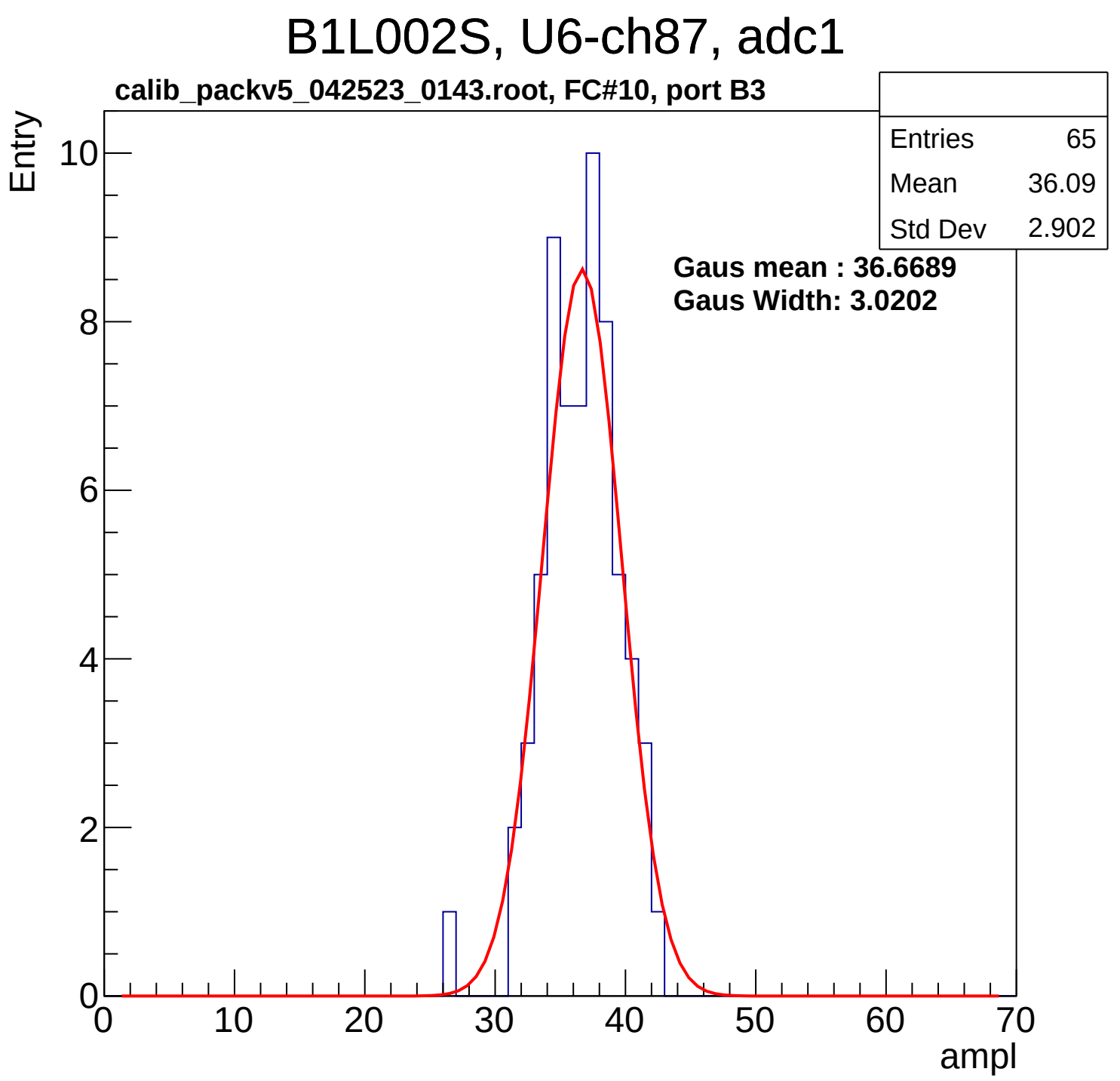

B1L002S, U6-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.15
Std Dev 2.873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

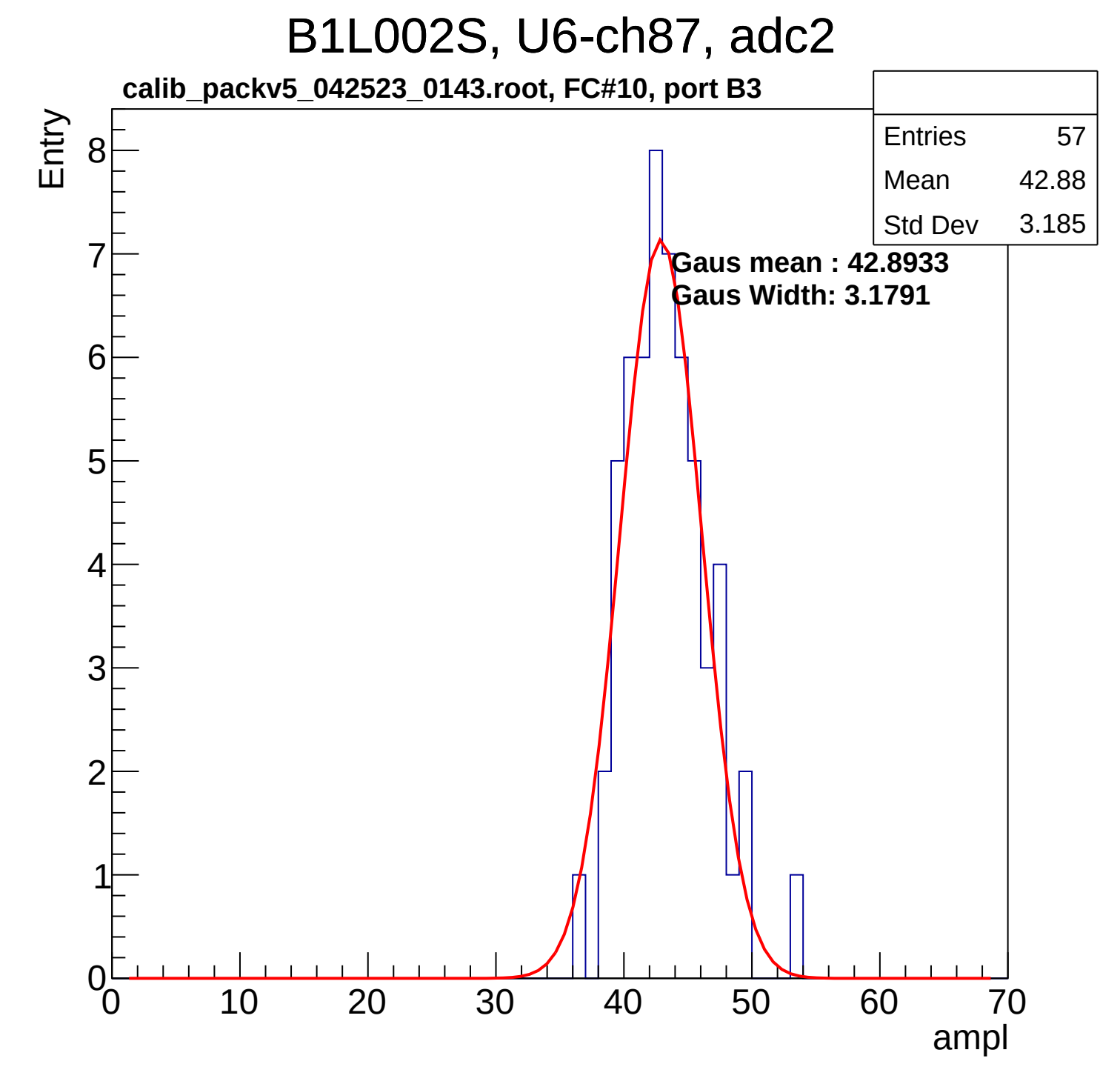

B1L002S, U6-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.98
Std Dev 2.947
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

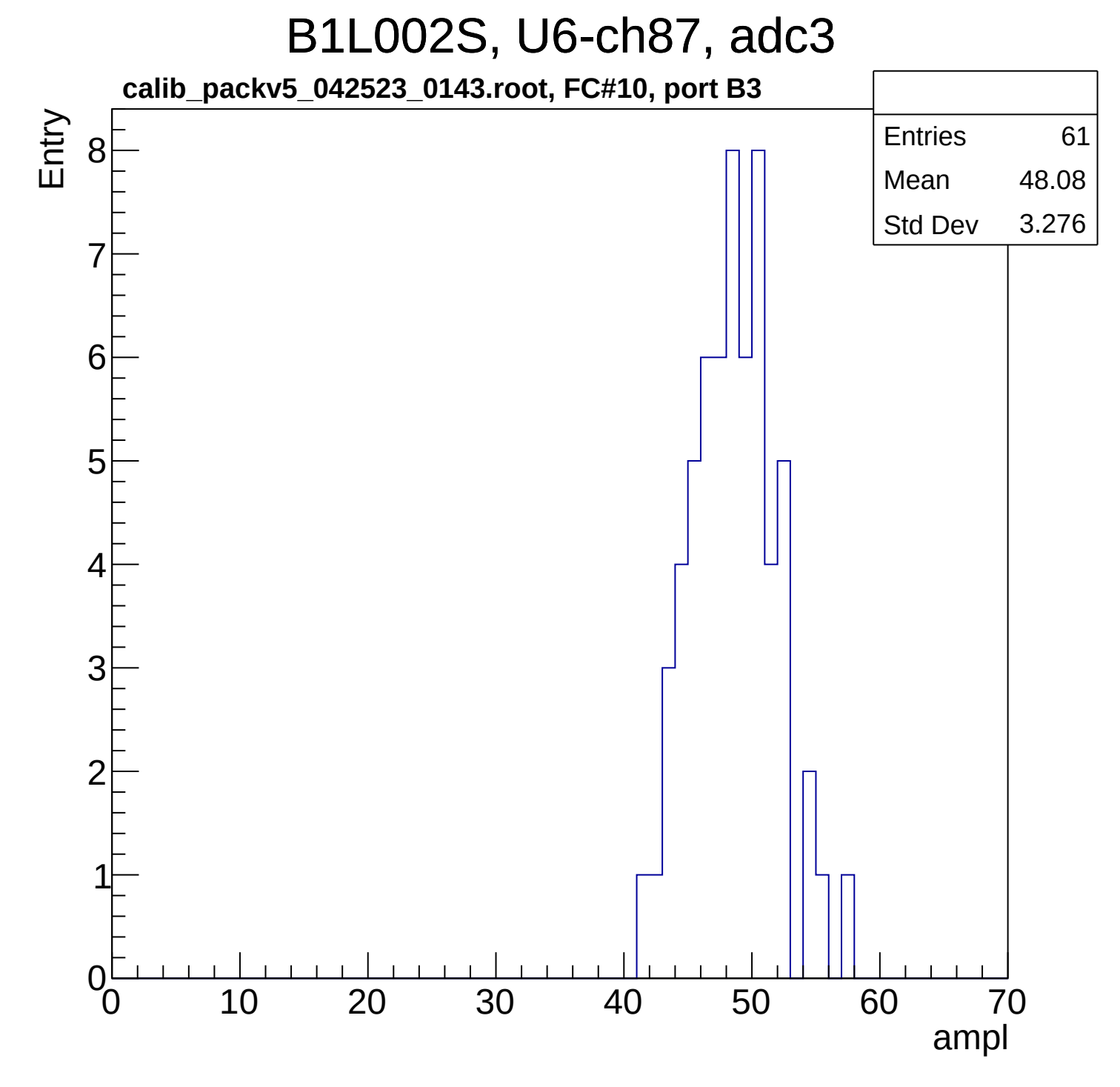

B1L002S, U6-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.71
Std Dev 11.43
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

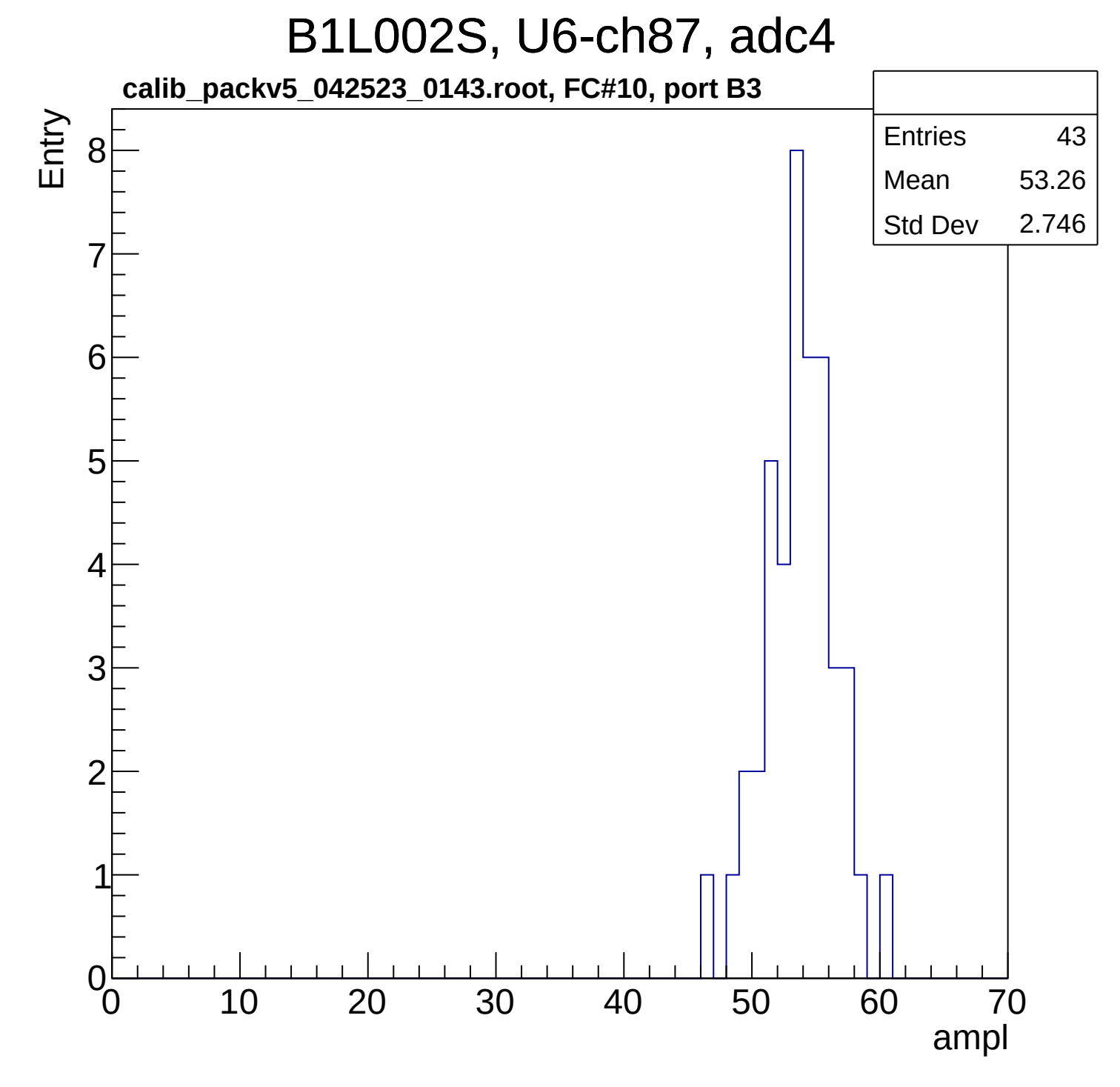

B1L002S, U6-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

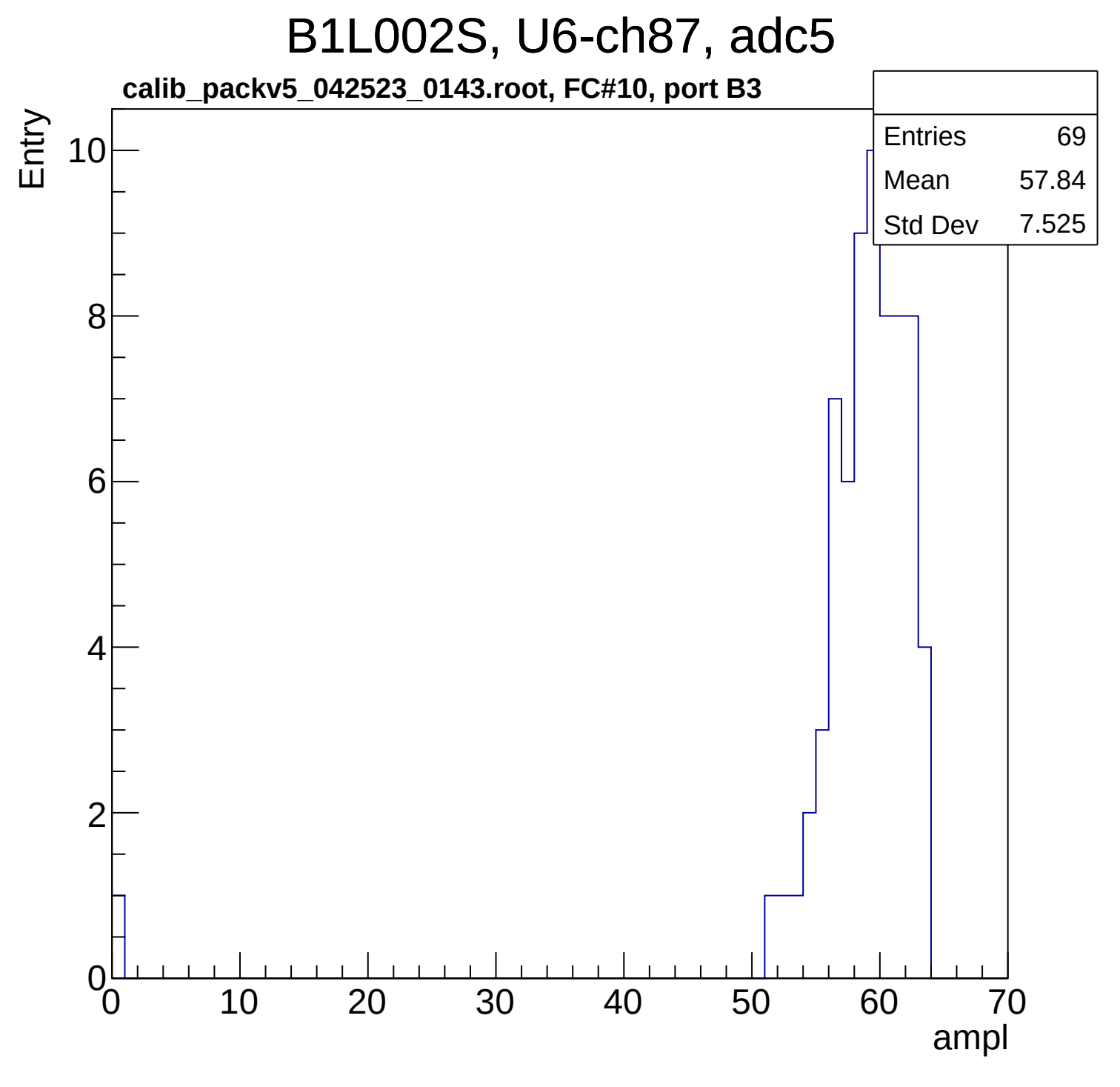

B1L002S, U6-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 29.69
Std Dev 3.052
Gaus mean : 30.0975
Gaus Width: 2.9355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

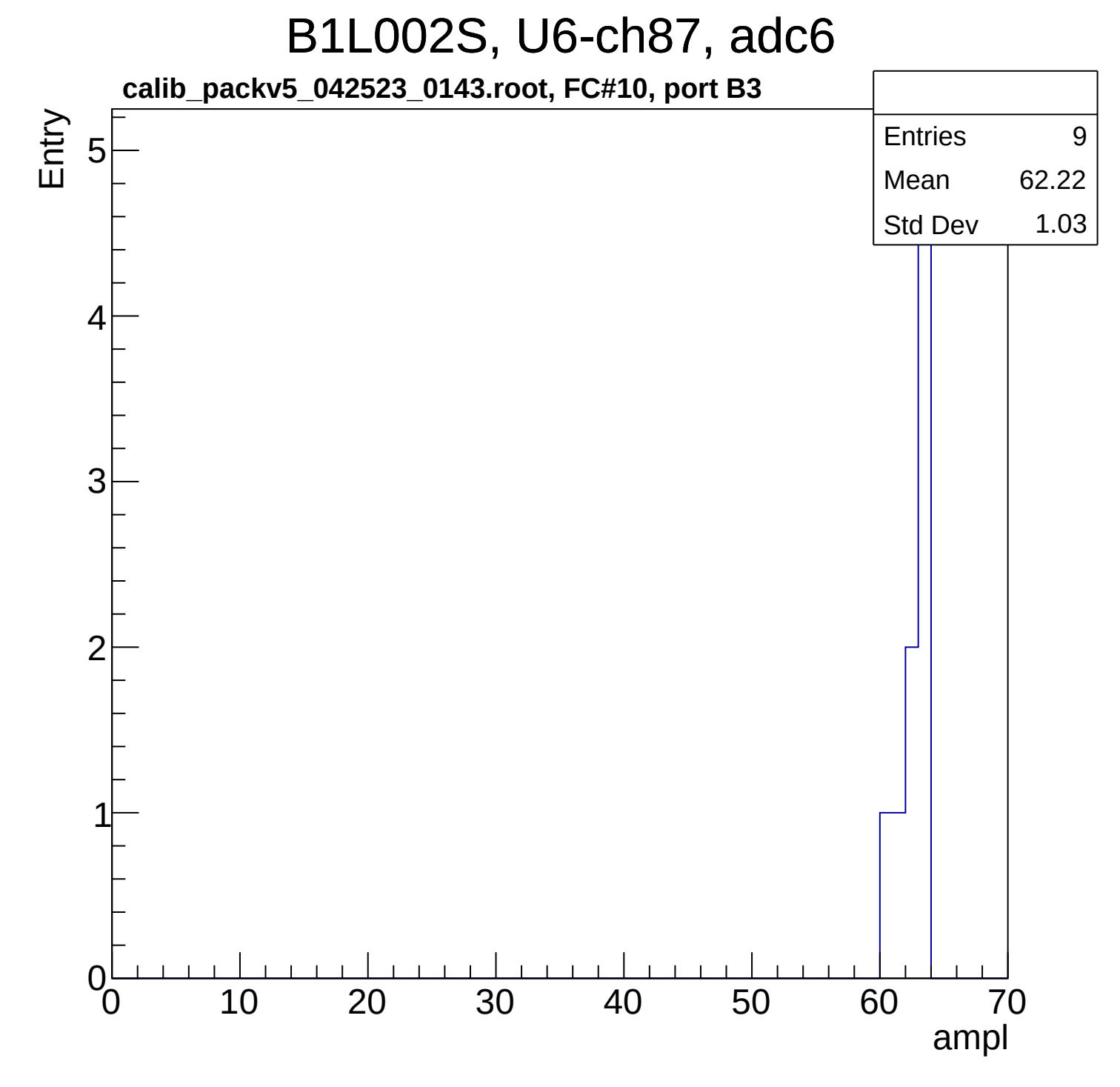

B1L002S, U6-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 36.09
Std Dev 2.902
Gaus mean : 36.6689
Gaus Width: 3.0202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

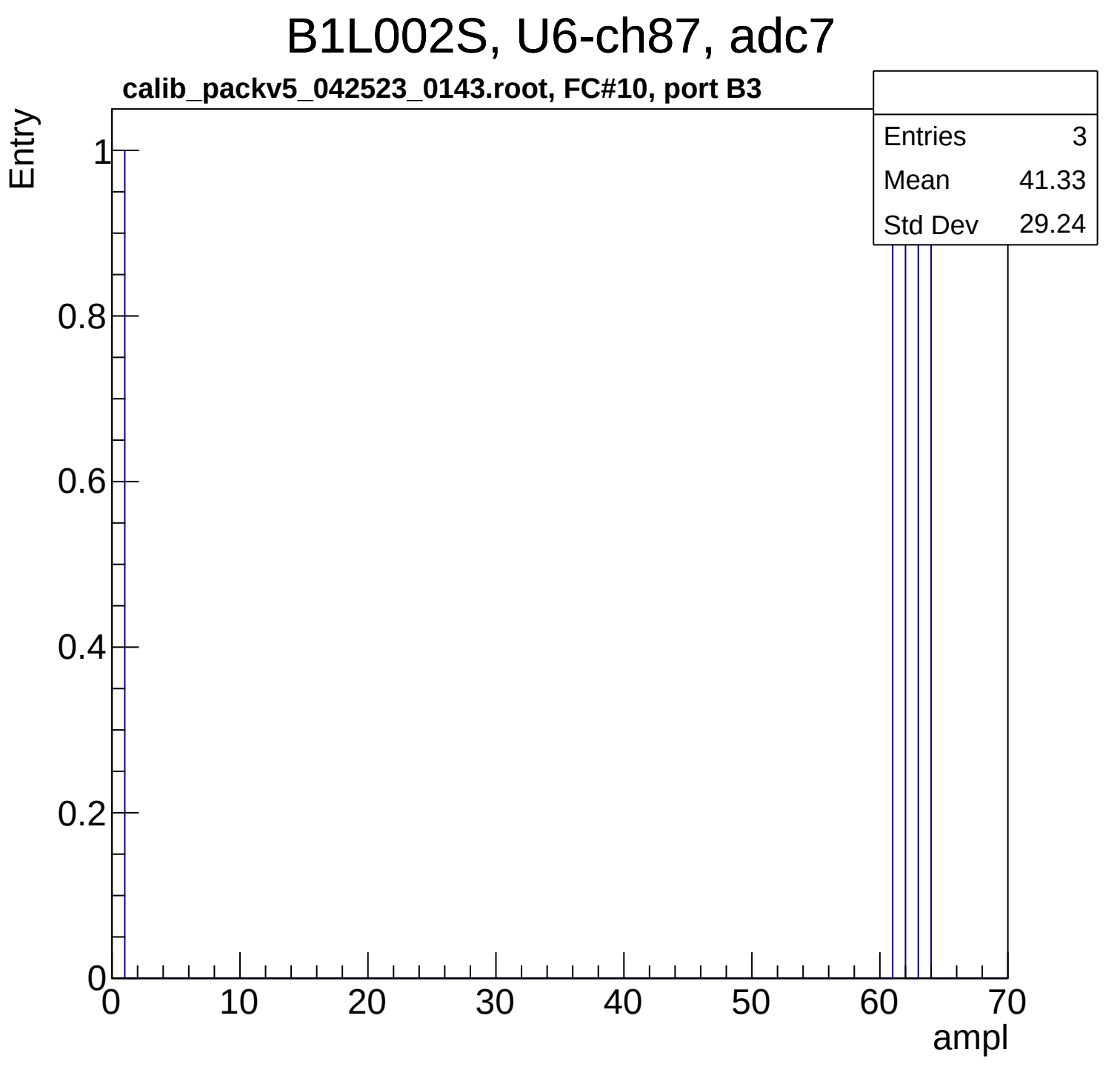

B1L002S, U6-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.88
Std Dev 3.185
Gaus mean : 42.8933
Gaus Width: 3.1791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

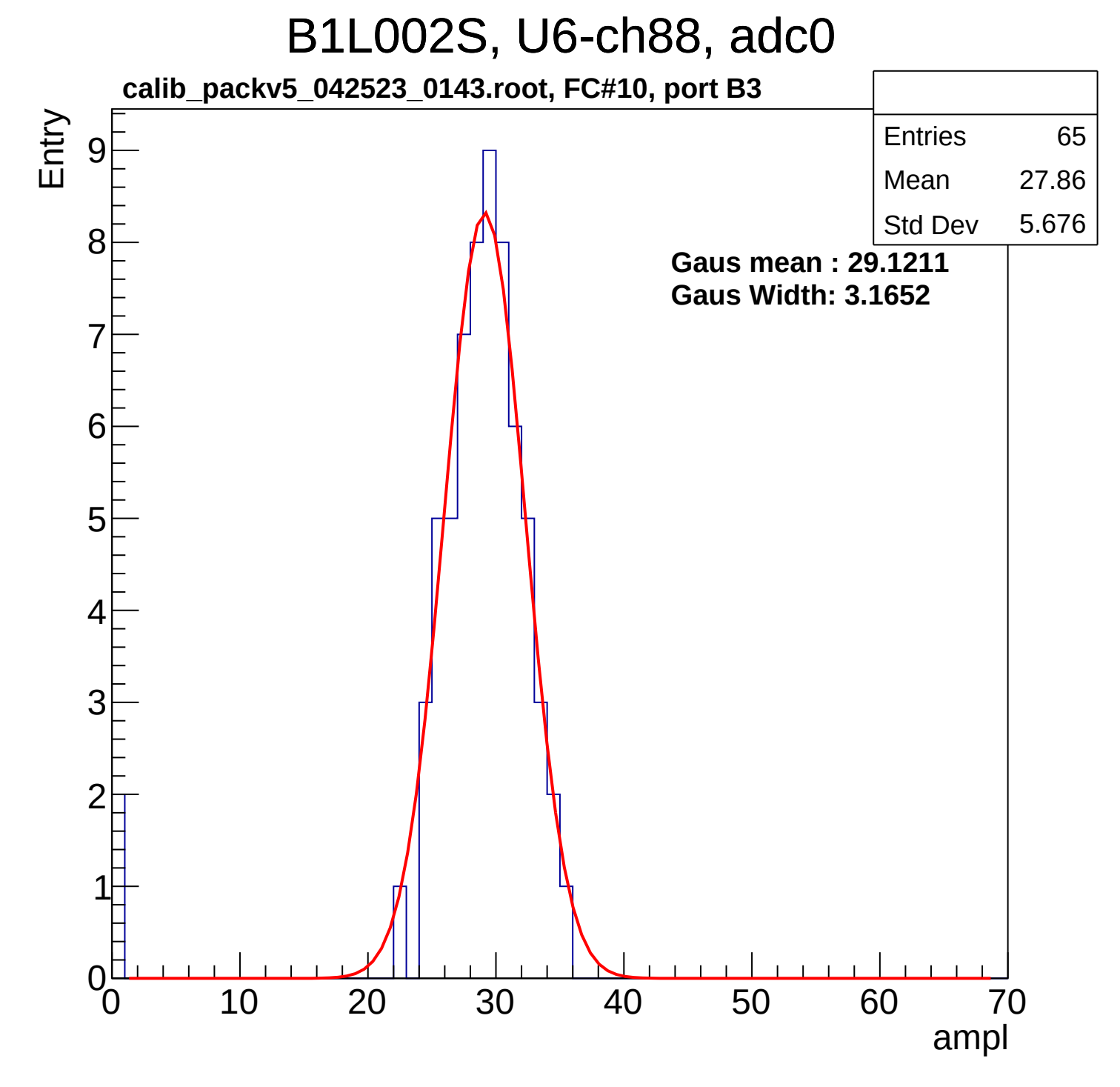

B1L002S, U6-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.08
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

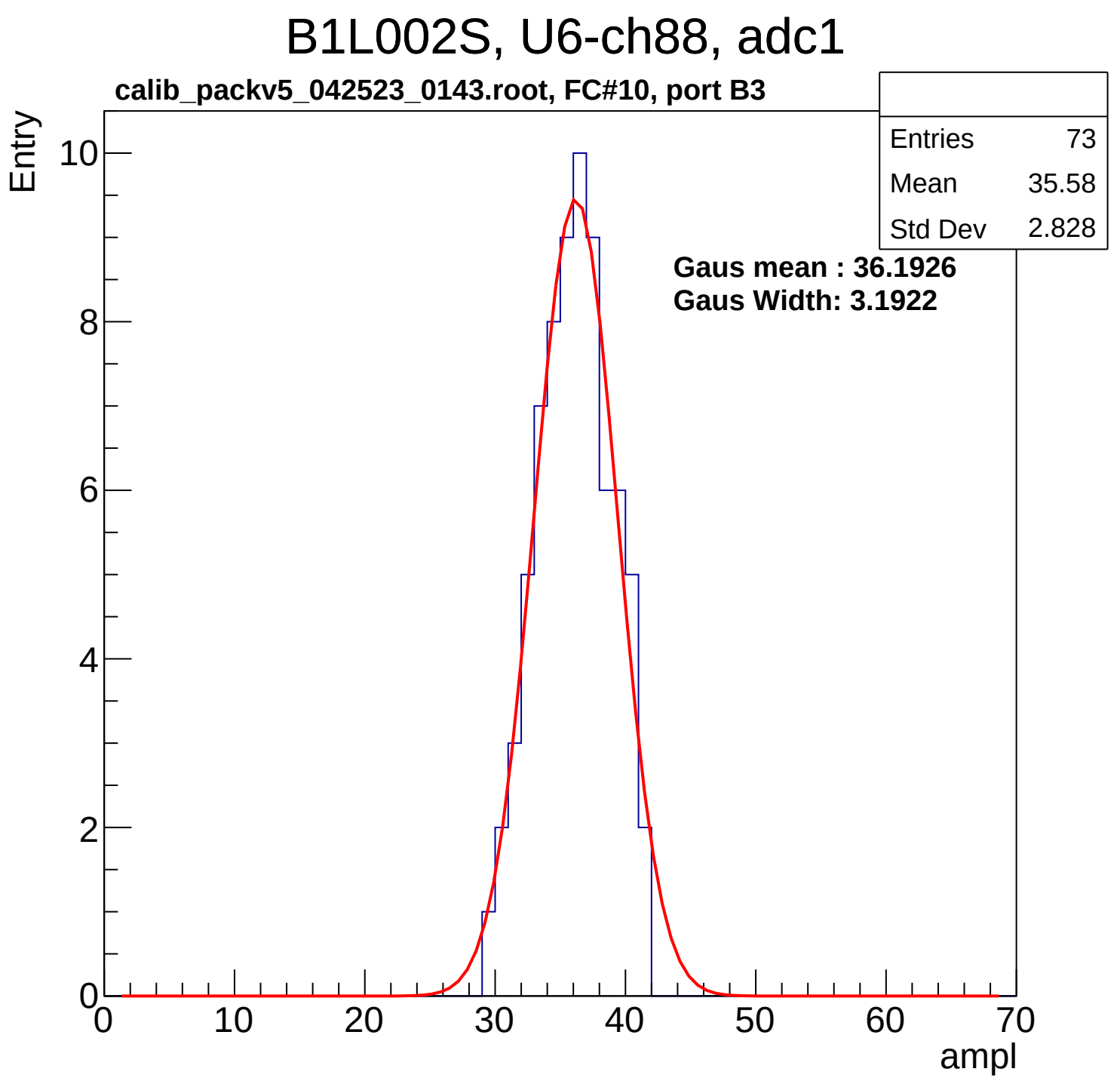

B1L002S, U6-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.26
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

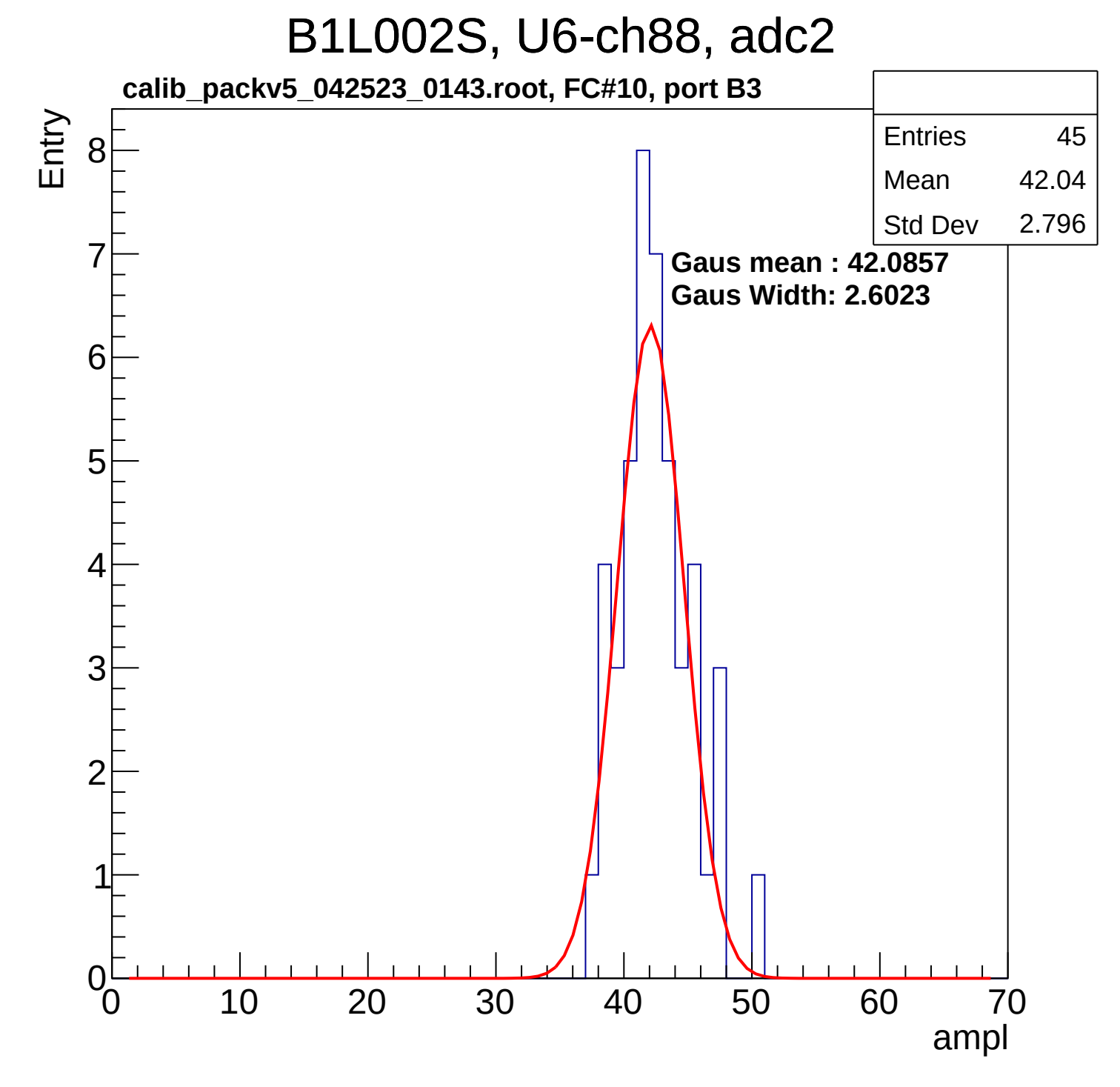

B1L002S, U6-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.84
Std Dev 7.525
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

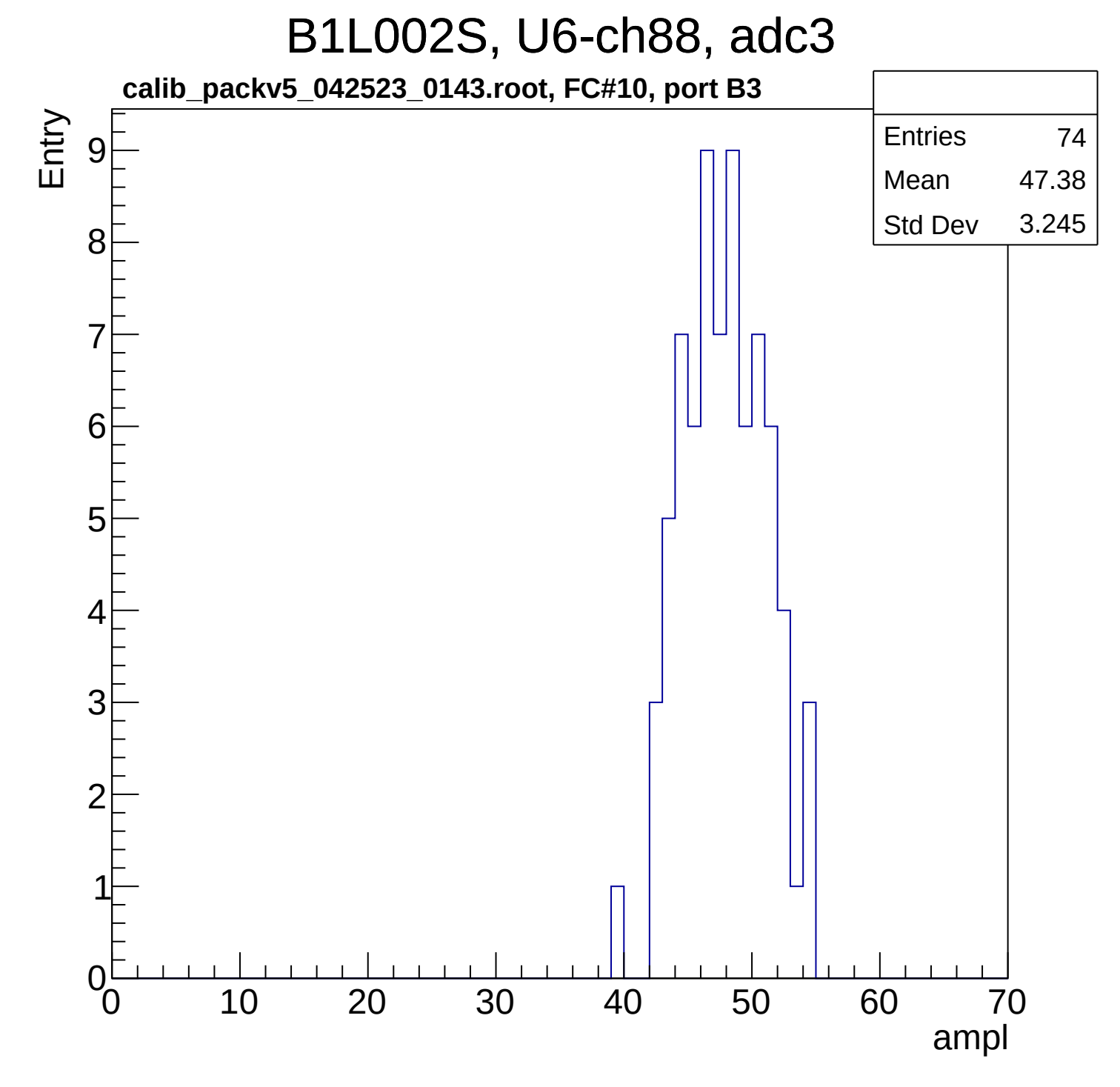

B1L002S, U6-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 62.22
Std Dev 1.03
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

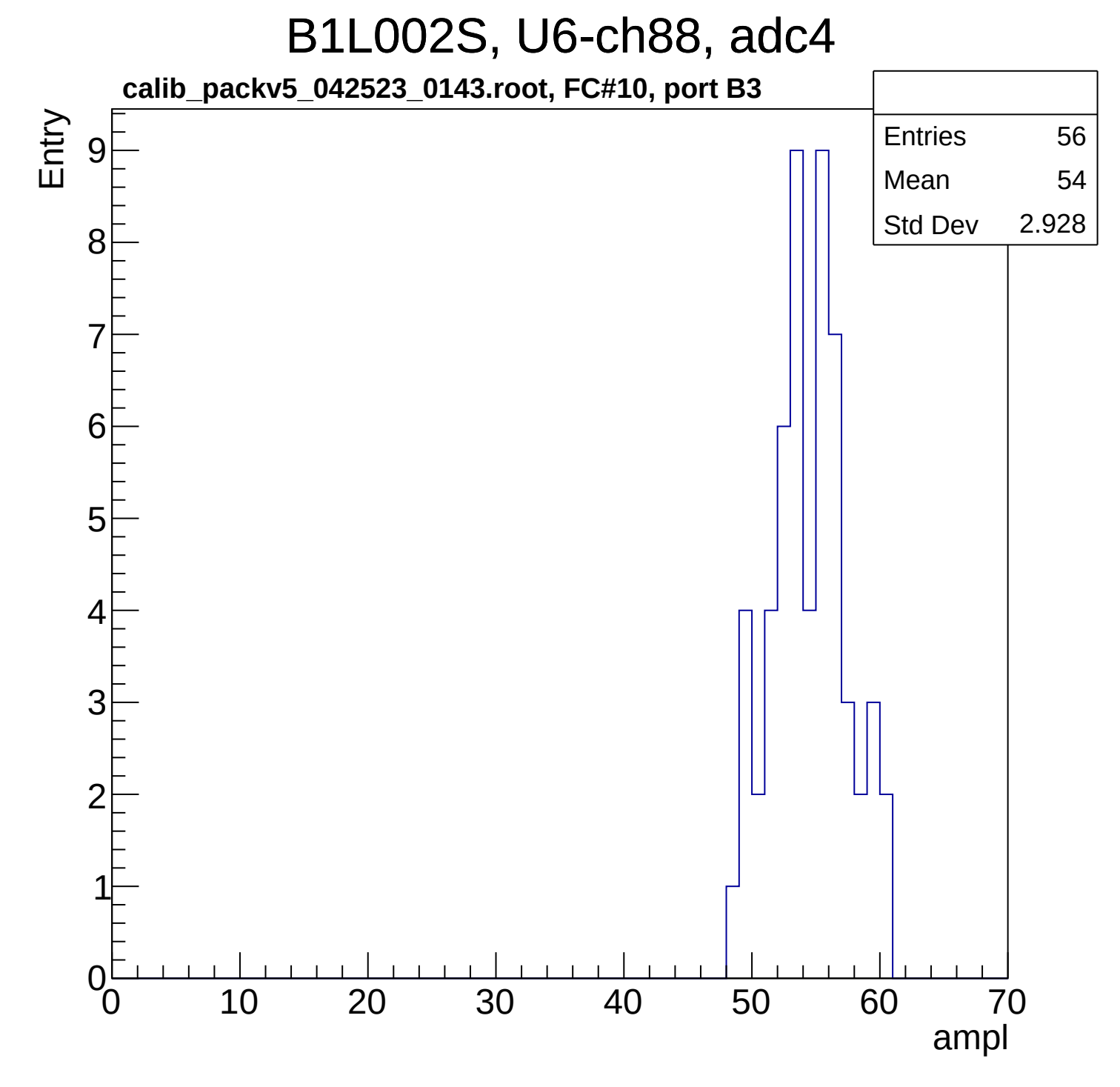

B1L002S, U6-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

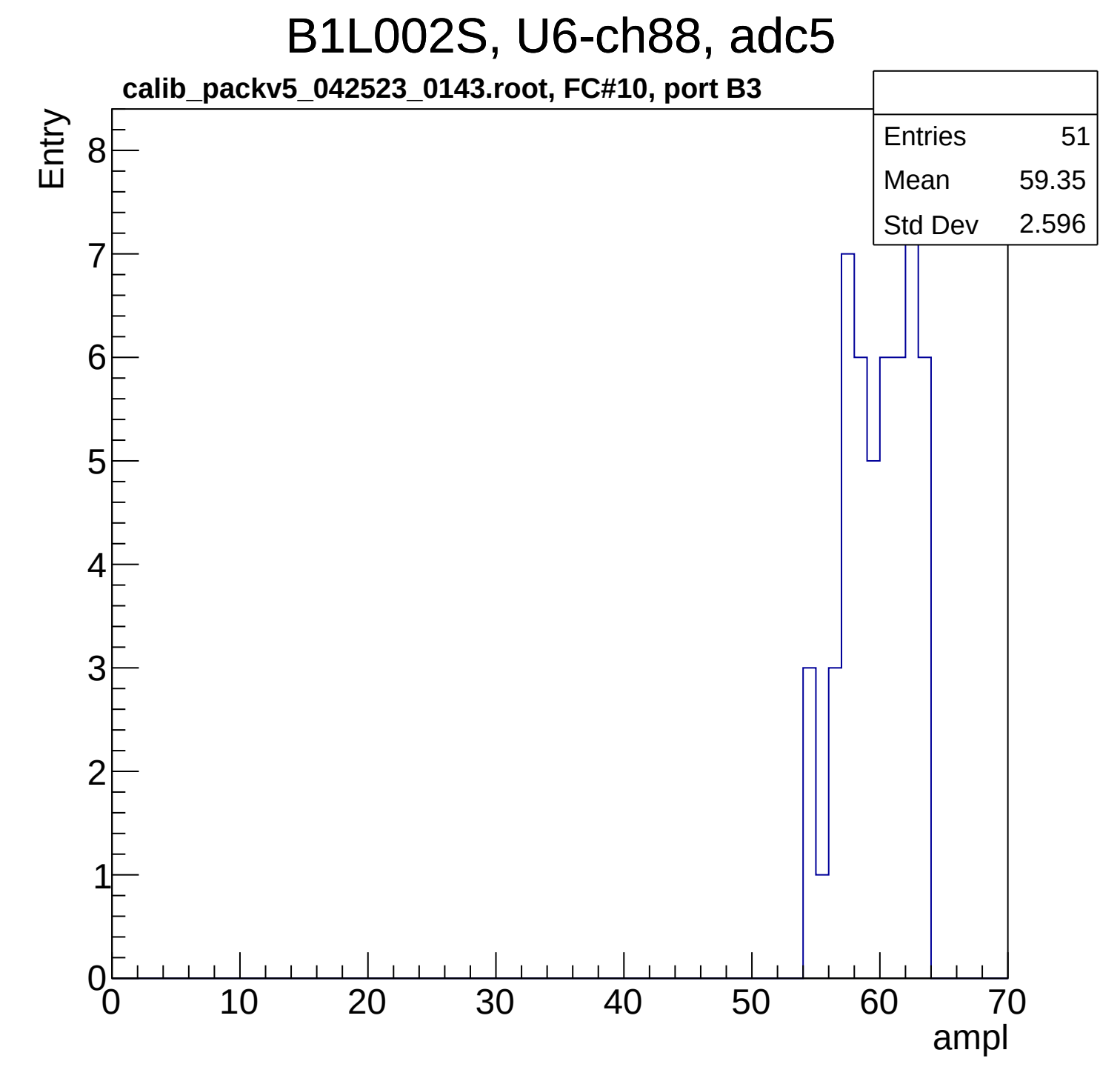

B1L002S, U6-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.86
Std Dev 5.676
Gaus mean : 29.1211
Gaus Width: 3.1652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

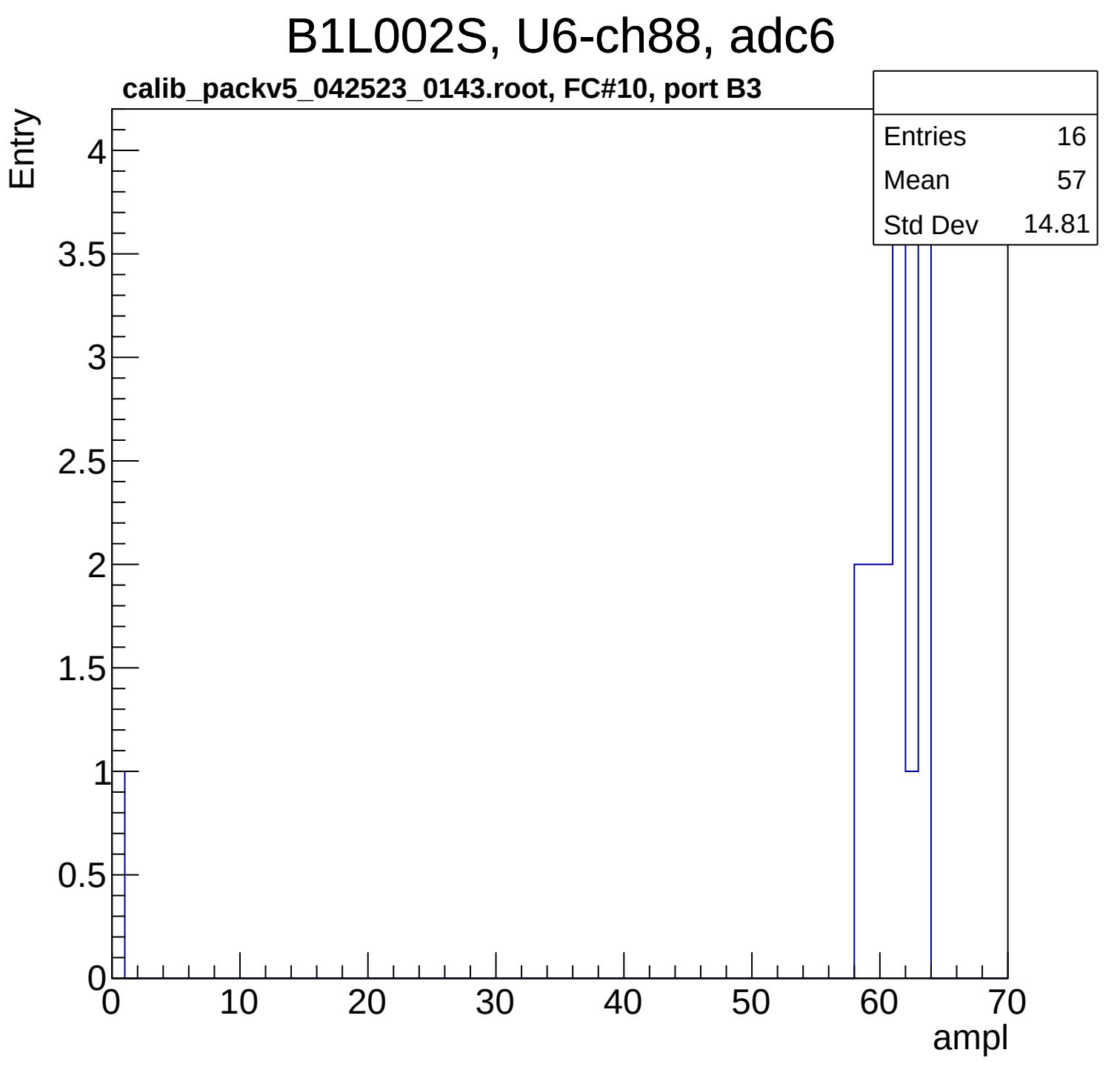

B1L002S, U6-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.58
Std Dev 2.828
Gaus mean : 36.1926
Gaus Width: 3.1922
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

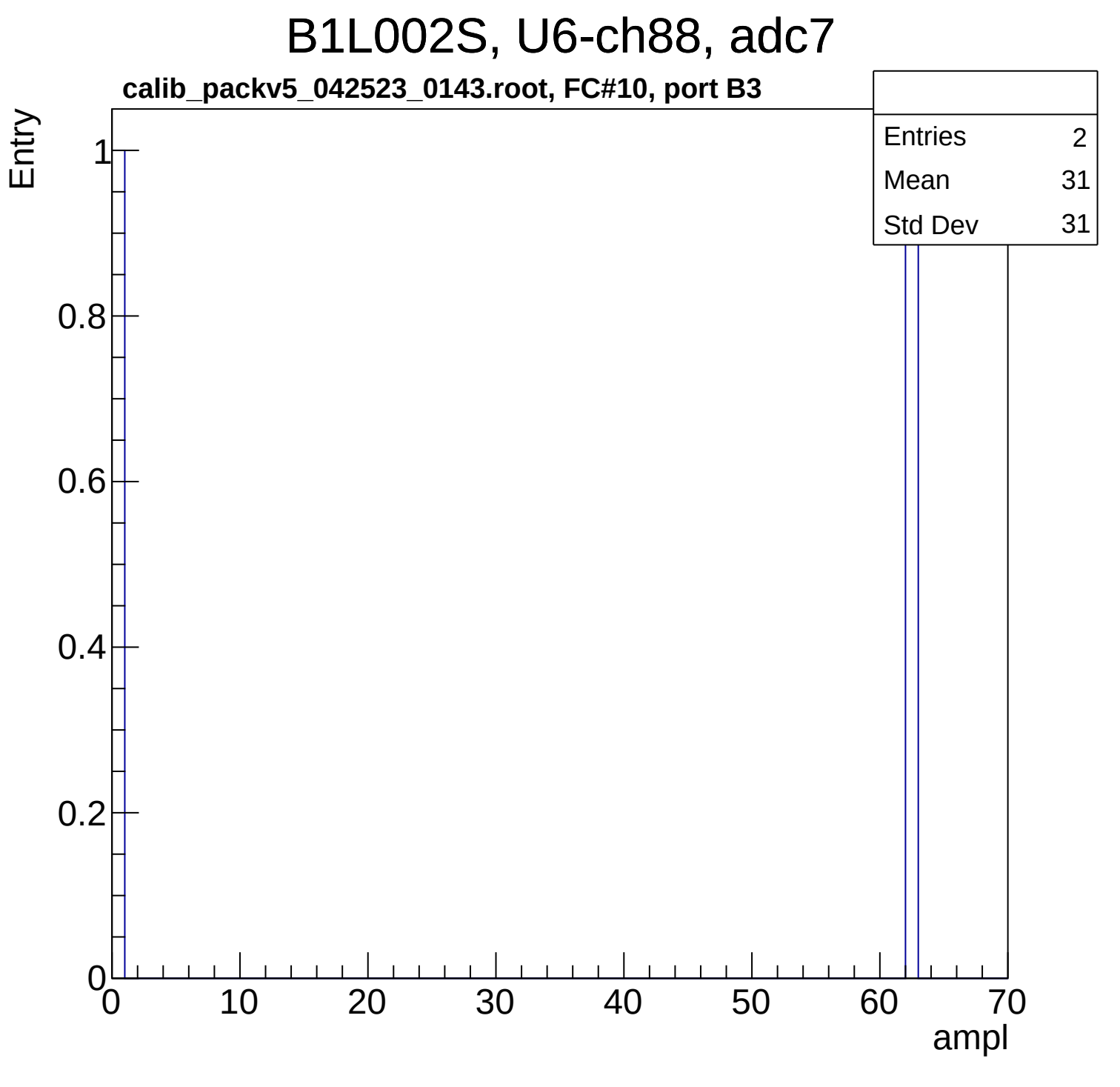

B1L002S, U6-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 42.04
Std Dev 2.796
Gaus mean : 42.0857
Gaus Width: 2.6023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

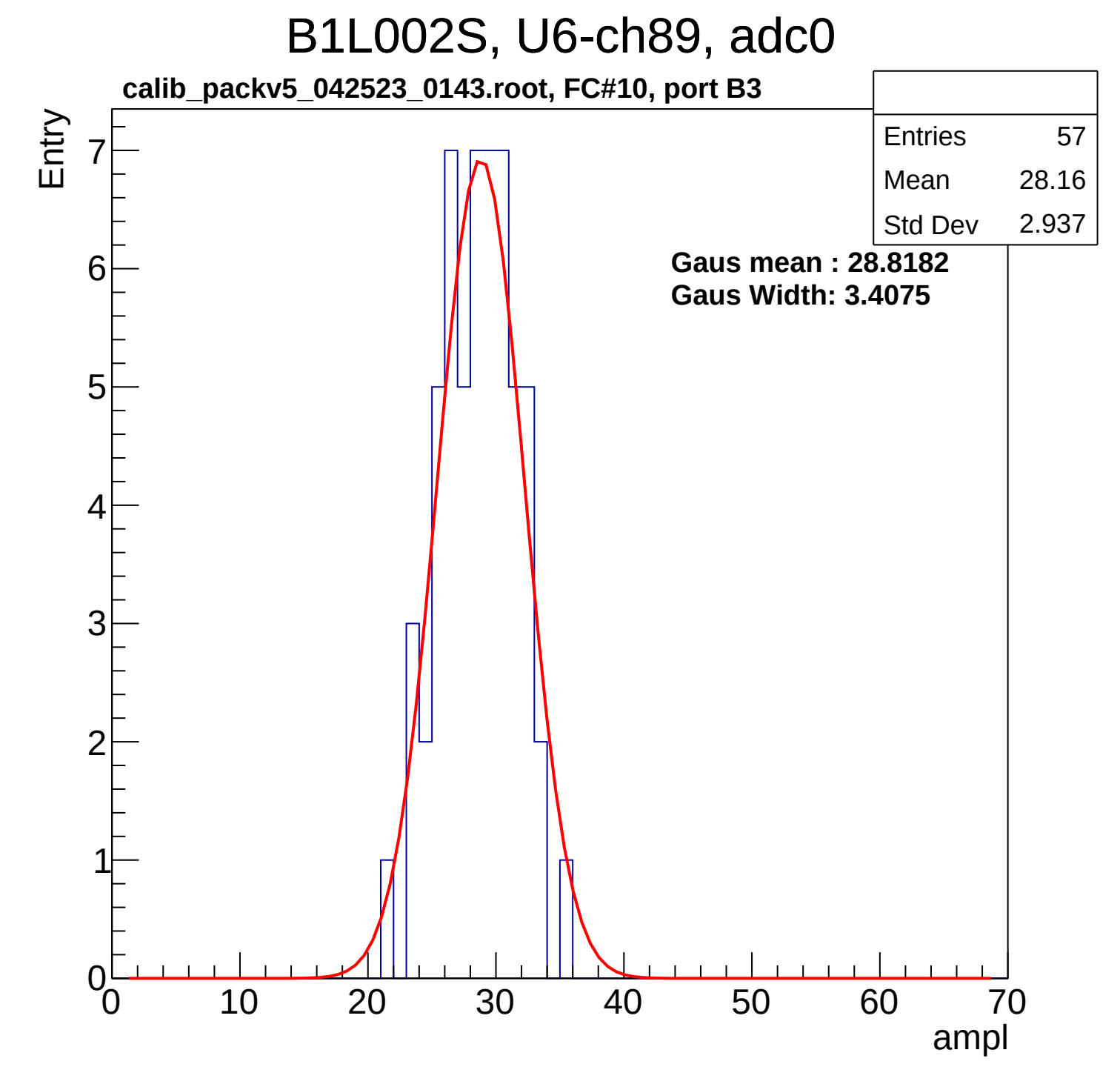

B1L002S, U6-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.38
Std Dev 3.245
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

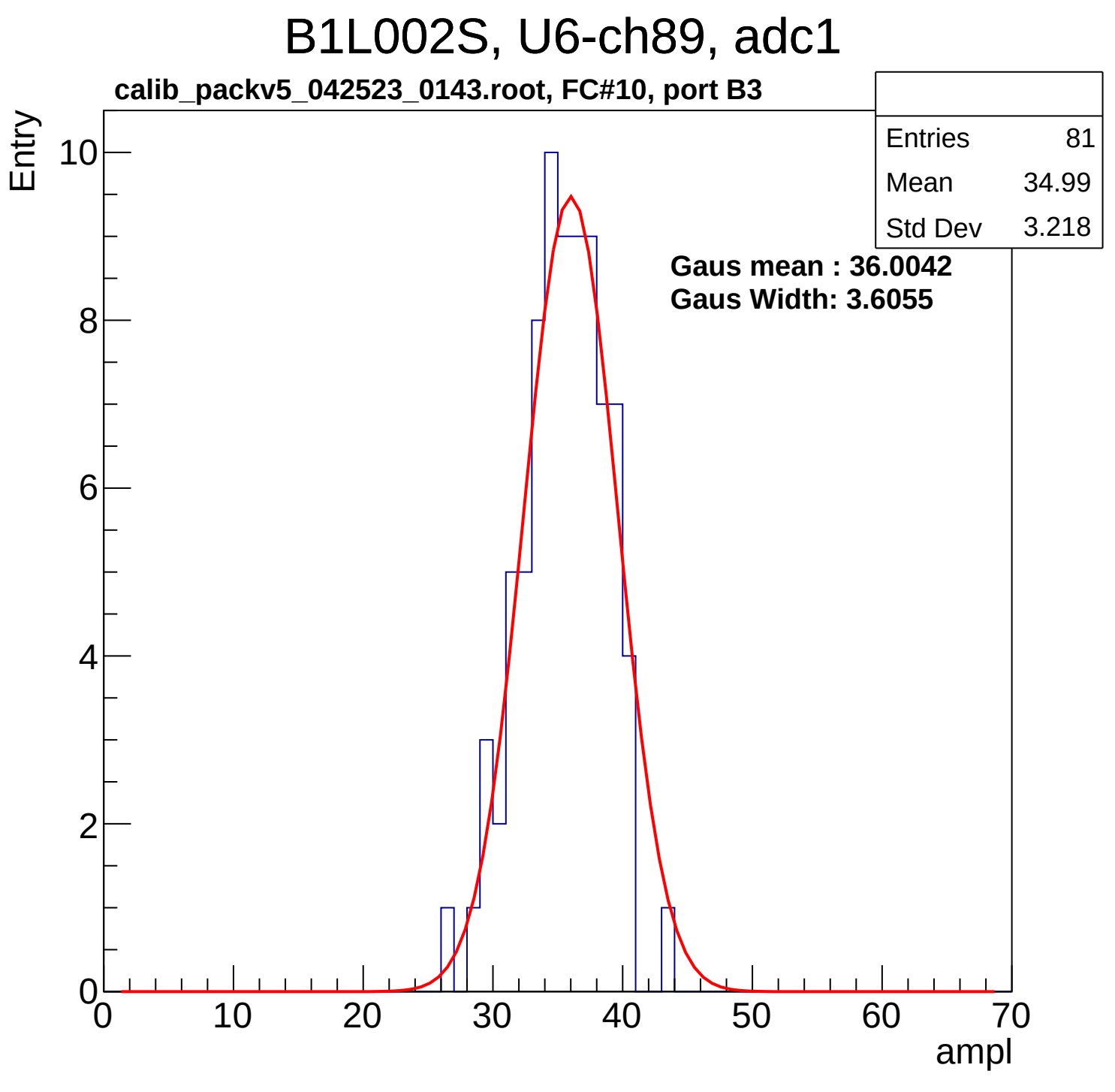

B1L002S, U6-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54
Std Dev 2.928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

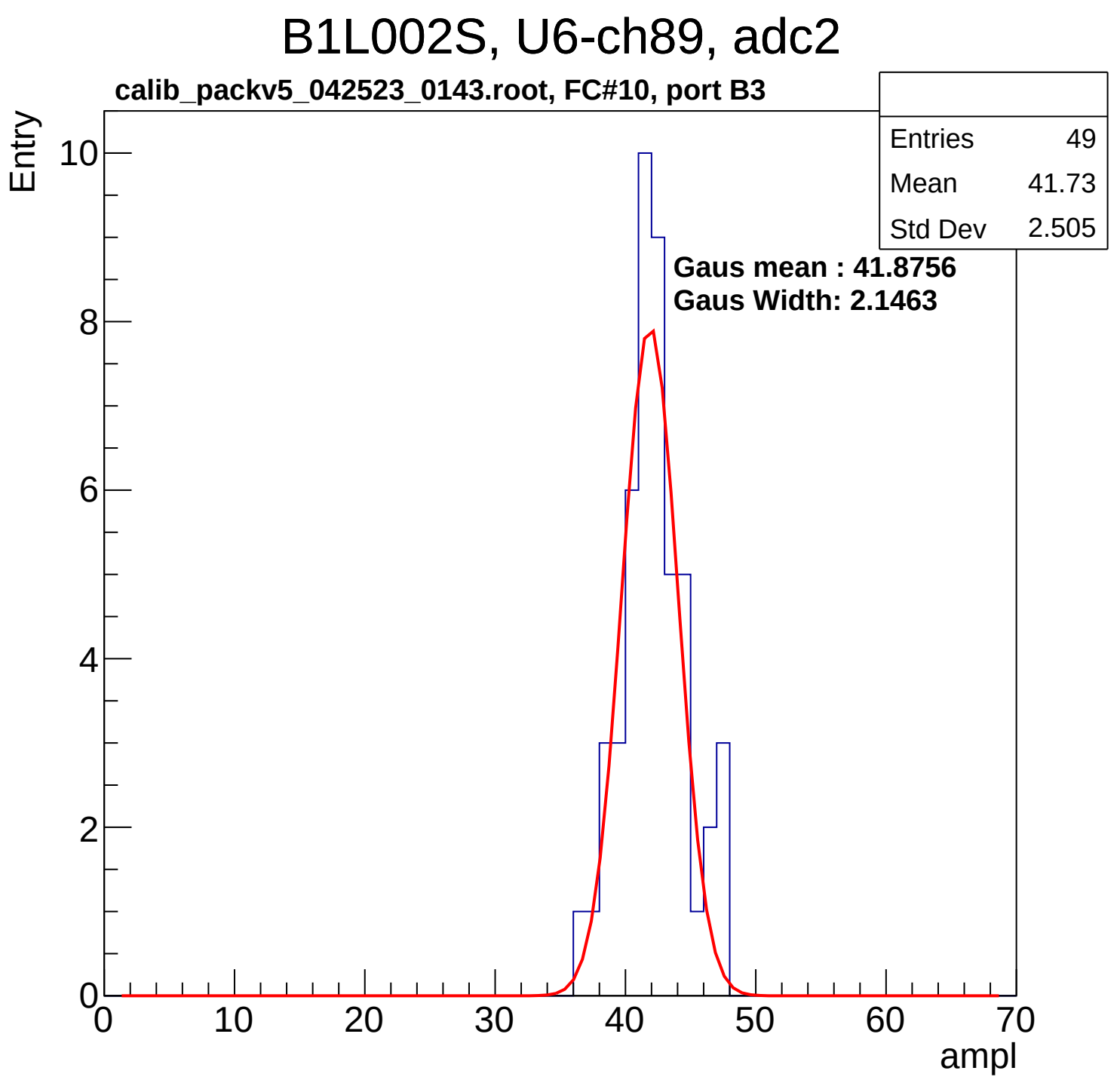

B1L002S, U6-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.35
Std Dev 2.596
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

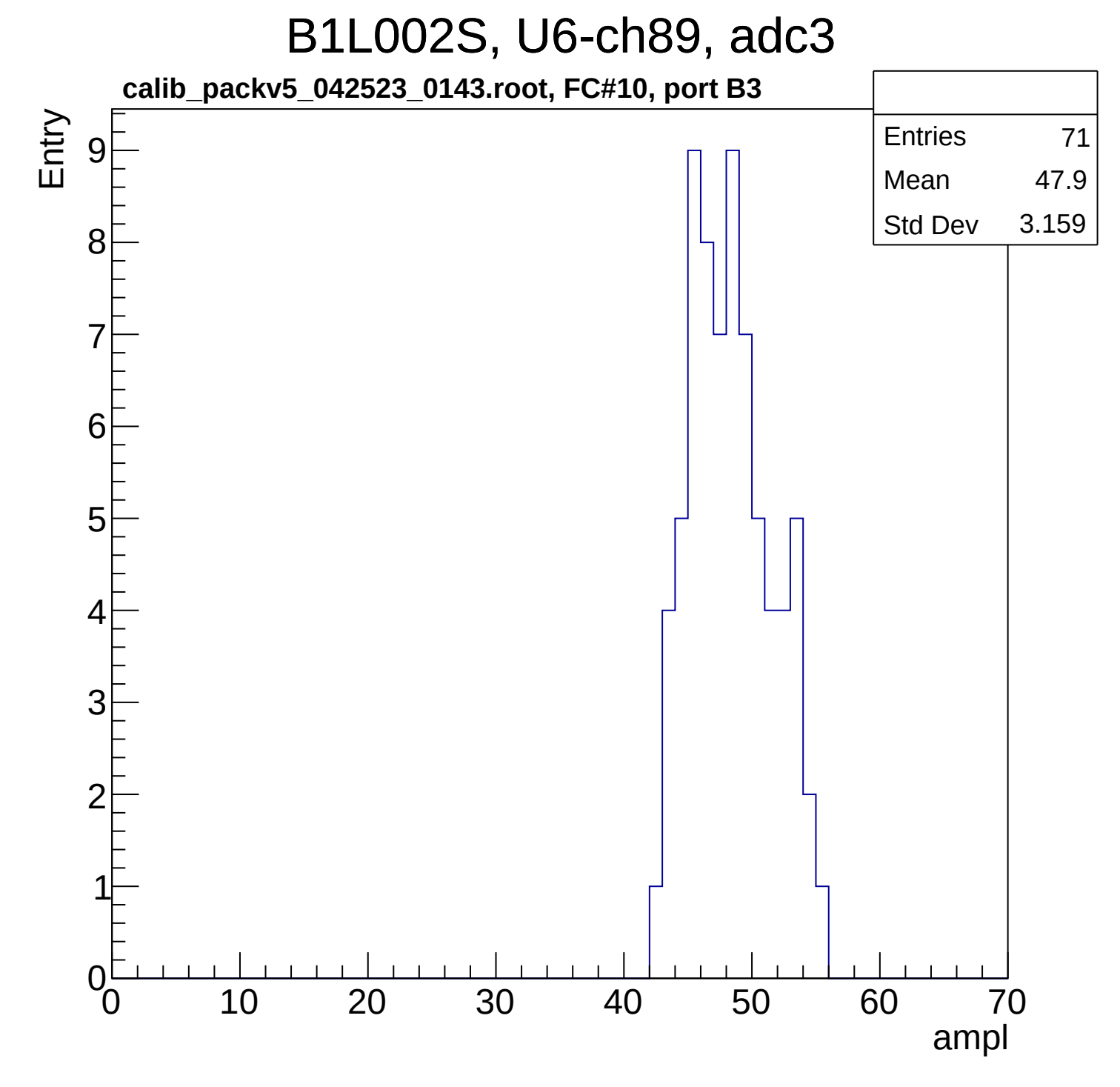

B1L002S, U6-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57
Std Dev 14.81
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

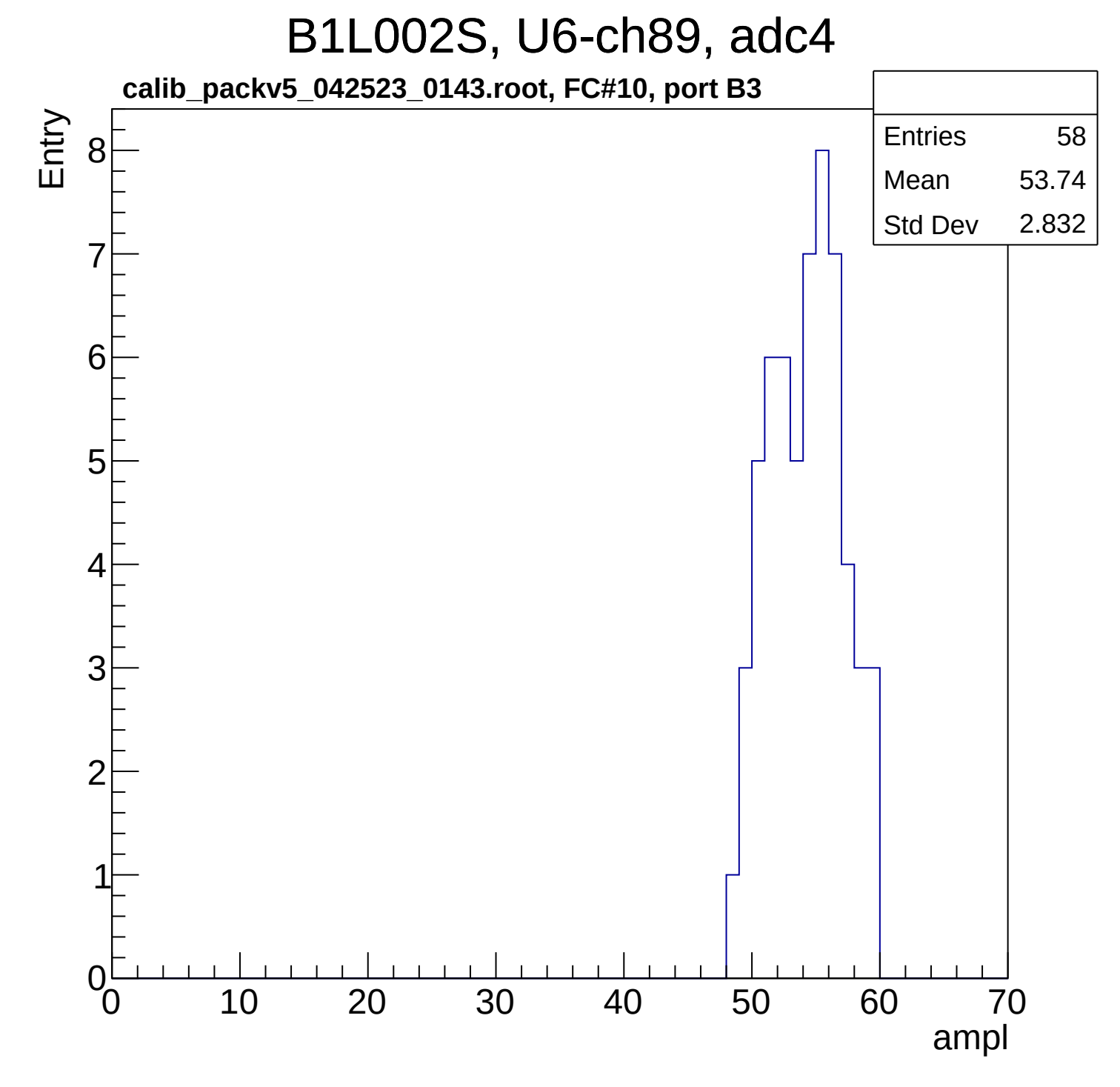

B1L002S, U6-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

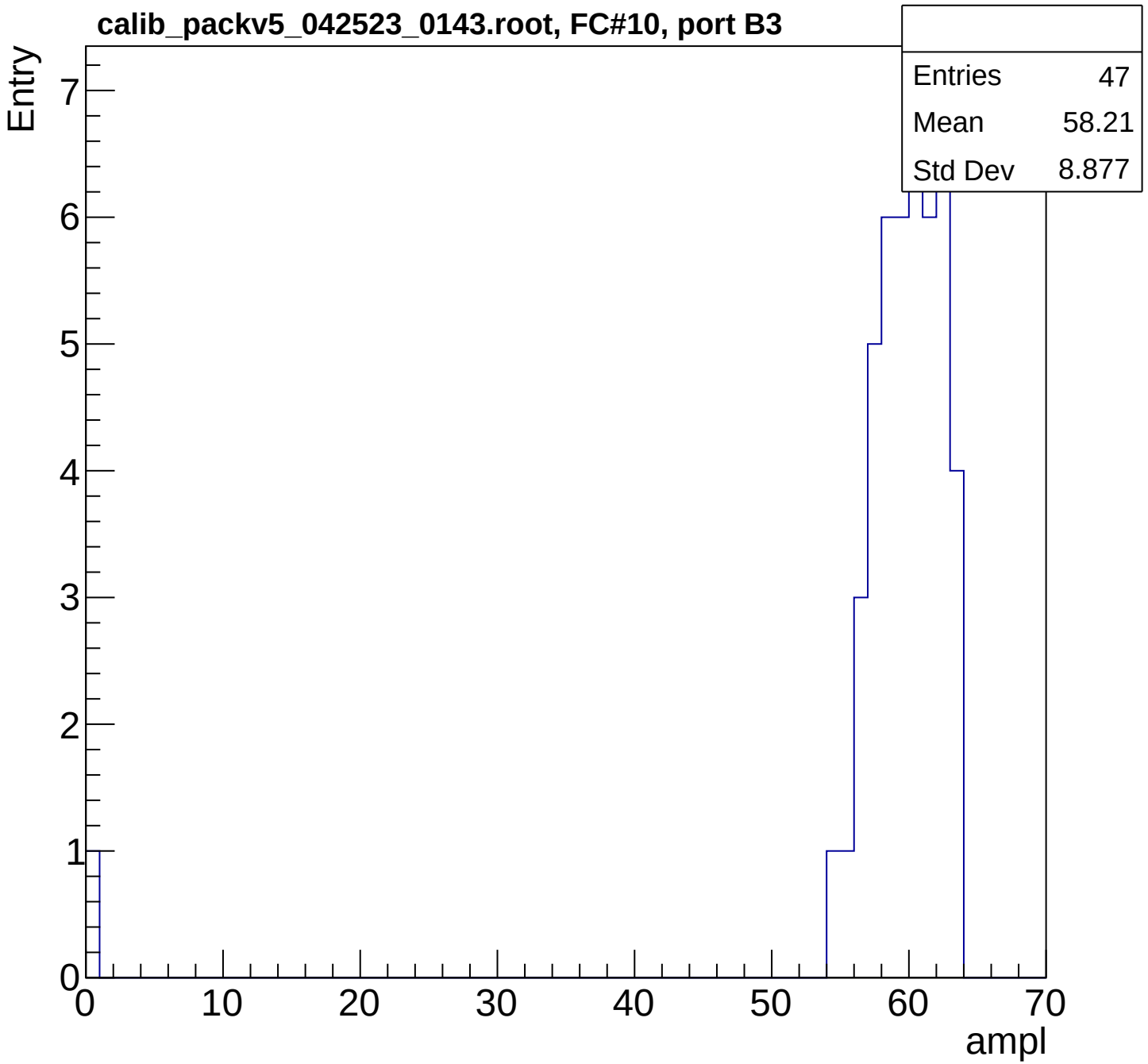

B1L002S, U6-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.16
Std Dev 2.937
Gaus mean : 28.8182
Gaus Width: 3.4075
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

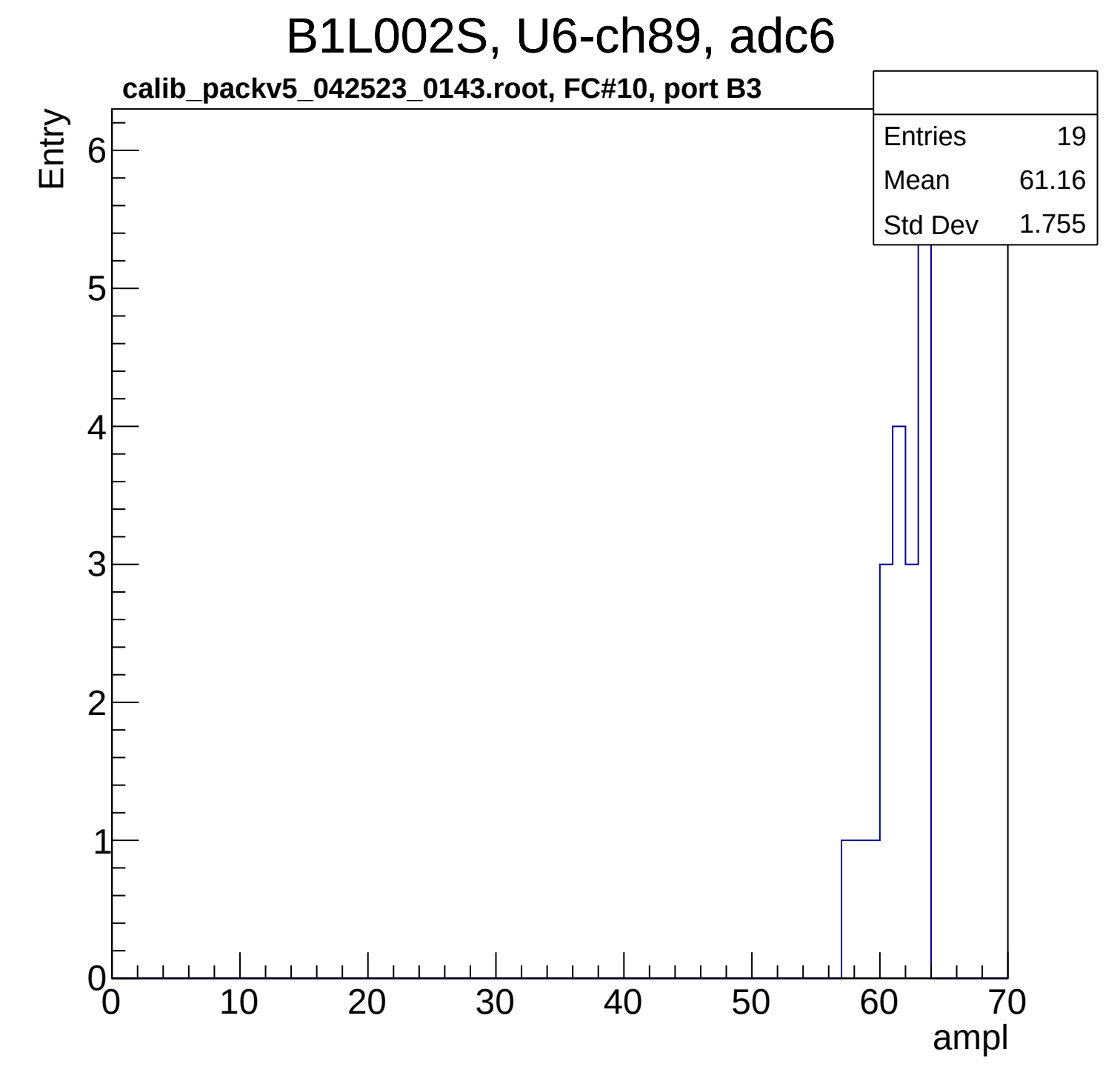

B1L002S, U6-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.99
Std Dev 3.218
Gaus mean : 36.0042
Gaus Width: 3.6055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

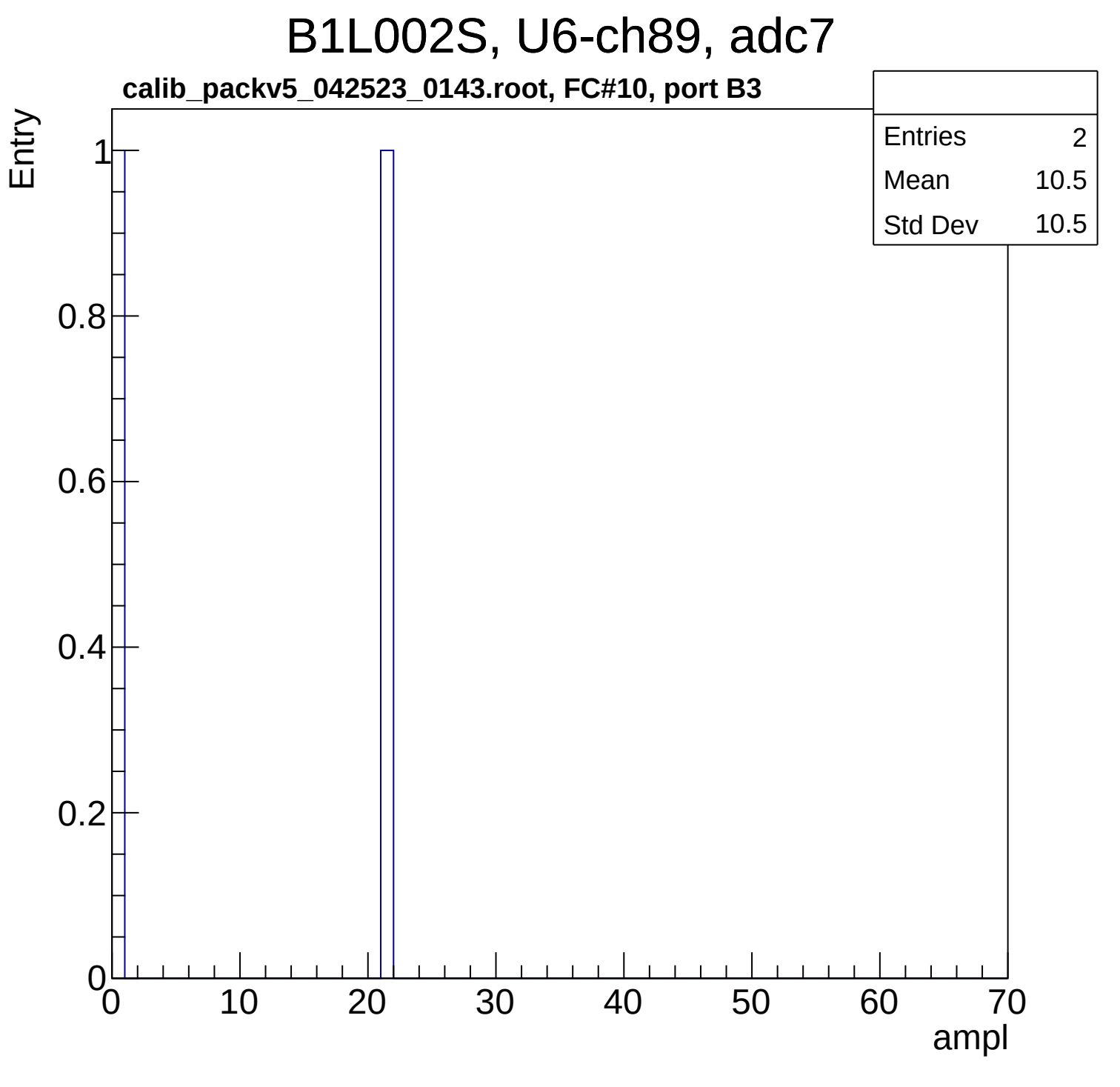

B1L002S, U6-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.73
Std Dev 2.505
Gaus mean : 41.8756
Gaus Width: 2.1463
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

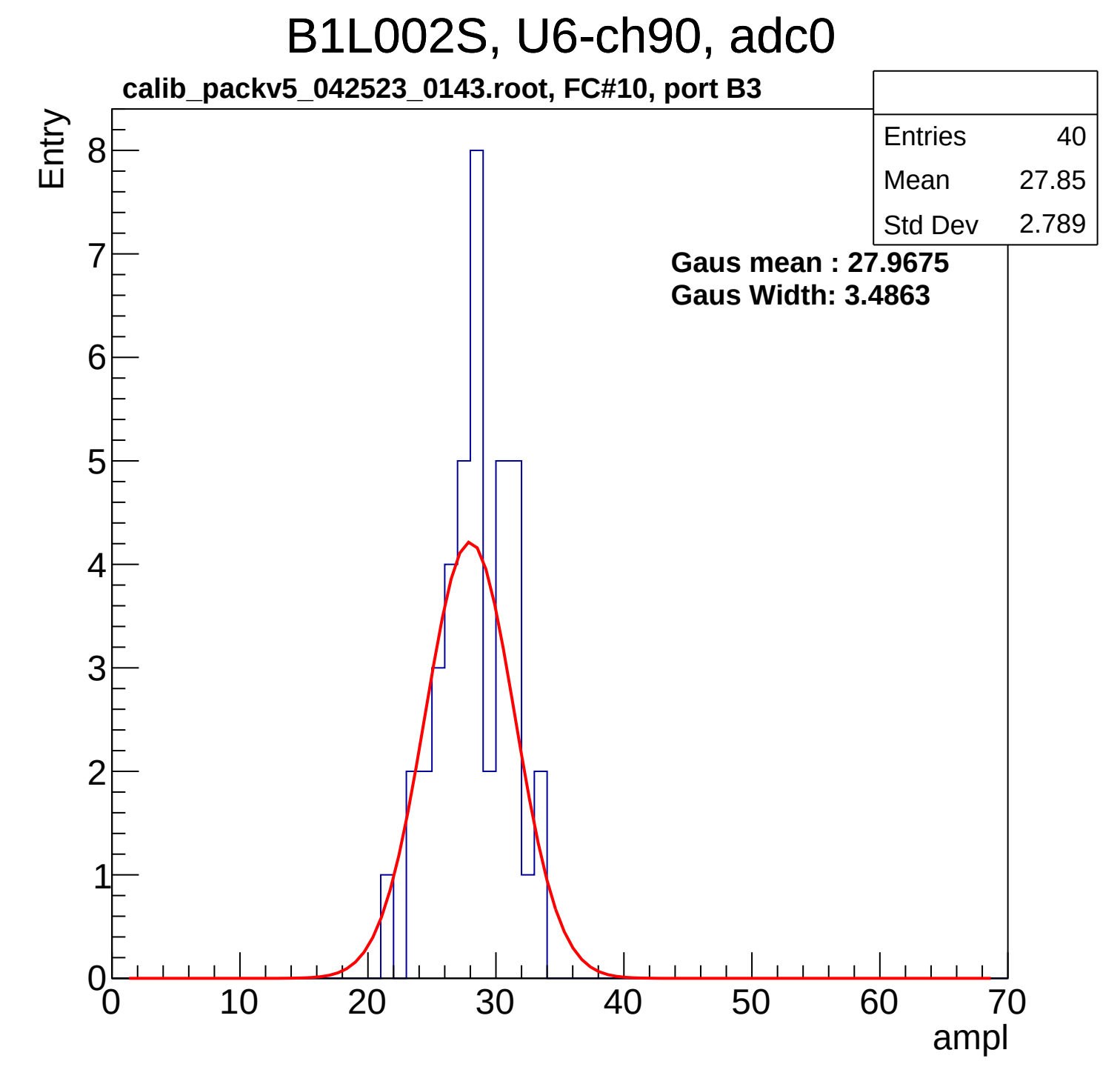

B1L002S, U6-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 47.9
Std Dev 3.159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

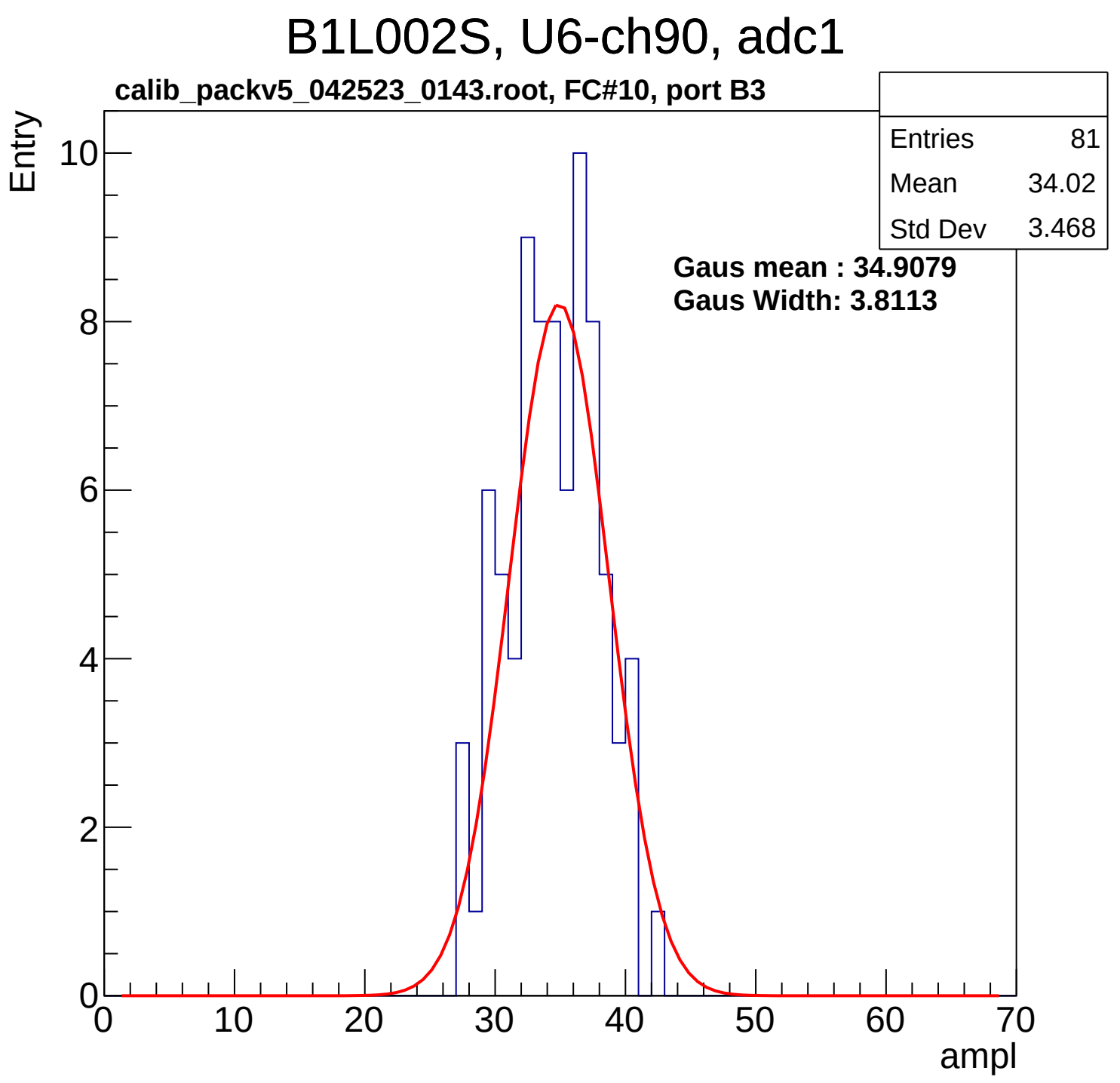

B1L002S, U6-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.74
Std Dev 2.832
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

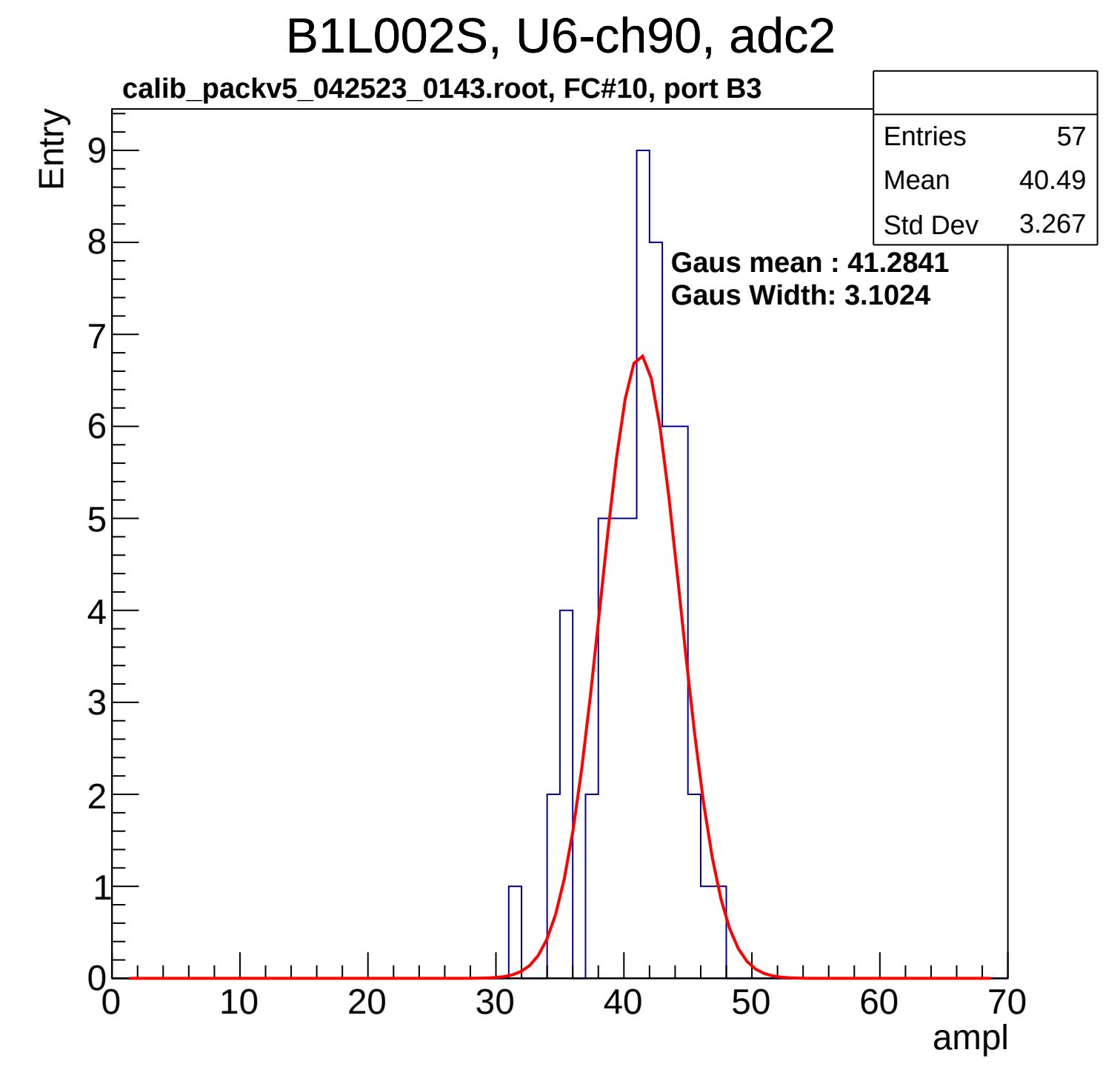

B1L002S, U6-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.21
Std Dev 8.877
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

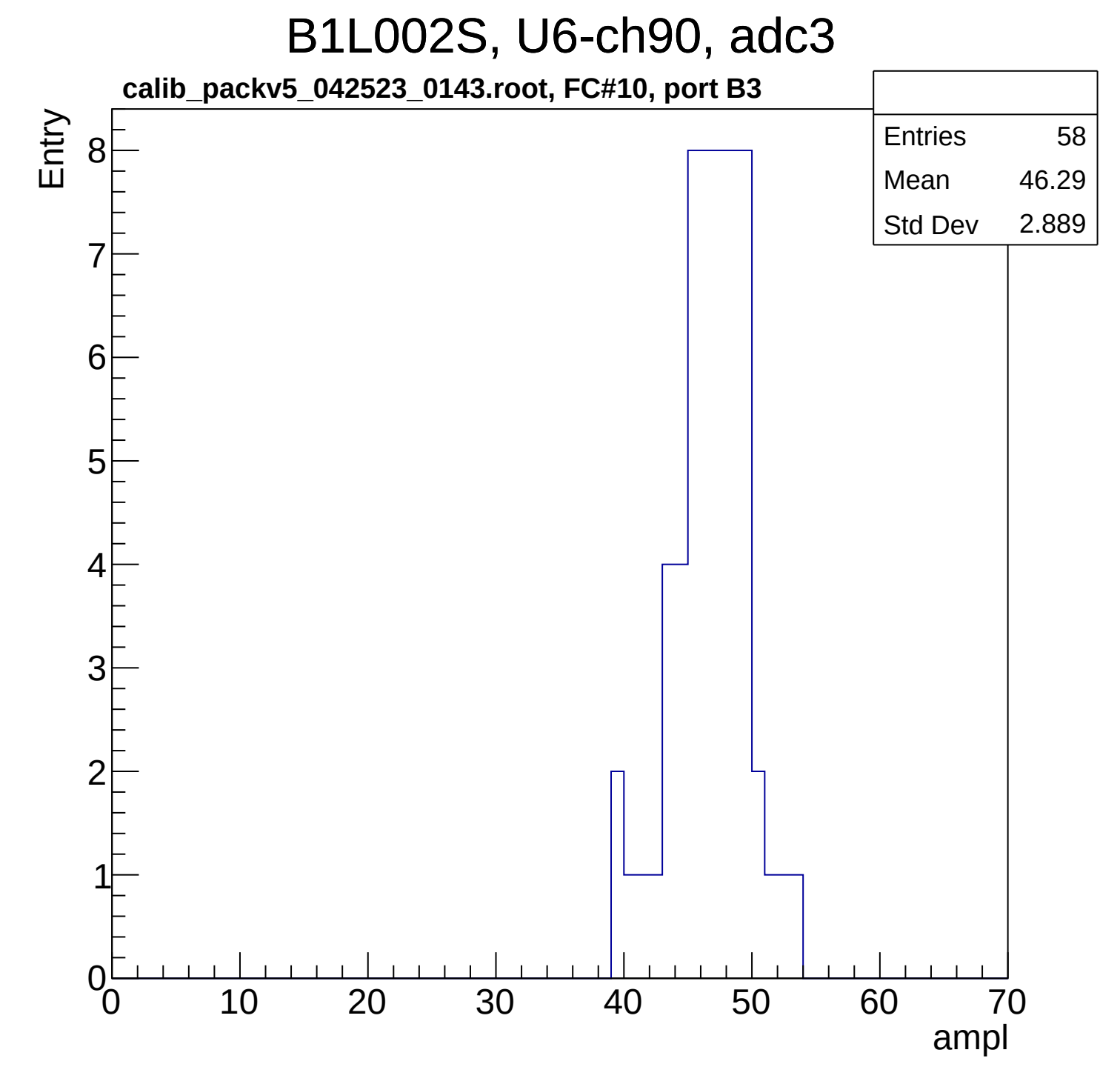

B1L002S, U6-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.16
Std Dev 1.755
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

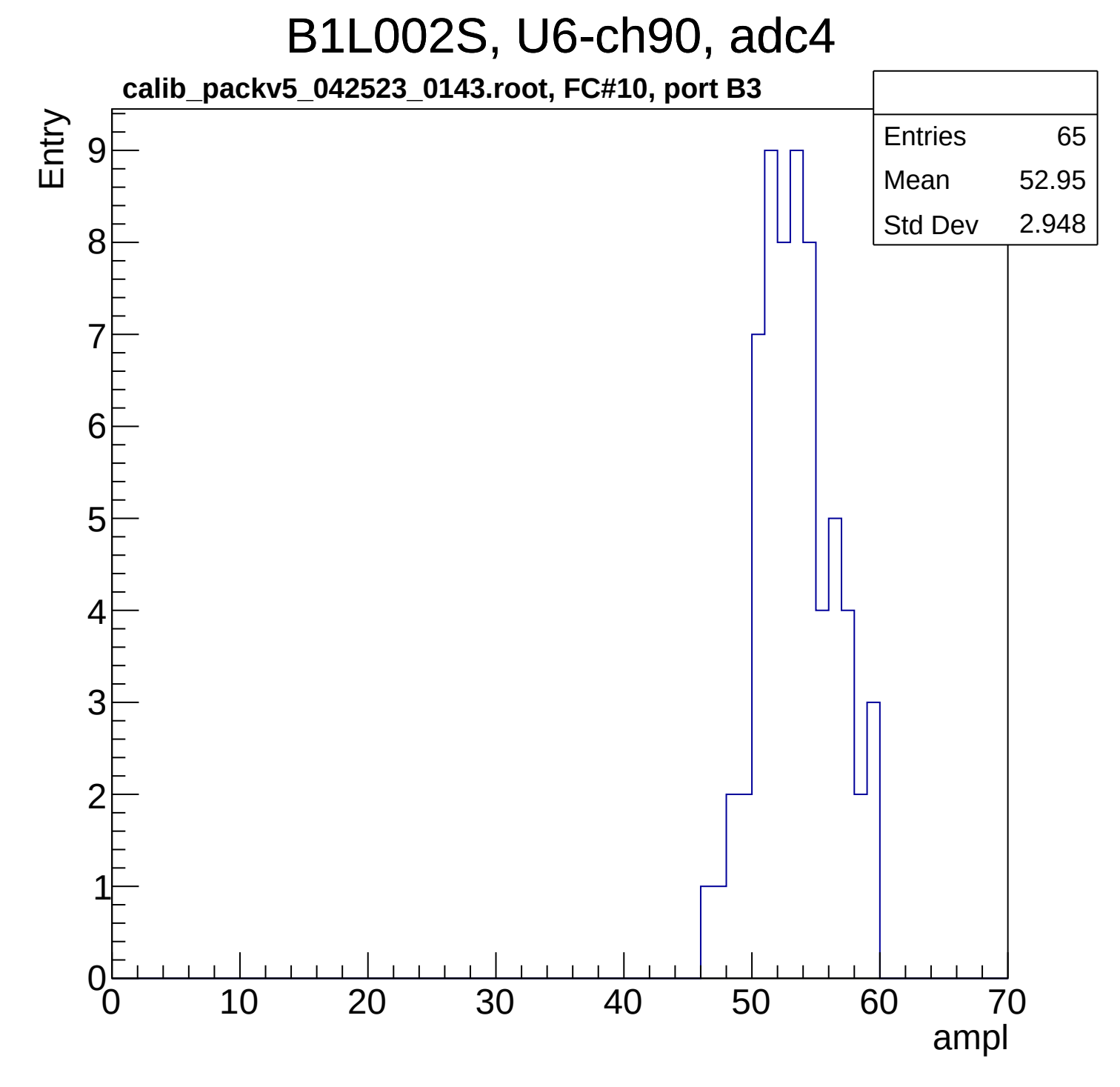

B1L002S, U6-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

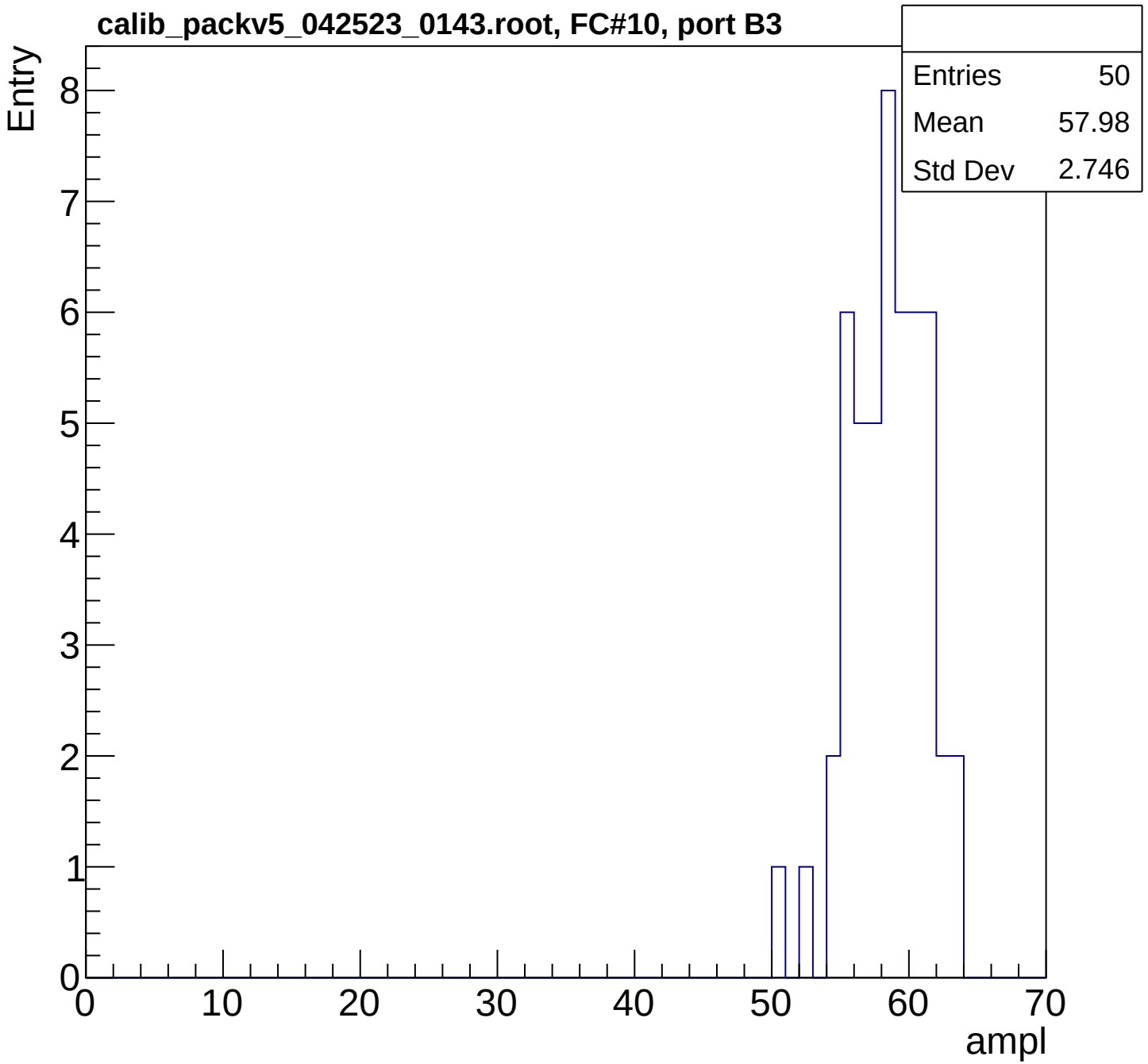

B1L002S, U6-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 27.85
Std Dev 2.789
Gaus mean : 27.9675
Gaus Width: 3.4863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

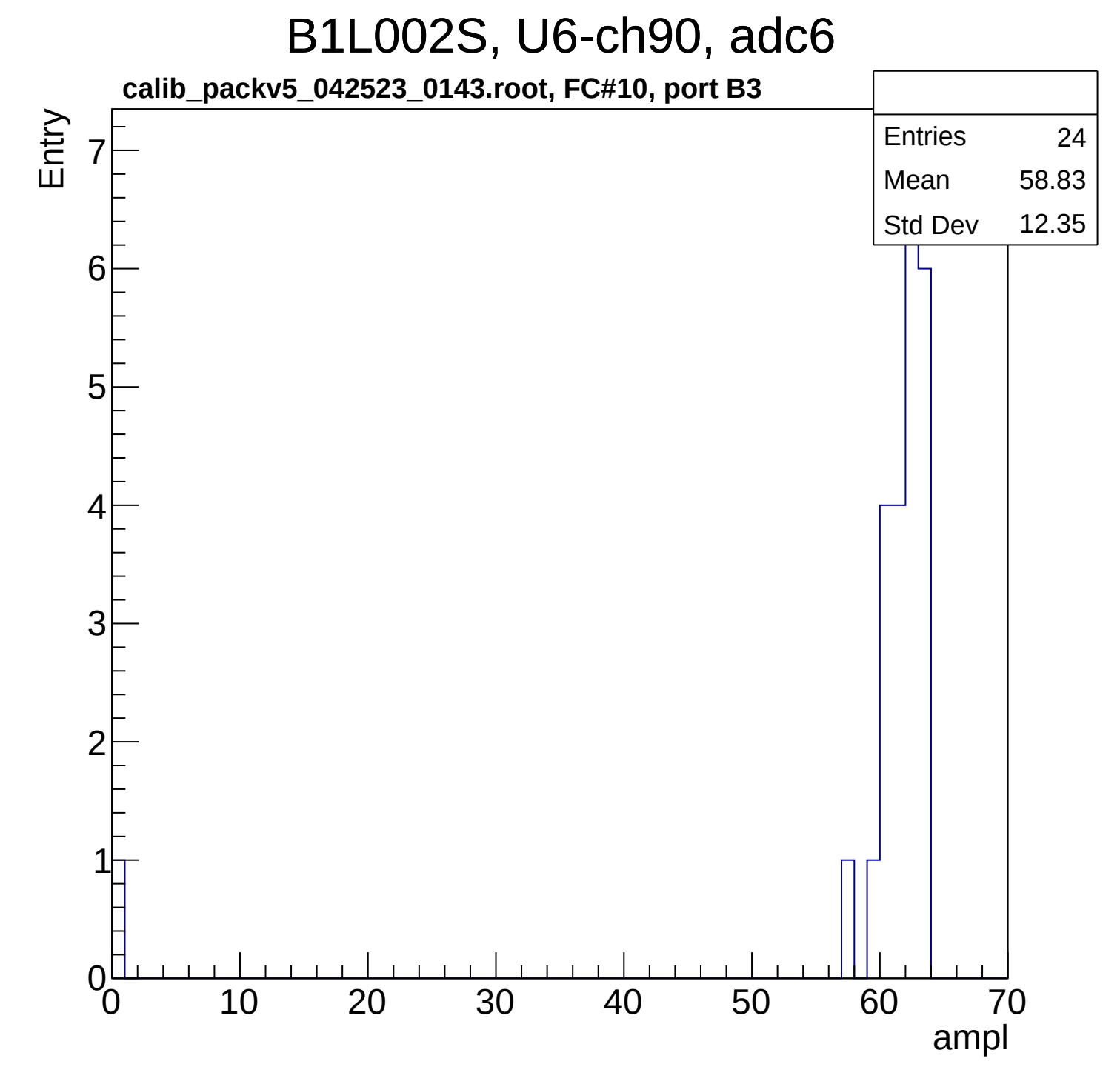

B1L002S, U6-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.02
Std Dev 3.468
Gaus mean : 34.9079
Gaus Width: 3.8113
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

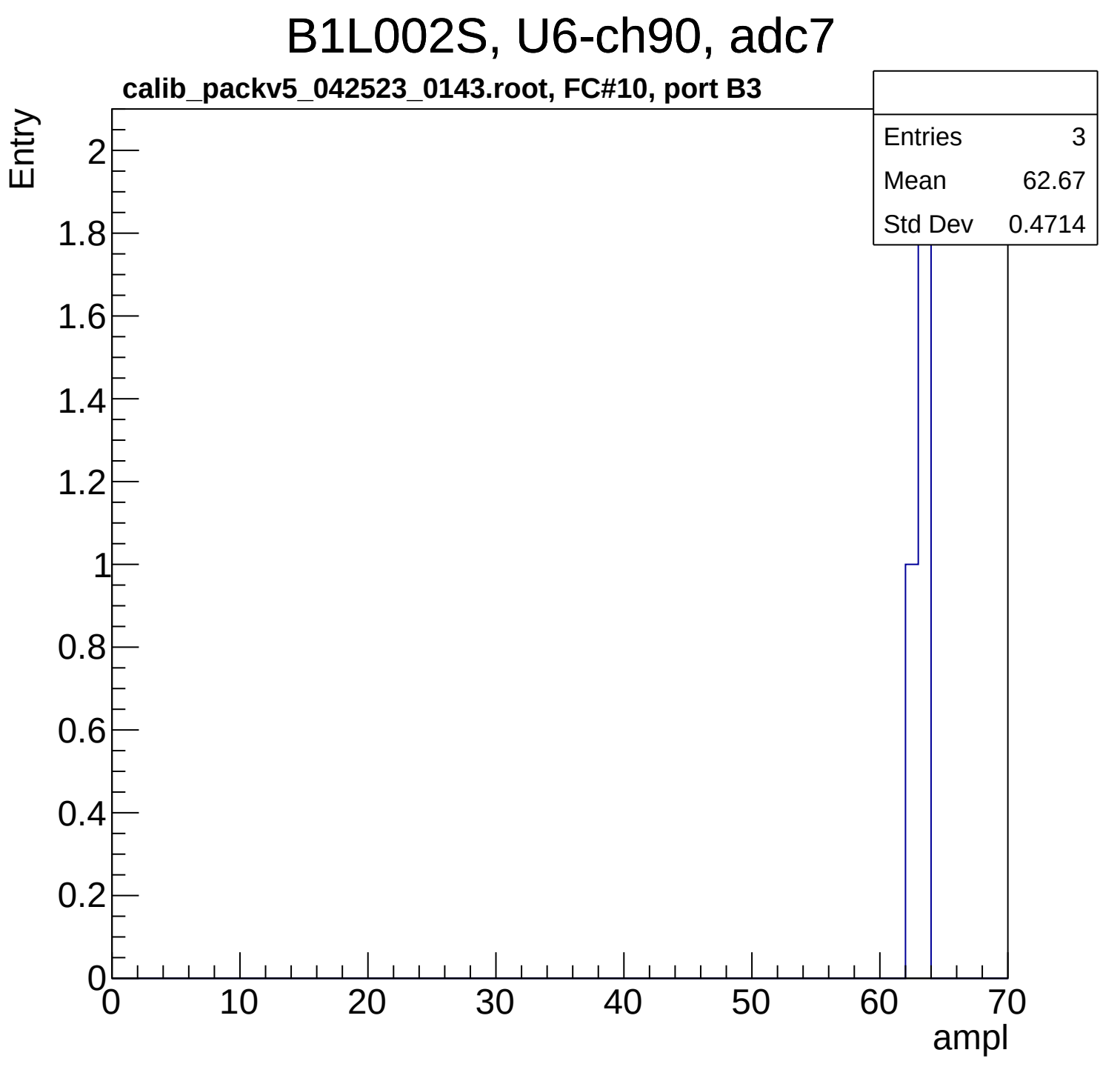

B1L002S, U6-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.49
Std Dev 3.267
Gaus mean : 41.2841
Gaus Width: 3.1024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

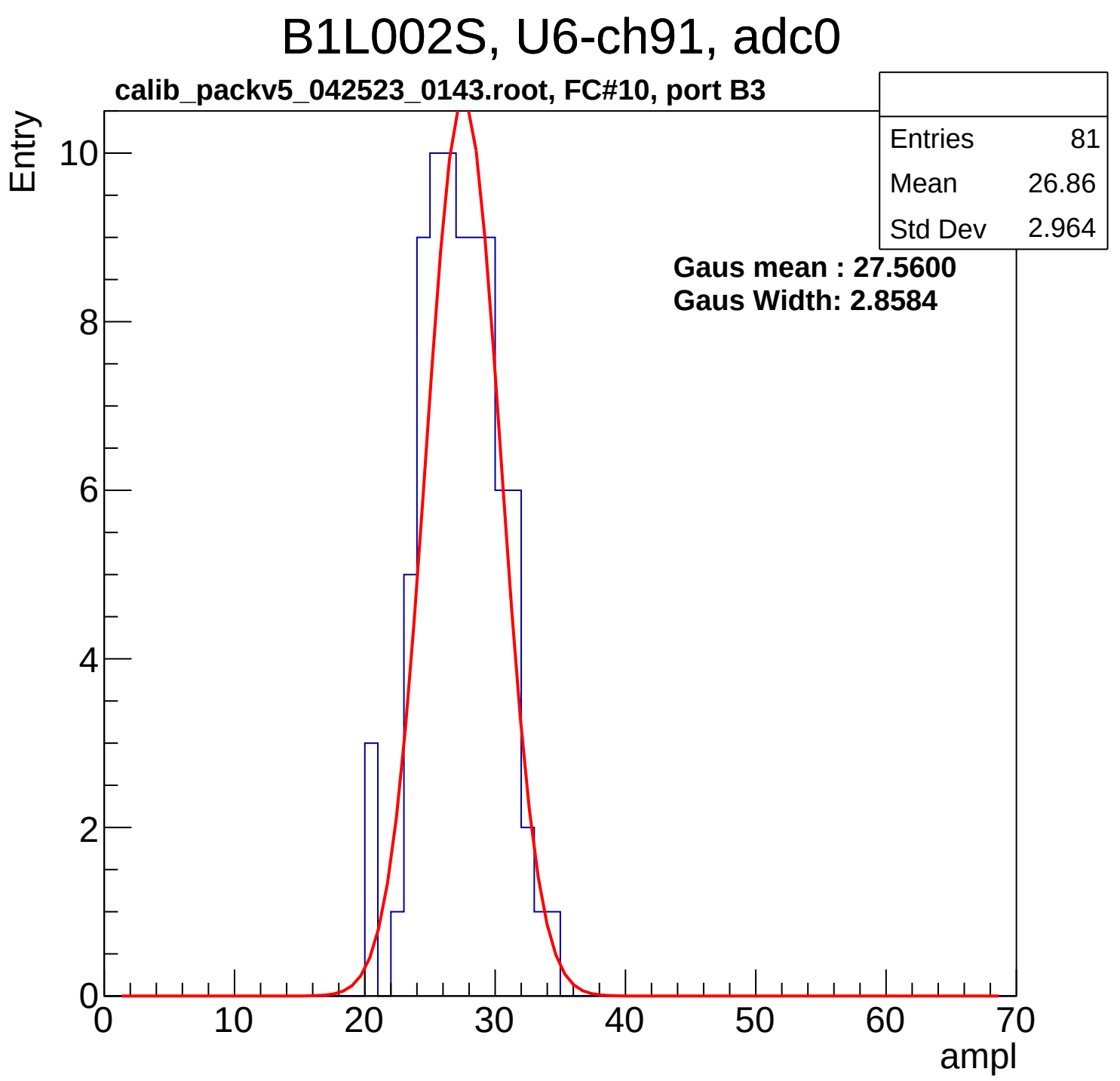

B1L002S, U6-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.29
Std Dev 2.889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

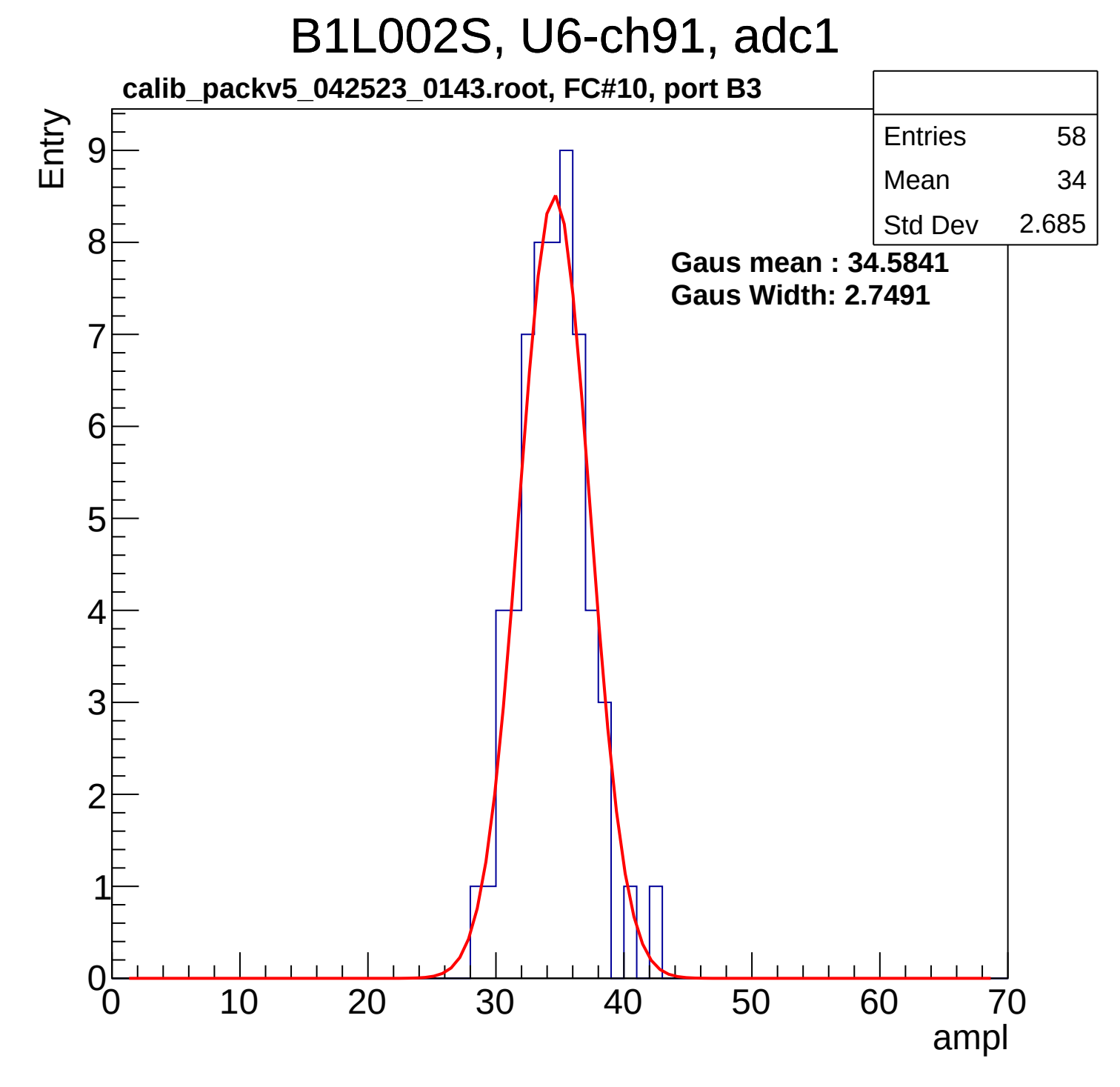

B1L002S, U6-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.95
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

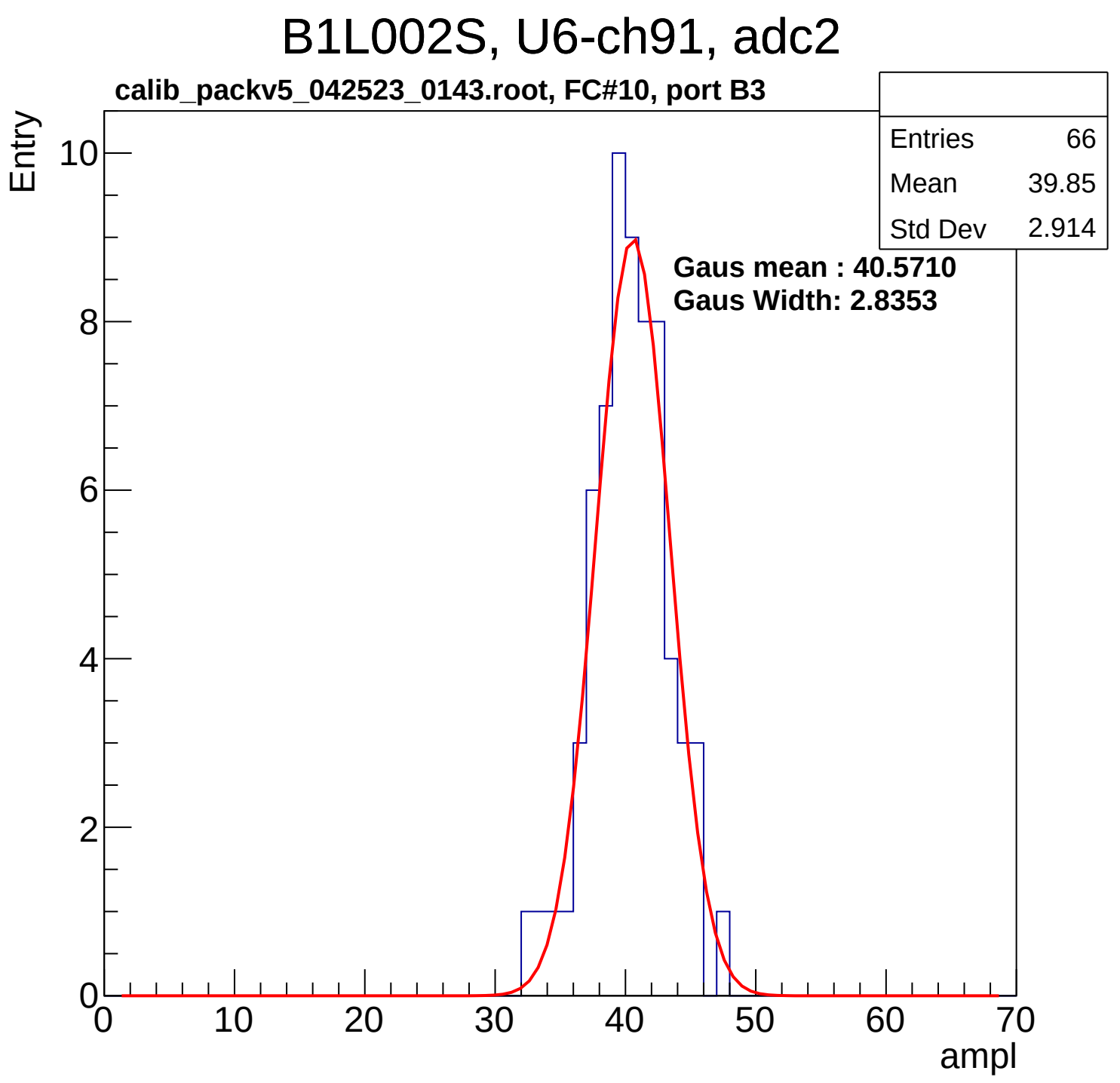

B1L002S, U6-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.98
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

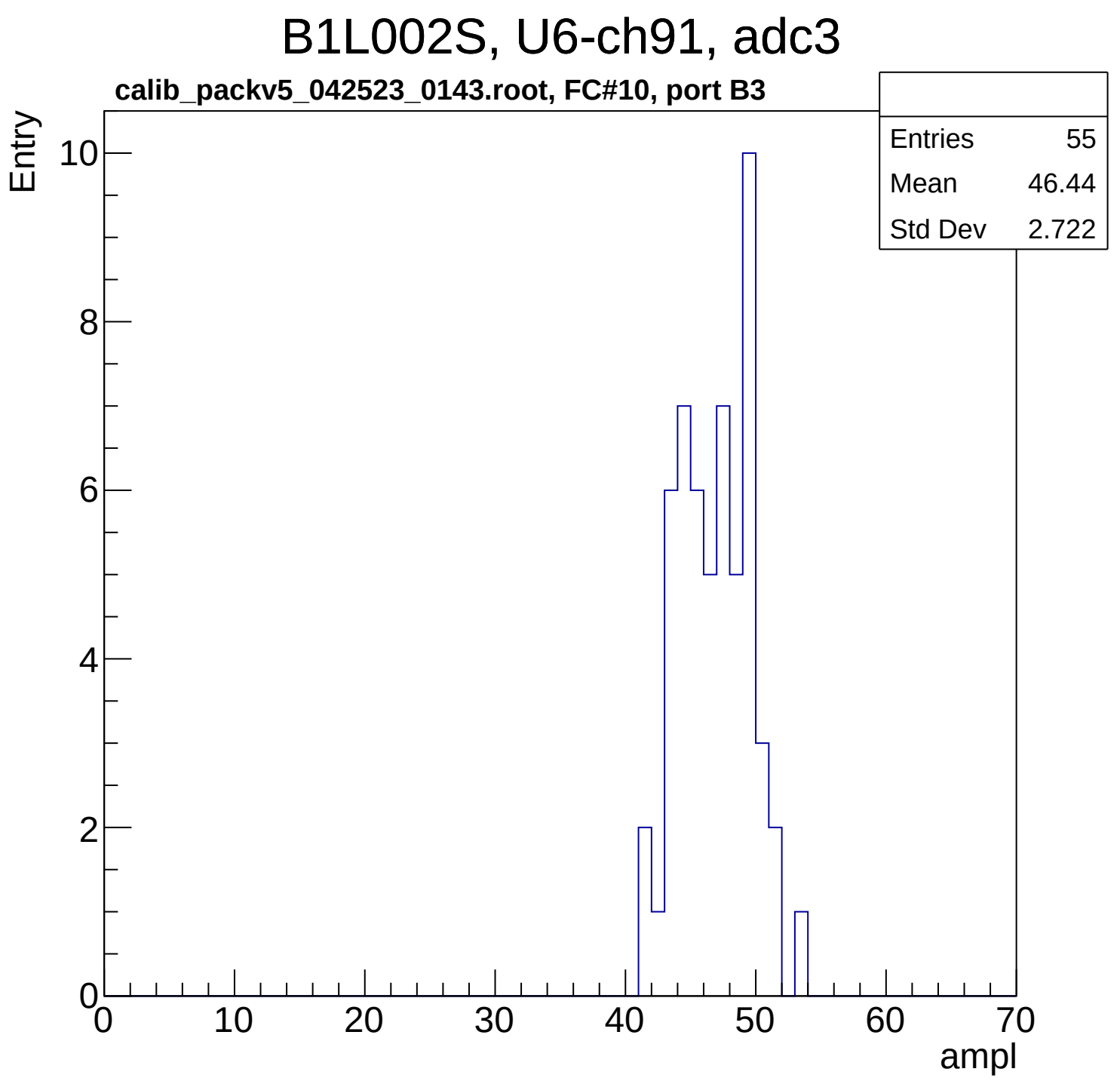

B1L002S, U6-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.83
Std Dev 12.35
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

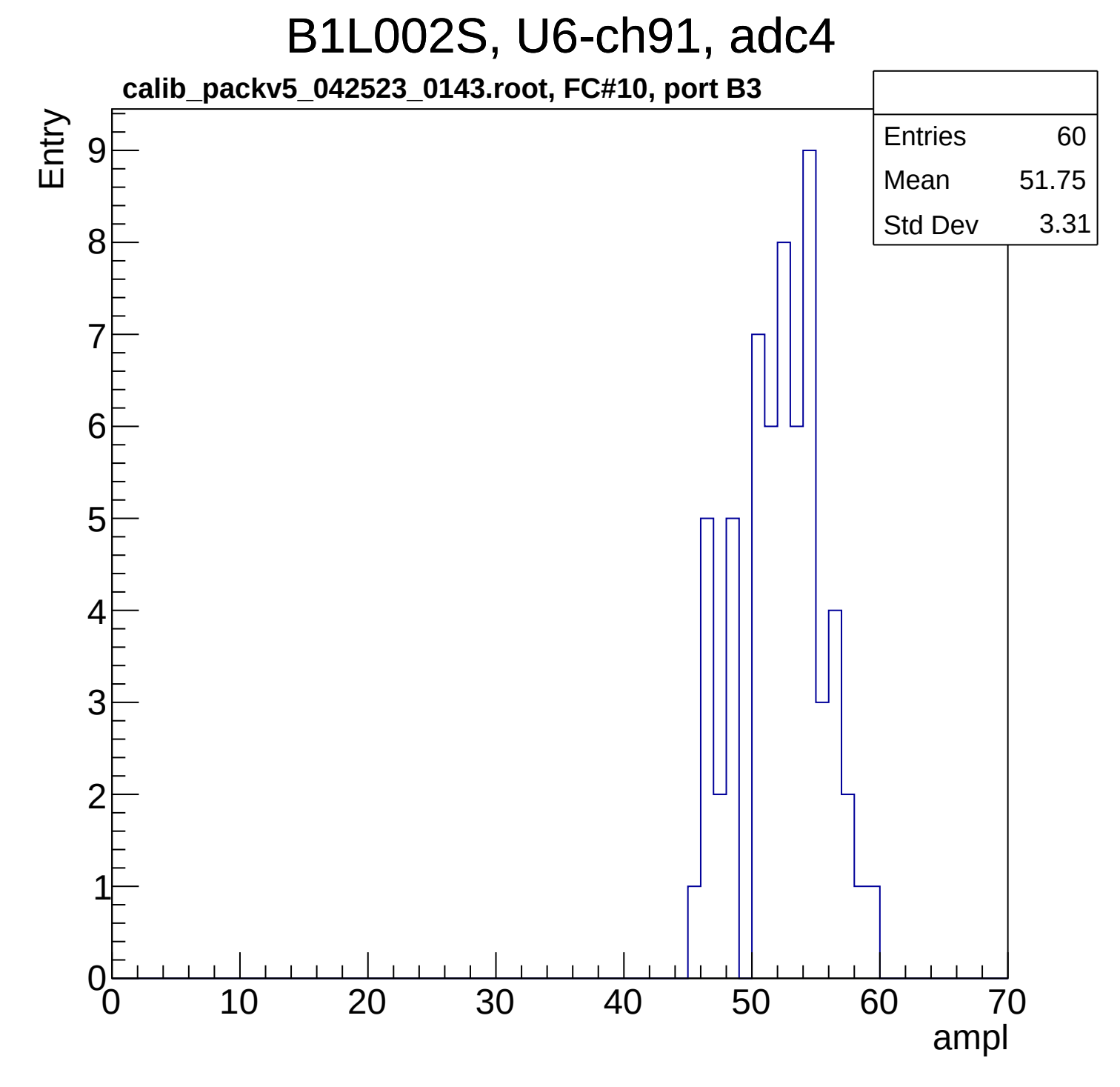

B1L002S, U6-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

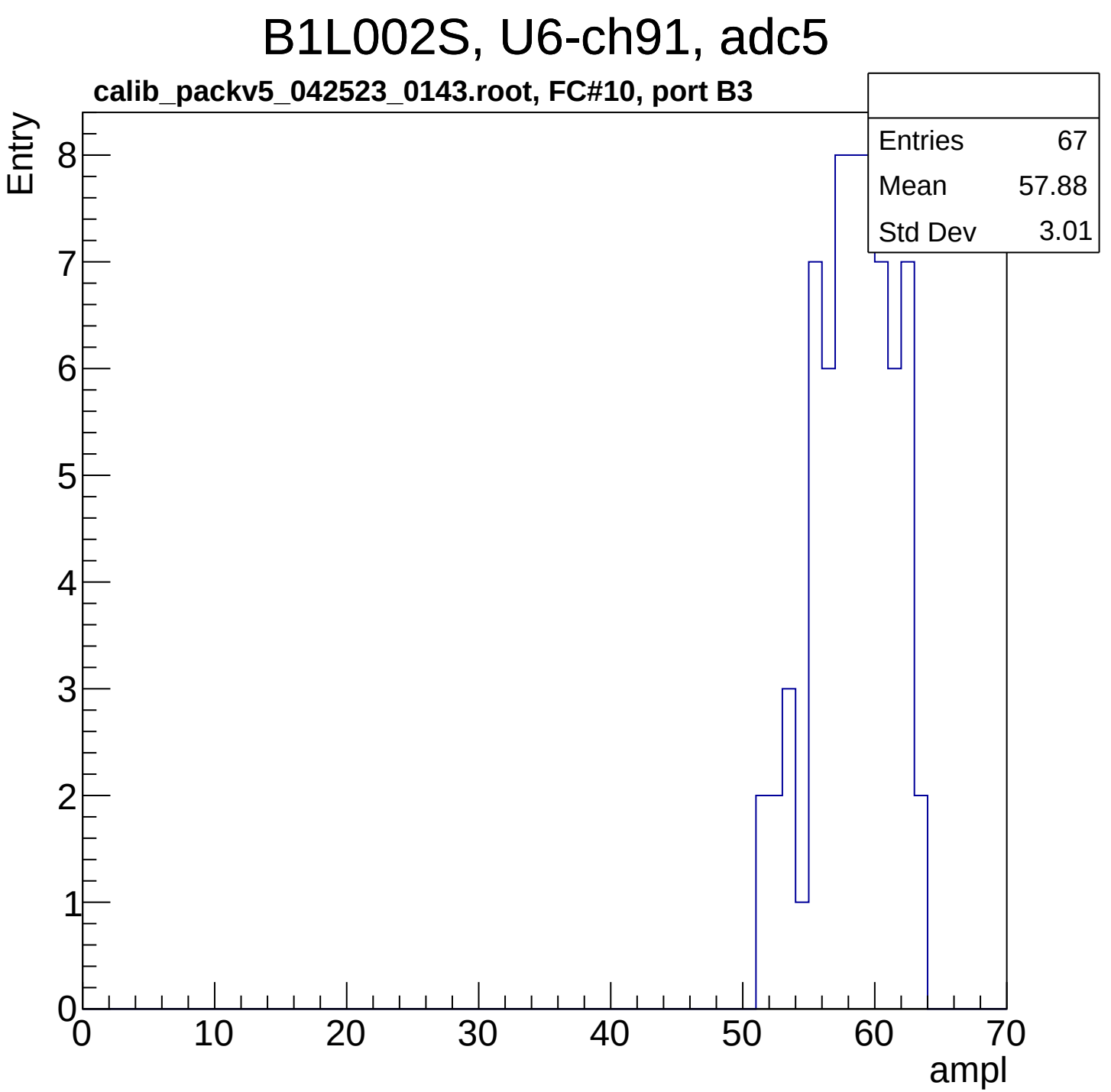

B1L002S, U6-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 26.86
Std Dev 2.964
Gaus mean : 27.5600
Gaus Width: 2.8584
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

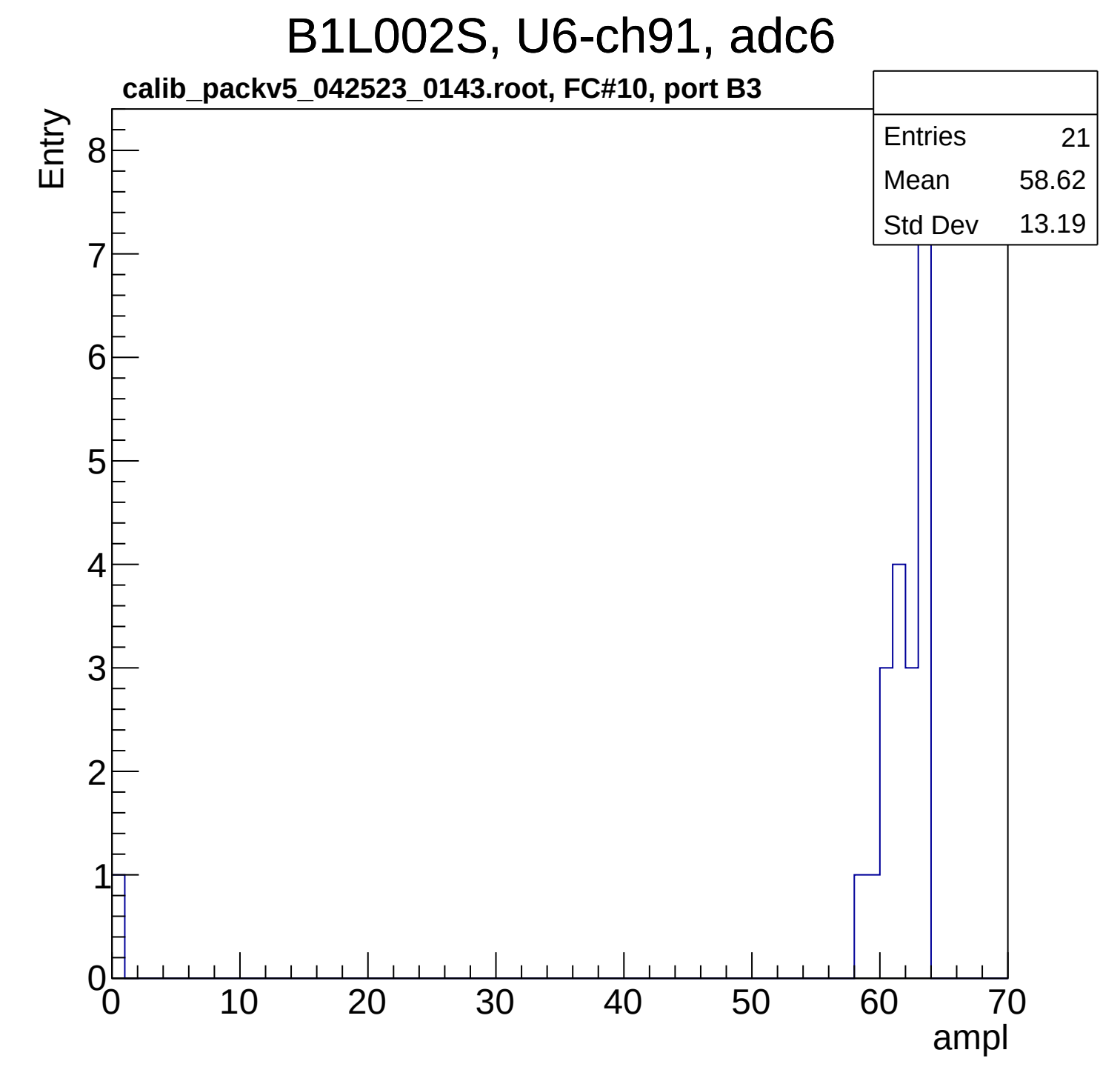

B1L002S, U6-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34
Std Dev 2.685
Gaus mean : 34.5841
Gaus Width: 2.7491
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

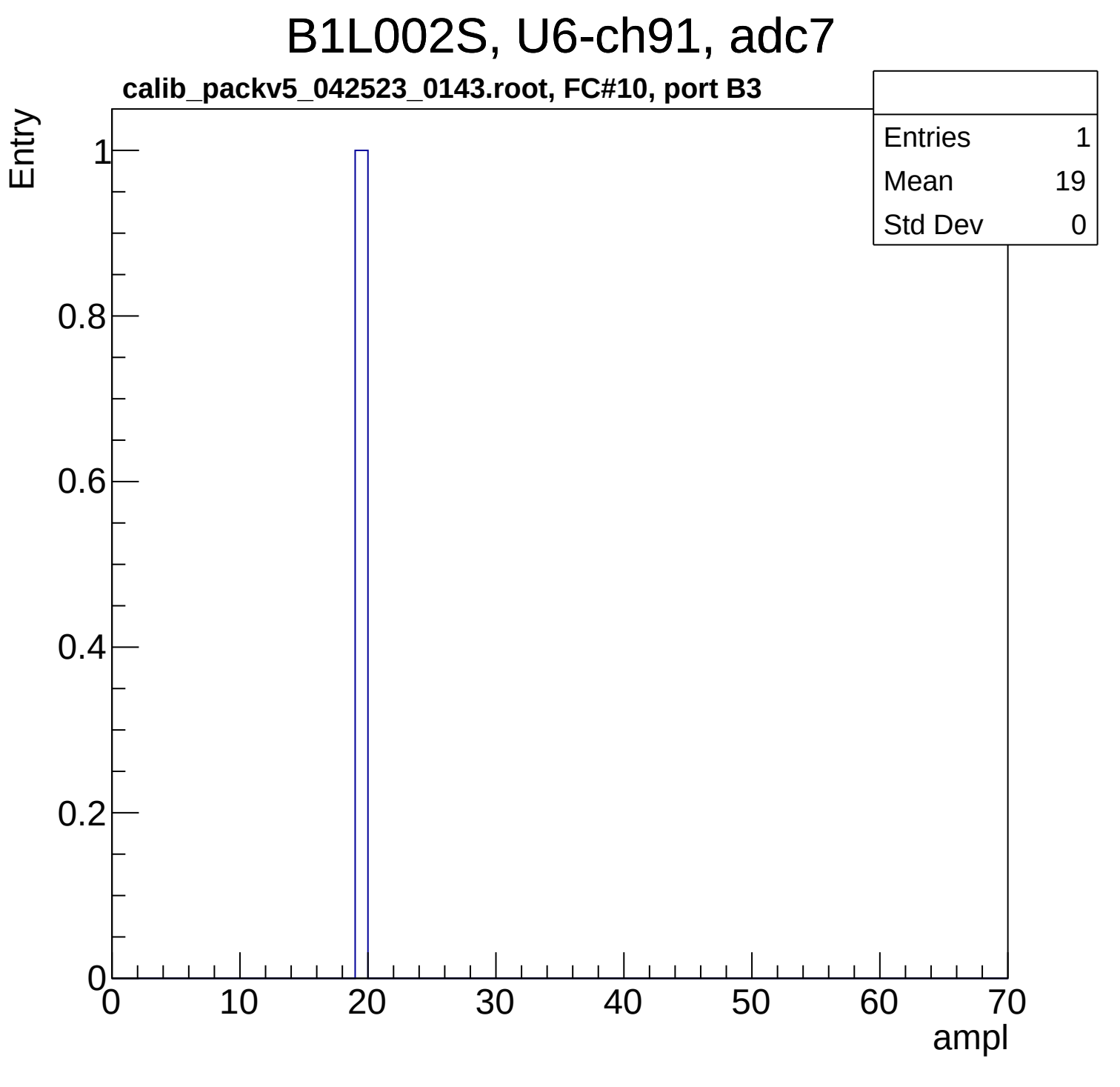

B1L002S, U6-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 39.85
Std Dev 2.914
Gaus mean : 40.5710
Gaus Width: 2.8353
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

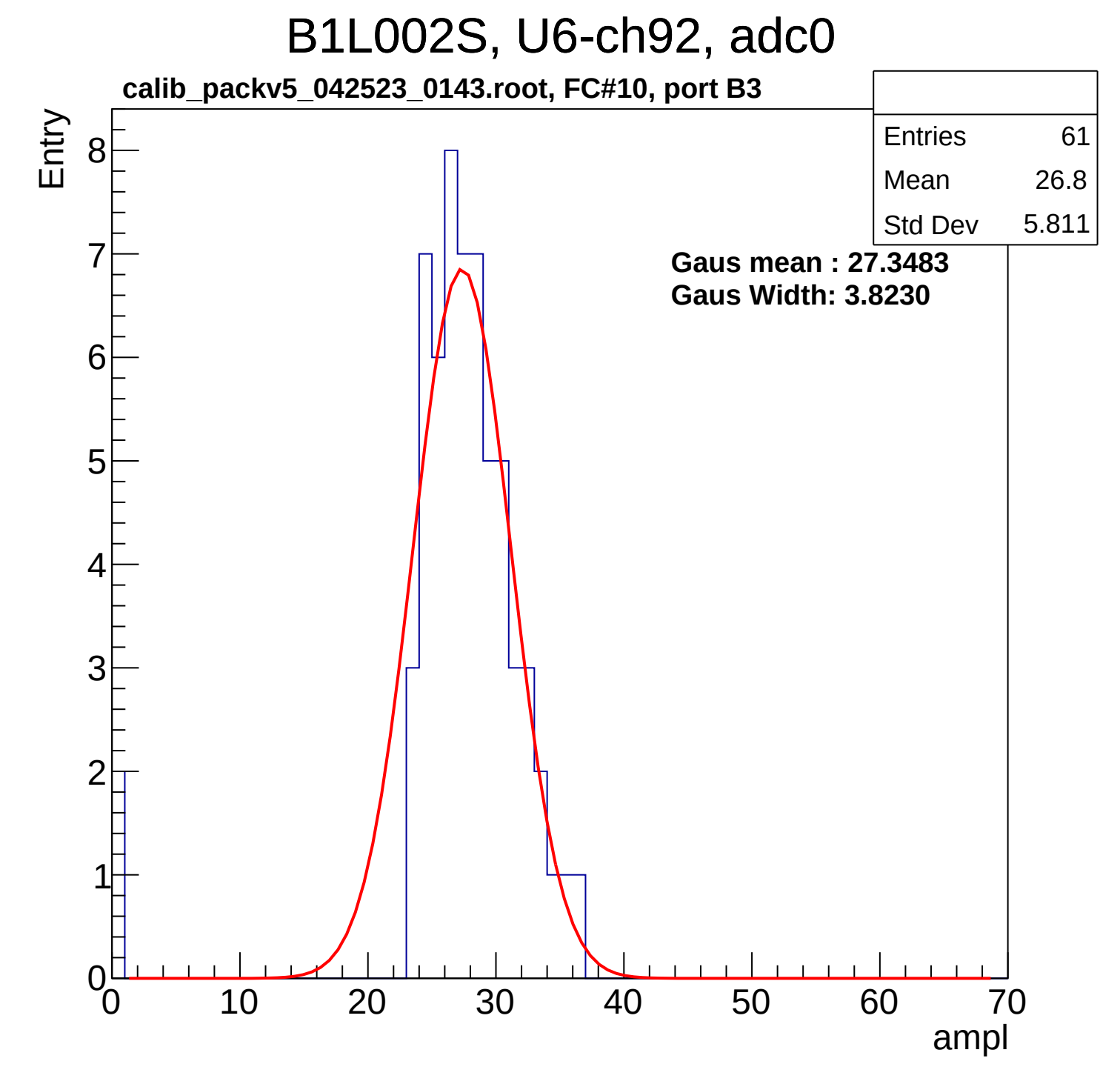

B1L002S, U6-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.44
Std Dev 2.722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

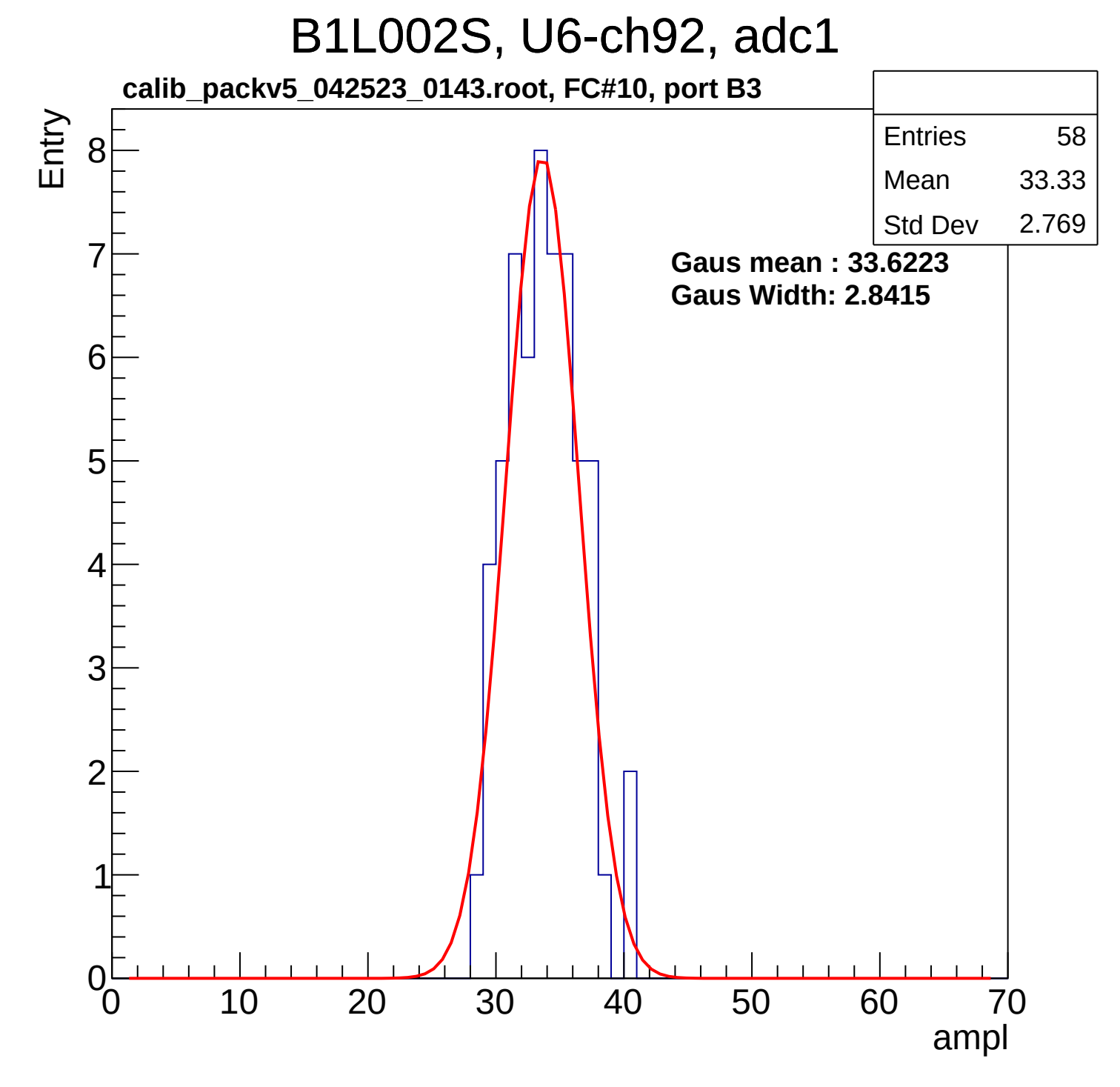

B1L002S, U6-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.75
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

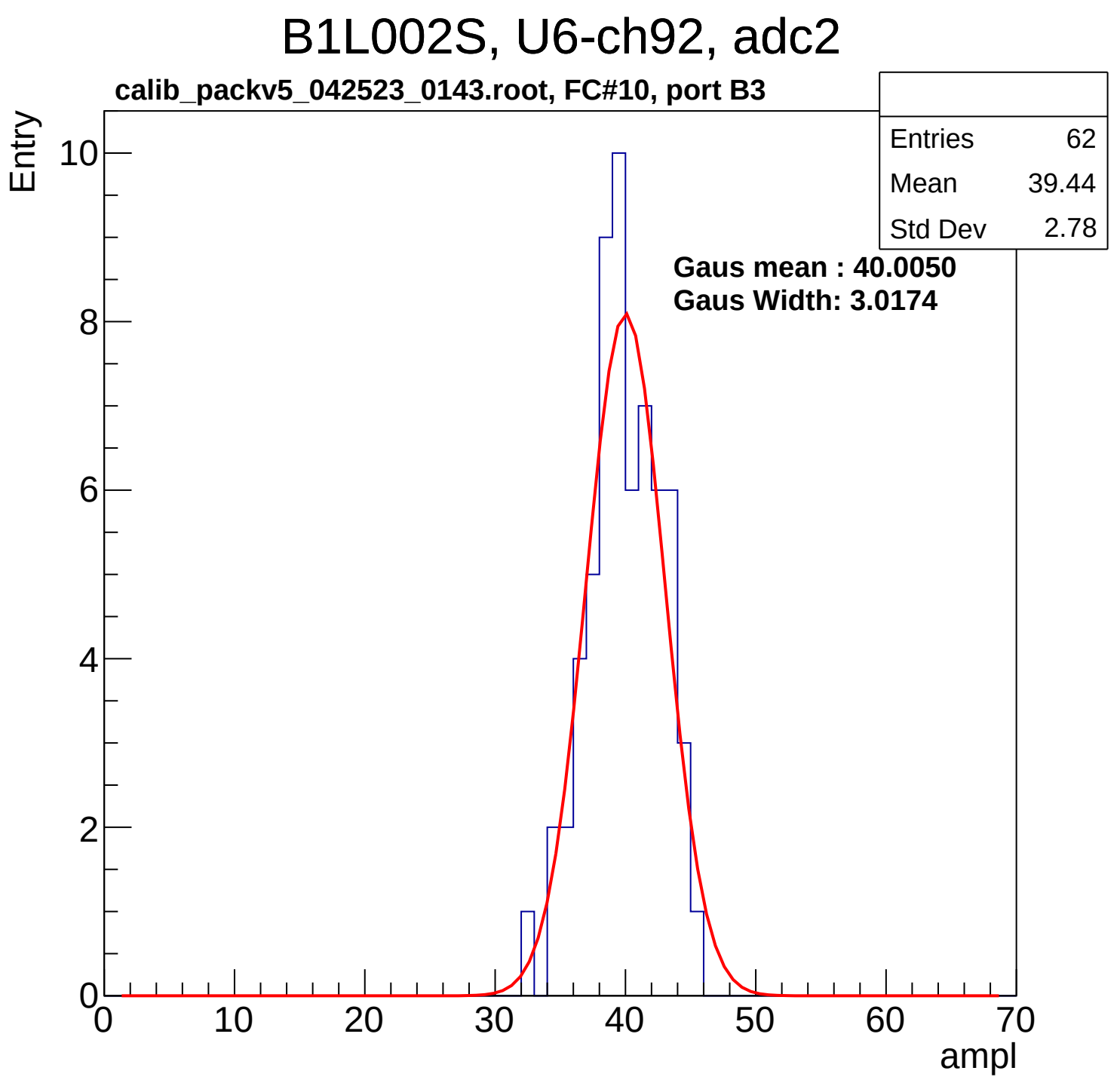

B1L002S, U6-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.88
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

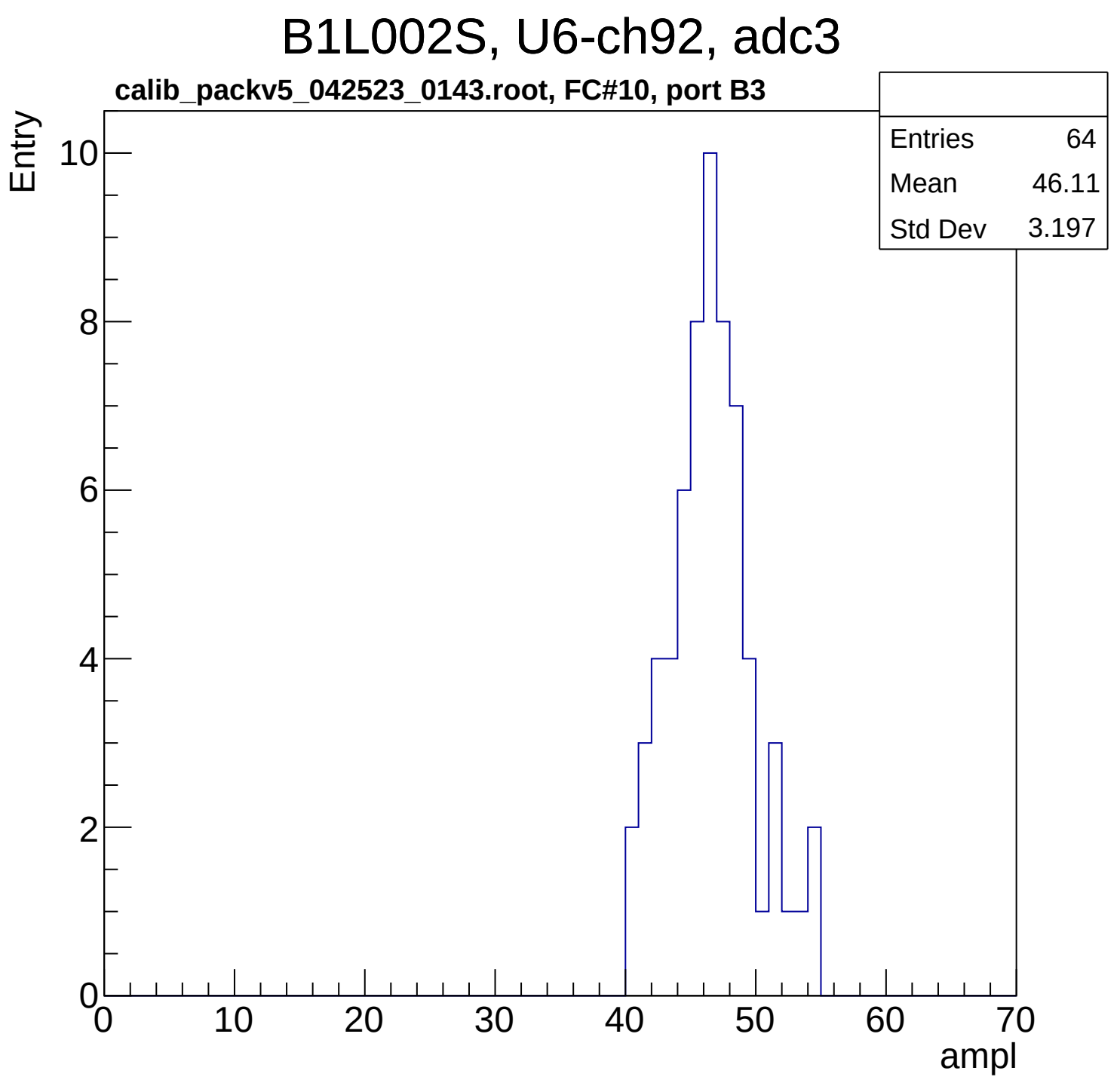

B1L002S, U6-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.62
Std Dev 13.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

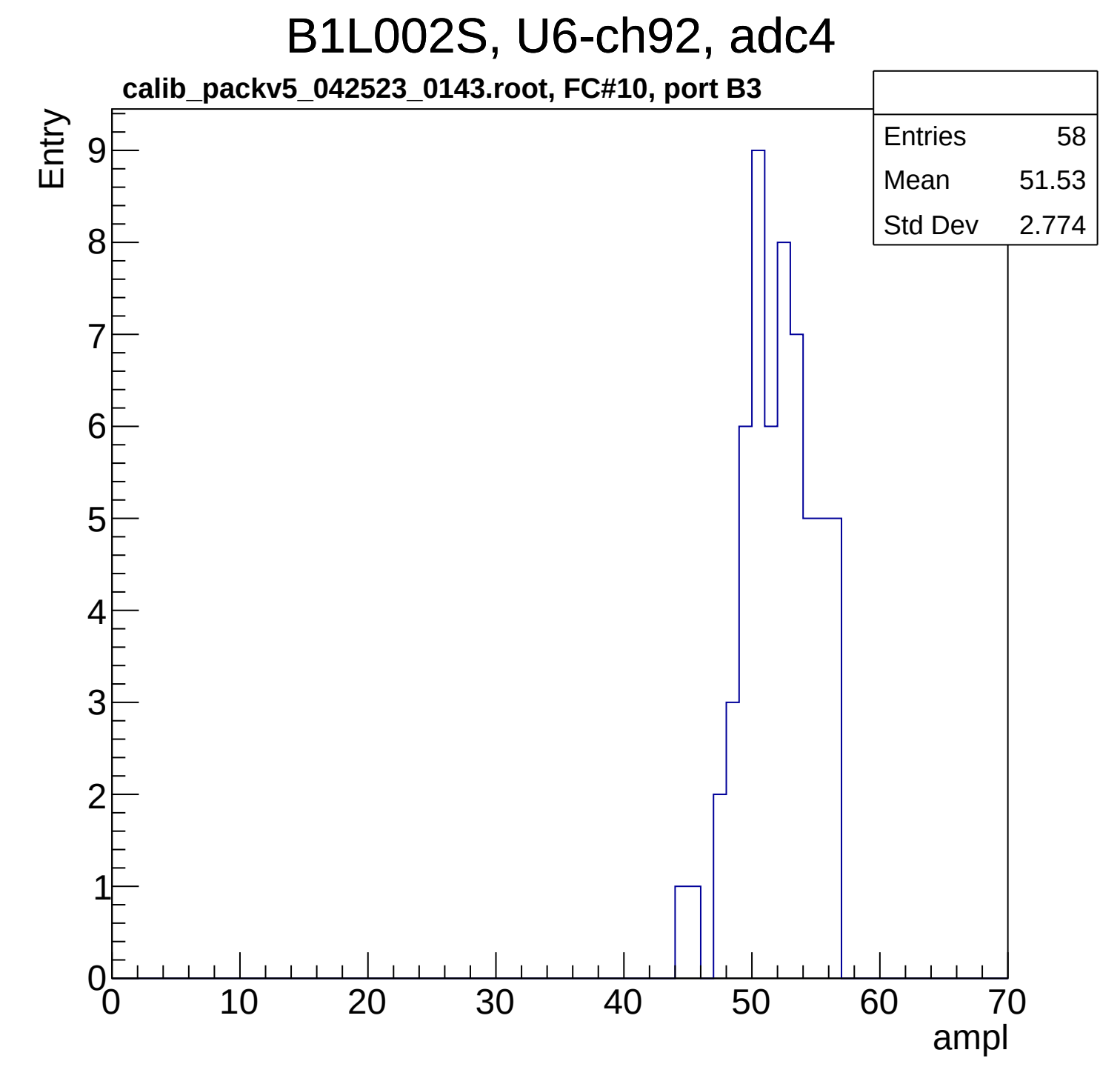

B1L002S, U6-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

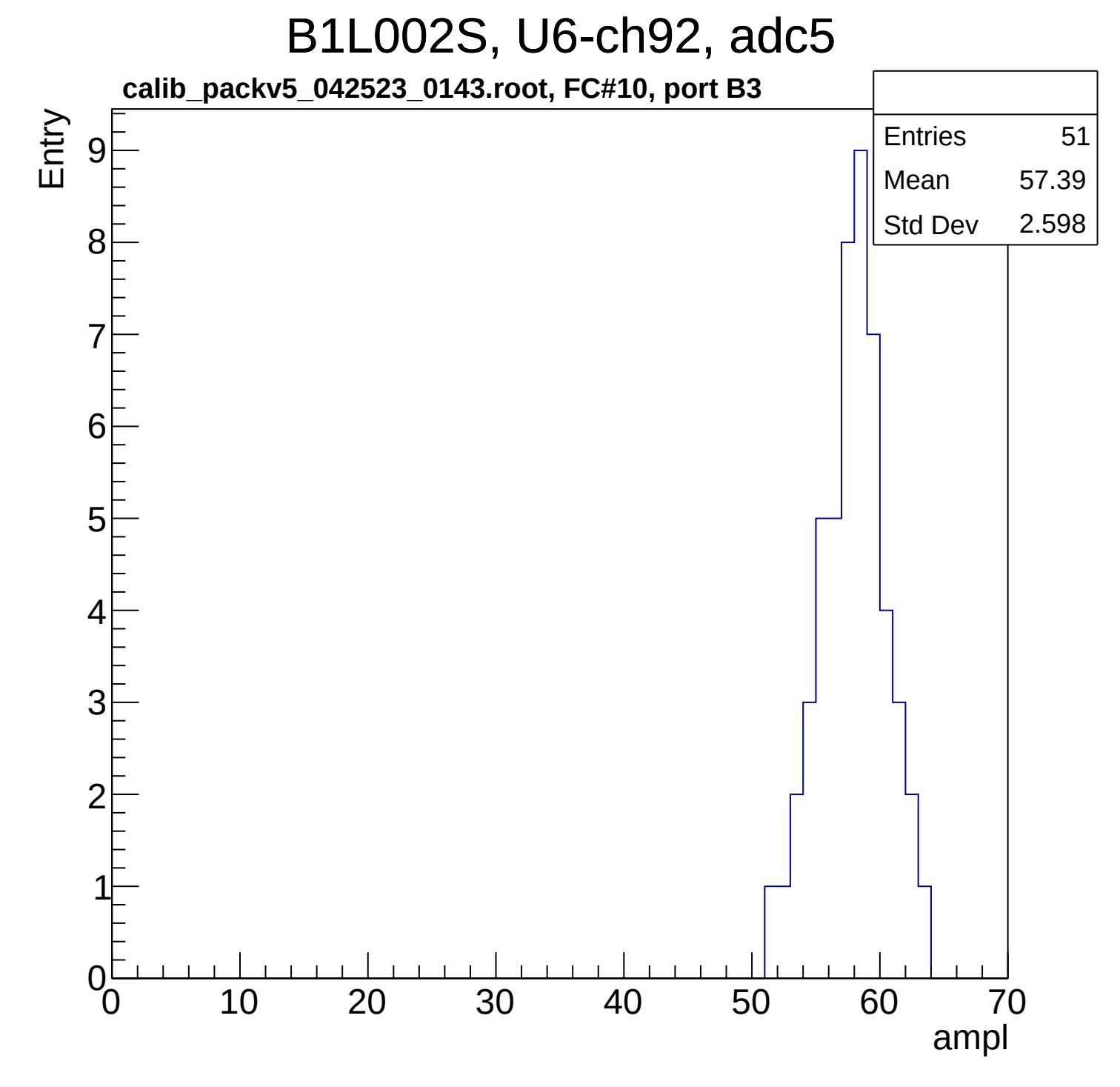

B1L002S, U6-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.8
Std Dev 5.811
Gaus mean : 27.3483
Gaus Width: 3.8230
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

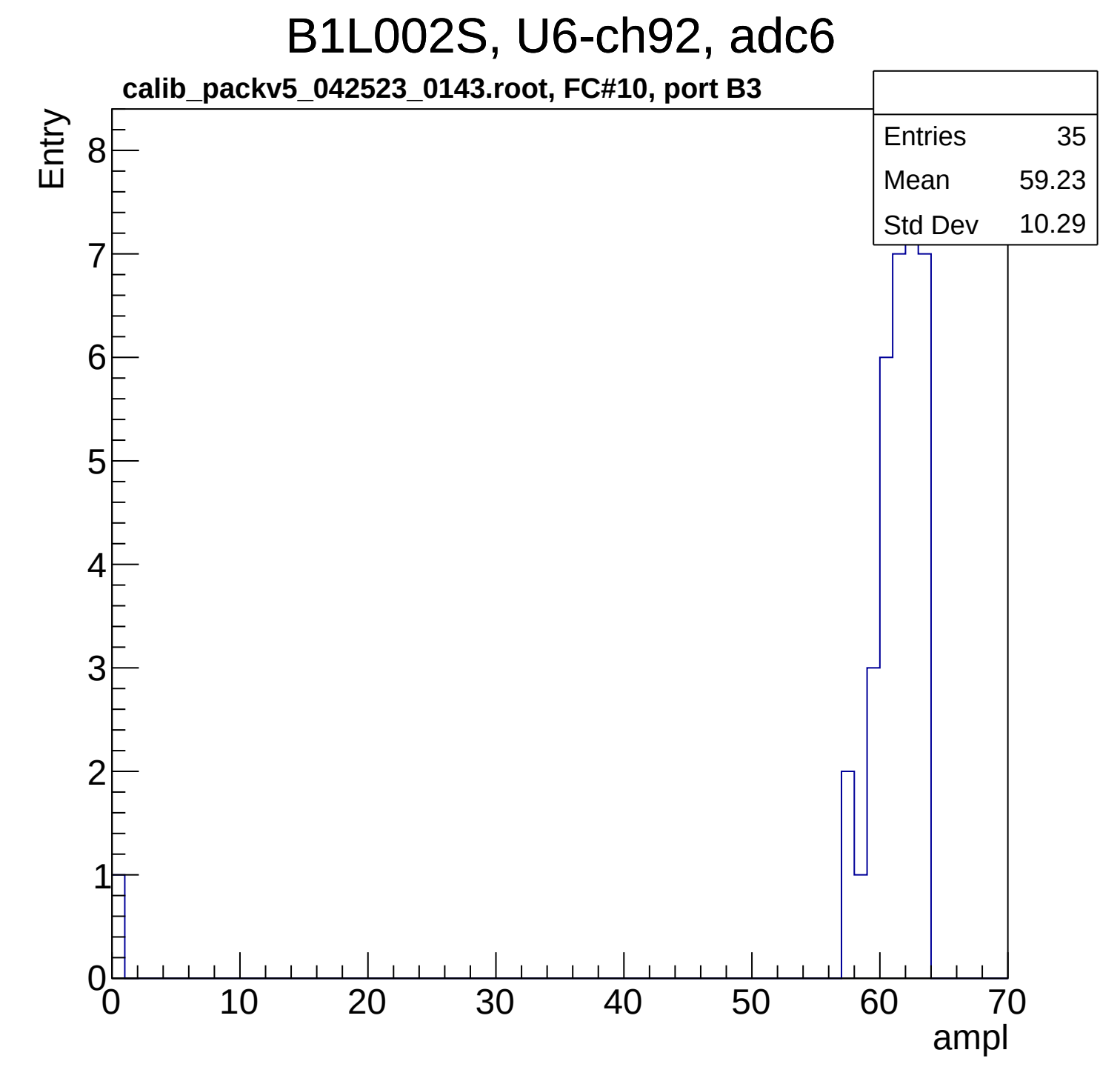

B1L002S, U6-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.33
Std Dev 2.769
Gaus mean : 33.6223
Gaus Width: 2.8415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

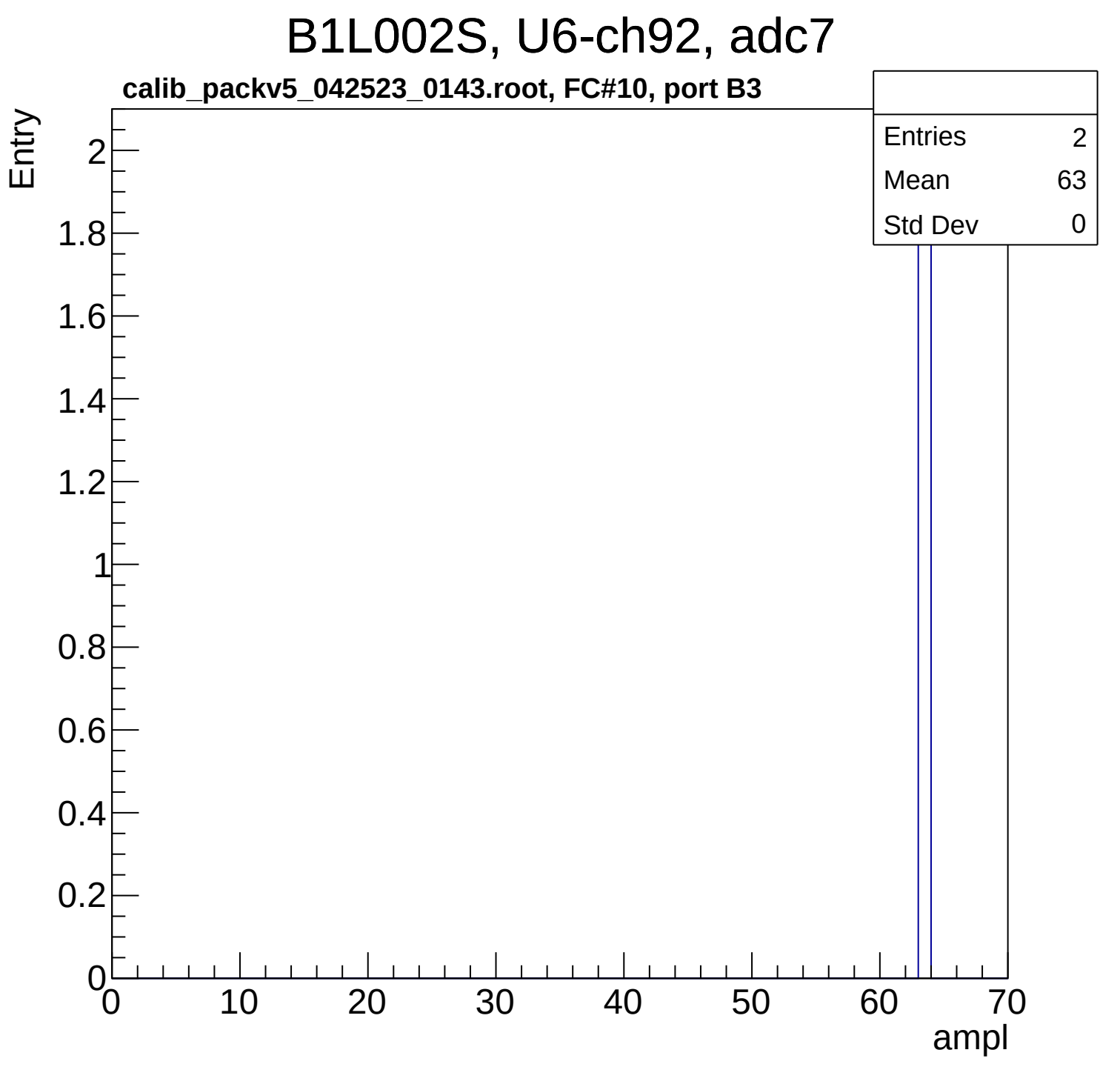

B1L002S, U6-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 39.44
Std Dev 2.78
Gaus mean : 40.0050
Gaus Width: 3.0174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

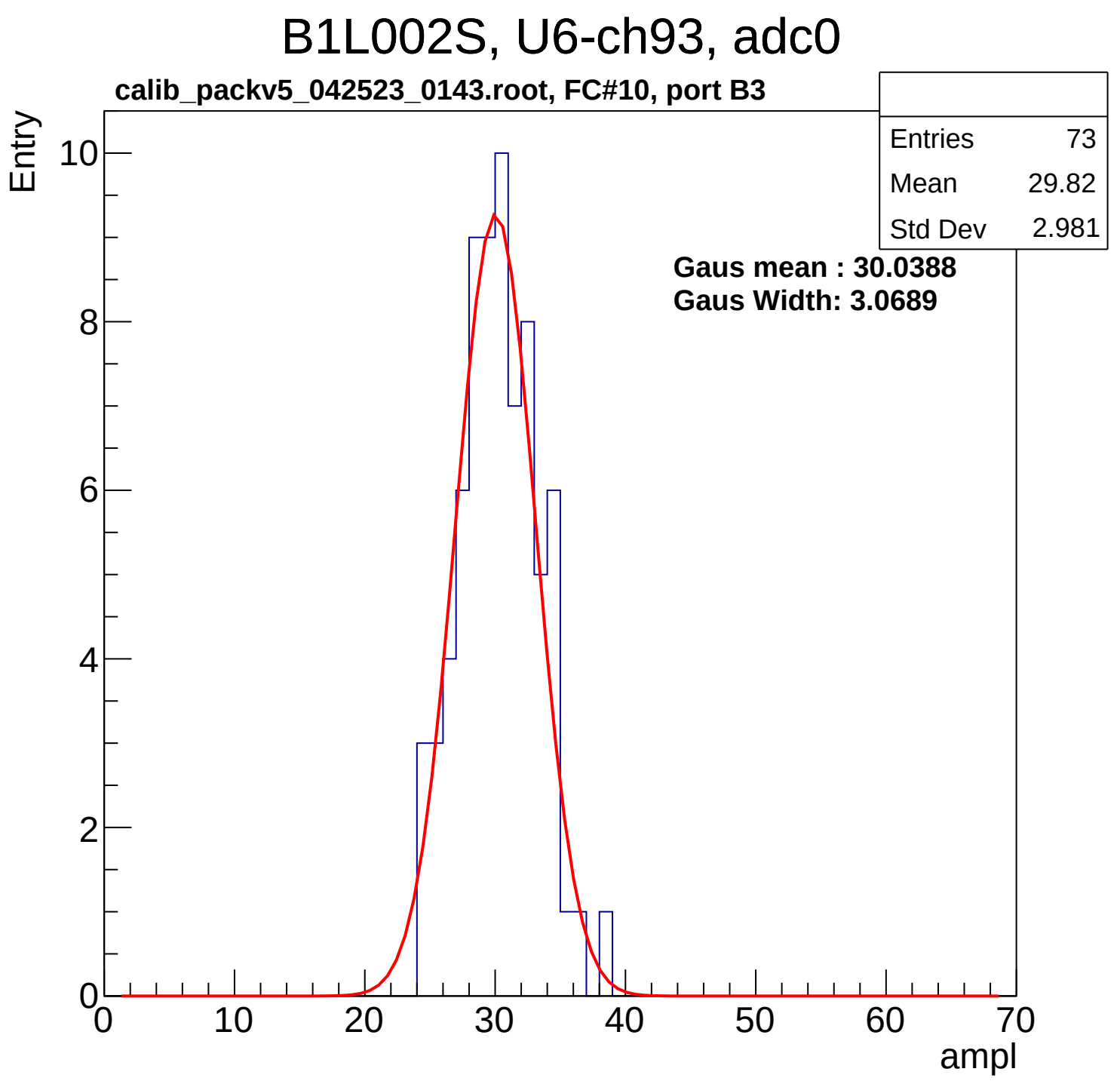

B1L002S, U6-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.11
Std Dev 3.197
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

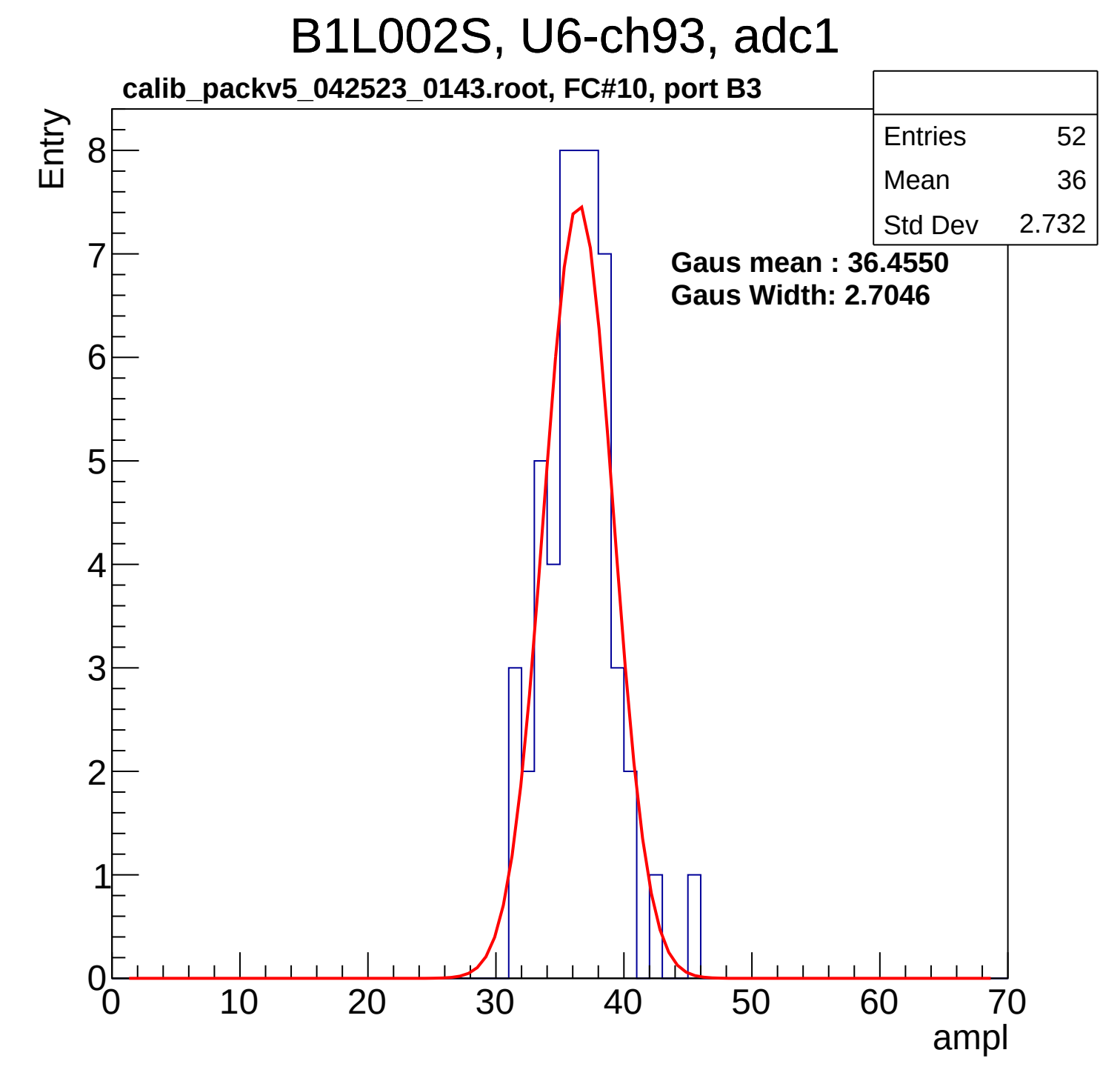

B1L002S, U6-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 51.53
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

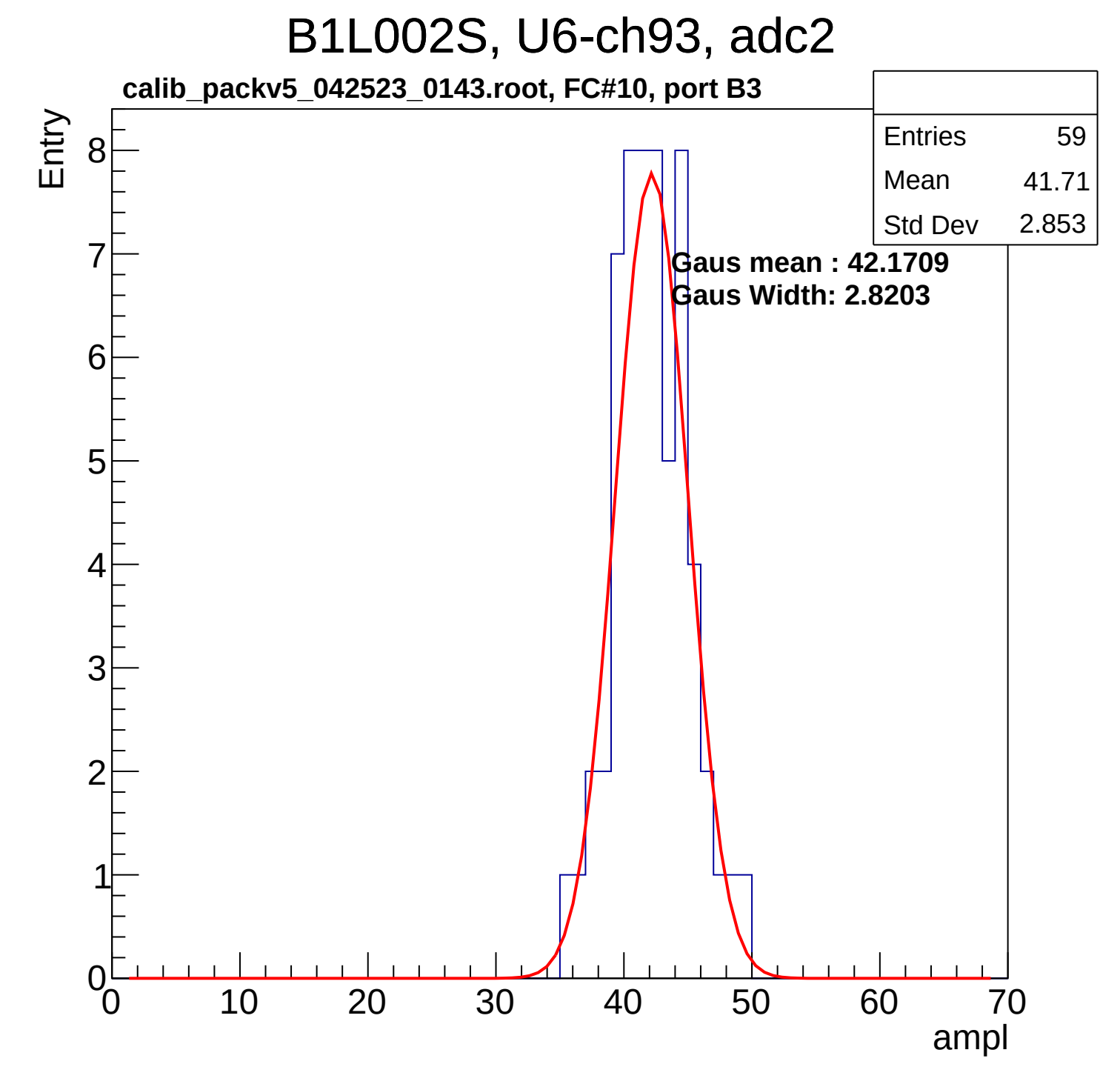

B1L002S, U6-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.39
Std Dev 2.598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

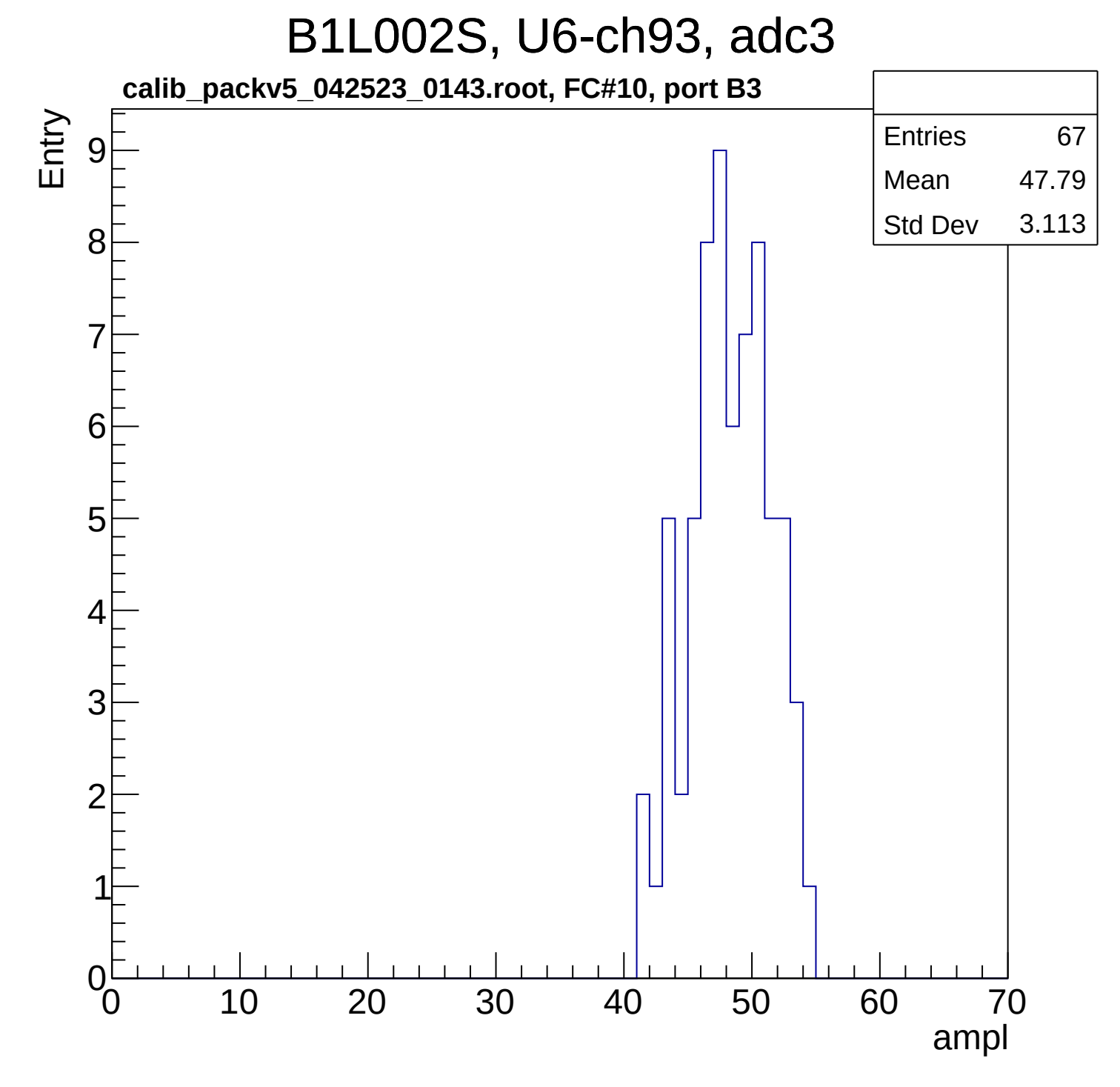

B1L002S, U6-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 59.23
Std Dev 10.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

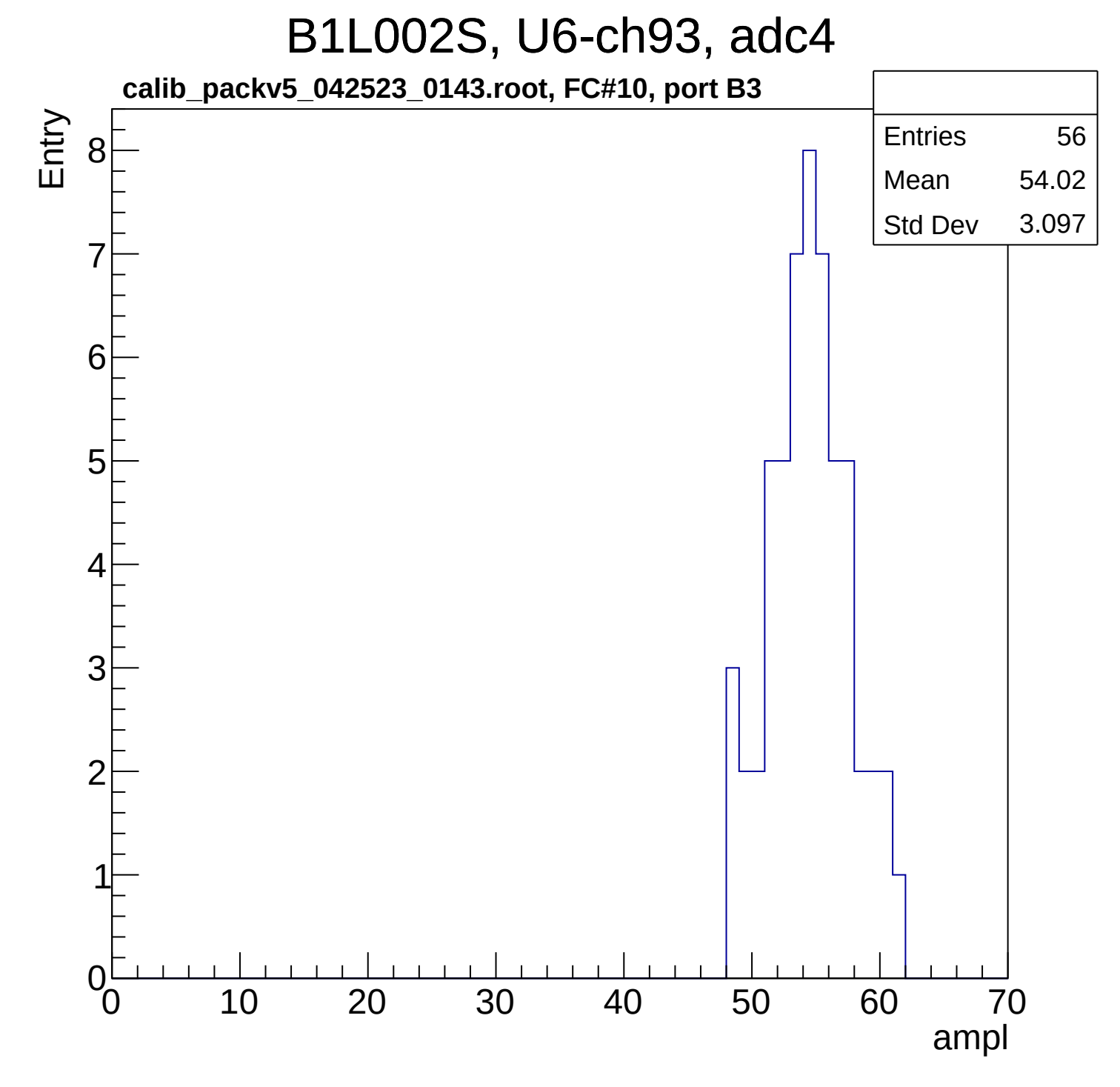

B1L002S, U6-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

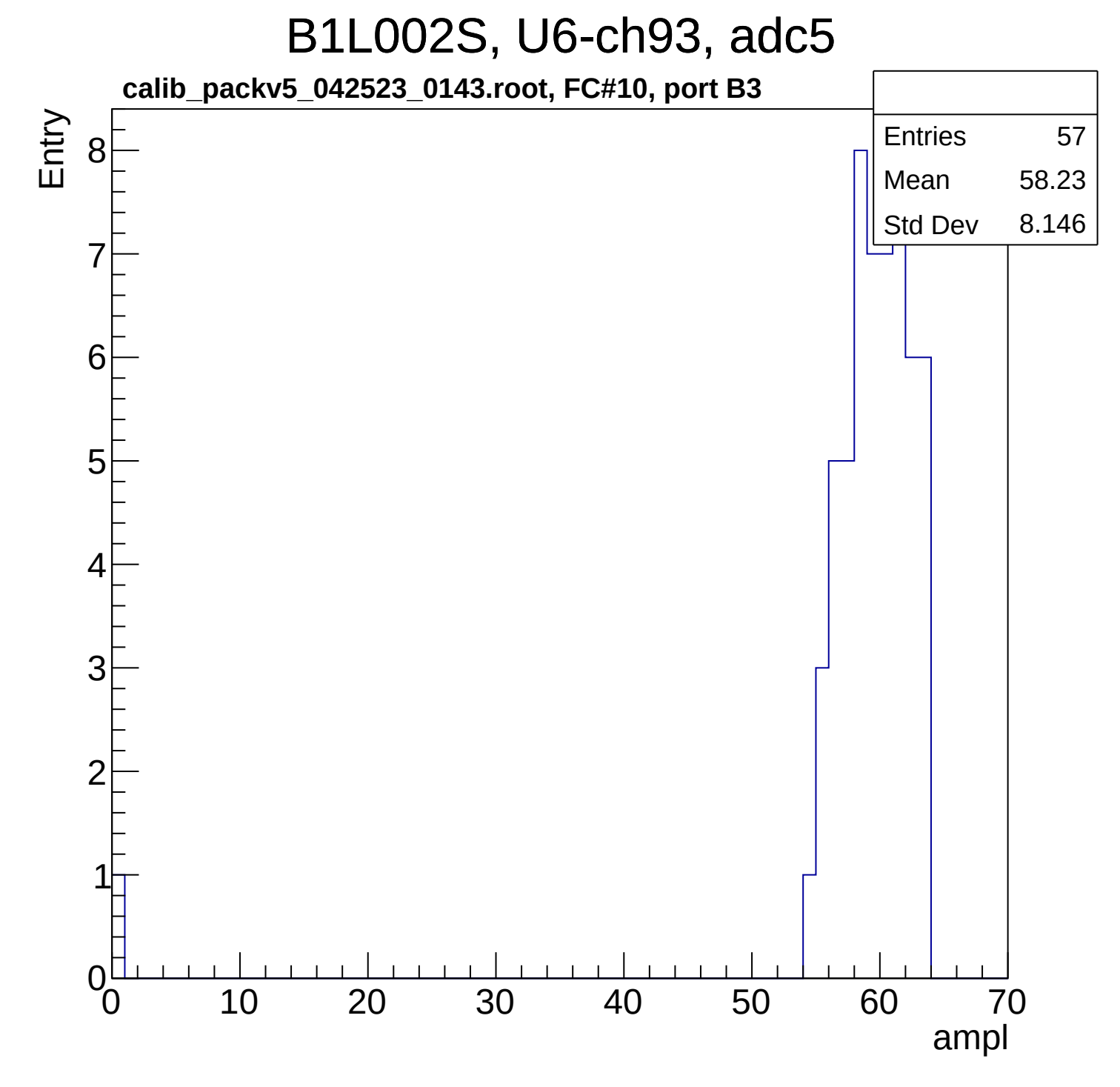

B1L002S, U6-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.82
Std Dev 2.981
Gaus mean : 30.0388
Gaus Width: 3.0689
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

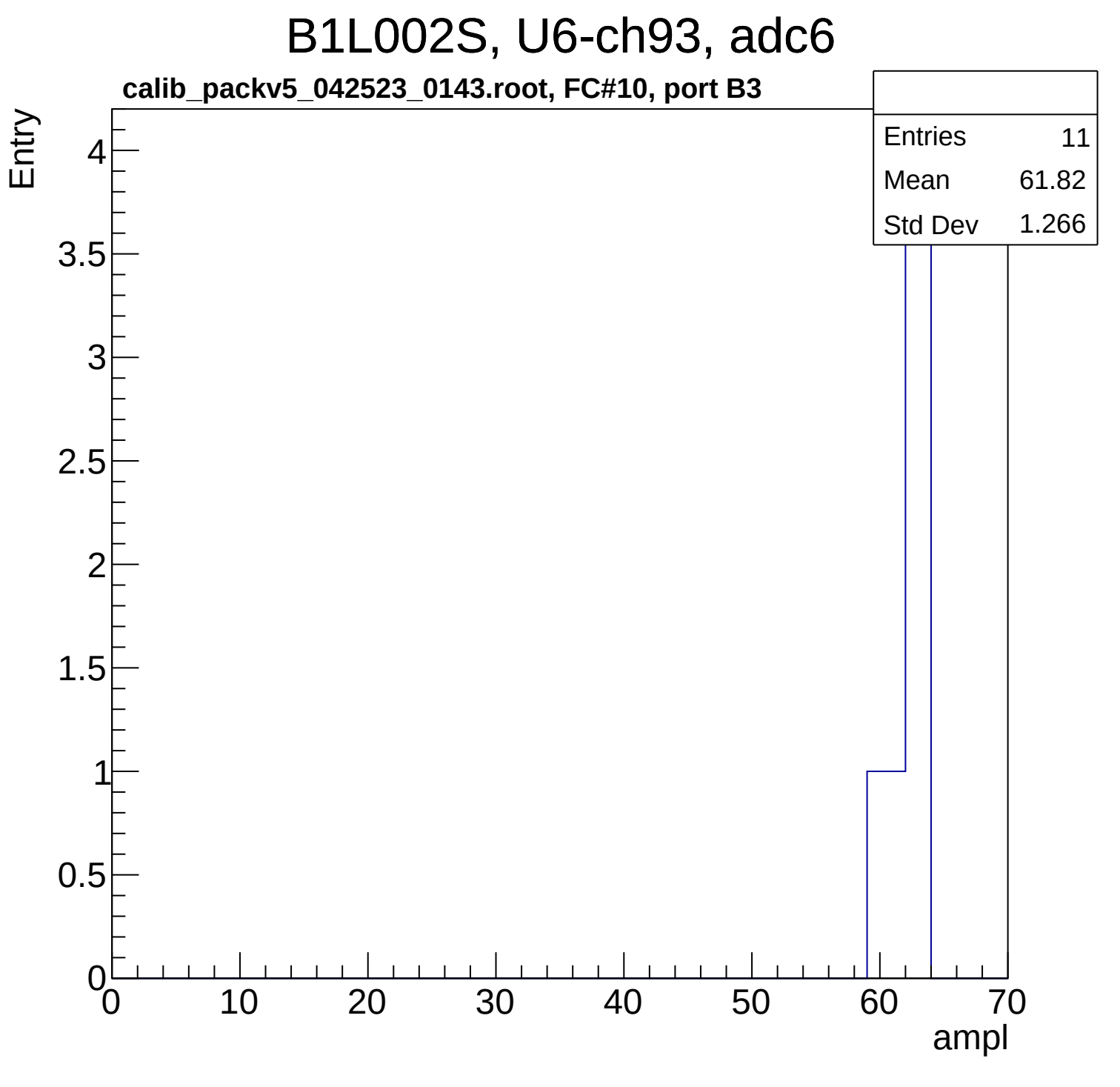

B1L002S, U6-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 36
Std Dev 2.732
Gaus mean : 36.4550
Gaus Width: 2.7046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

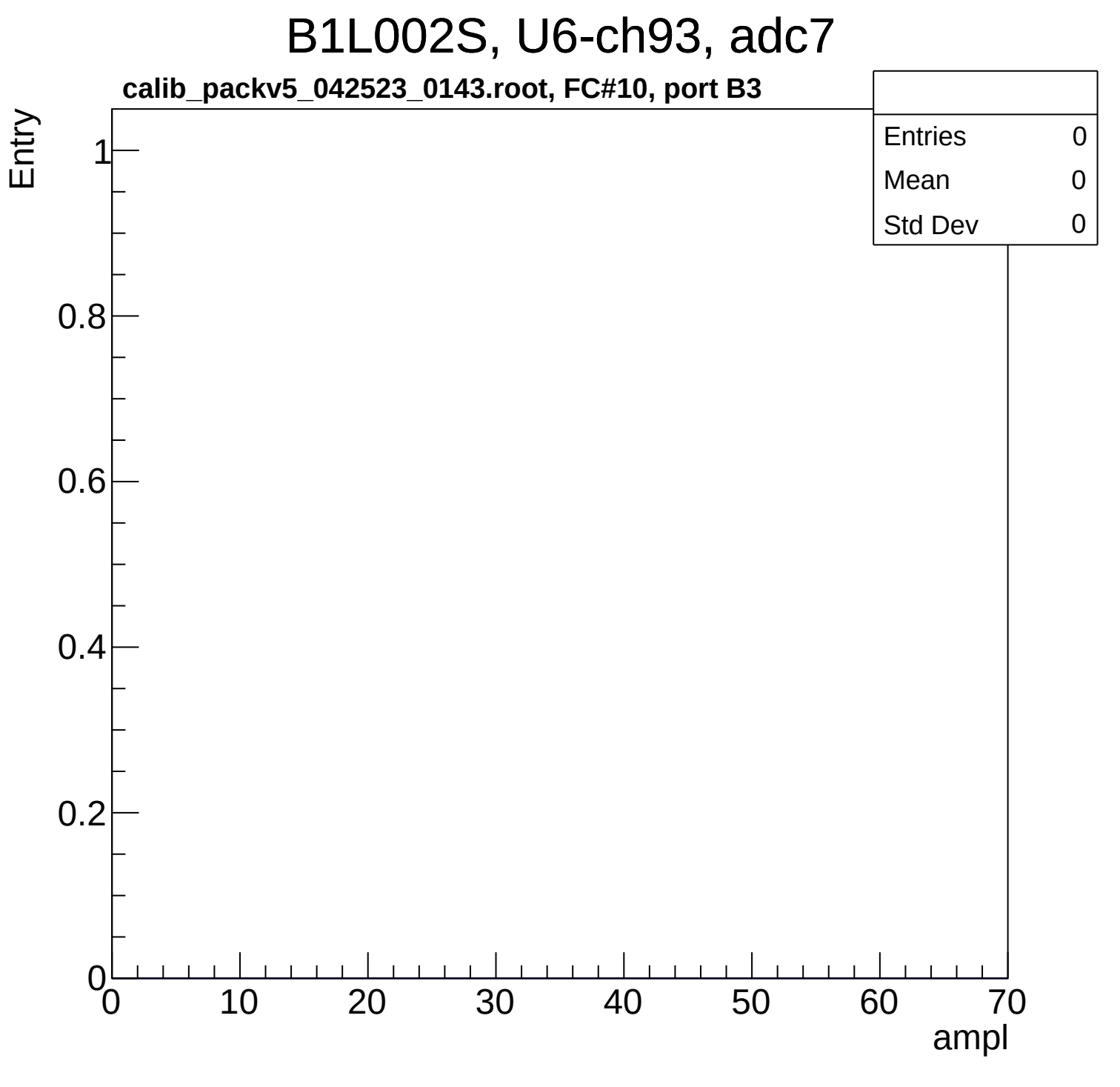

B1L002S, U6-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.71
Std Dev 2.853
Gaus mean : 42.1709
Gaus Width: 2.8203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

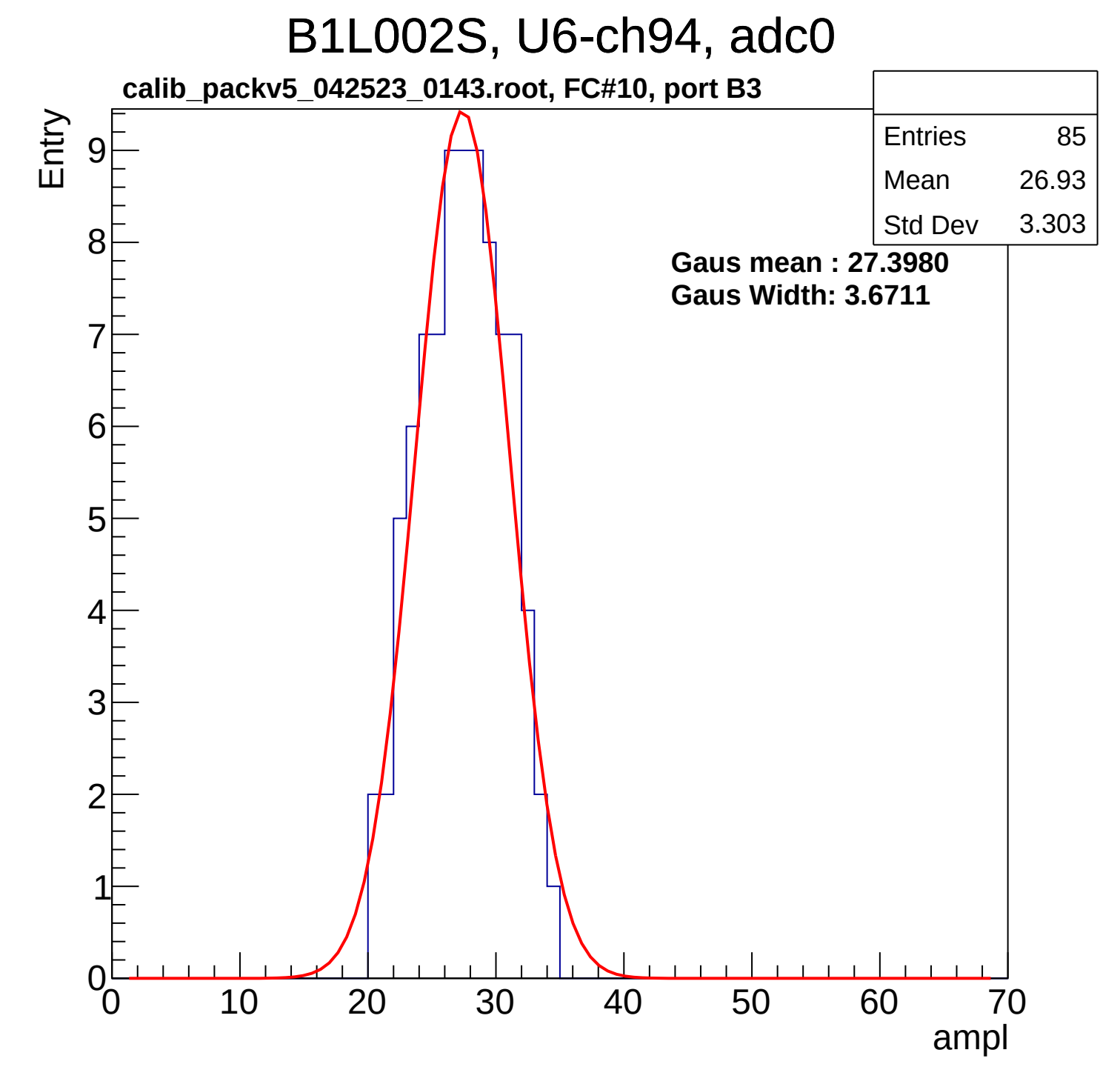

B1L002S, U6-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.79
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

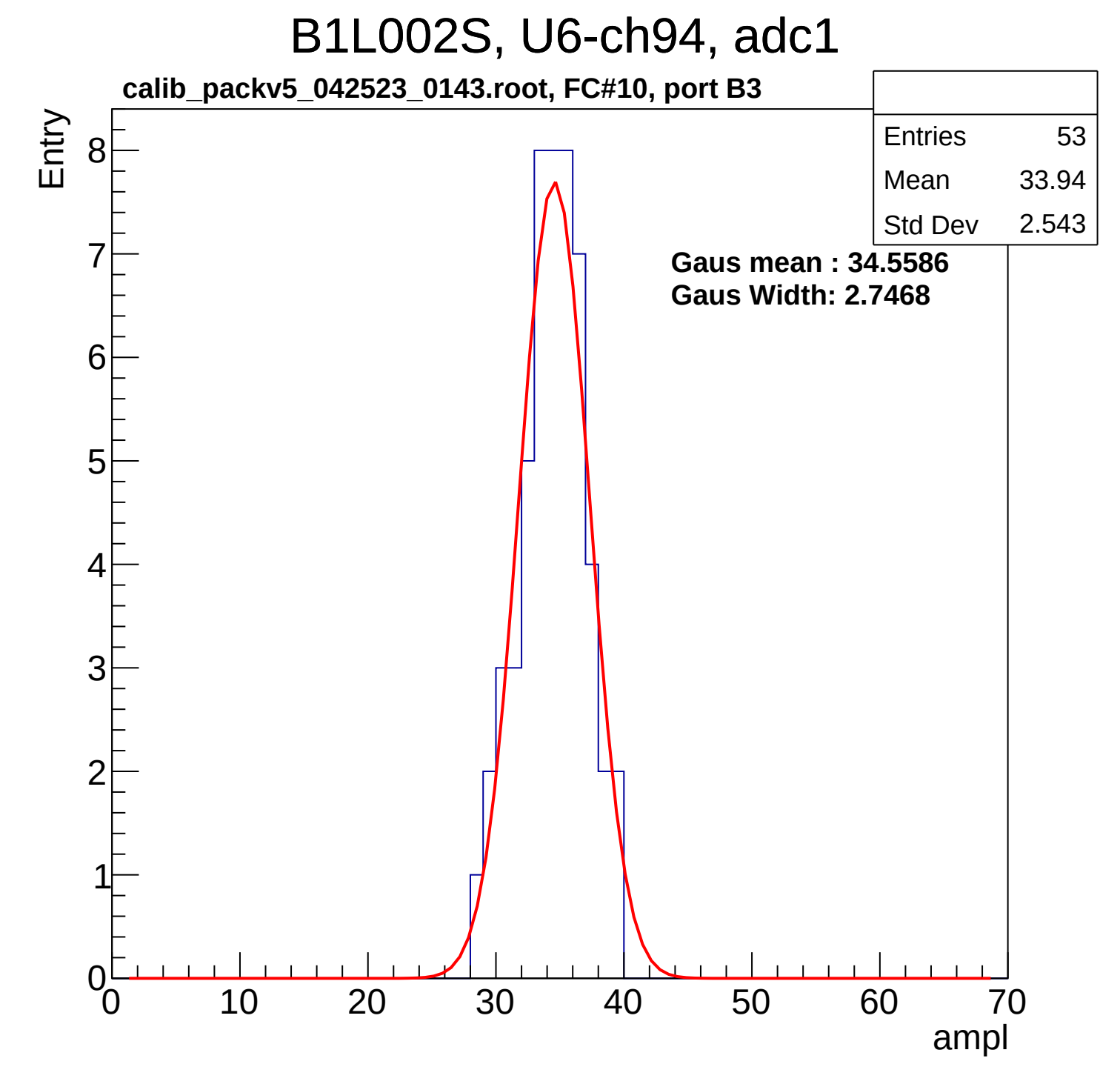

B1L002S, U6-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.02
Std Dev 3.097
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

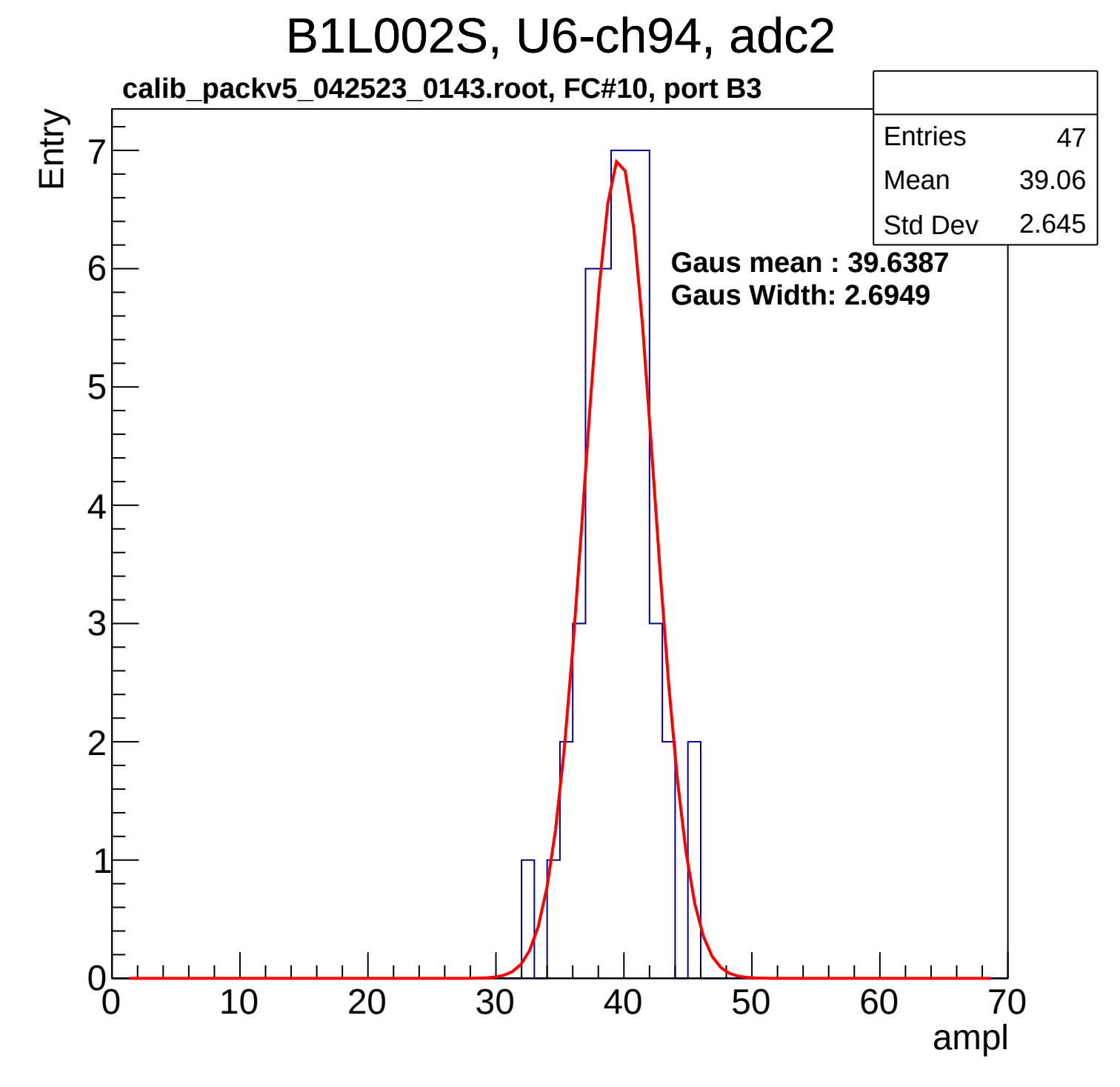

B1L002S, U6-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.23
Std Dev 8.146
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

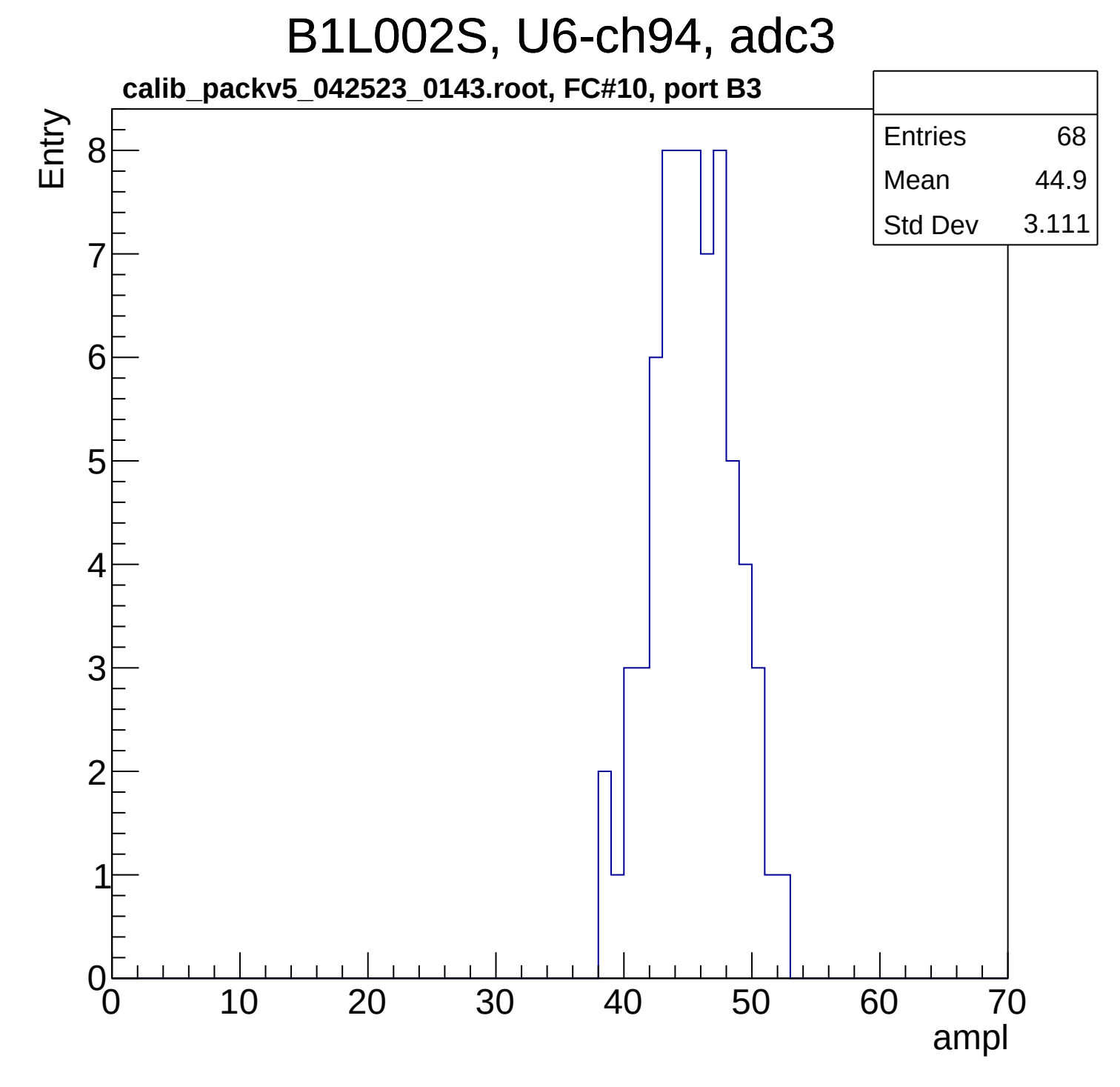

B1L002S, U6-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.82
Std Dev 1.266
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

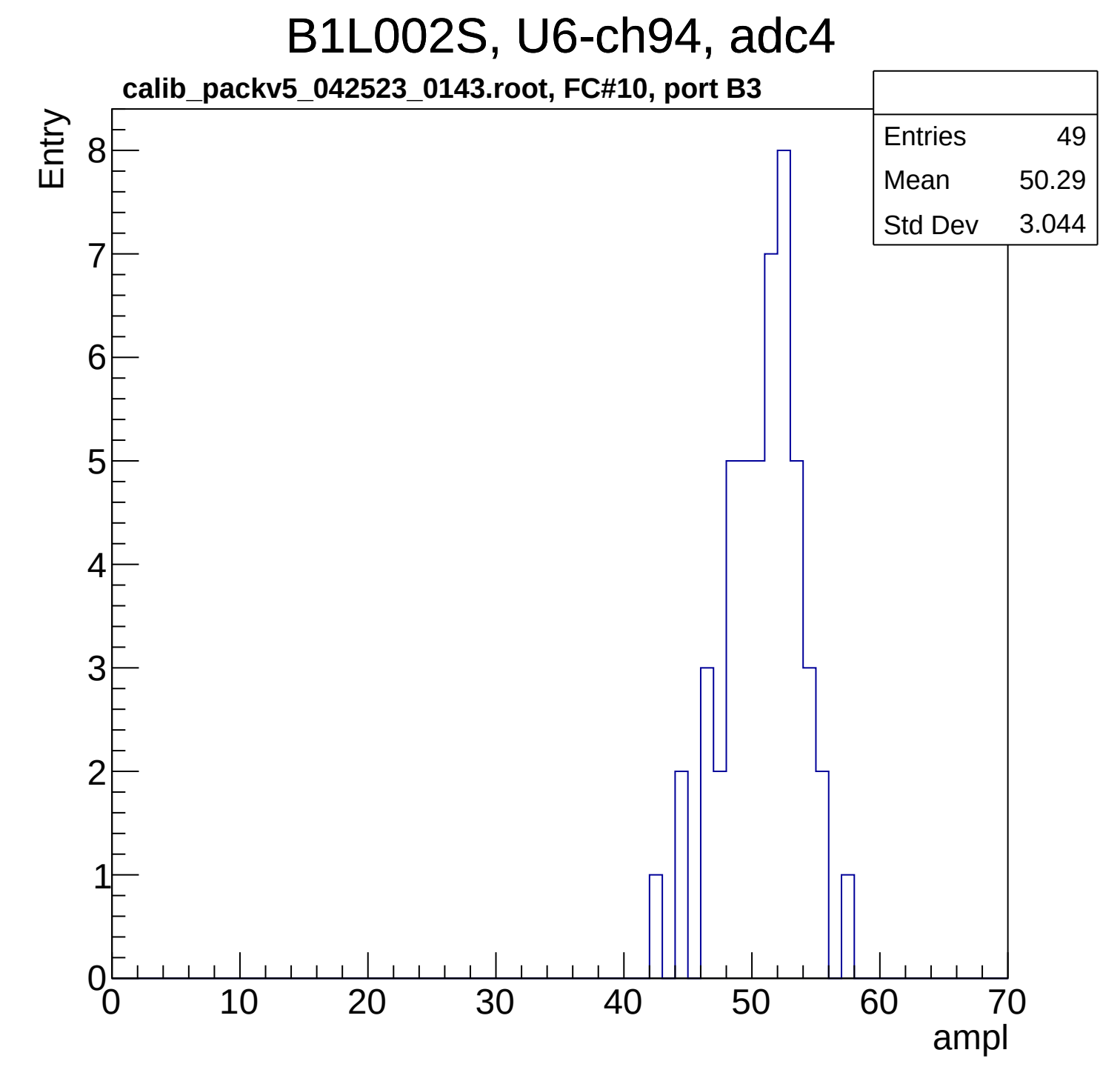

B1L002S, U6-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

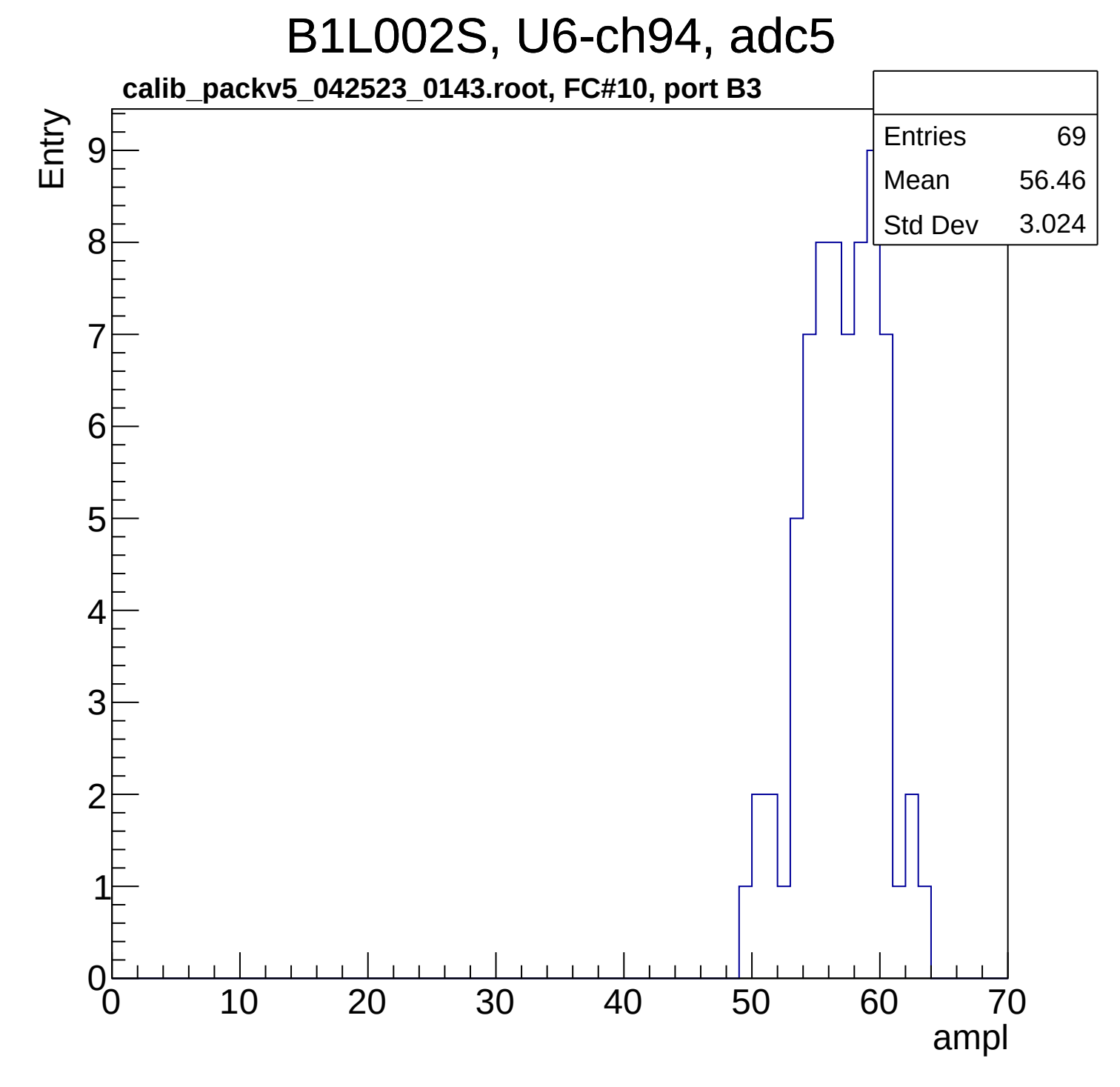

B1L002S, U6-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 26.93
Std Dev 3.303
Gaus mean : 27.3980
Gaus Width: 3.6711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

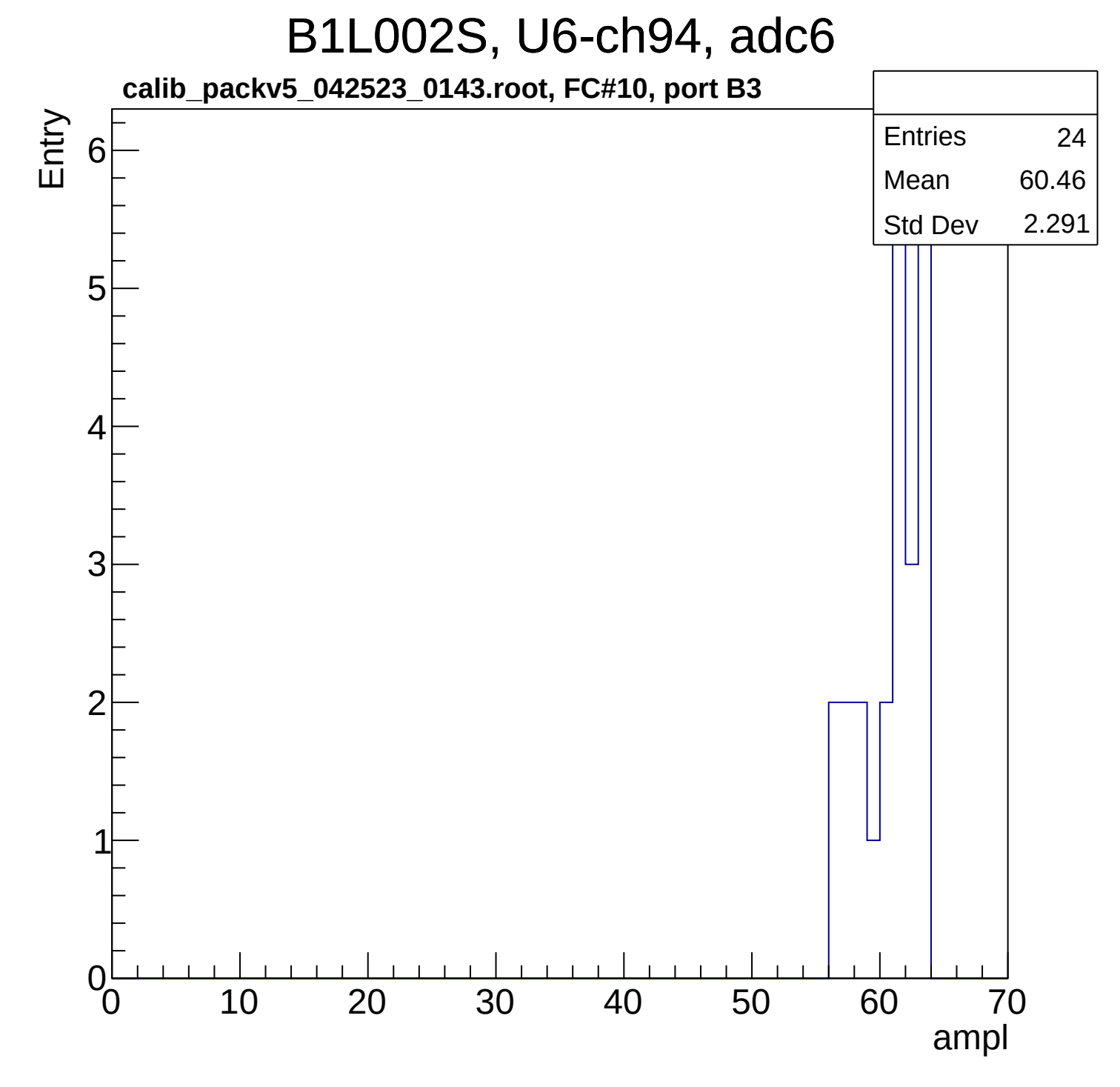

B1L002S, U6-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.94
Std Dev 2.543
Gaus mean : 34.5586
Gaus Width: 2.7468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

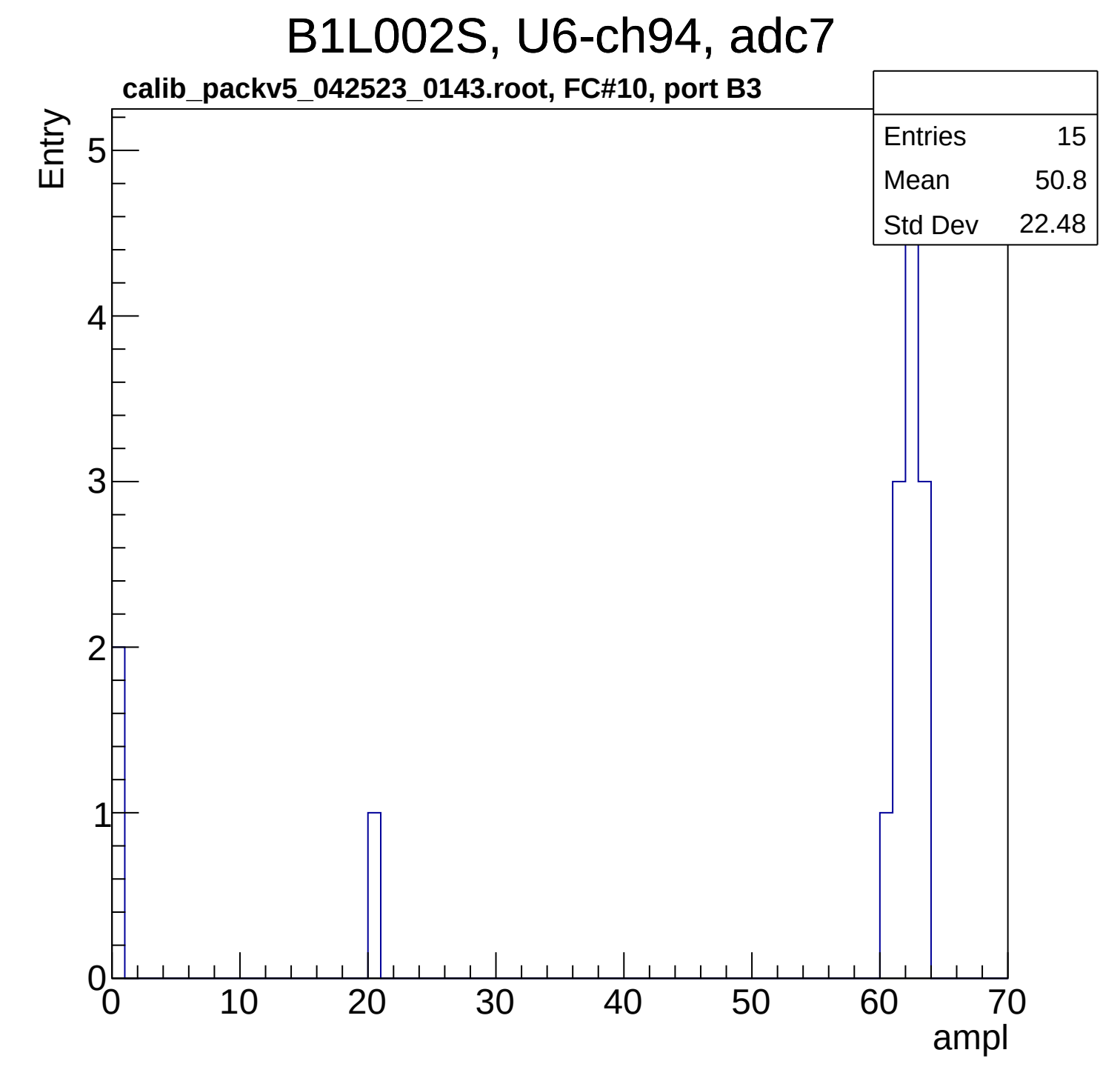

B1L002S, U6-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 39.06
Std Dev 2.645
Gaus mean : 39.6387
Gaus Width: 2.6949
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

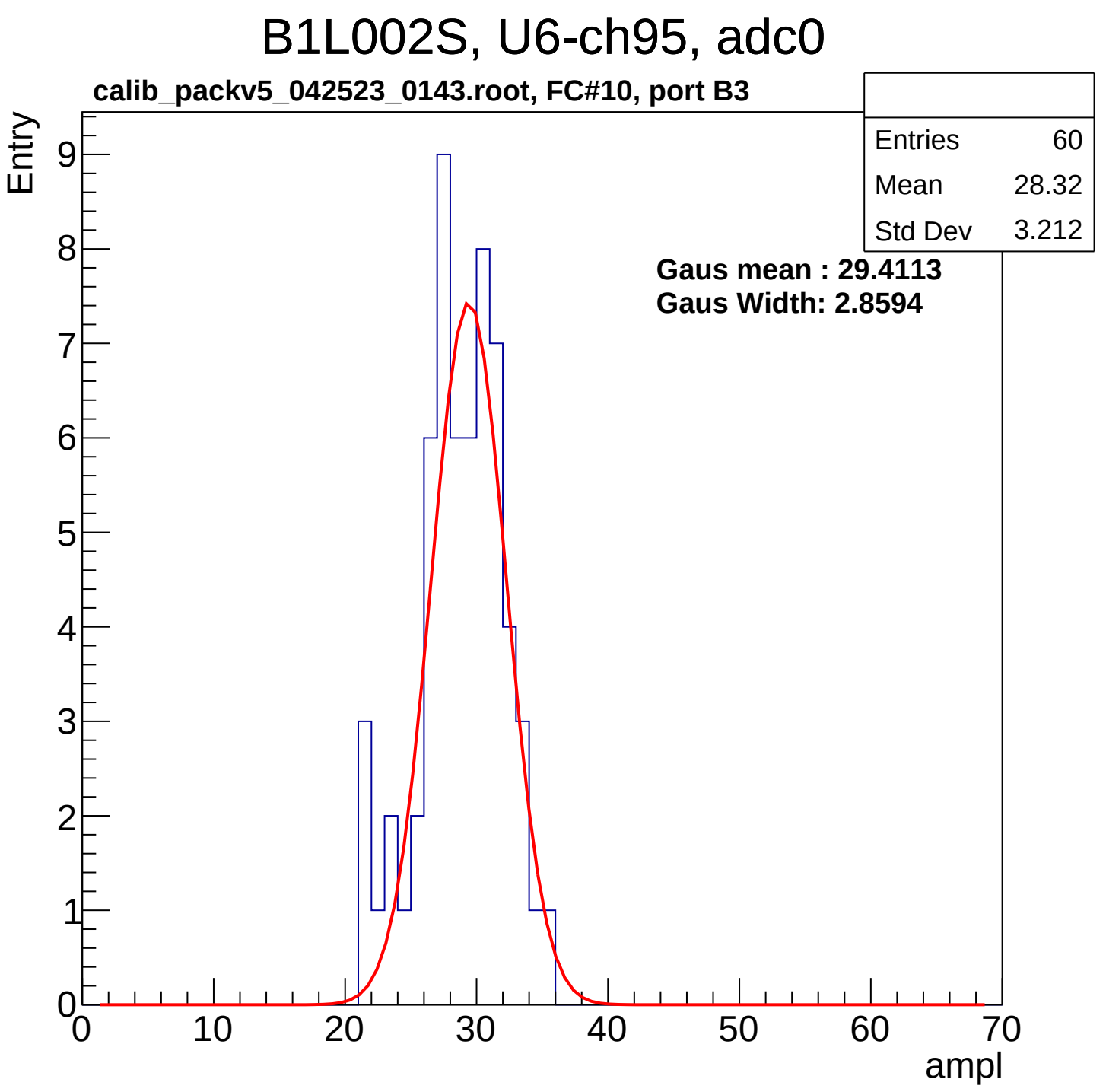

B1L002S, U6-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 44.9
Std Dev 3.111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

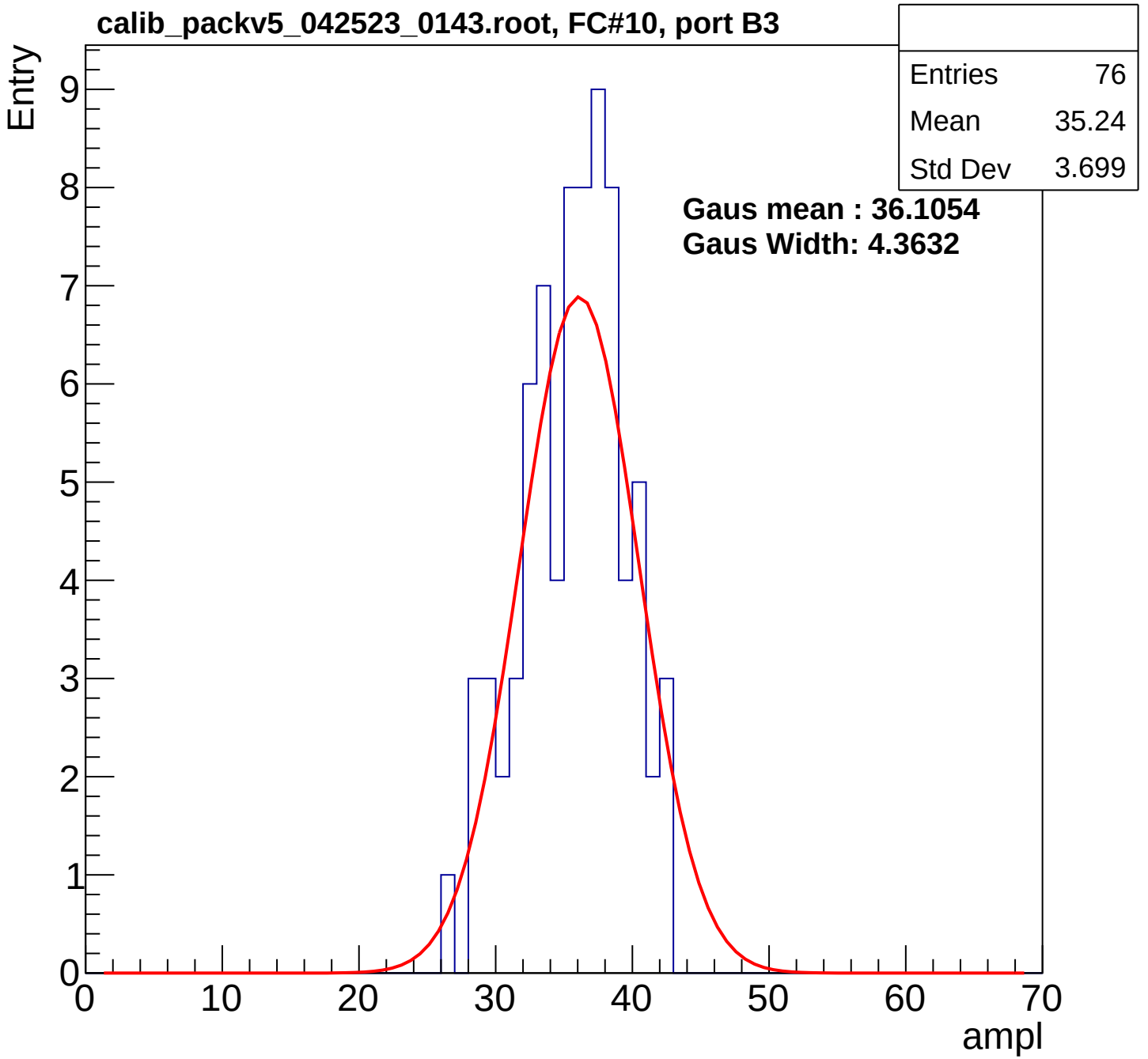

B1L002S, U6-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 50.29
Std Dev 3.044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

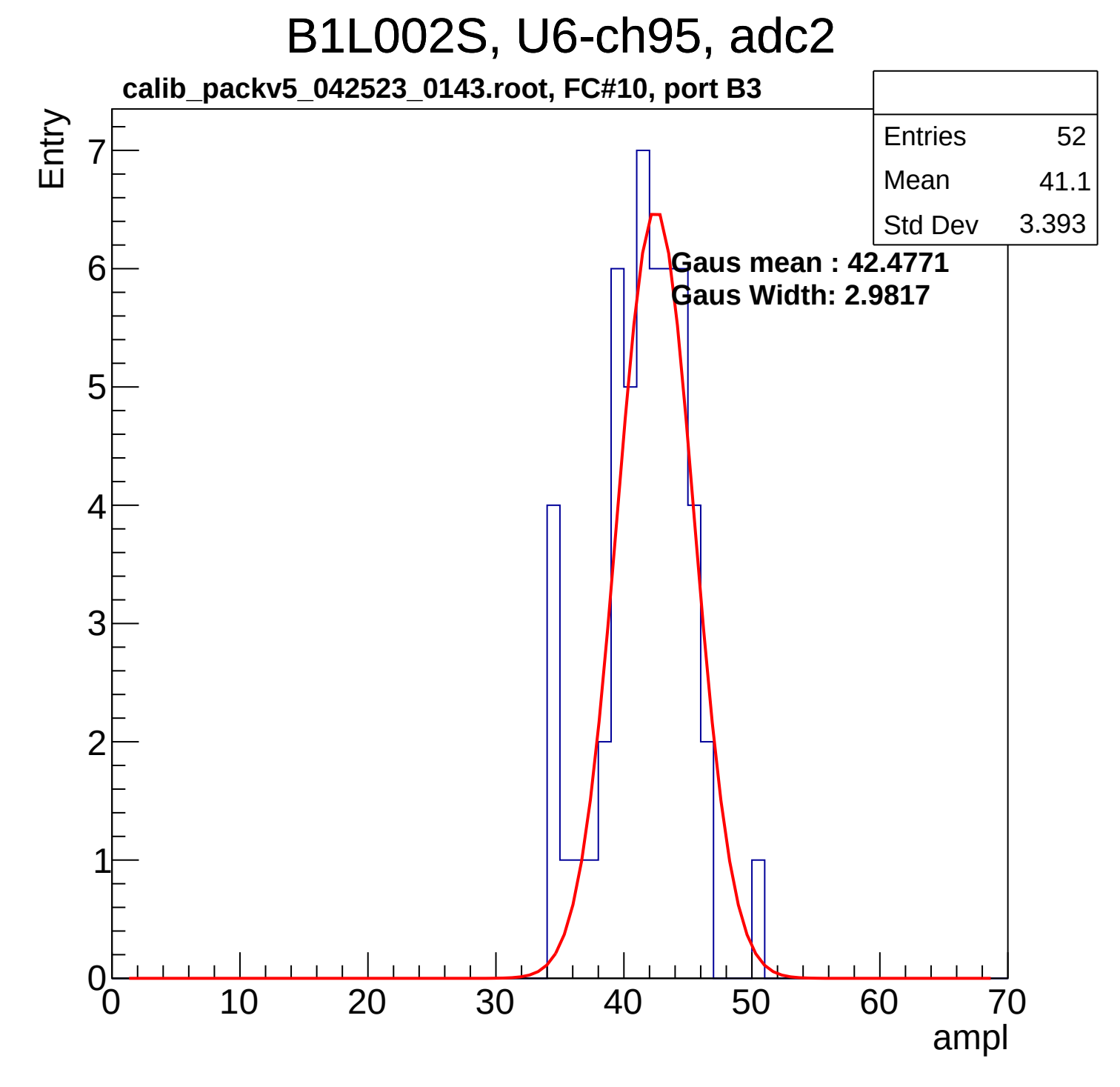

B1L002S, U6-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 56.46
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

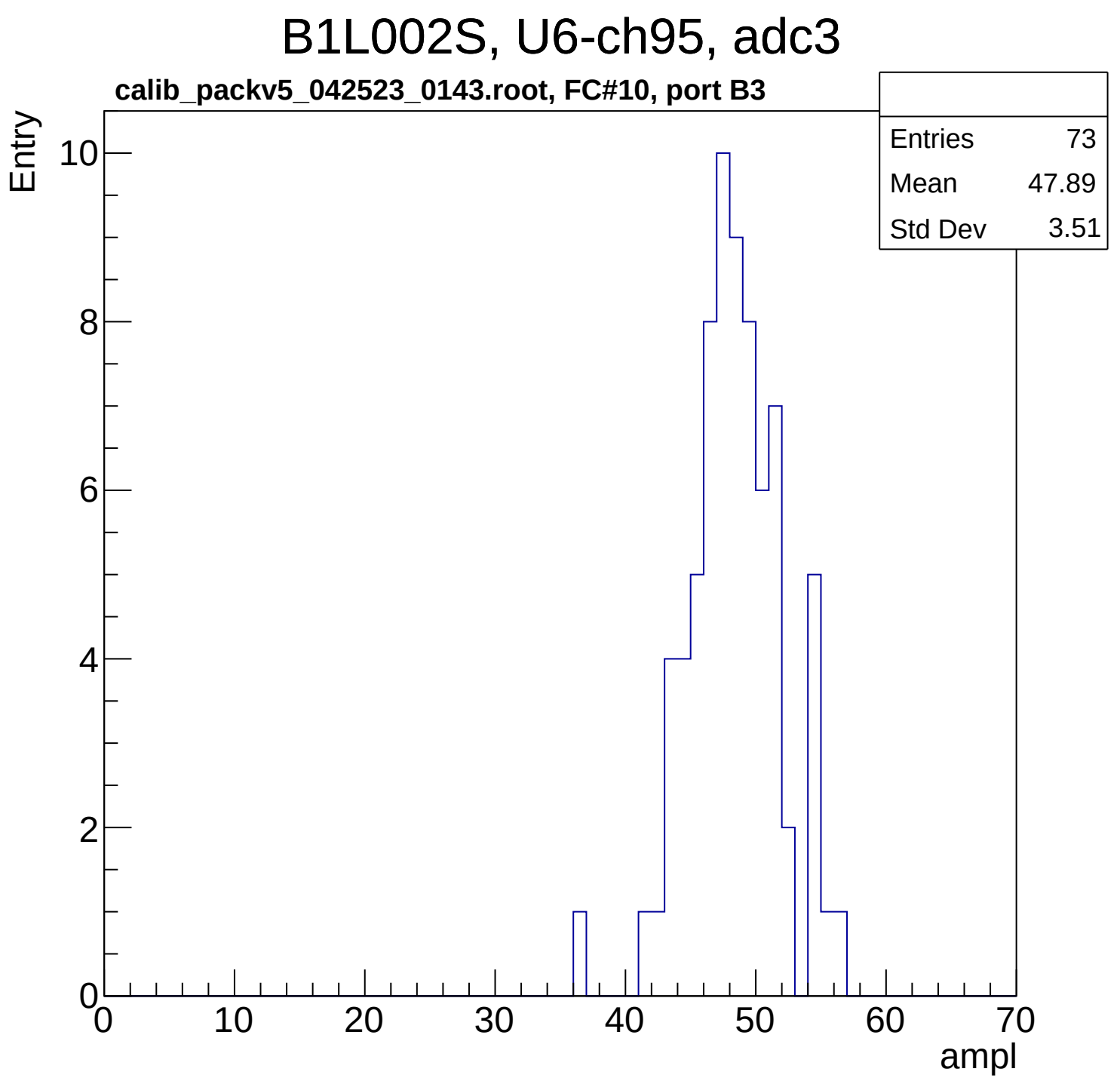

B1L002S, U6-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 60.46
Std Dev 2.291
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

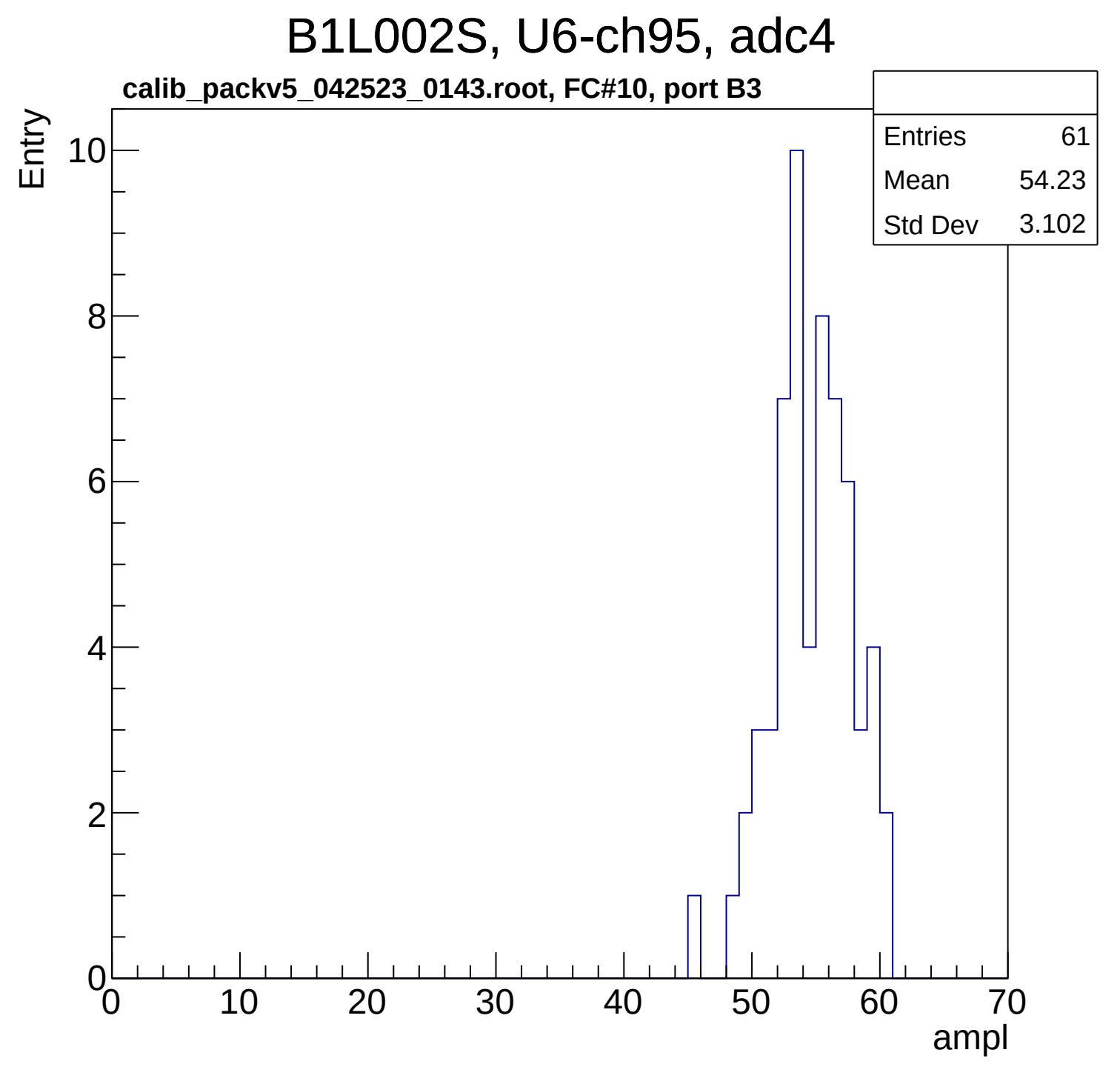

B1L002S, U6-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 50.8
Std Dev 22.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

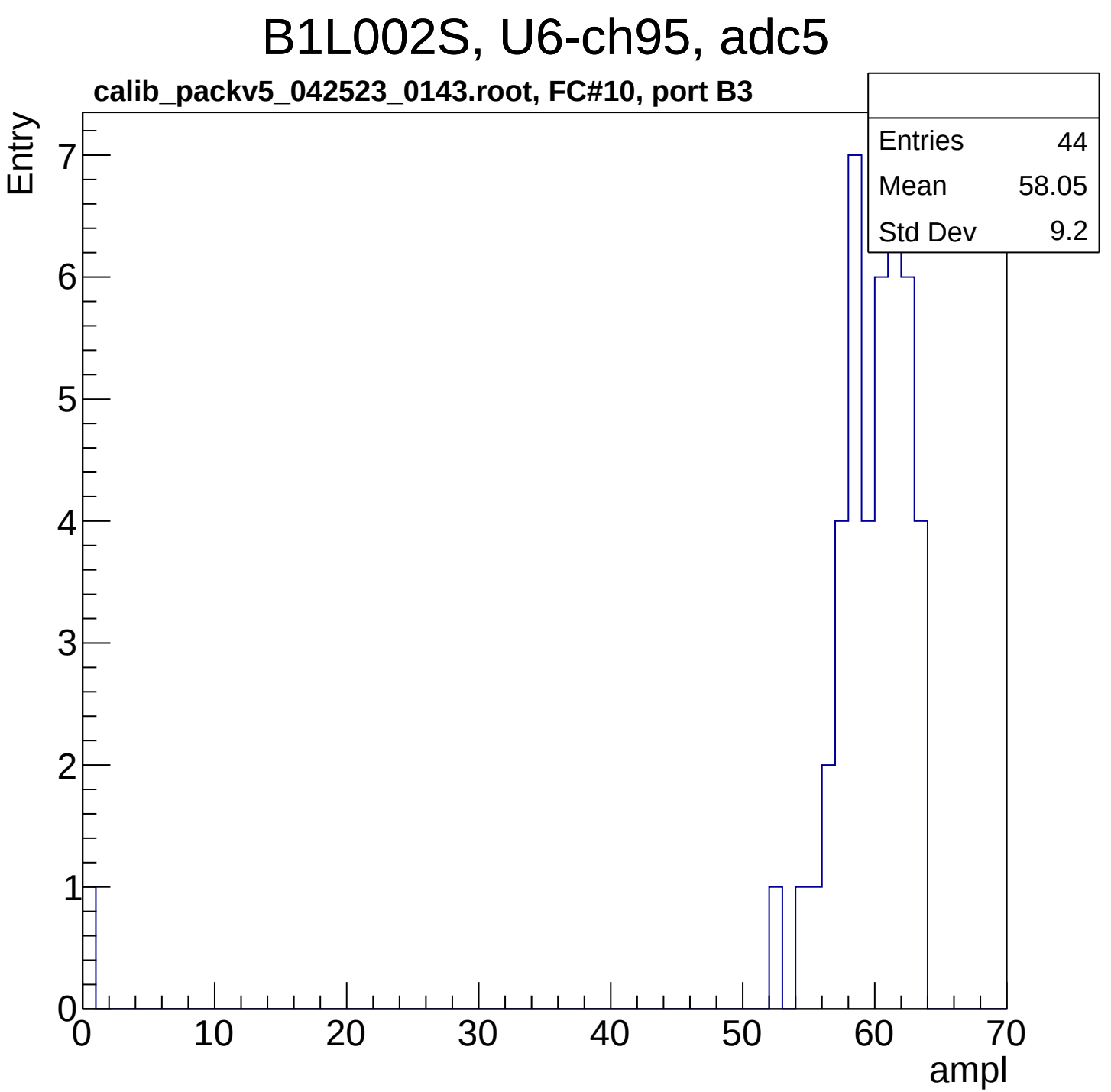

B1L002S, U6-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.32
Std Dev 3.212
Gaus mean : 29.4113
Gaus Width: 2.8594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

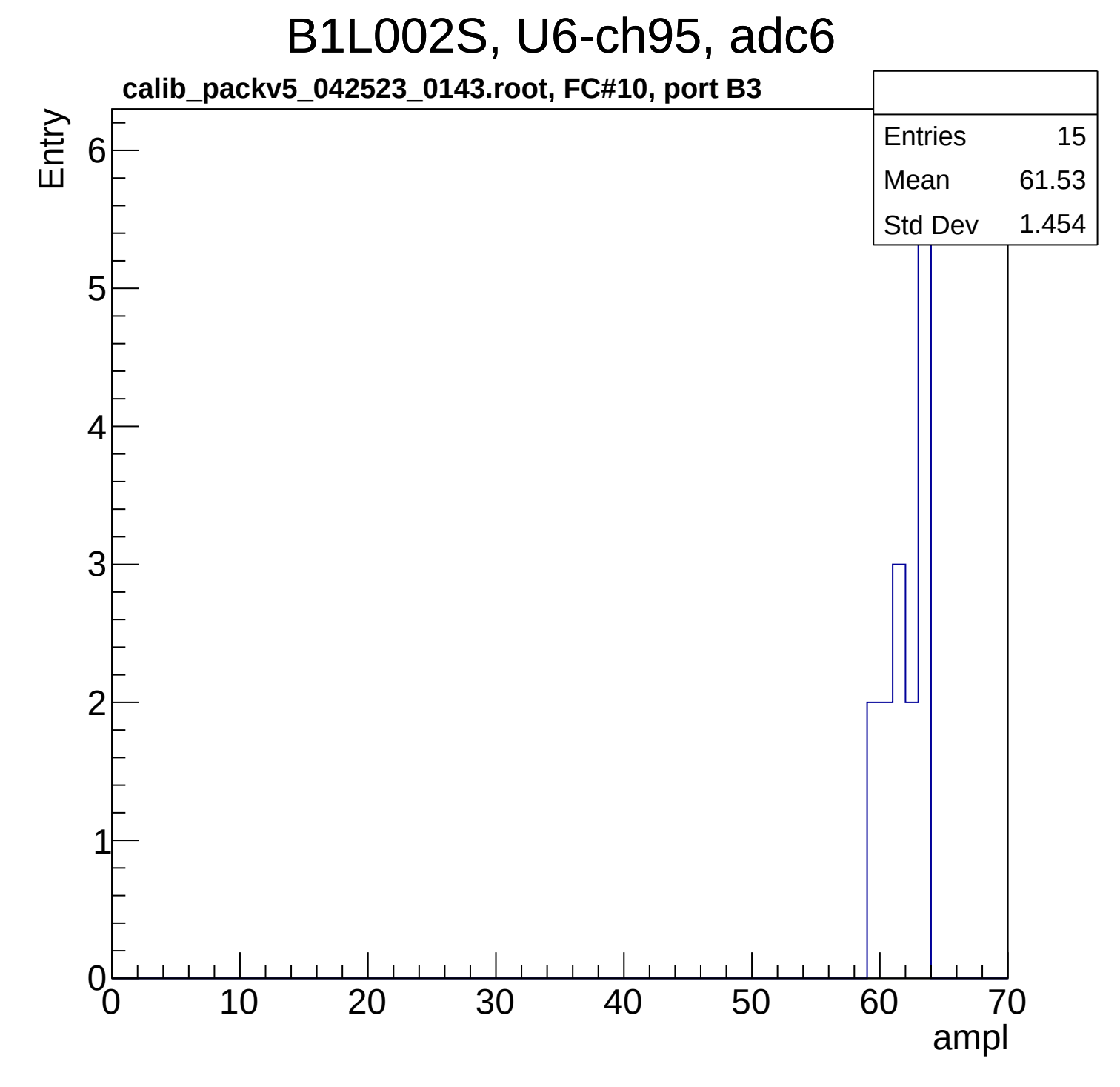

B1L002S, U6-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35.24
Std Dev 3.699
Gaus mean : 36.1054
Gaus Width: 4.3632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

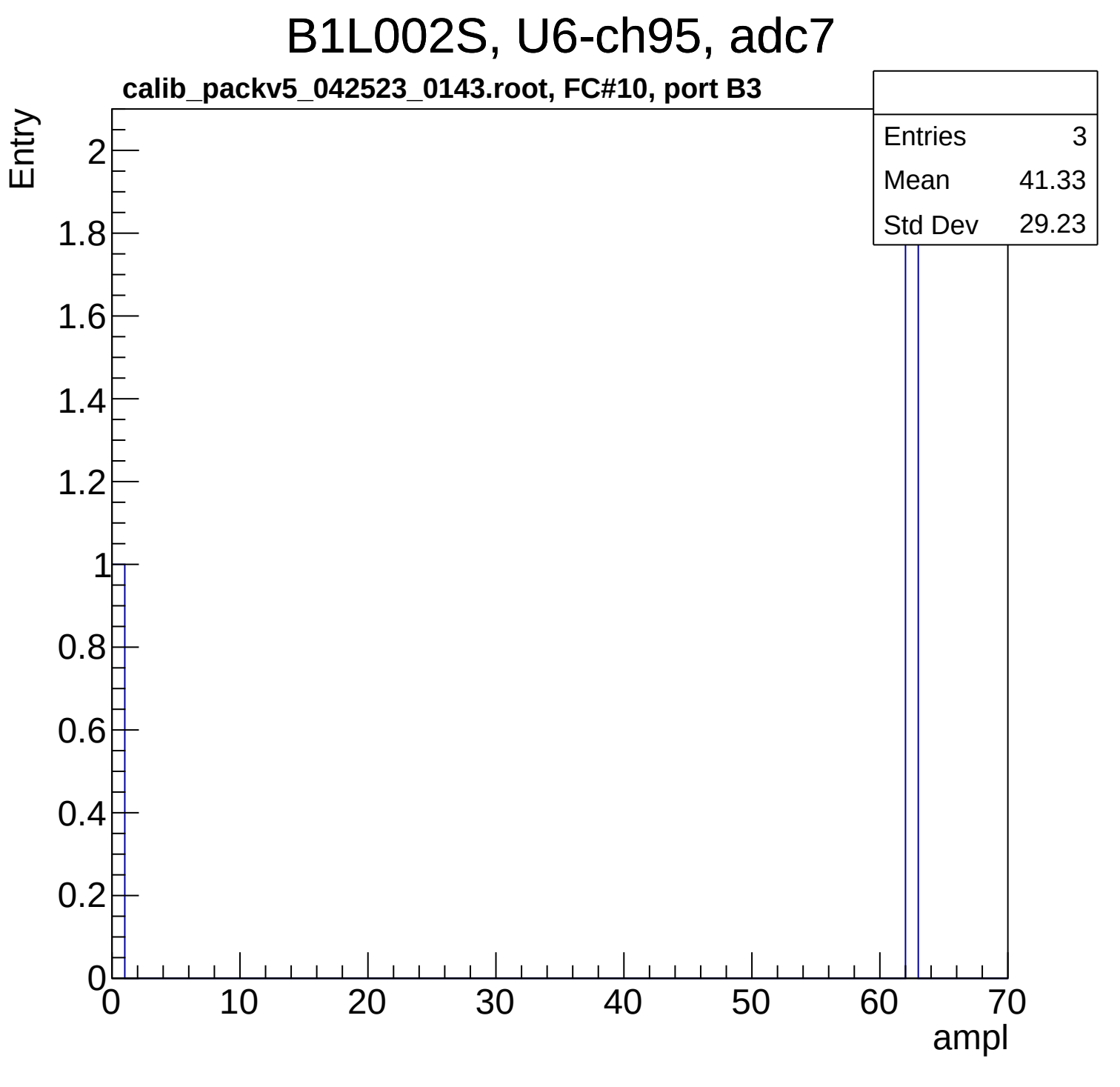

B1L002S, U6-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.1
Std Dev 3.393
Gaus mean : 42.4771
Gaus Width: 2.9817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

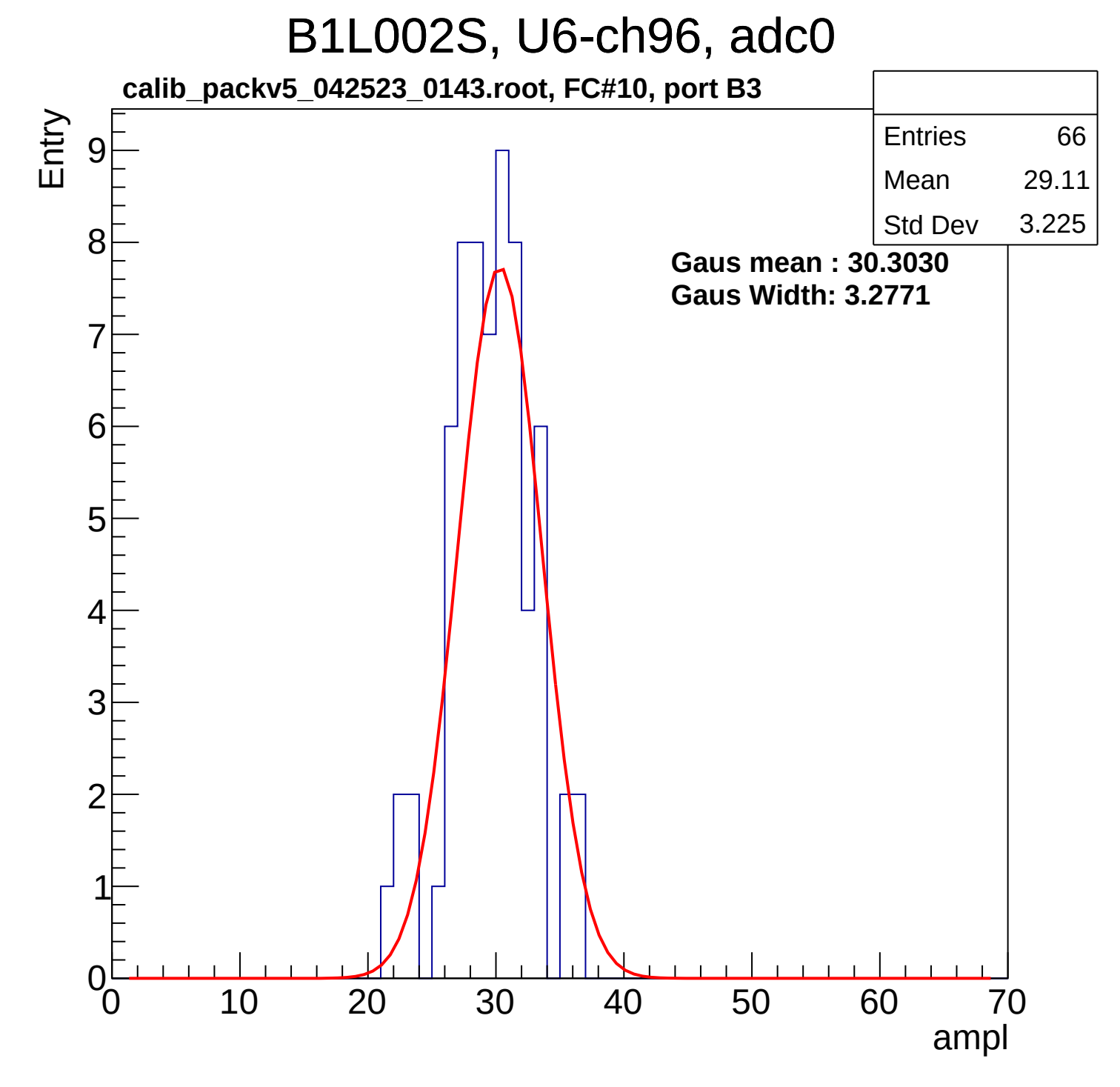

B1L002S, U6-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.89
Std Dev 3.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

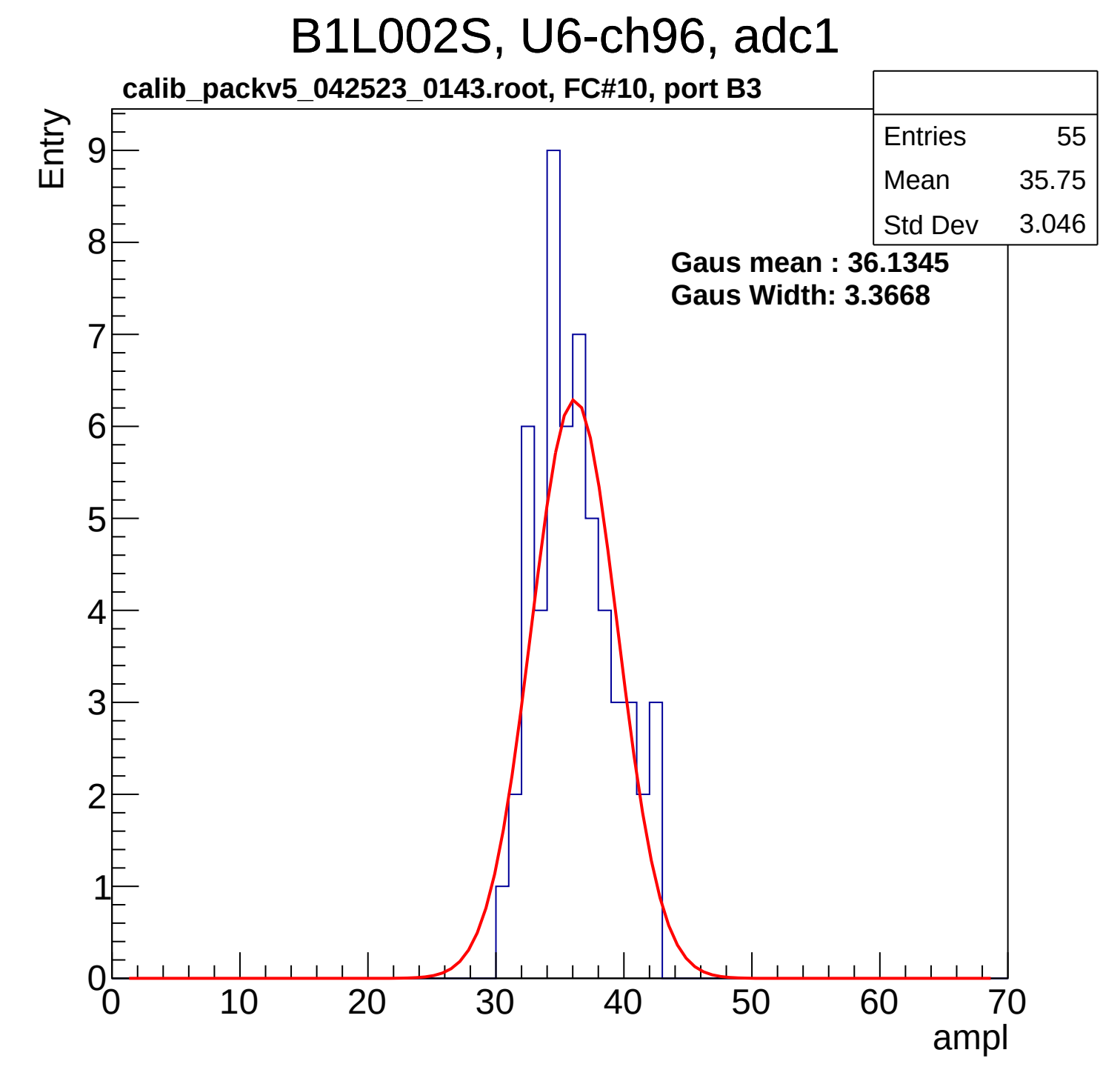

B1L002S, U6-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.23
Std Dev 3.102
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

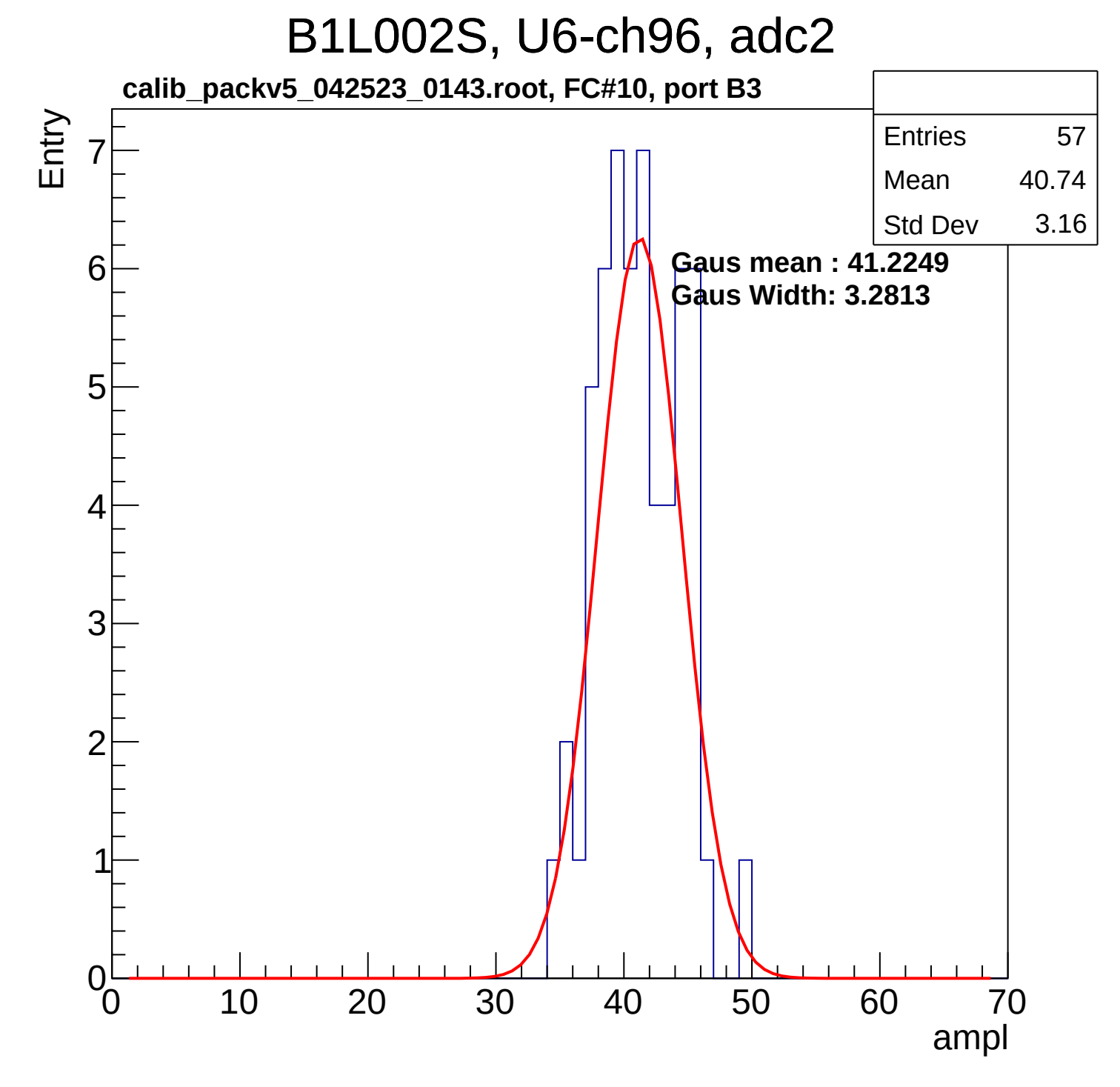

B1L002S, U6-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.05
Std Dev 9.2
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

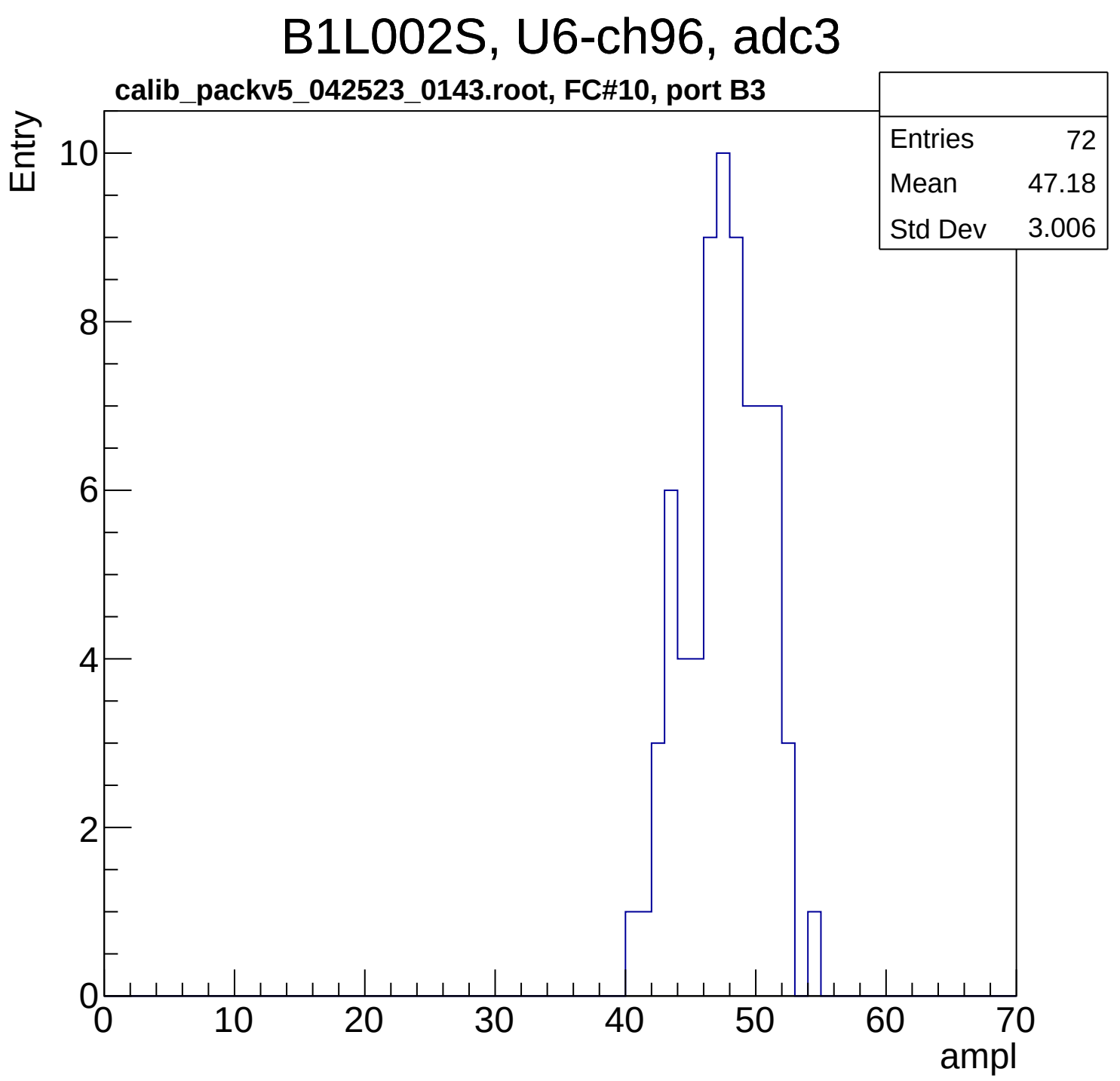

B1L002S, U6-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.53
Std Dev 1.454
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

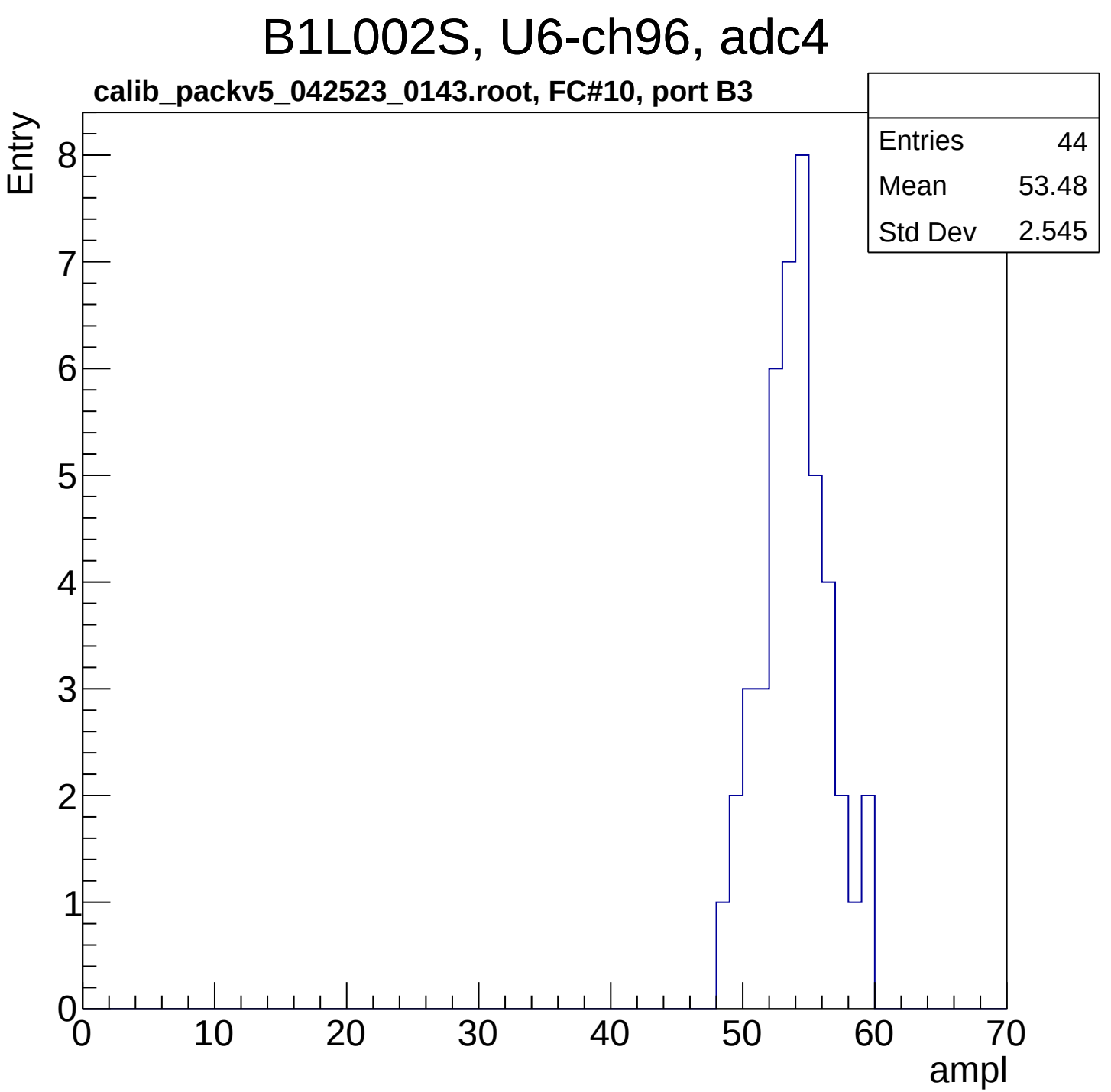

B1L002S, U6-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

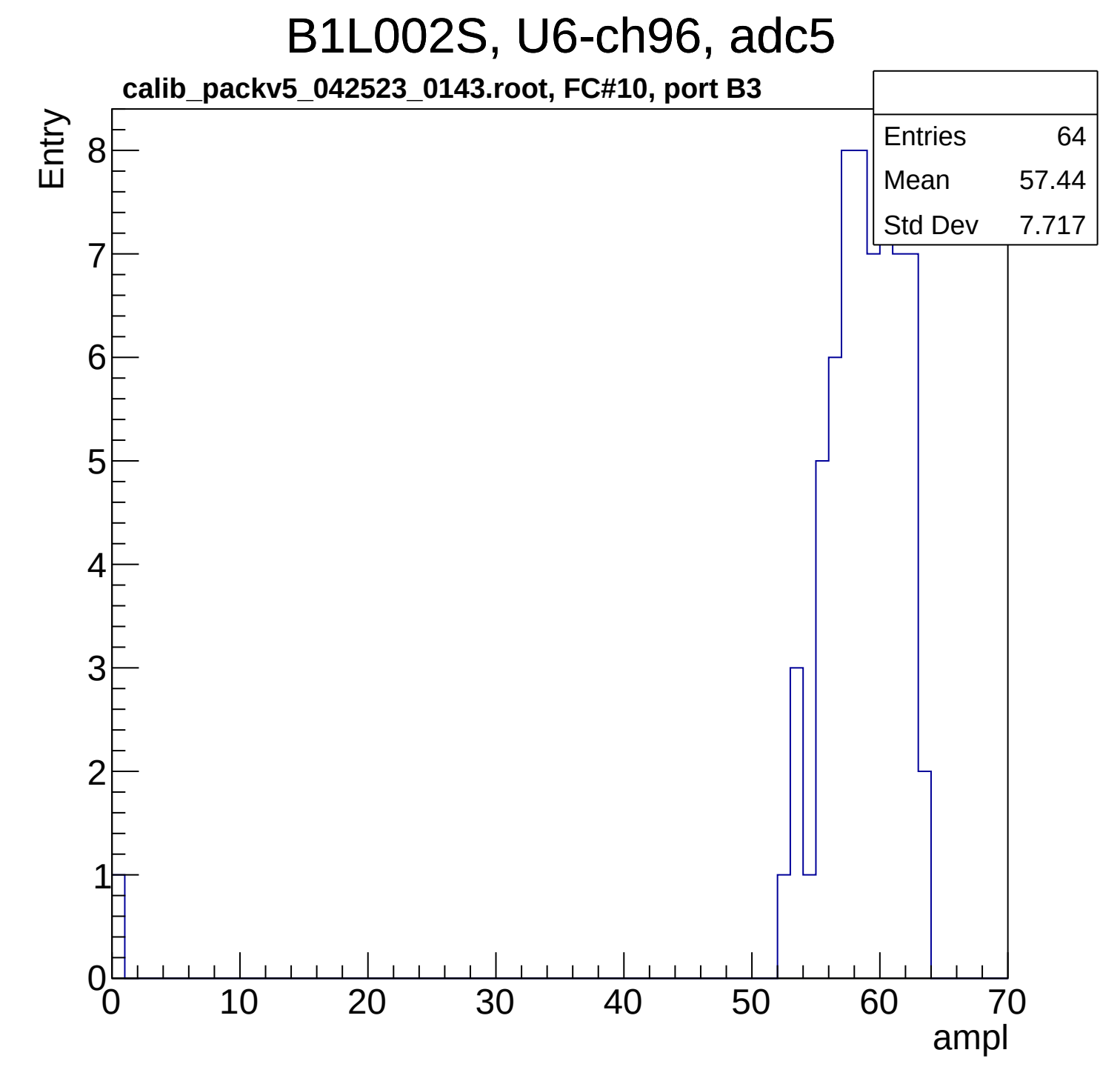

B1L002S, U6-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.11
Std Dev 3.225
Gaus mean : 30.3030
Gaus Width: 3.2771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

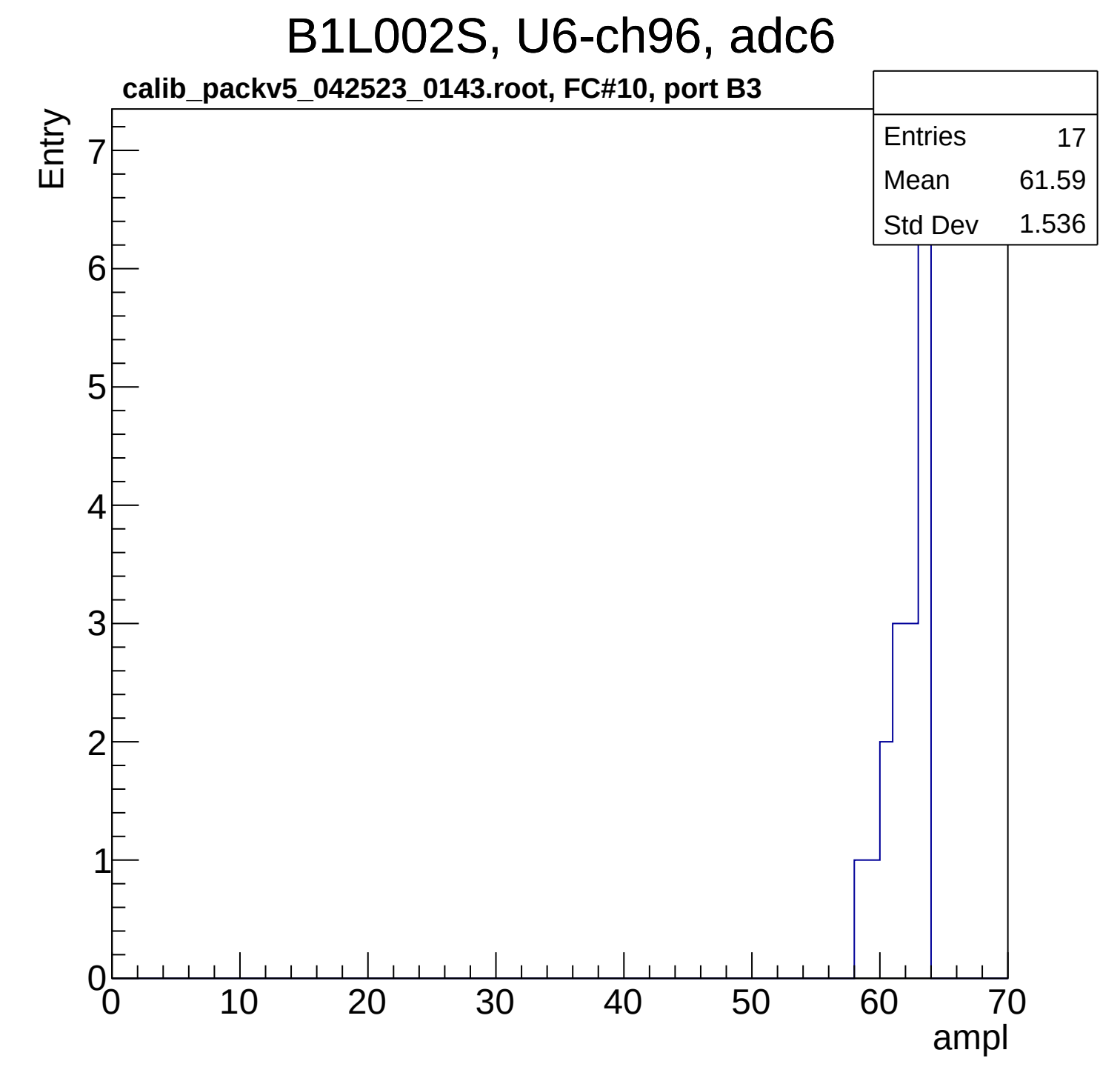

B1L002S, U6-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.75
Std Dev 3.046
Gaus mean : 36.1345
Gaus Width: 3.3668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

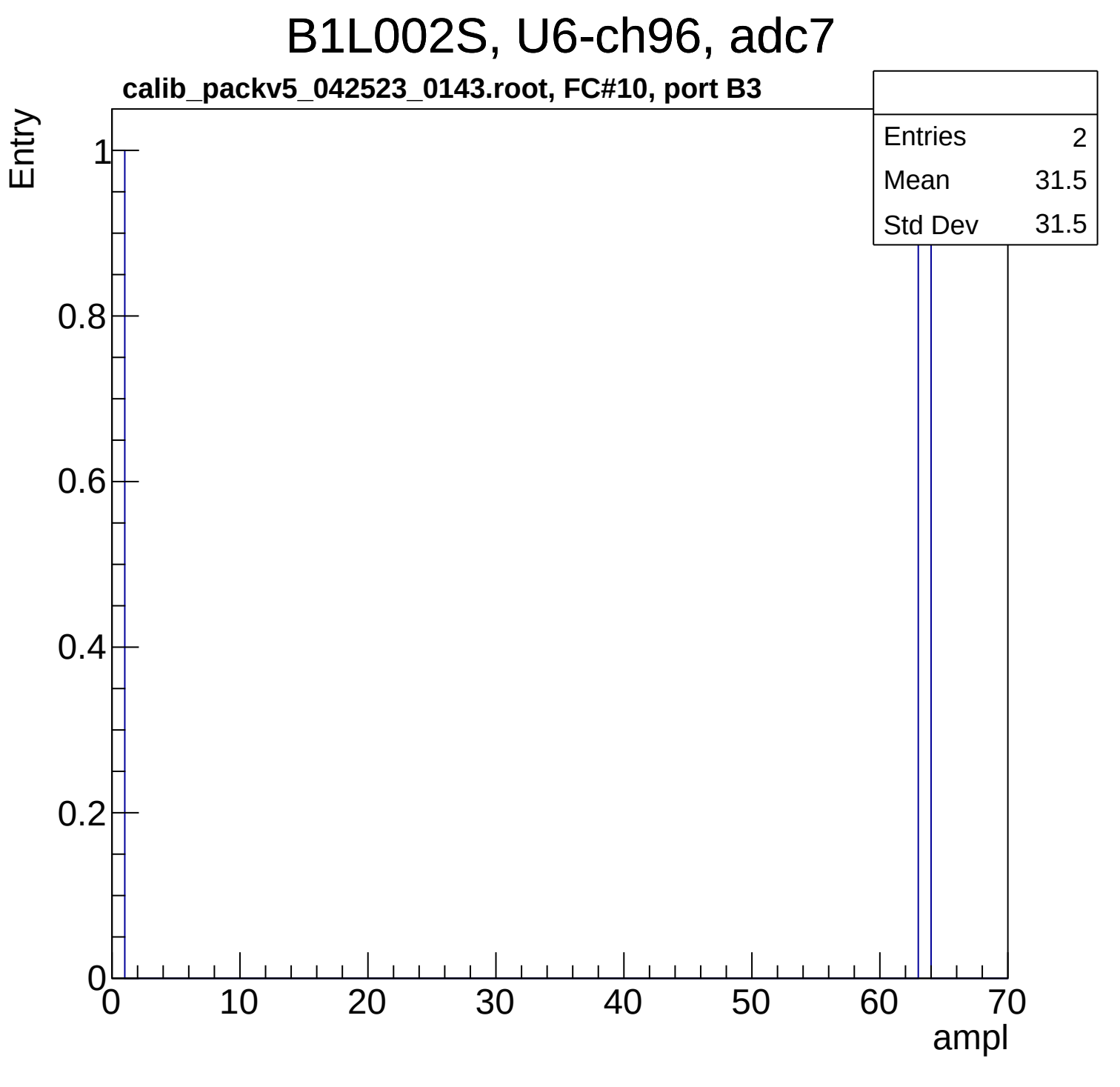

B1L002S, U6-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.74
Std Dev 3.16
Gaus mean : 41.2249
Gaus Width: 3.2813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

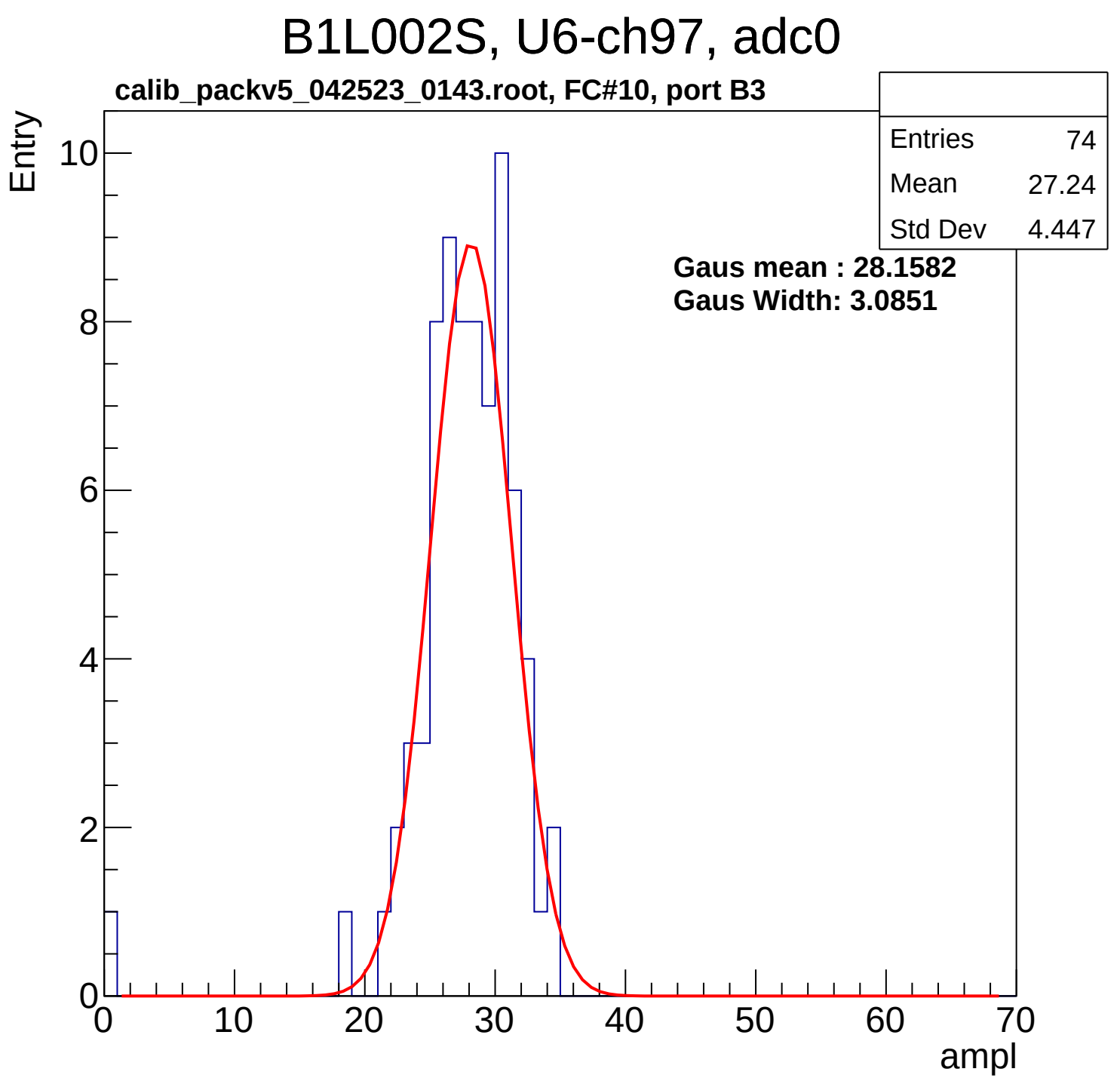

B1L002S, U6-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.18
Std Dev 3.006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

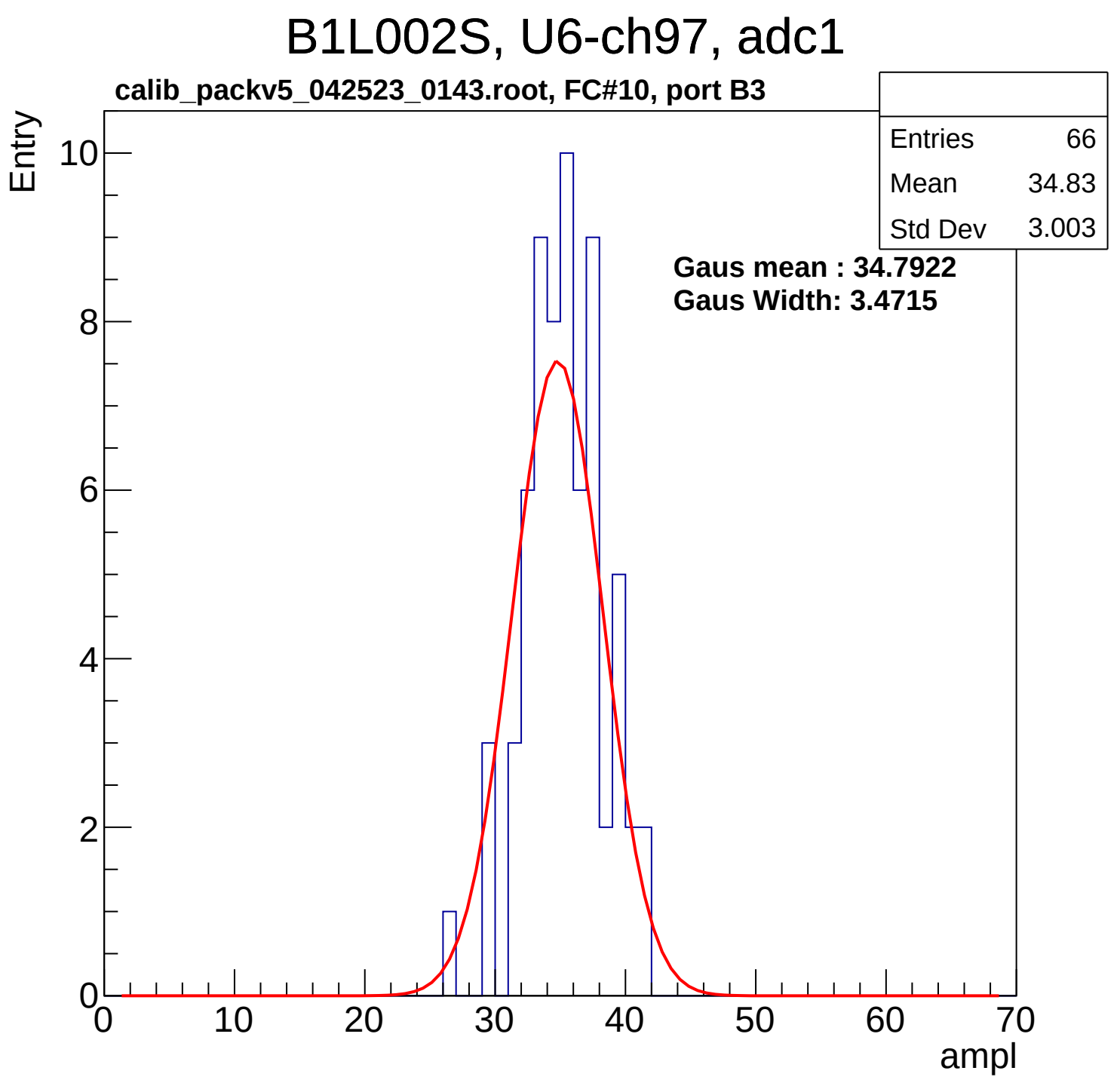

B1L002S, U6-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.48
Std Dev 2.545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

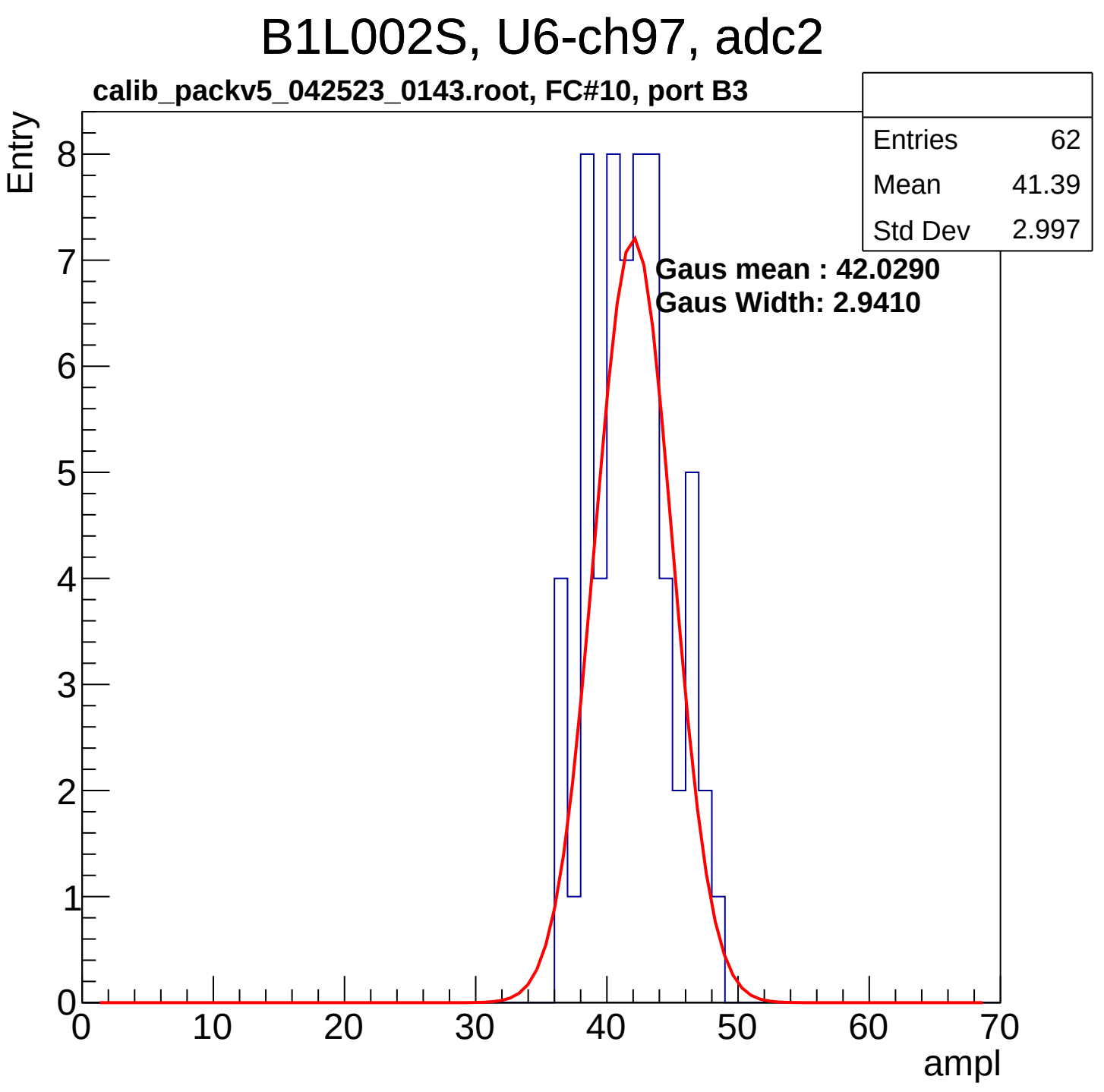

B1L002S, U6-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.44
Std Dev 7.717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

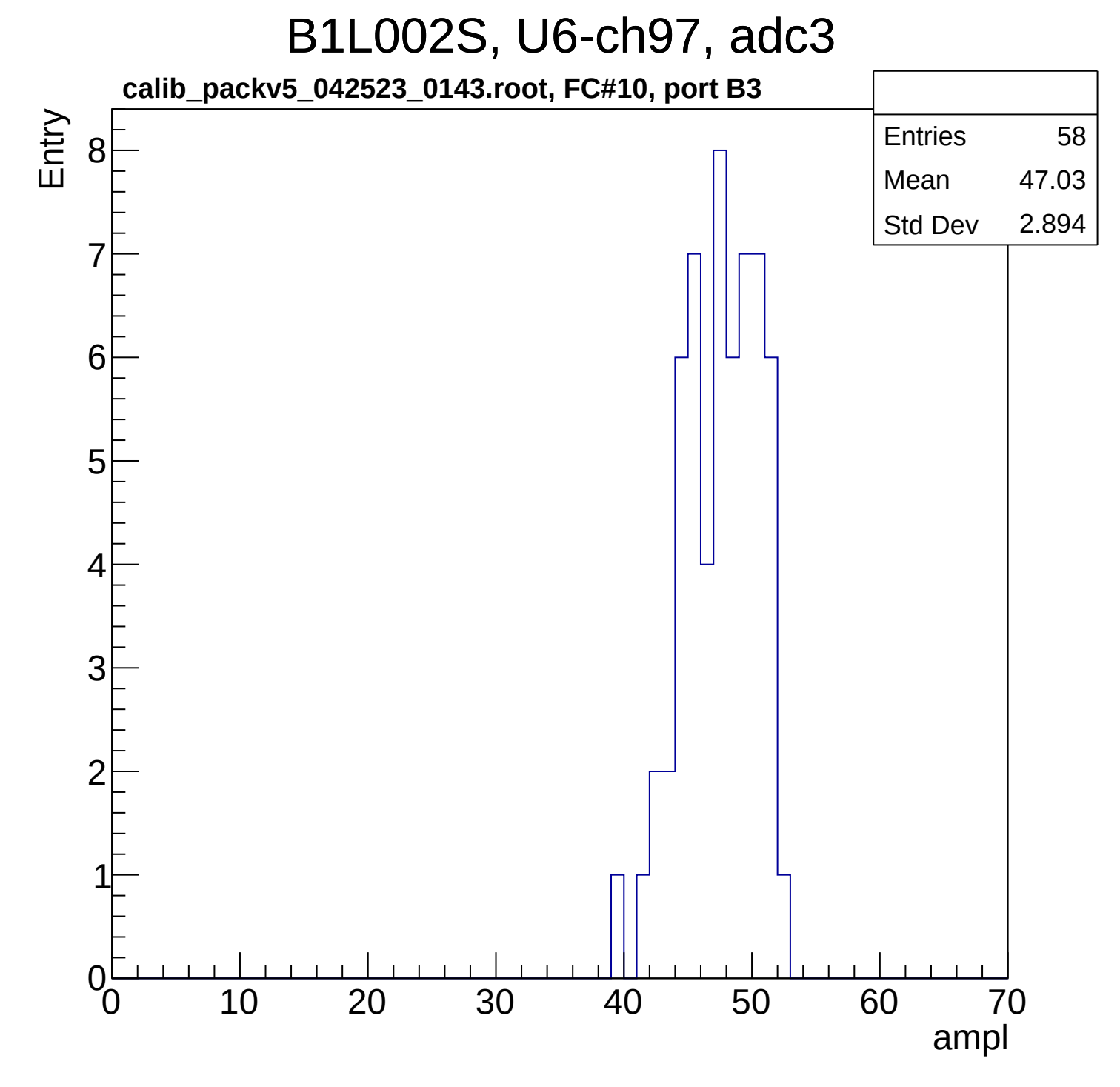

B1L002S, U6-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.59
Std Dev 1.536
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

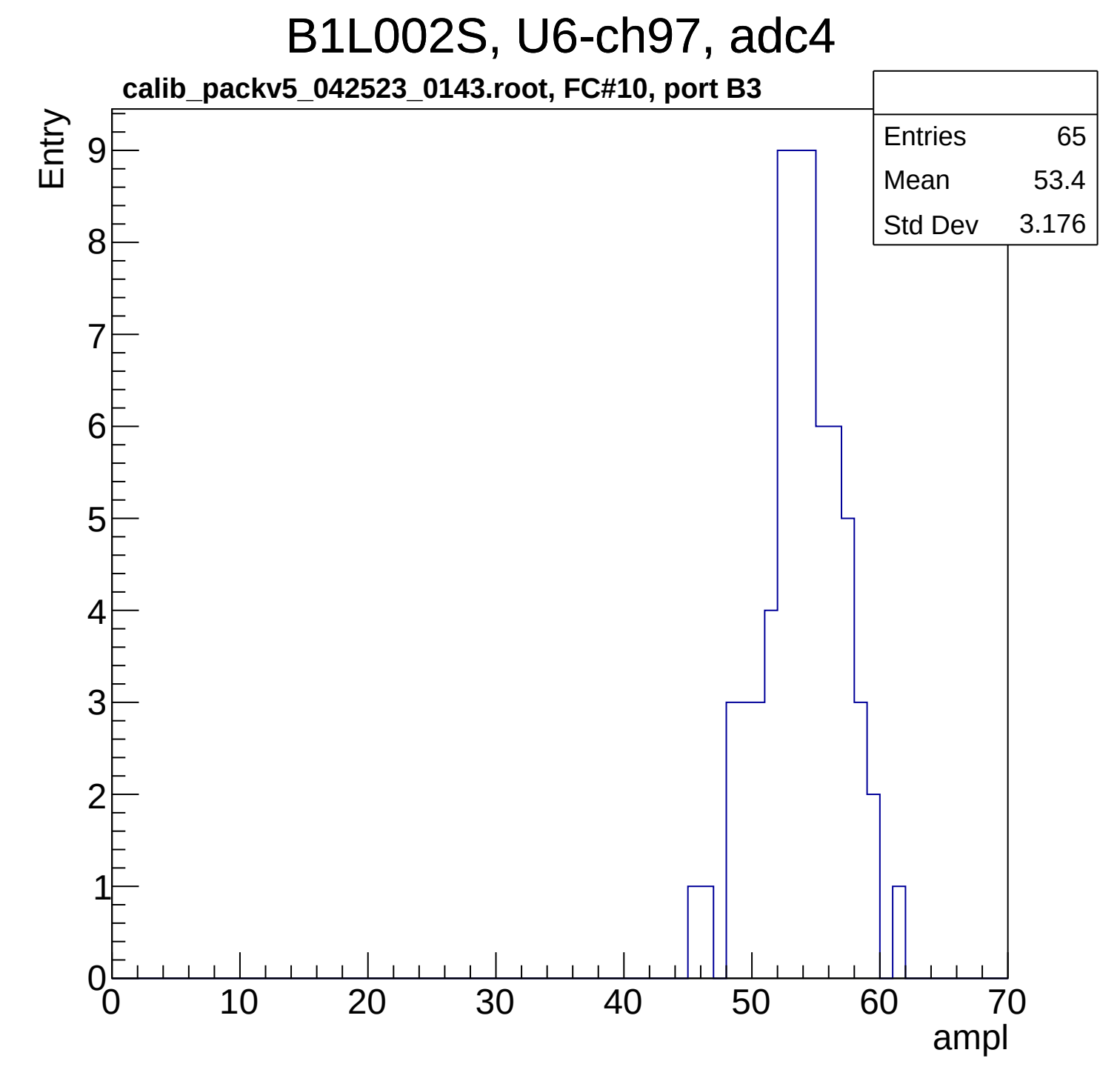

B1L002S, U6-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

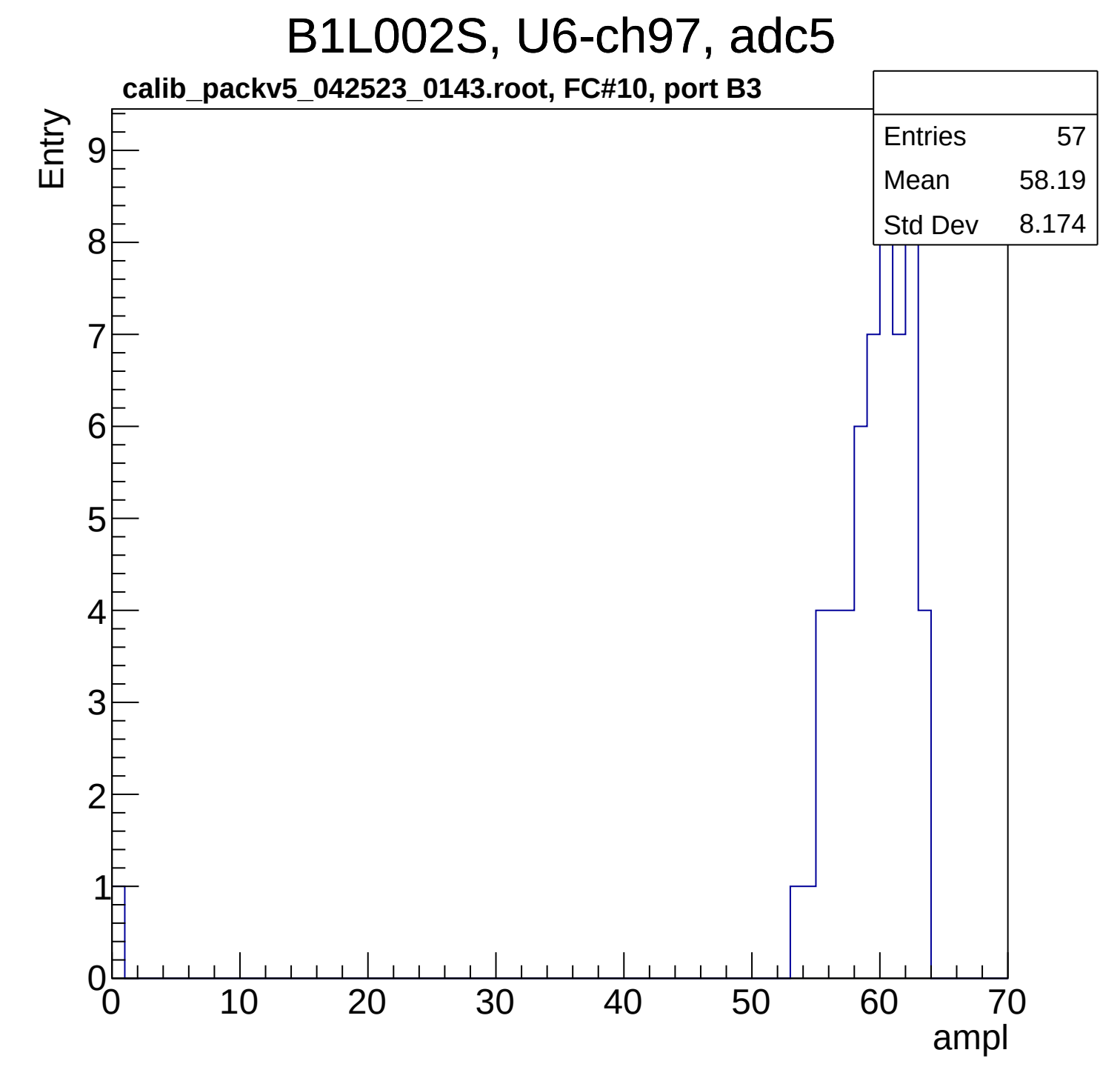

B1L002S, U6-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.24
Std Dev 4.447
Gaus mean : 28.1582
Gaus Width: 3.0851
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

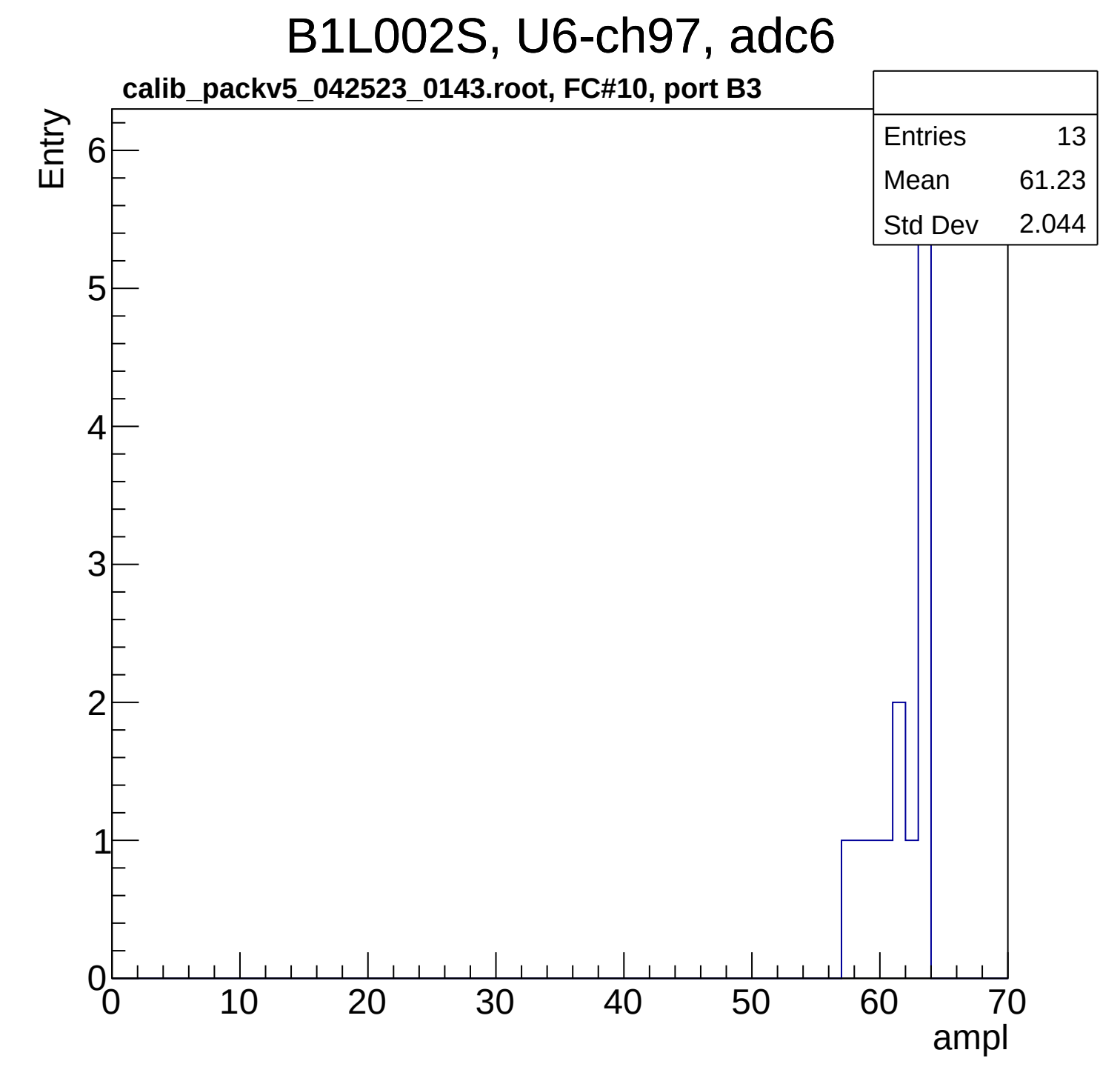

B1L002S, U6-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.83
Std Dev 3.003
Gaus mean : 34.7922
Gaus Width: 3.4715
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

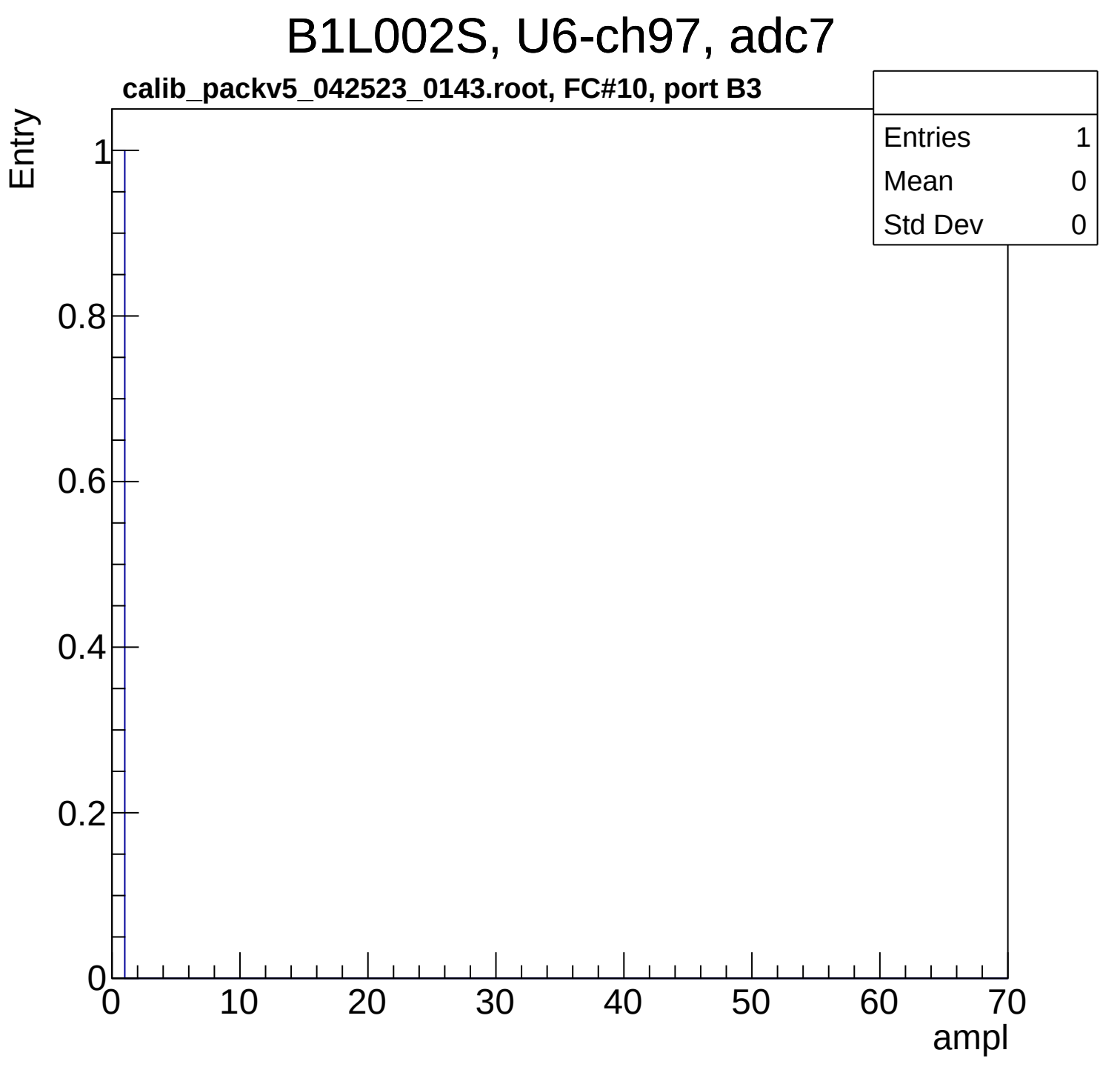

B1L002S, U6-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.39
Std Dev 2.997
Gaus mean : 42.0290
Gaus Width: 2.9410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

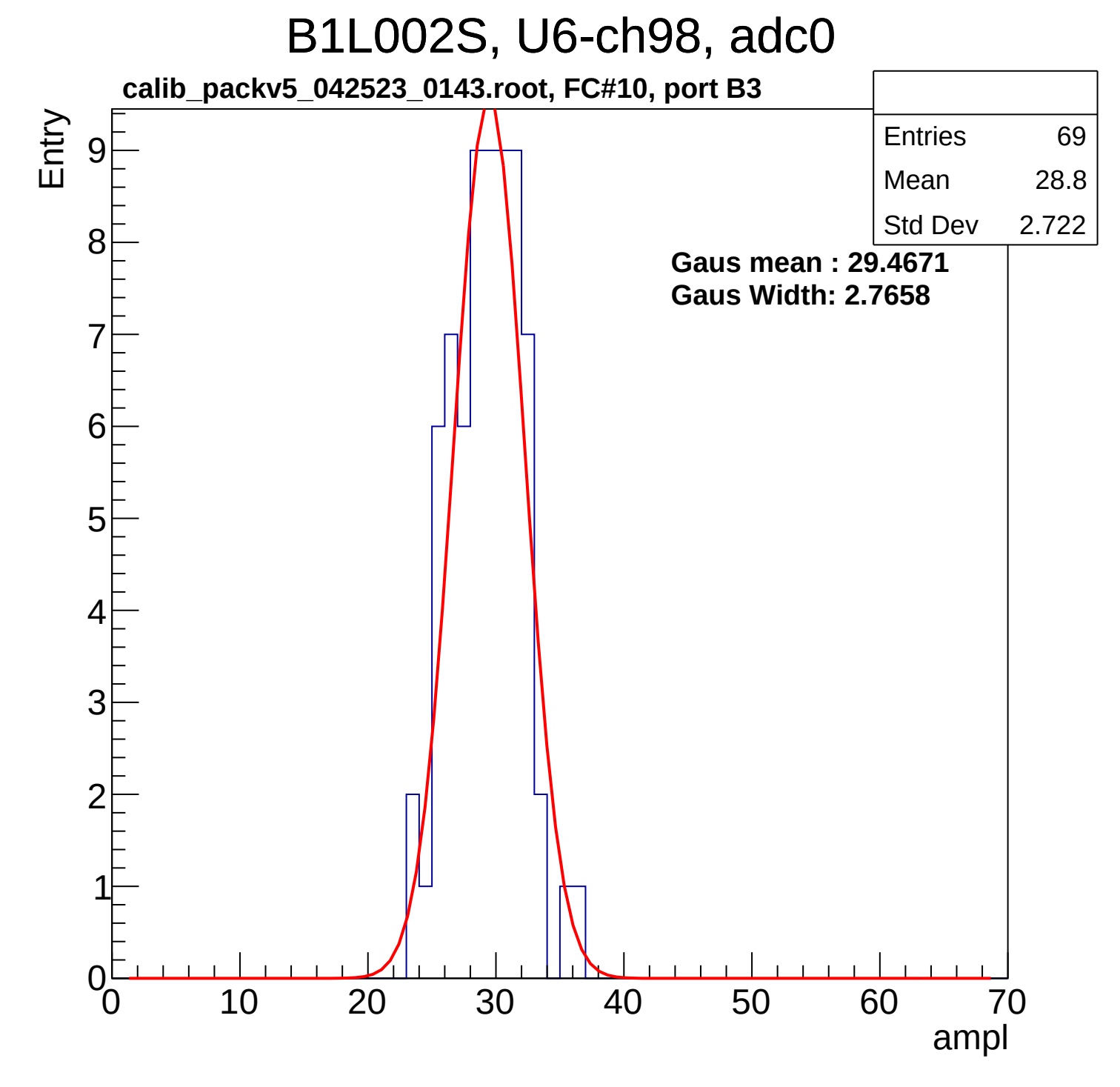

B1L002S, U6-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.03
Std Dev 2.894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

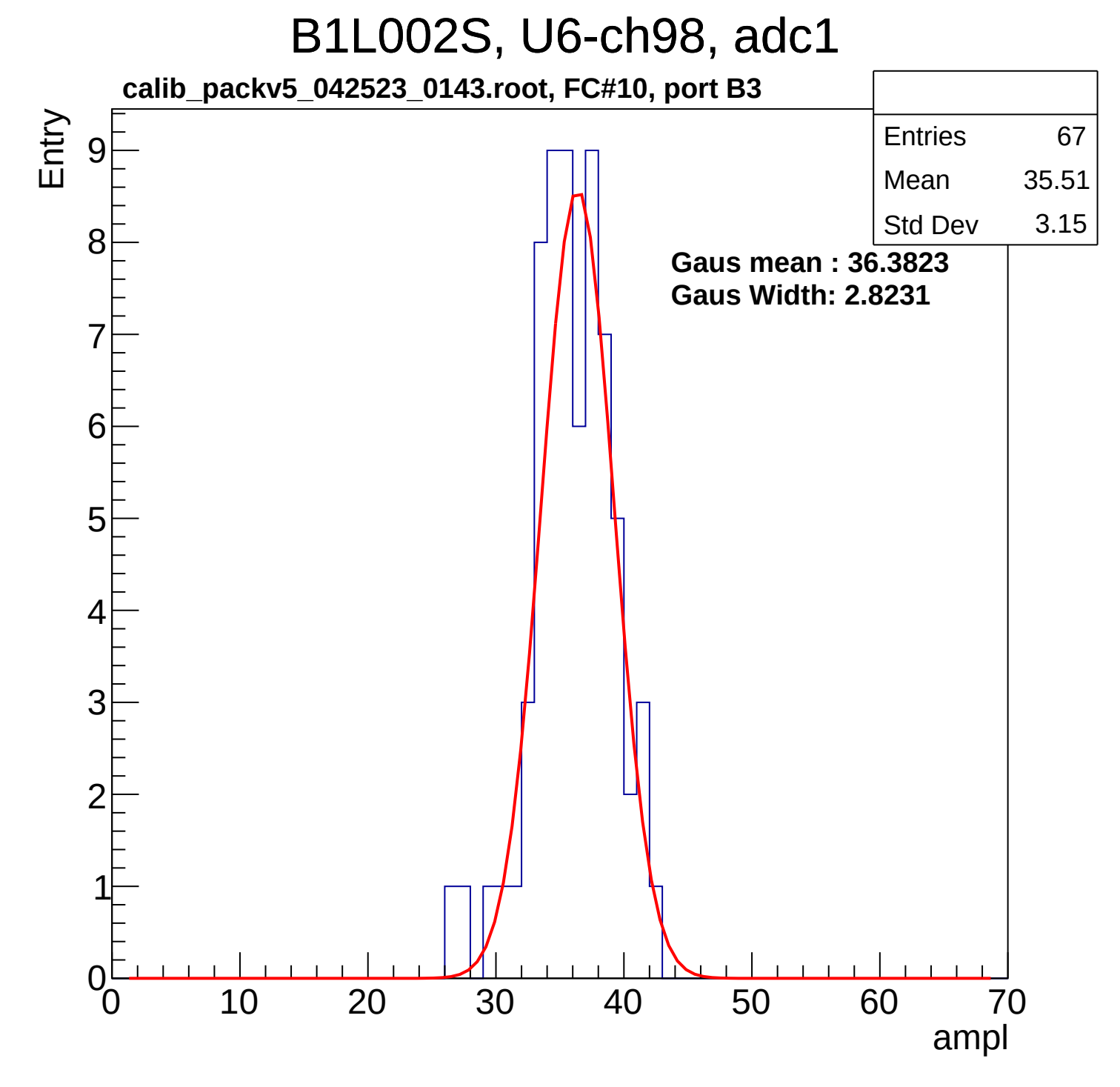

B1L002S, U6-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.4
Std Dev 3.176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

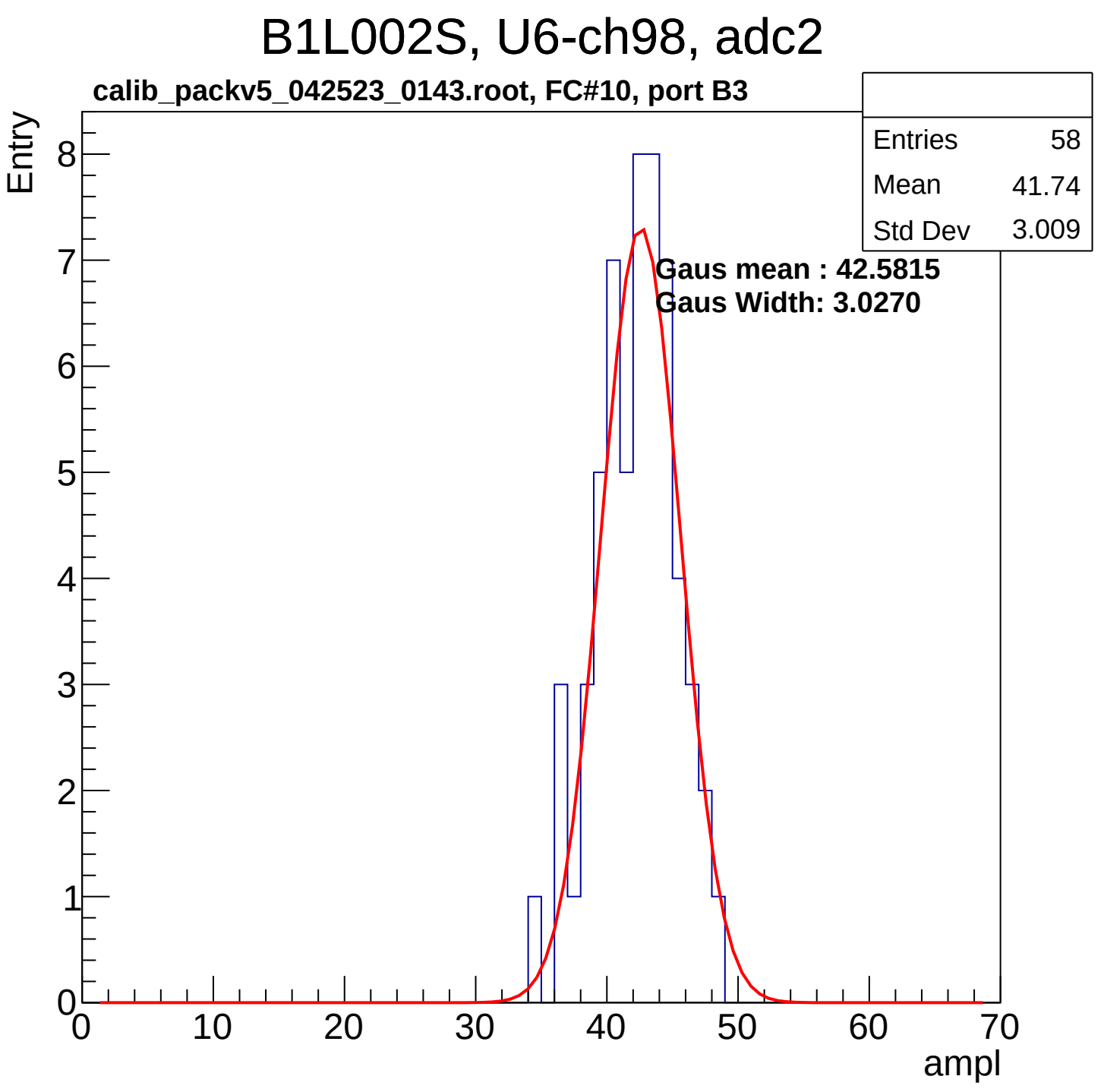

B1L002S, U6-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.19
Std Dev 8.174
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

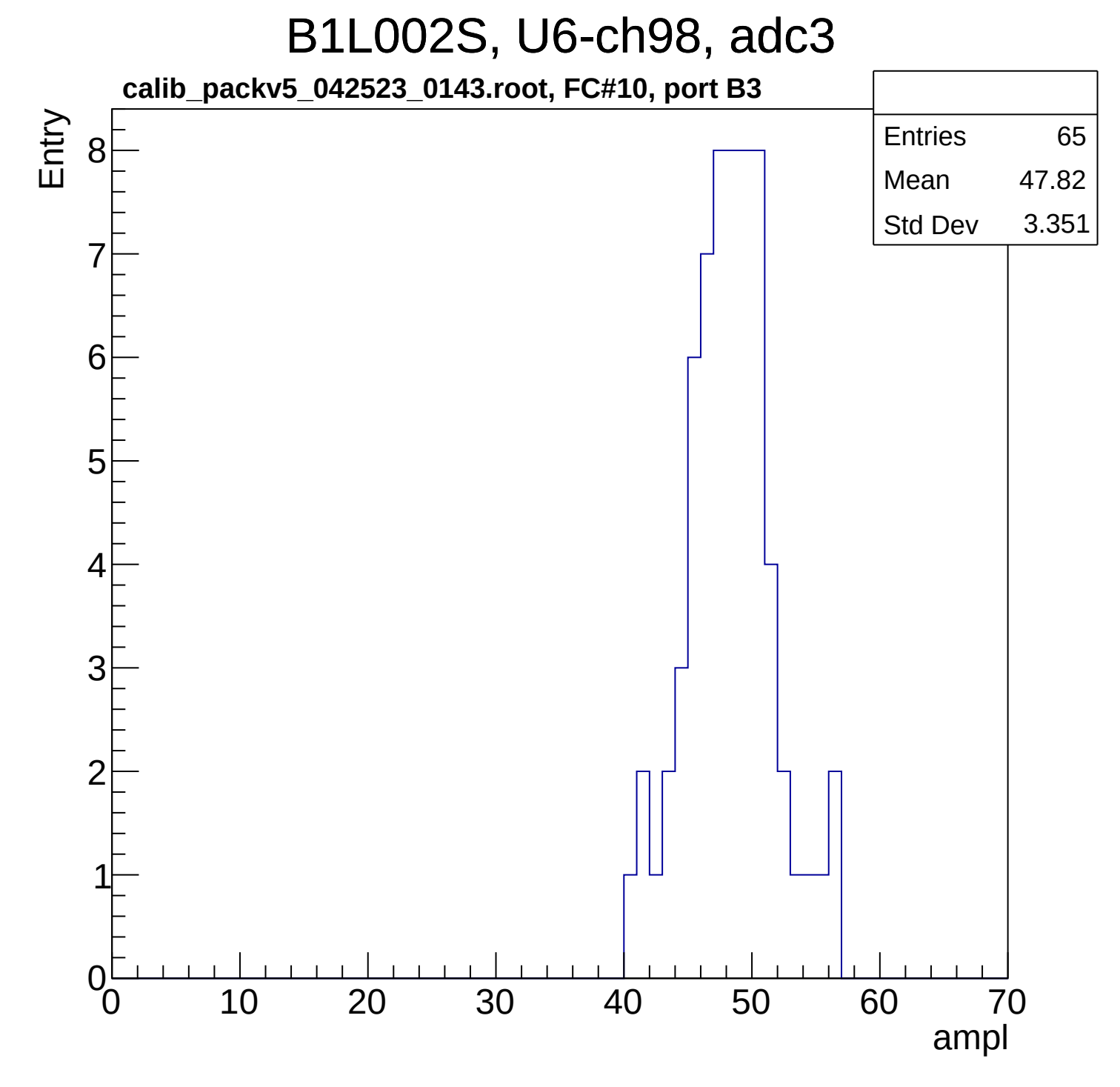

B1L002S, U6-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.23
Std Dev 2.044
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

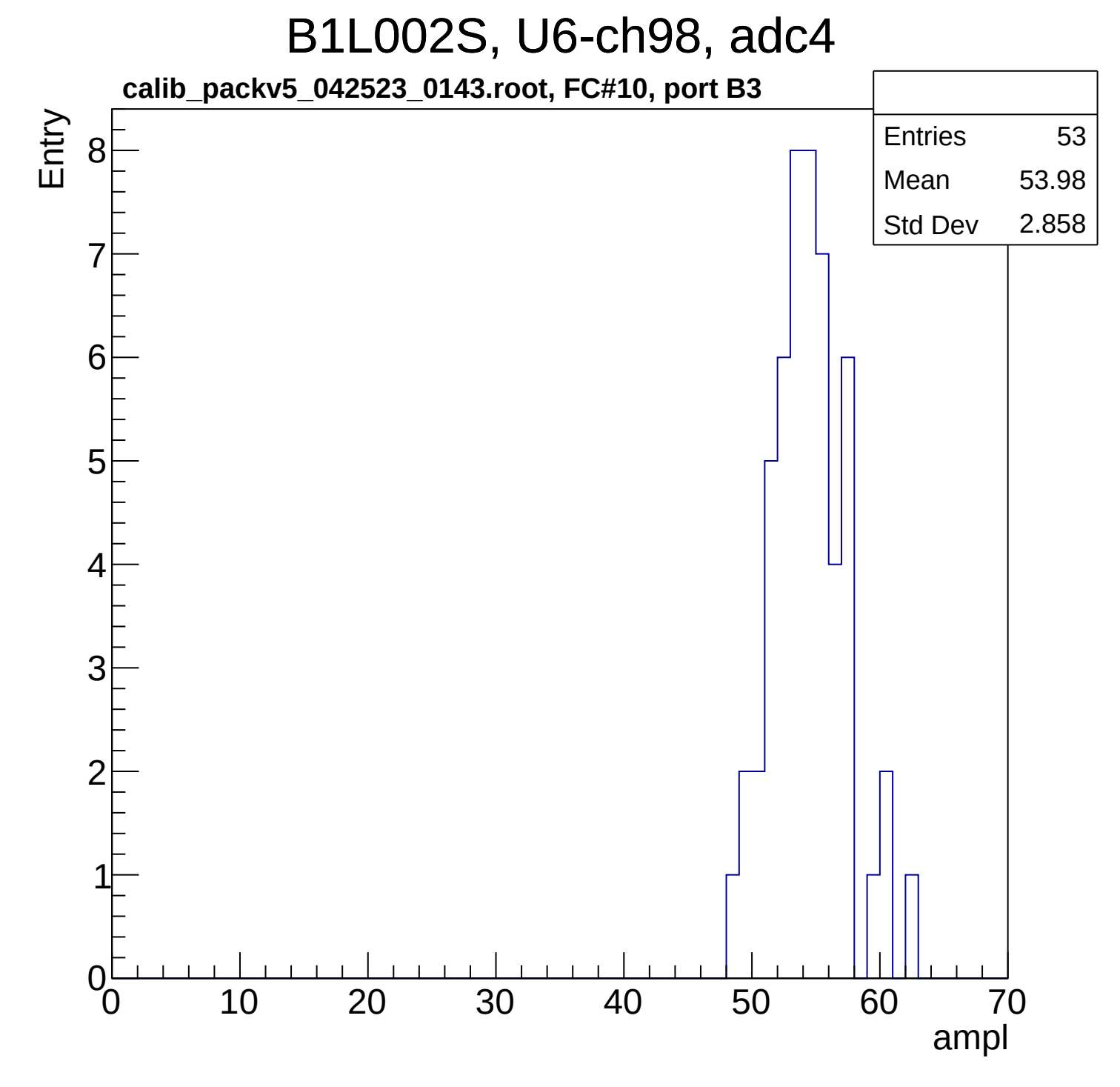

B1L002S, U6-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

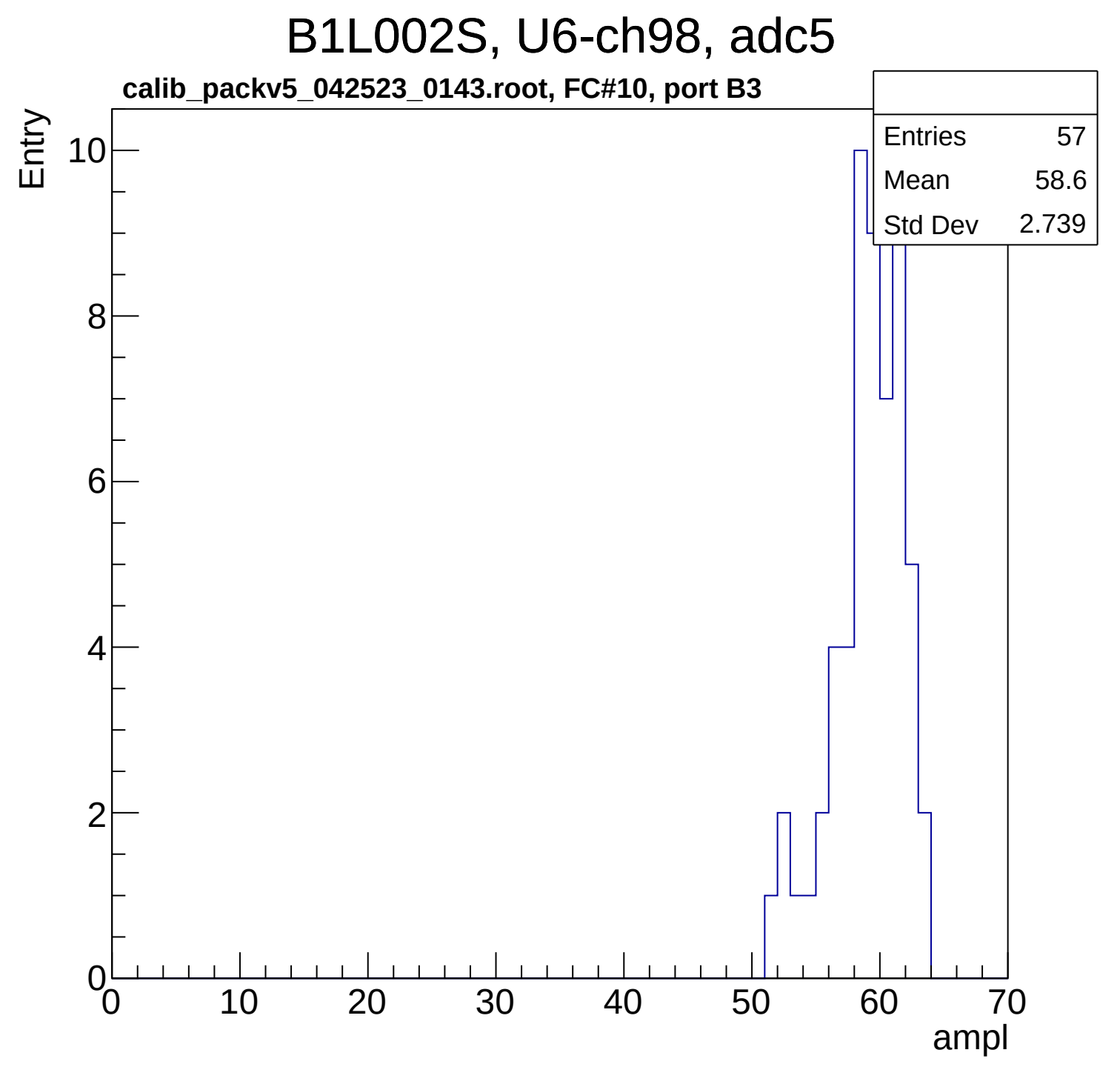

B1L002S, U6-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.8
Std Dev 2.722
Gaus mean : 29.4671
Gaus Width: 2.7658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

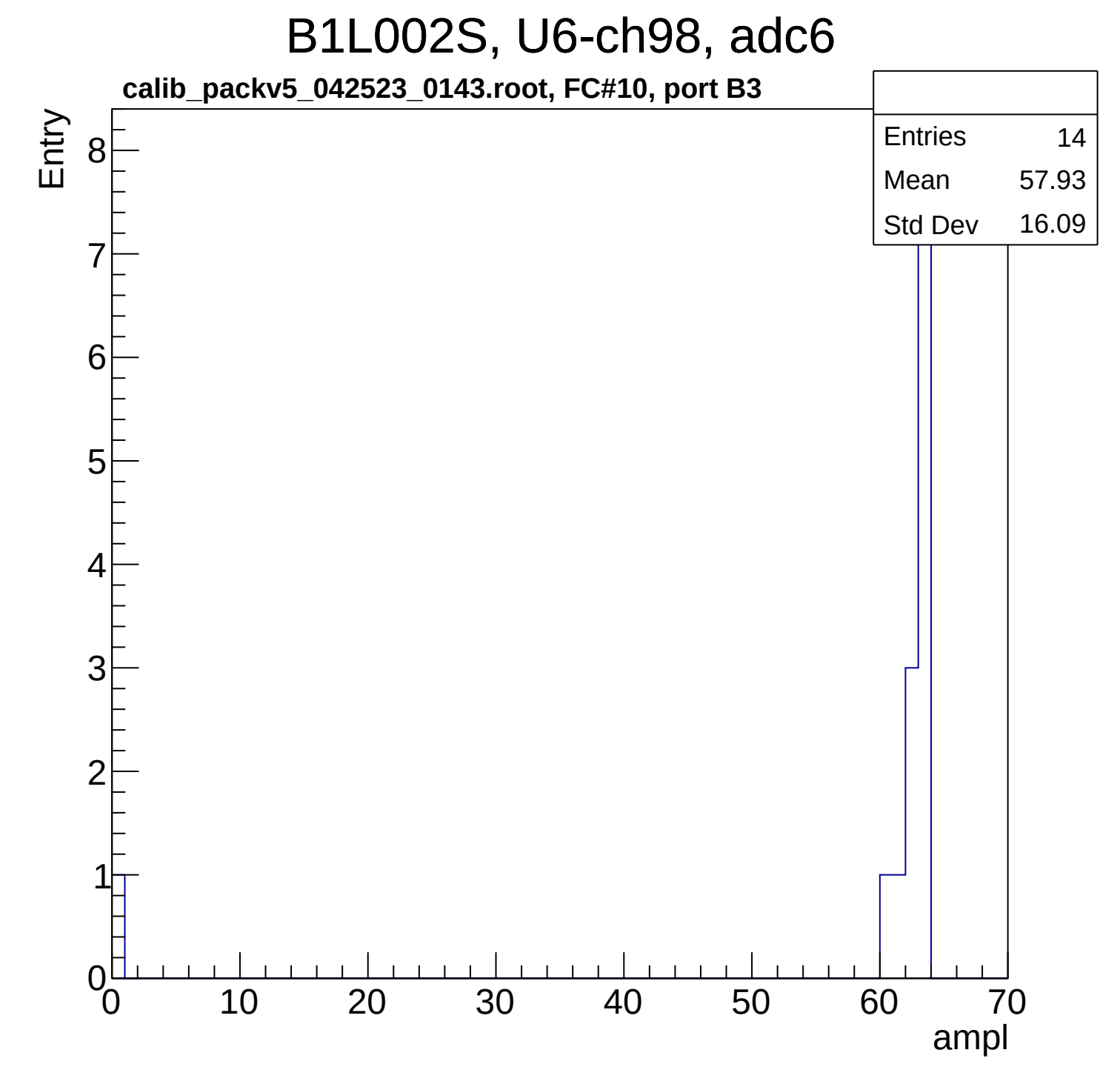

B1L002S, U6-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.51
Std Dev 3.15
Gaus mean : 36.3823
Gaus Width: 2.8231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

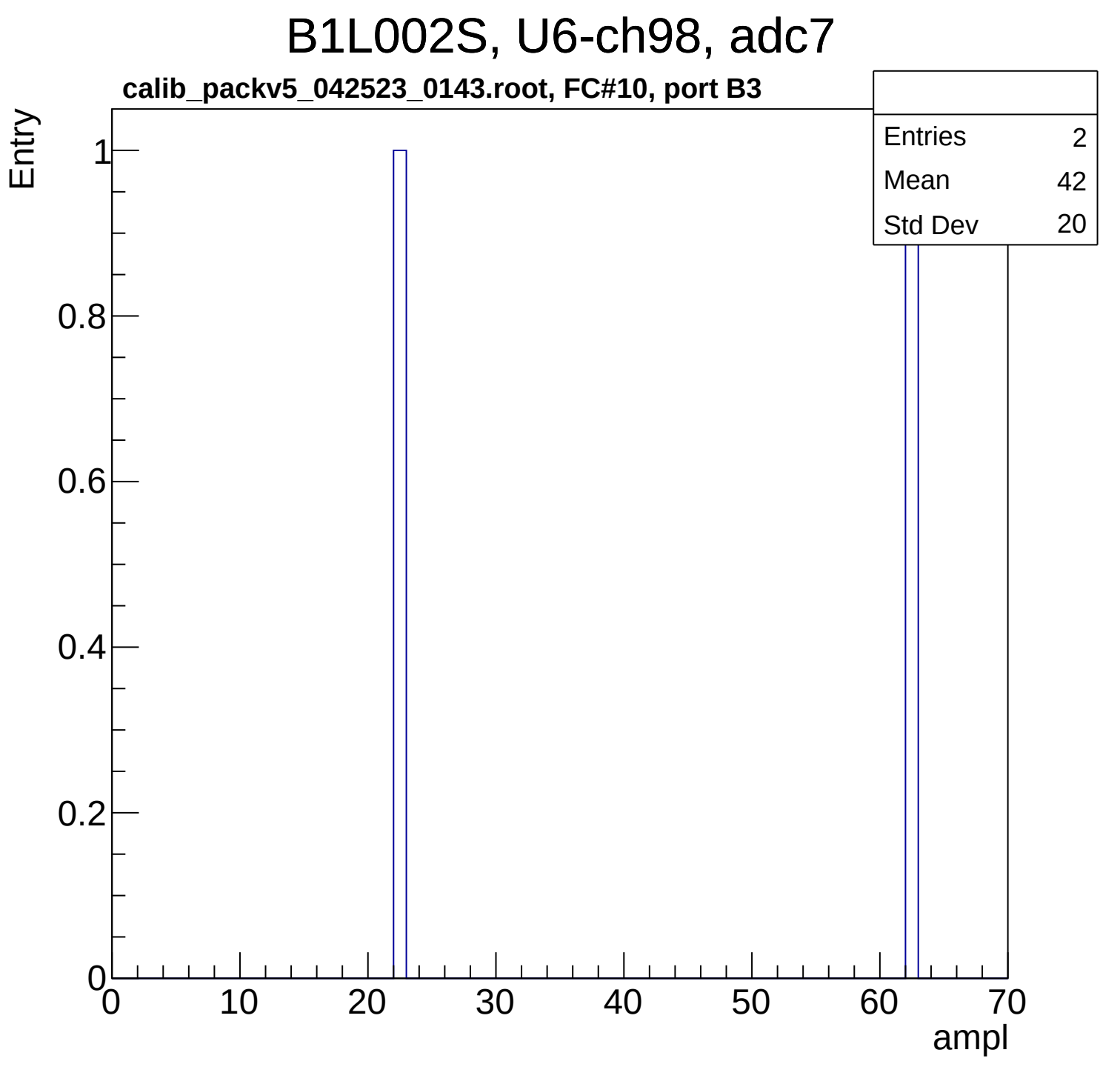

B1L002S, U6-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.74
Std Dev 3.009
Gaus mean : 42.5815
Gaus Width: 3.0270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

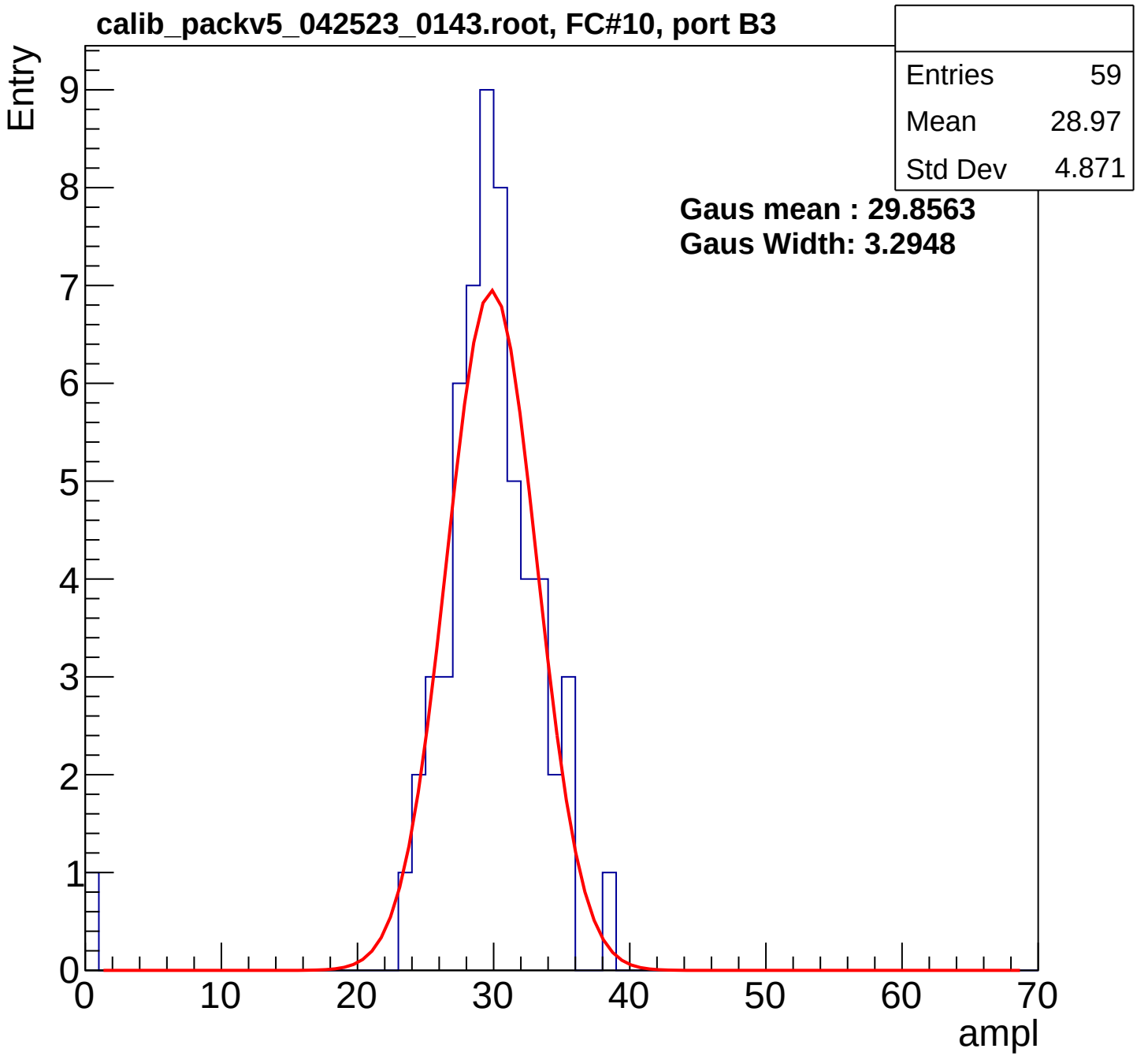

B1L002S, U6-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.82
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

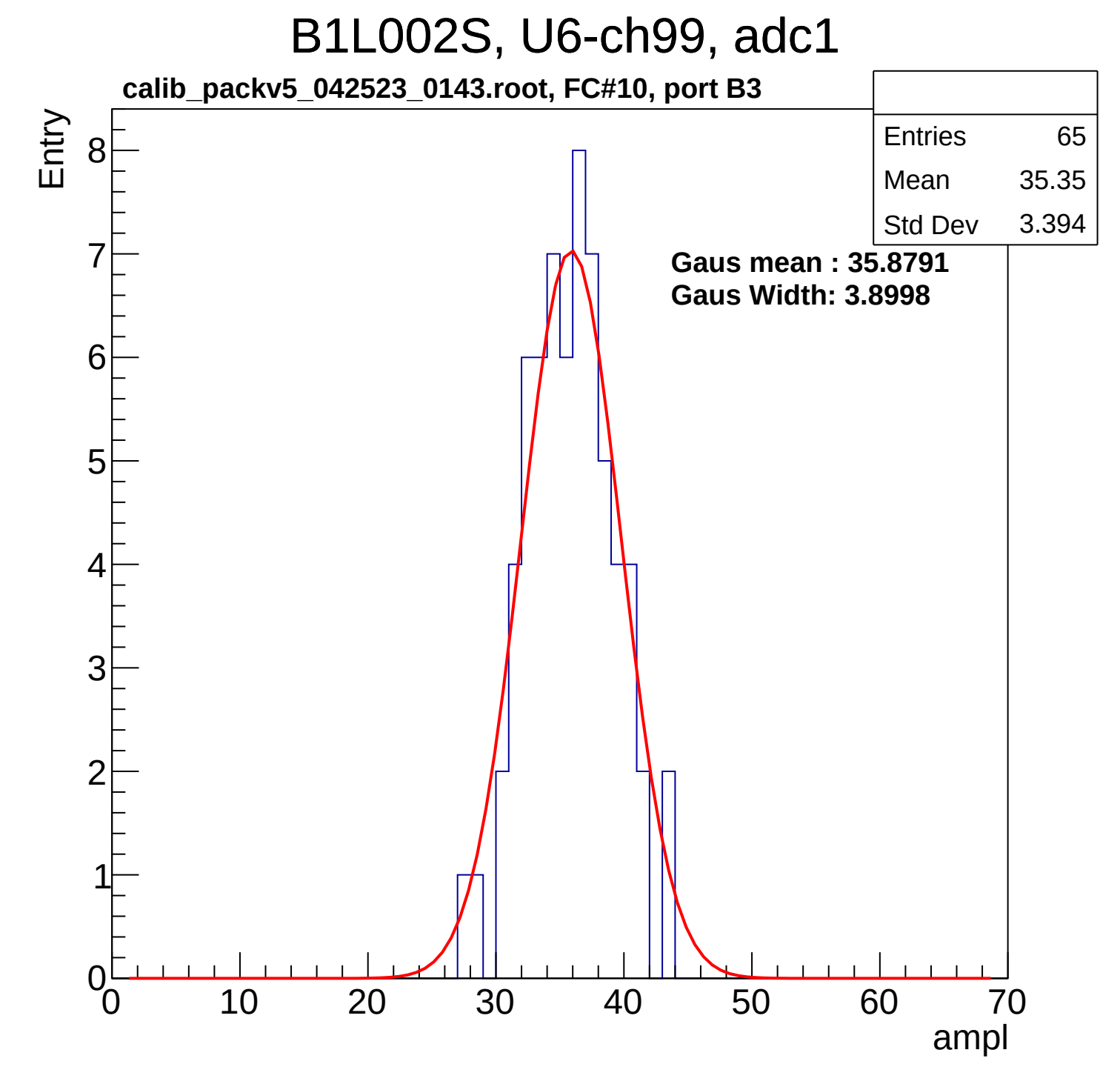

B1L002S, U6-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.98
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

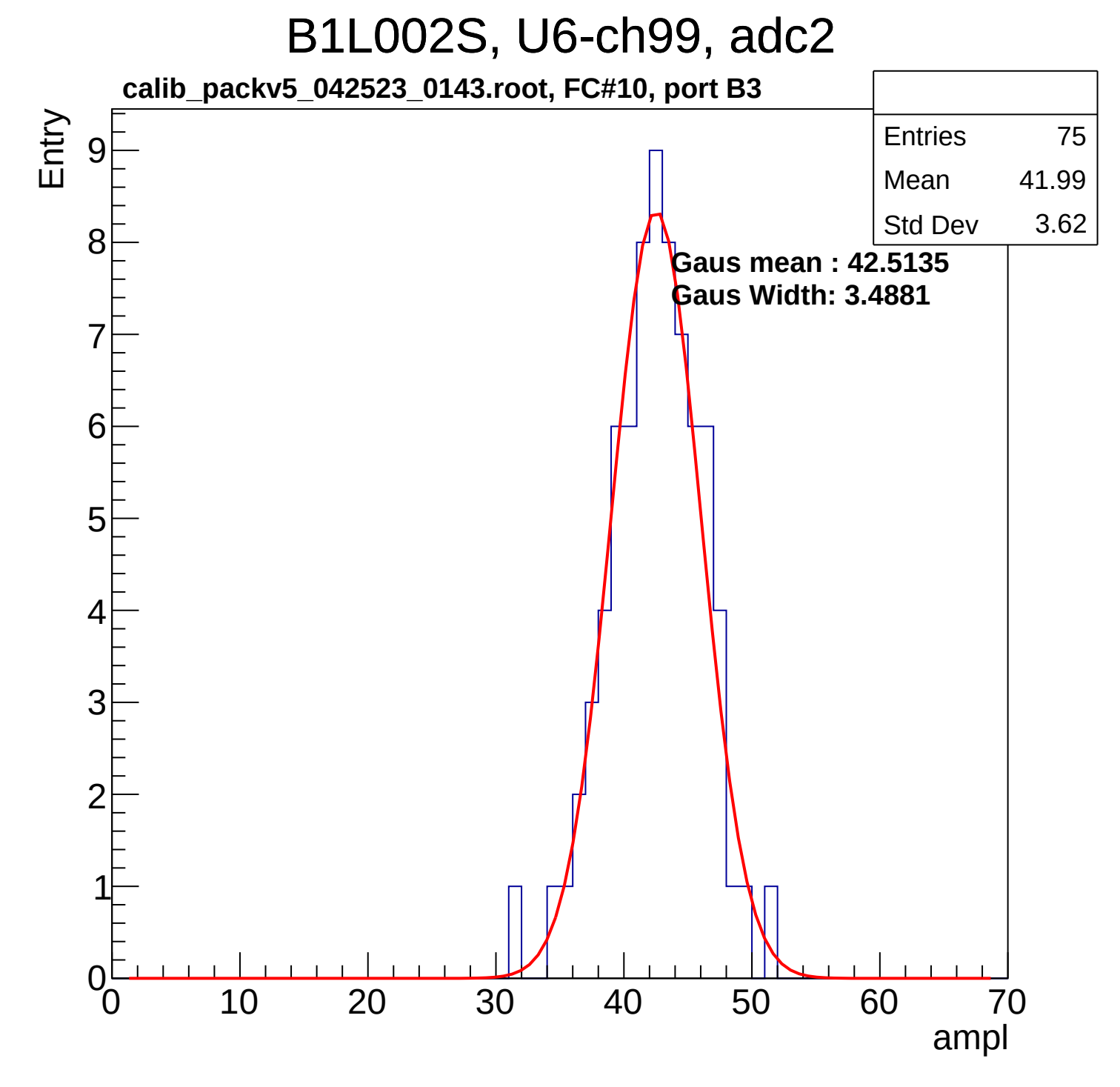

B1L002S, U6-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.6
Std Dev 2.739
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

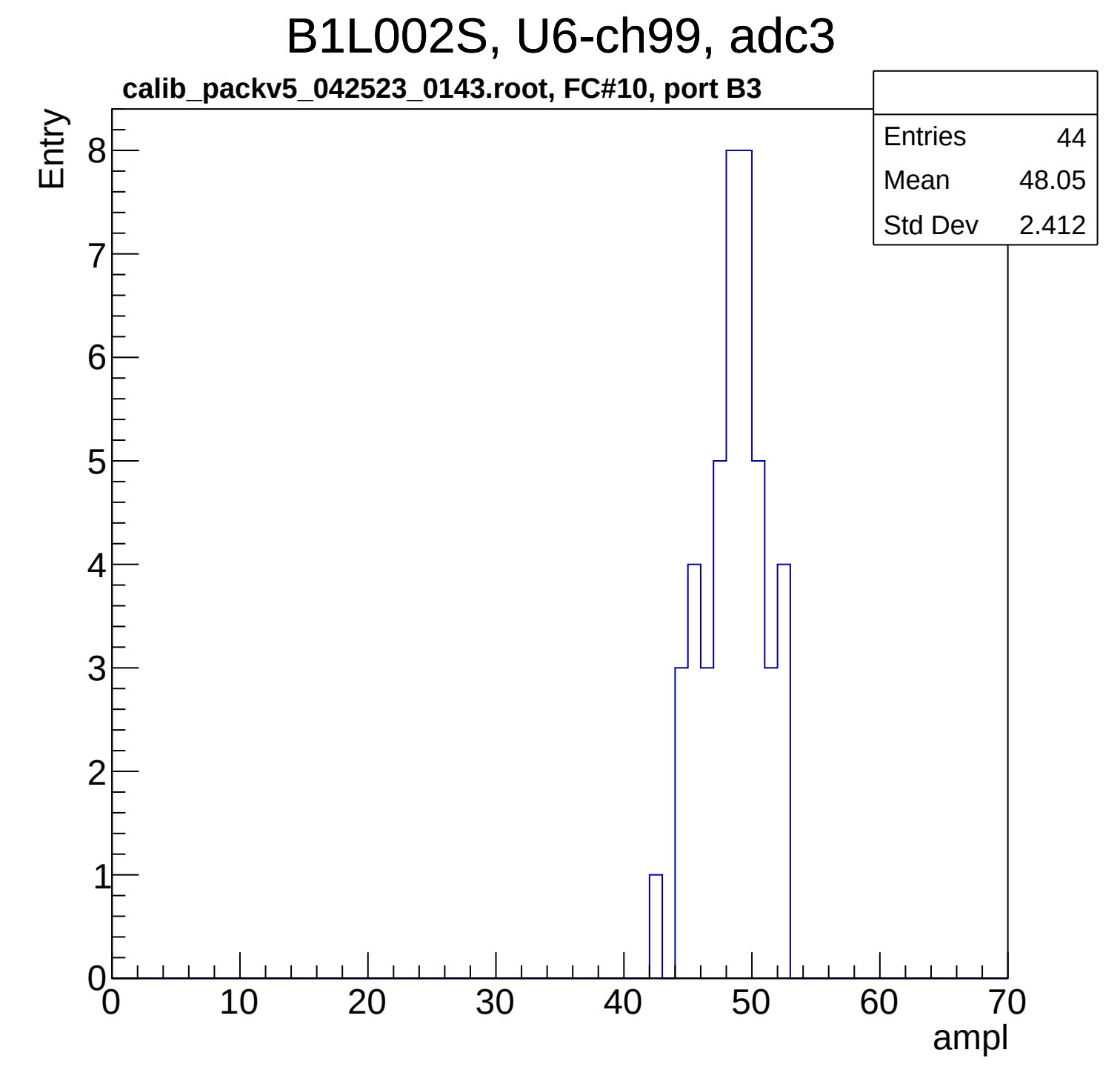

B1L002S, U6-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.93
Std Dev 16.09
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

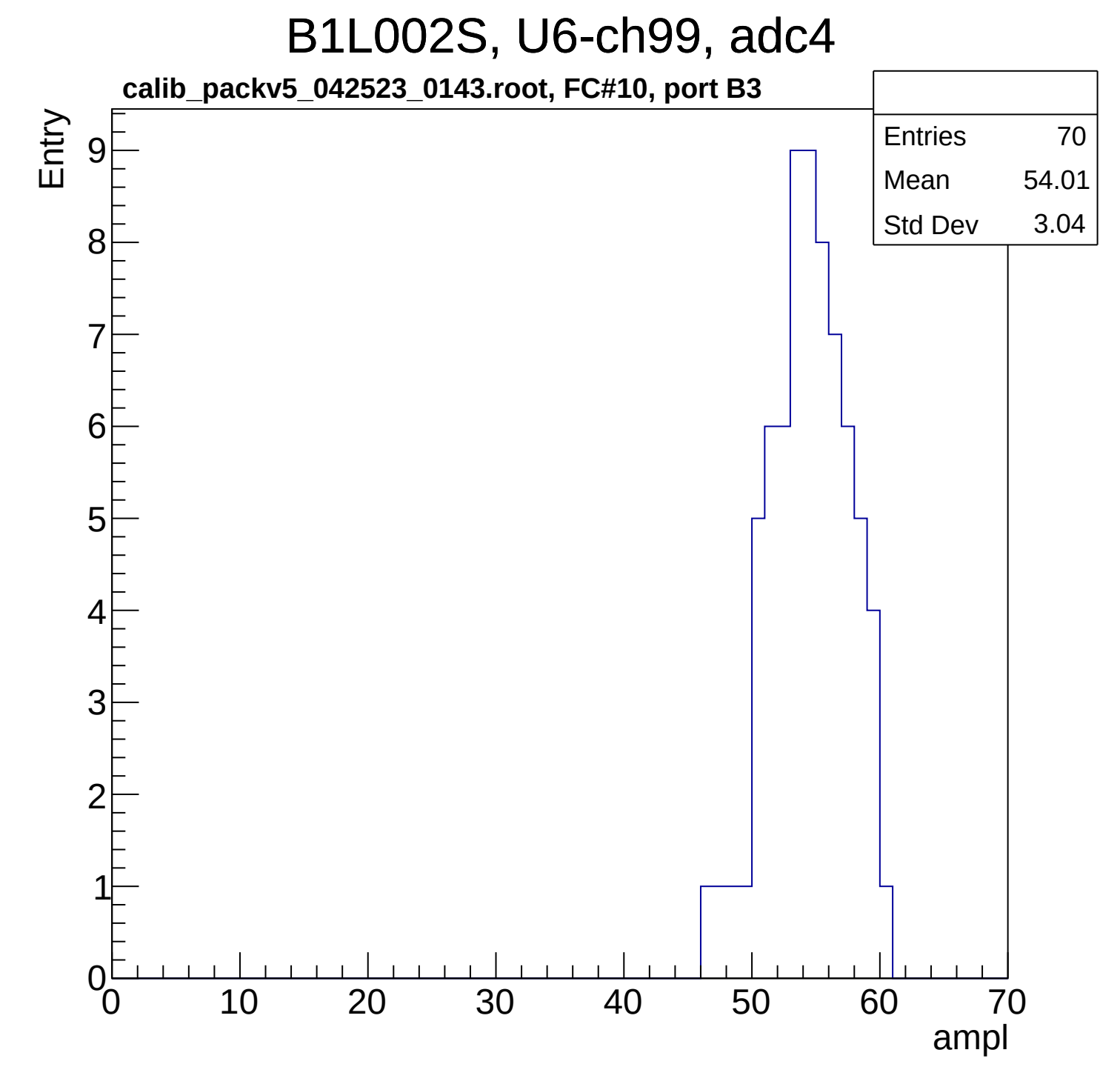

B1L002S, U6-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

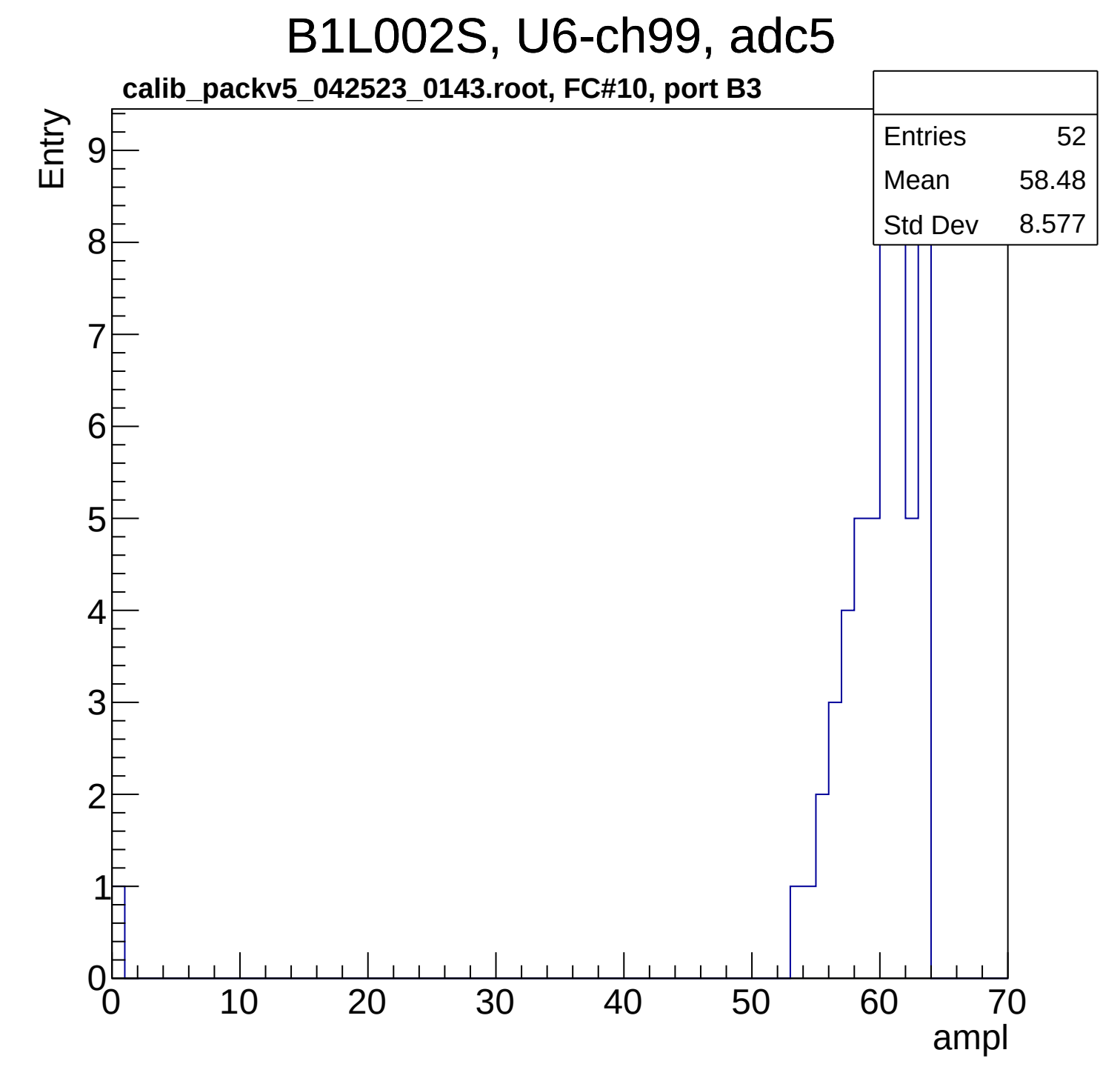

B1L002S, U6-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.97
Std Dev 4.871
Gaus mean : 29.8563
Gaus Width: 3.2948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

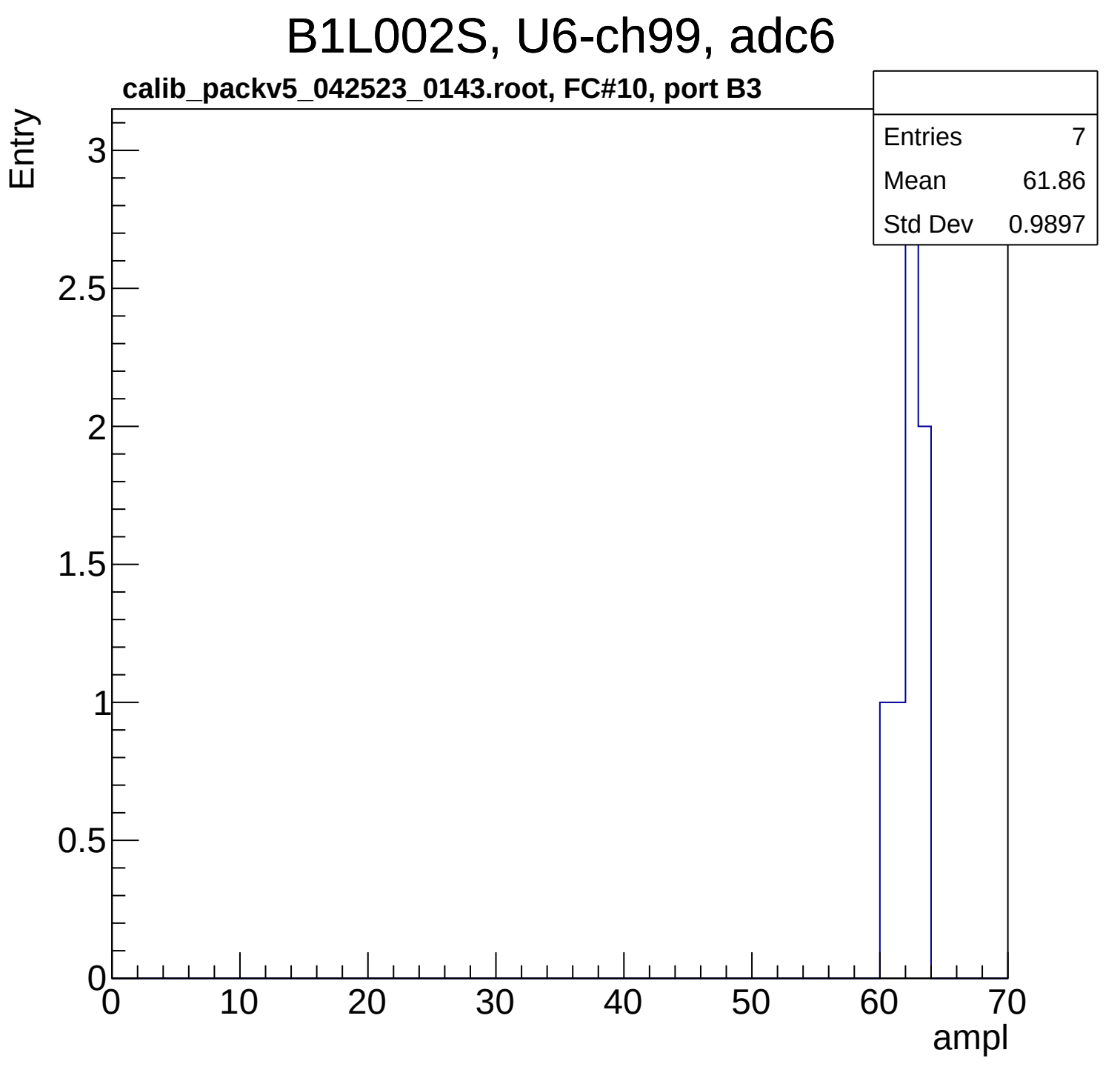

B1L002S, U6-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.35
Std Dev 3.394
Gaus mean : 35.8791
Gaus Width: 3.8998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

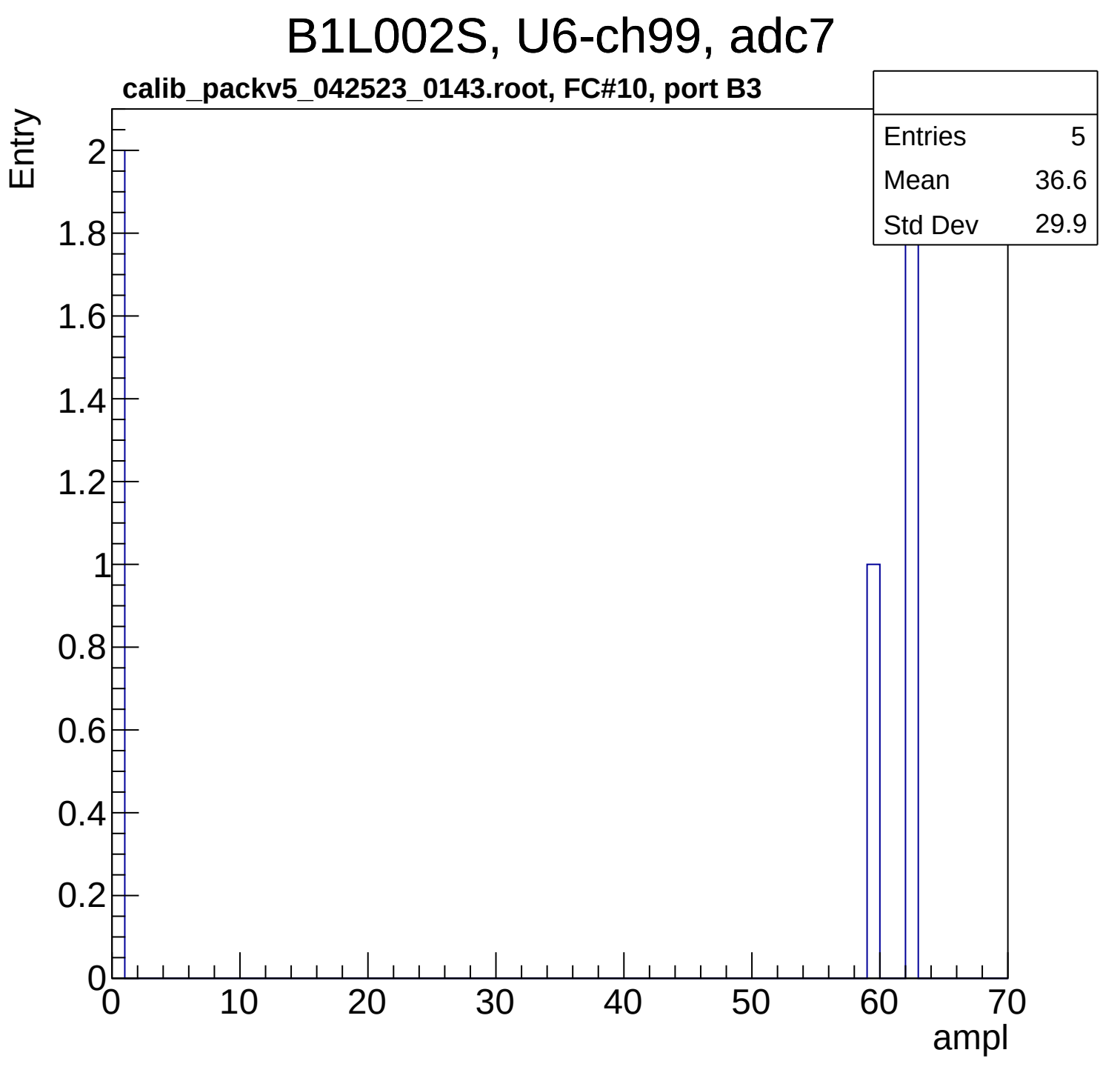

B1L002S, U6-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 41.99
Std Dev 3.62
Gaus mean : 42.5135
Gaus Width: 3.4881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

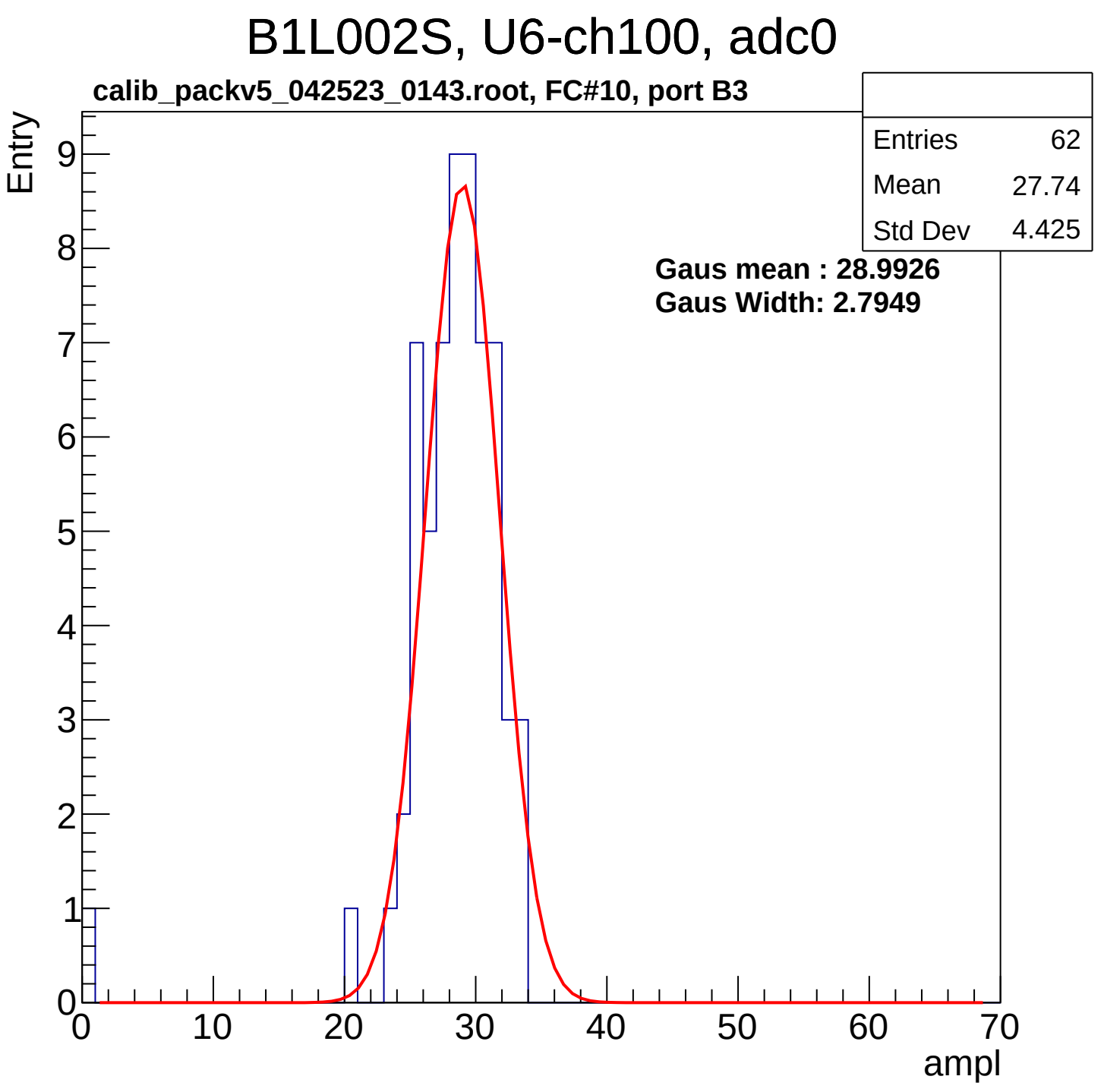

B1L002S, U6-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 48.05
Std Dev 2.412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

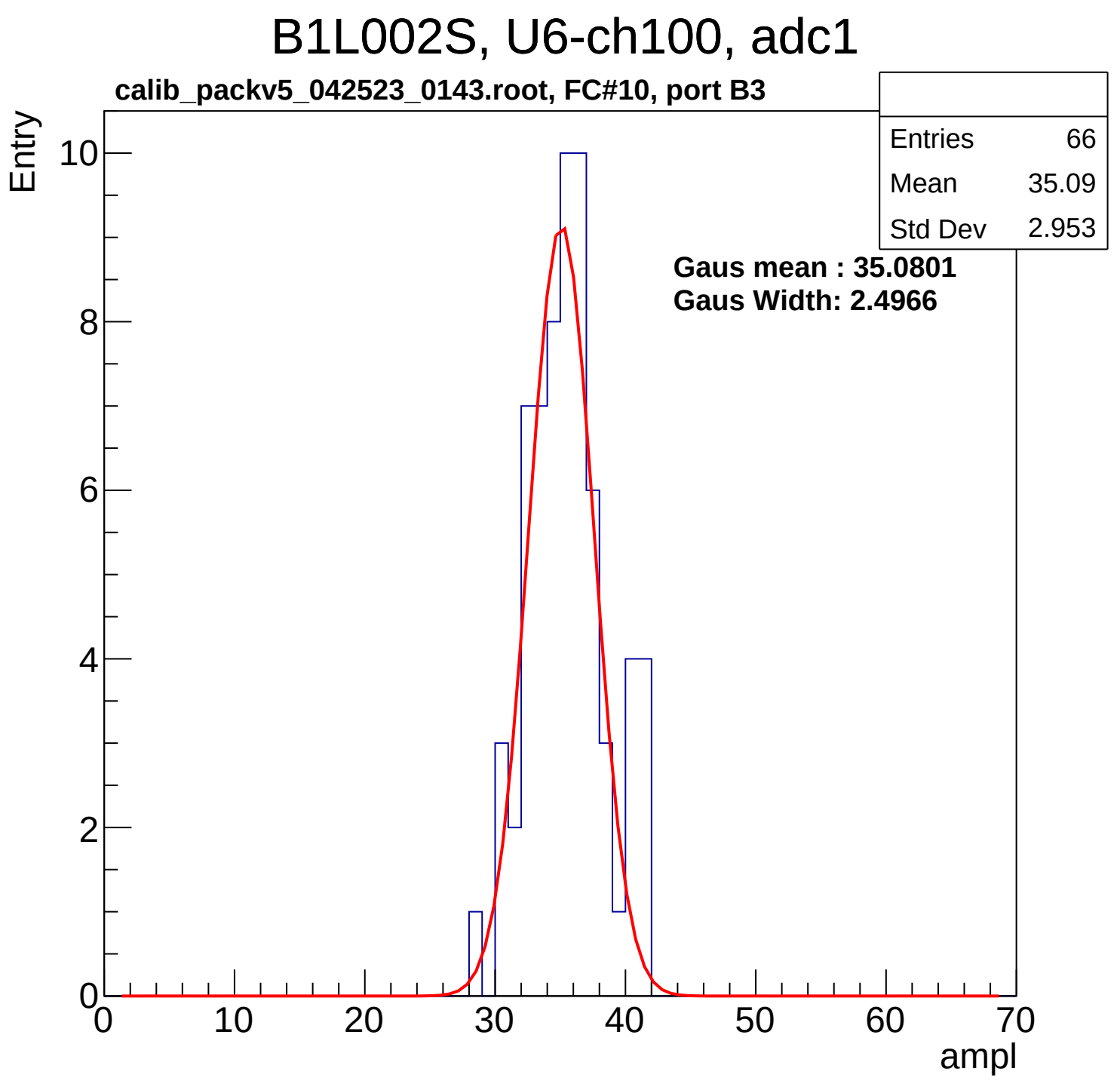

B1L002S, U6-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 54.01
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

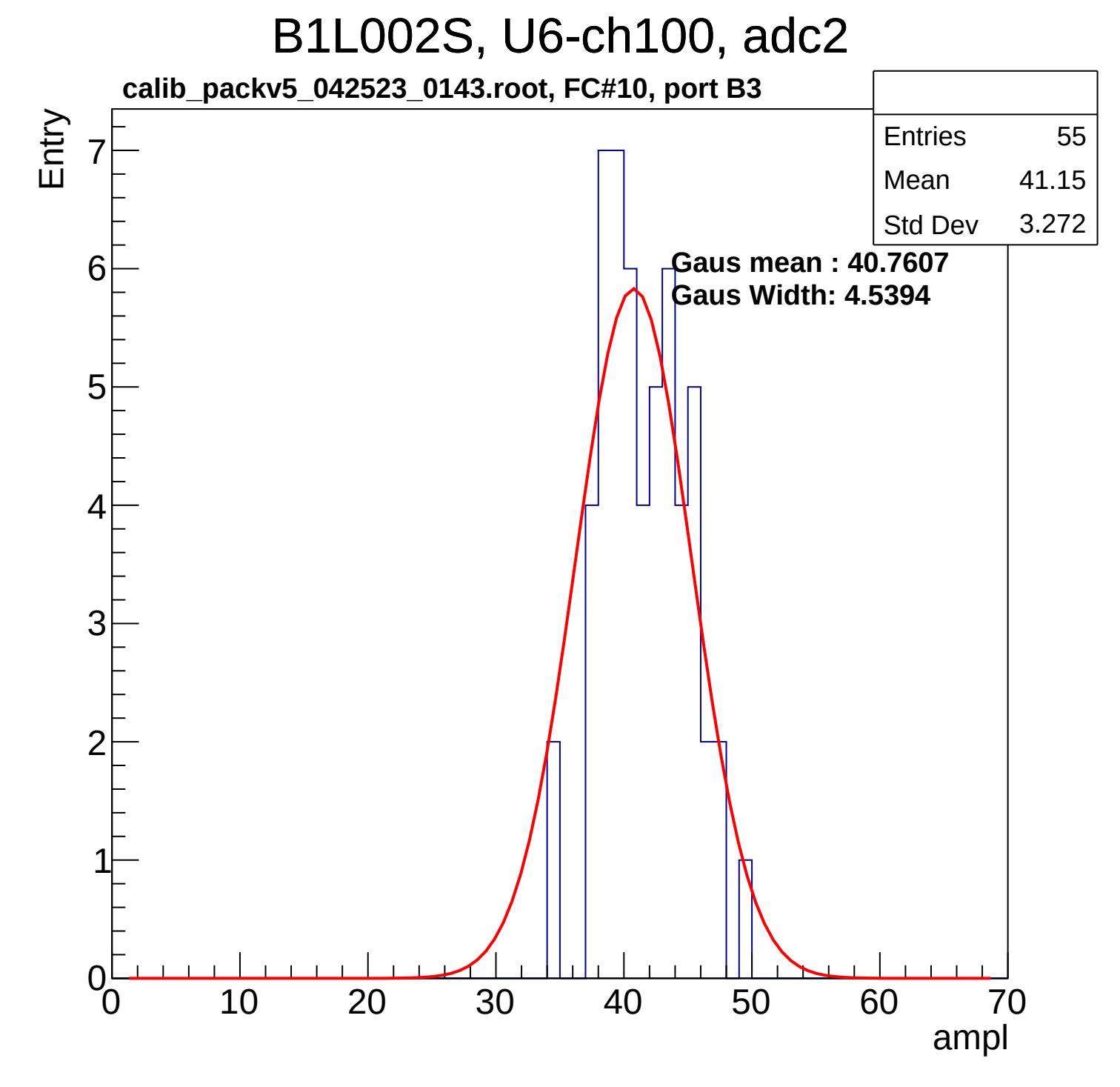

B1L002S, U6-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.48
Std Dev 8.577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

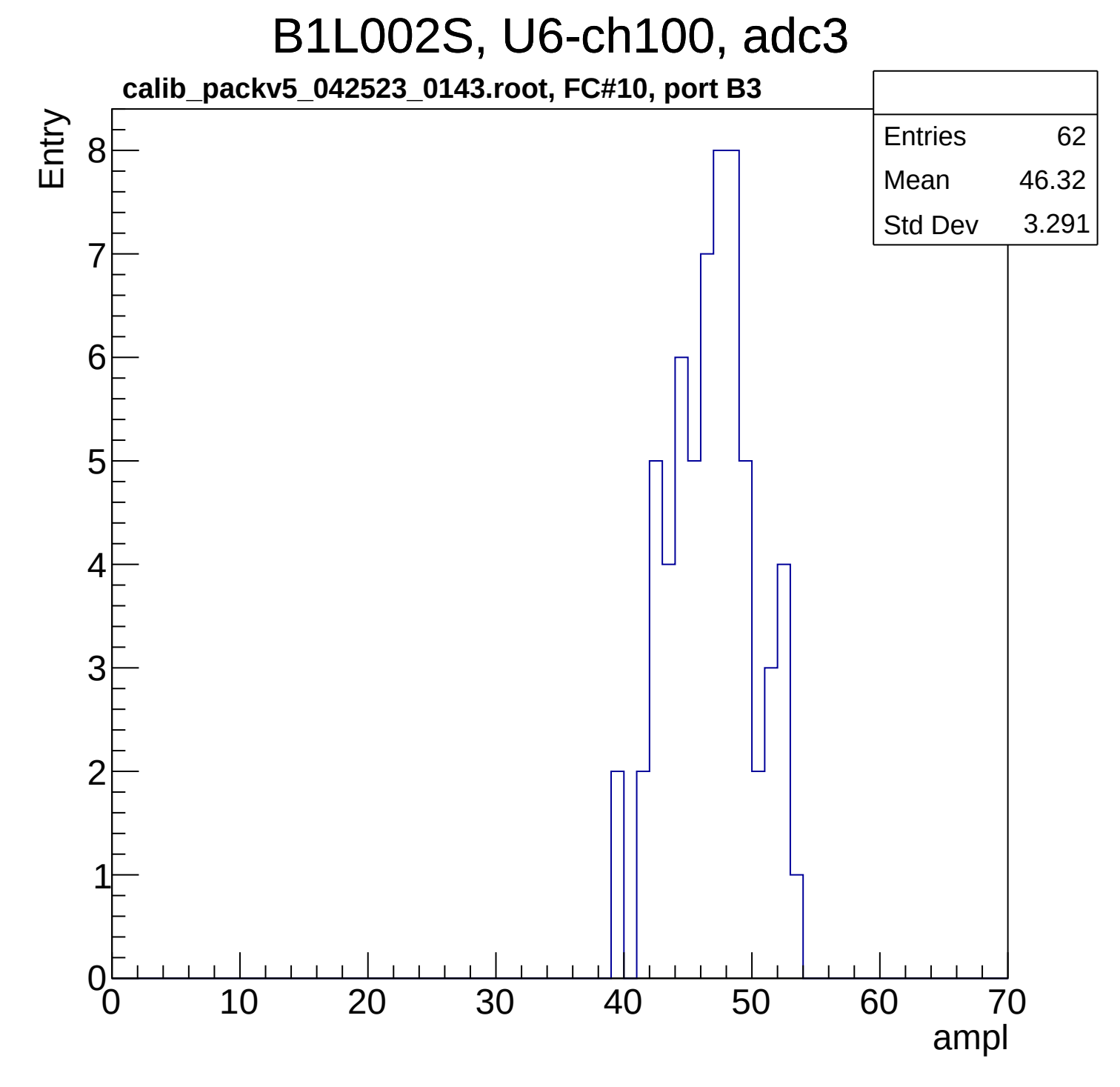

B1L002S, U6-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

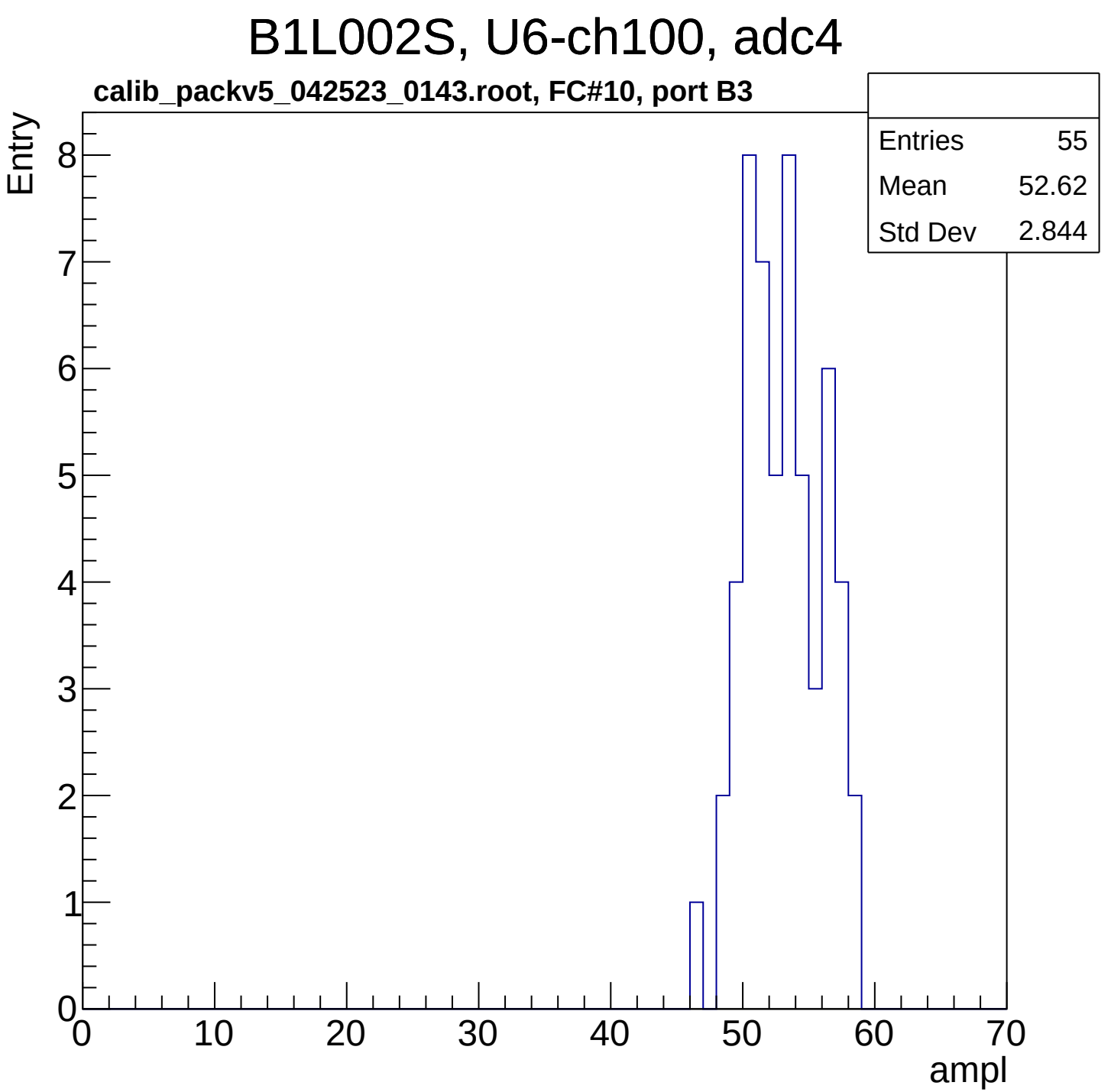

B1L002S, U6-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 36.6
Std Dev 29.9
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

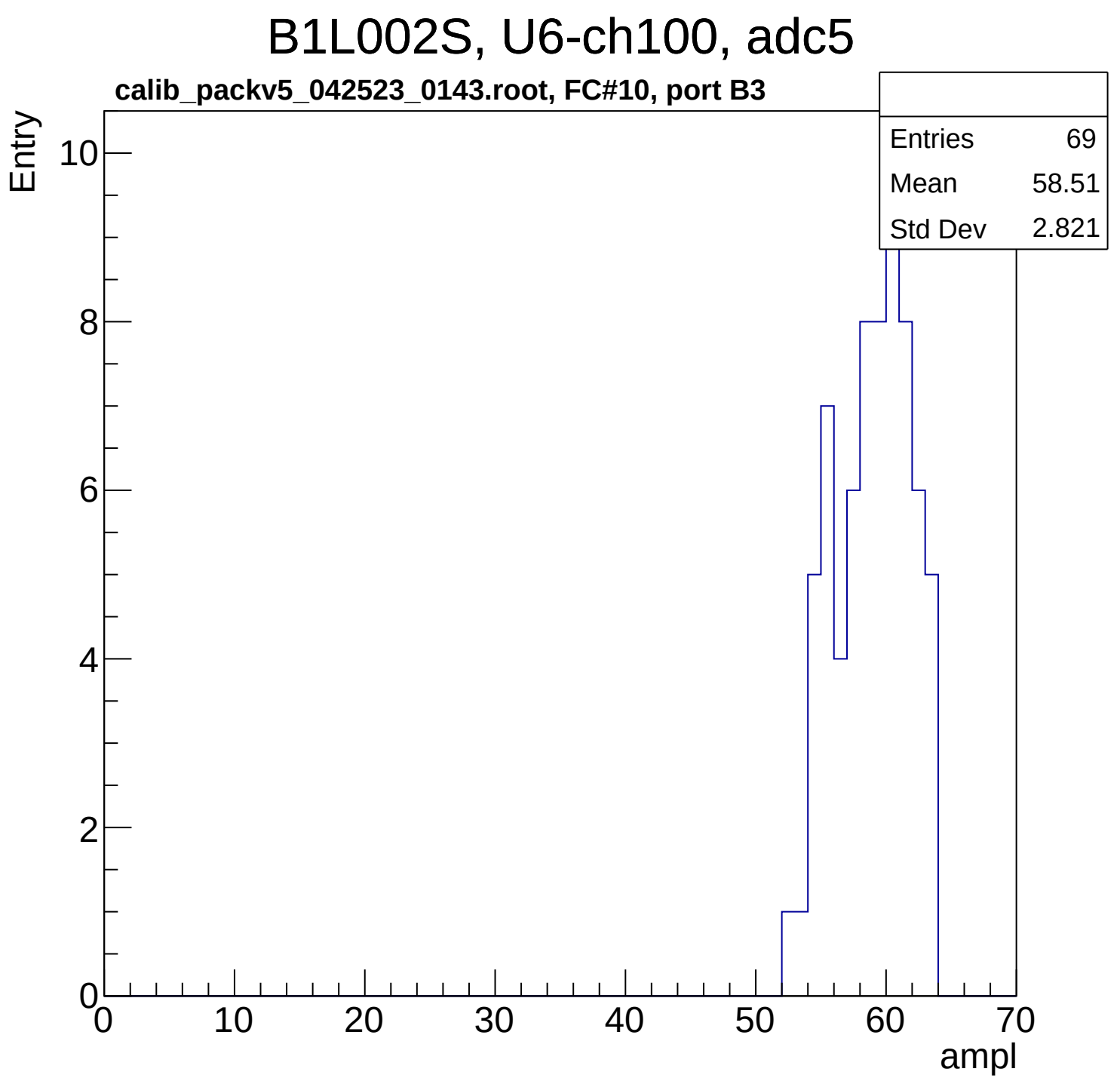

B1L002S, U6-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.74
Std Dev 4.425
Gaus mean : 28.9926
Gaus Width: 2.7949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

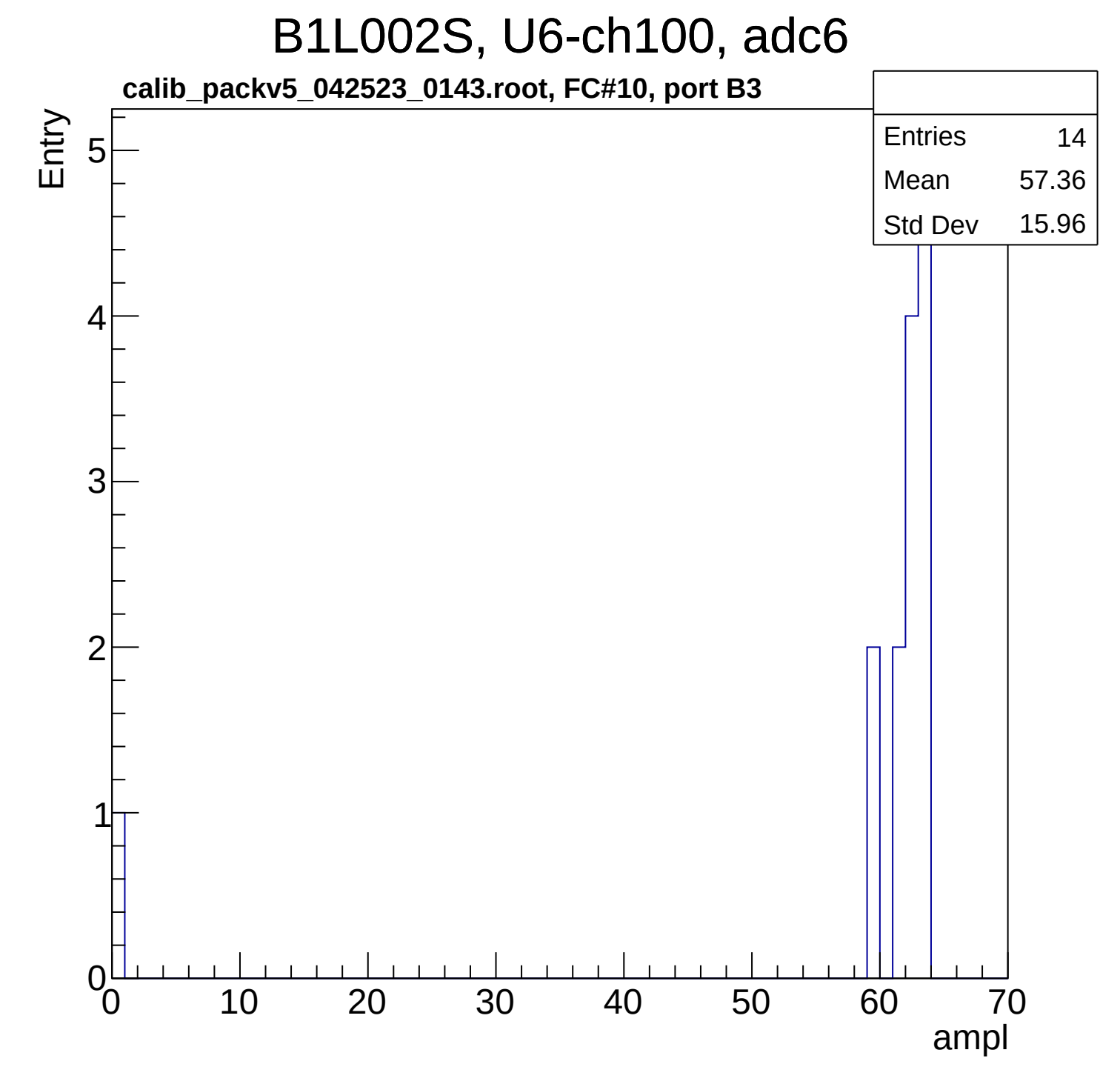

B1L002S, U6-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.09
Std Dev 2.953
Gaus mean : 35.0801
Gaus Width: 2.4966
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

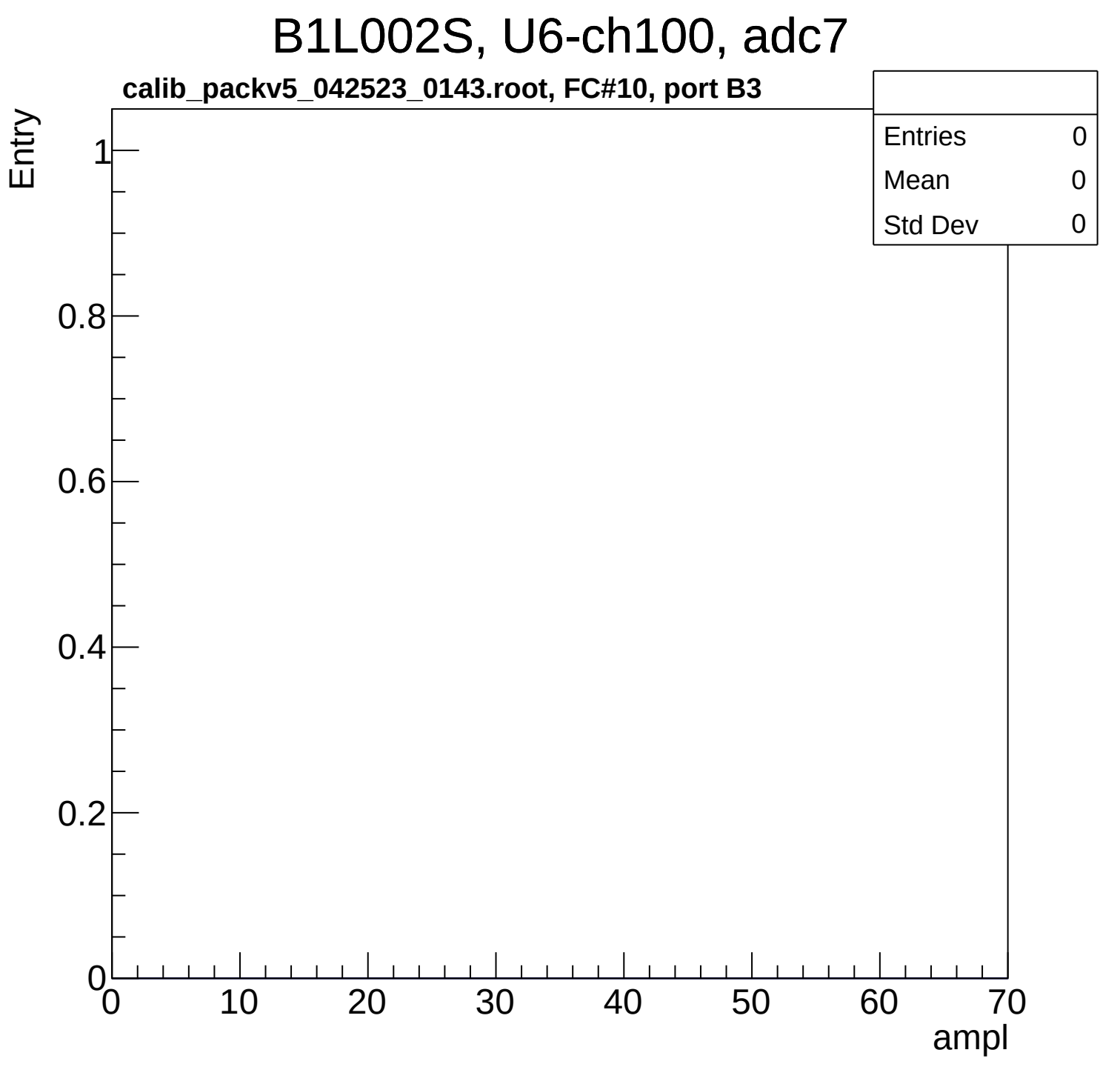

B1L002S, U6-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.15
Std Dev 3.272
Gaus mean : 40.7607
Gaus Width: 4.5394
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

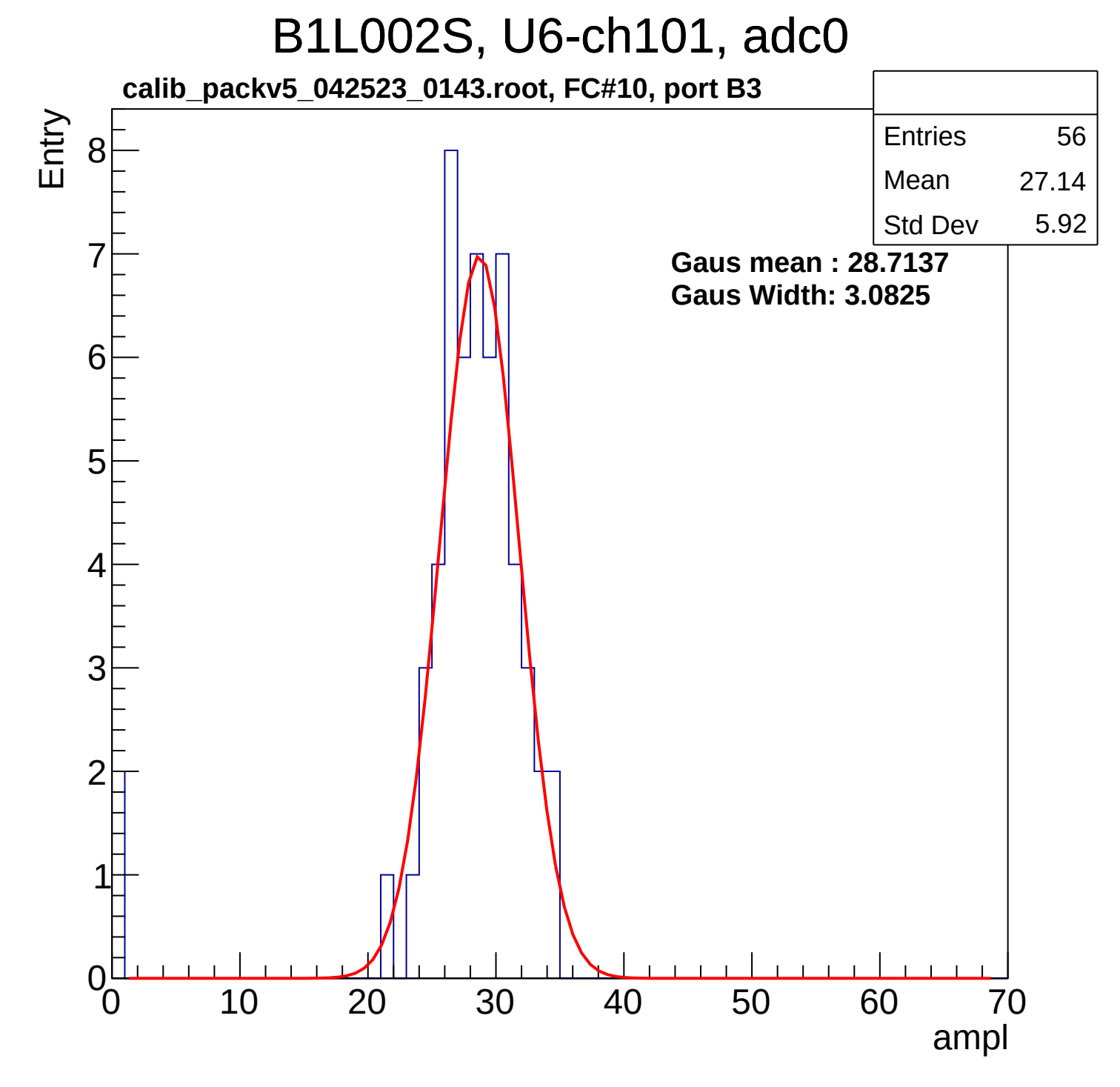

B1L002S, U6-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.32
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

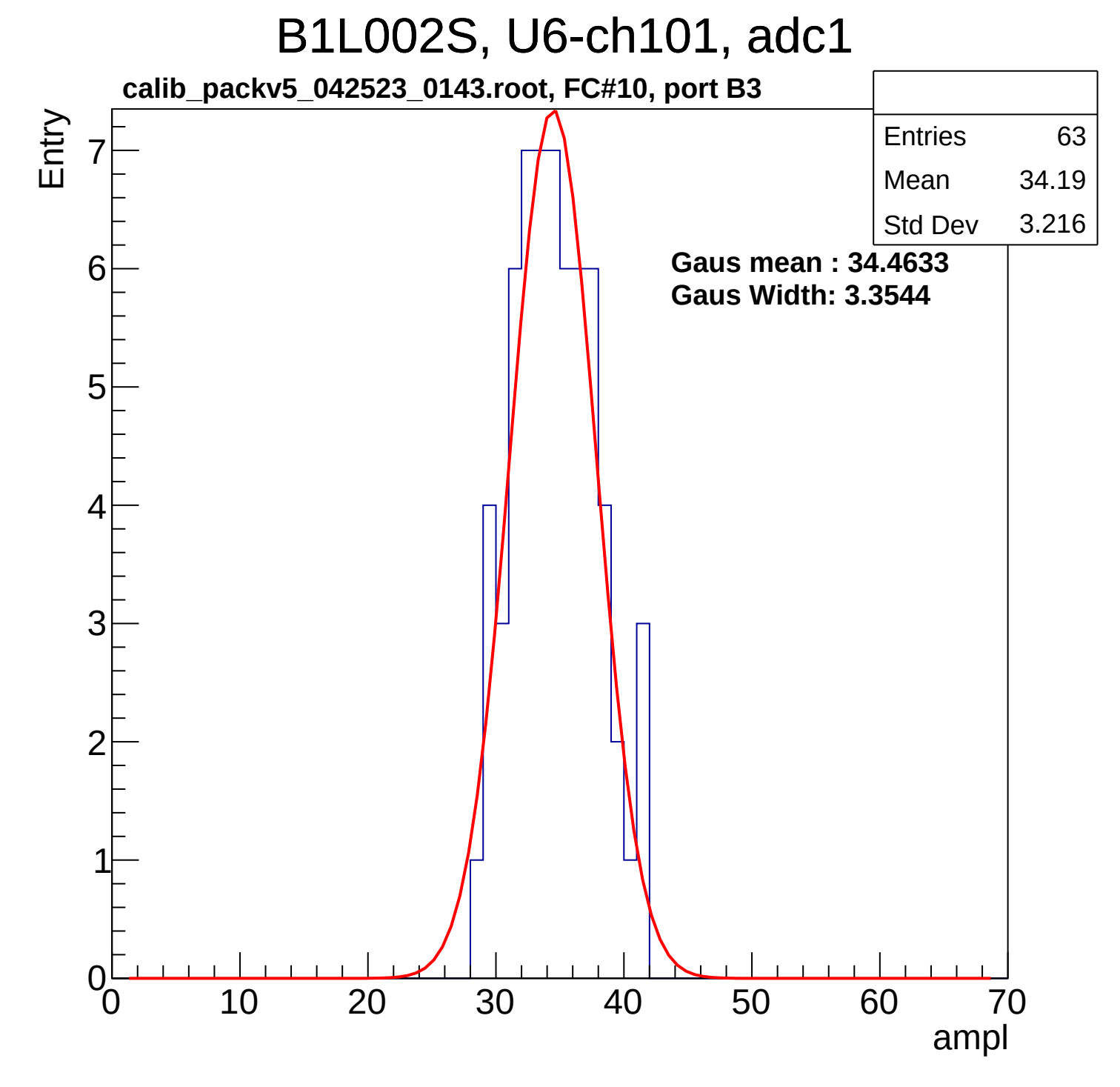

B1L002S, U6-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.62
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

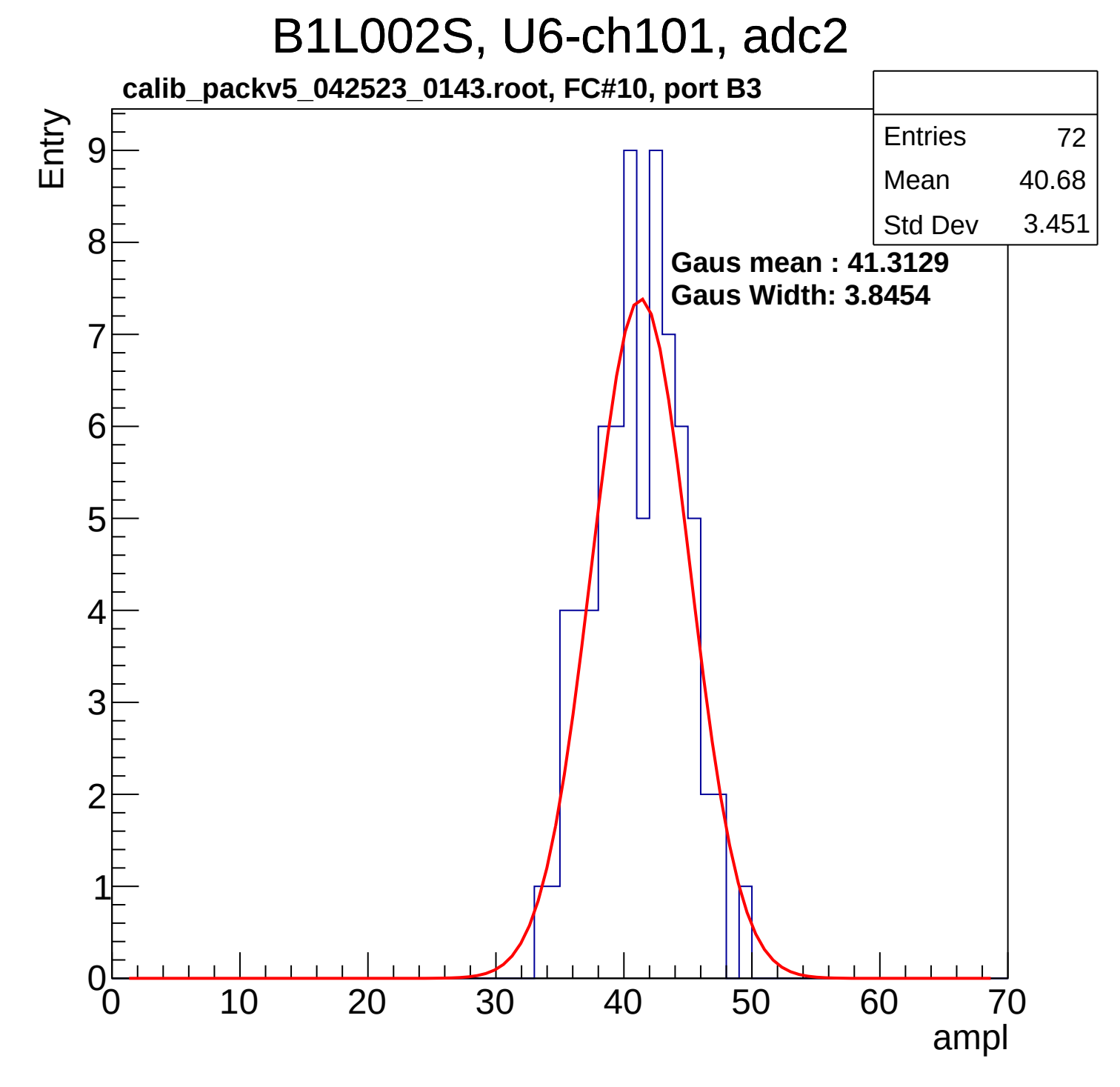

B1L002S, U6-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 58.51
Std Dev 2.821
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

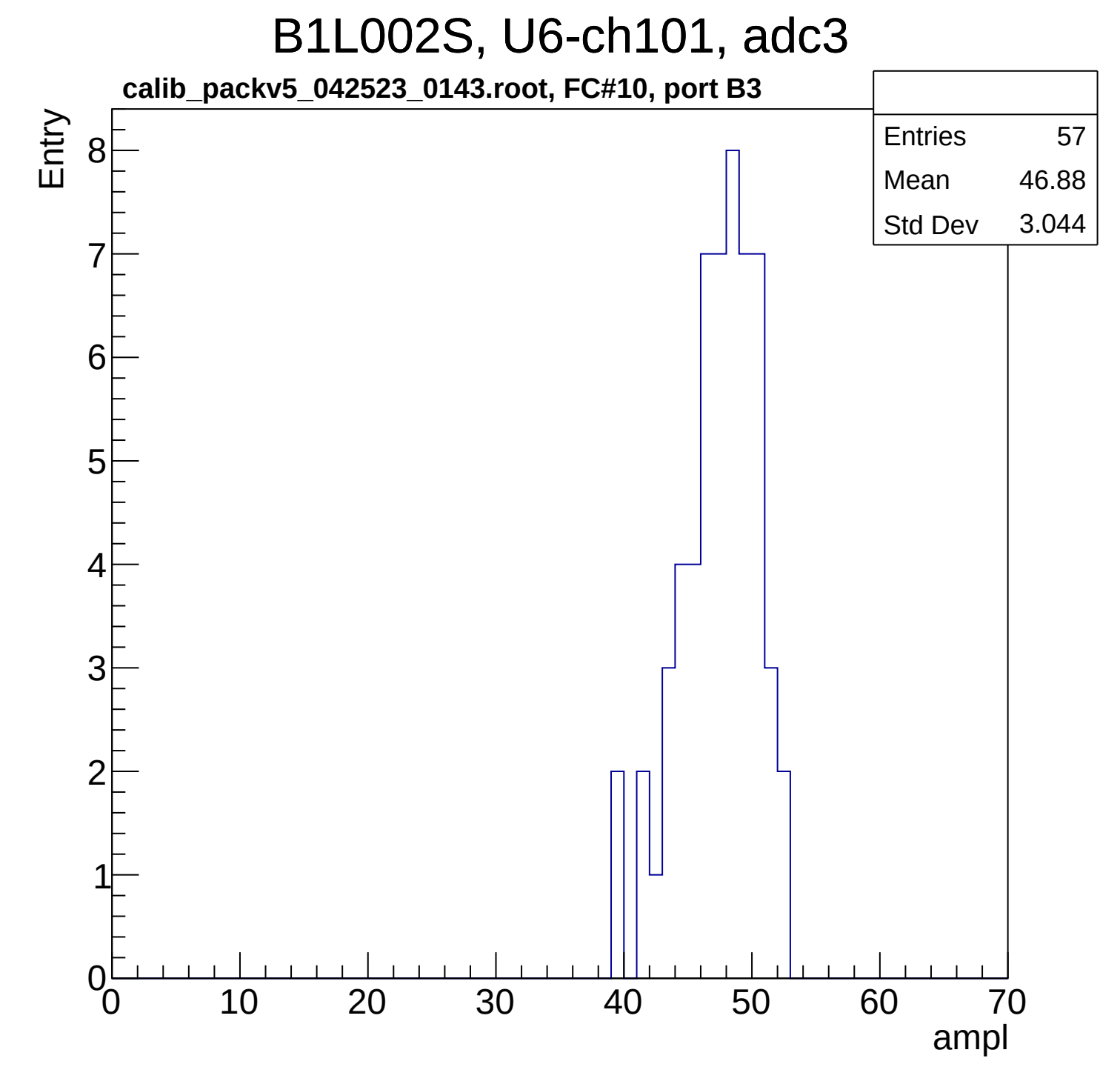

B1L002S, U6-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.36
Std Dev 15.96
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

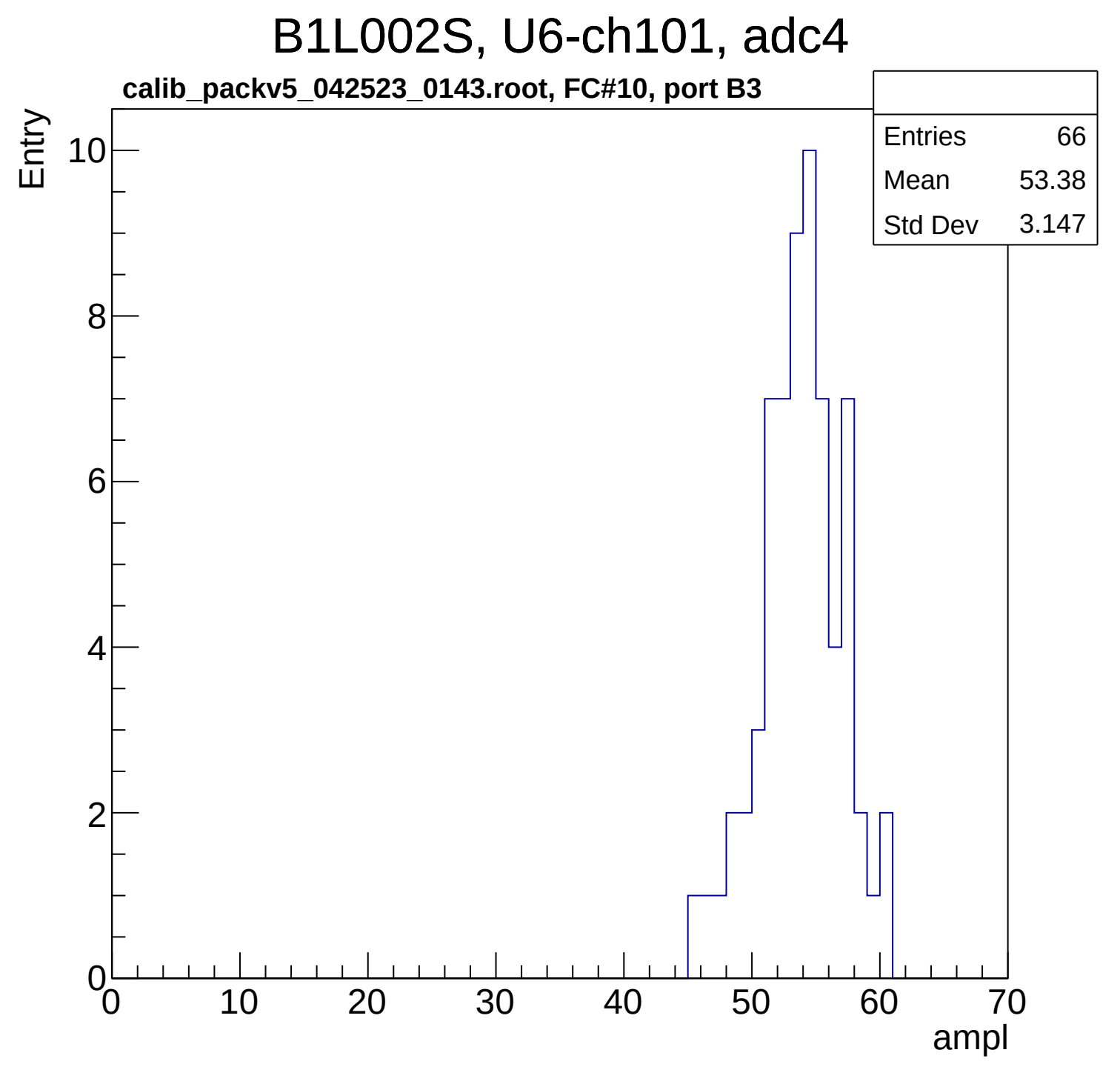

B1L002S, U6-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

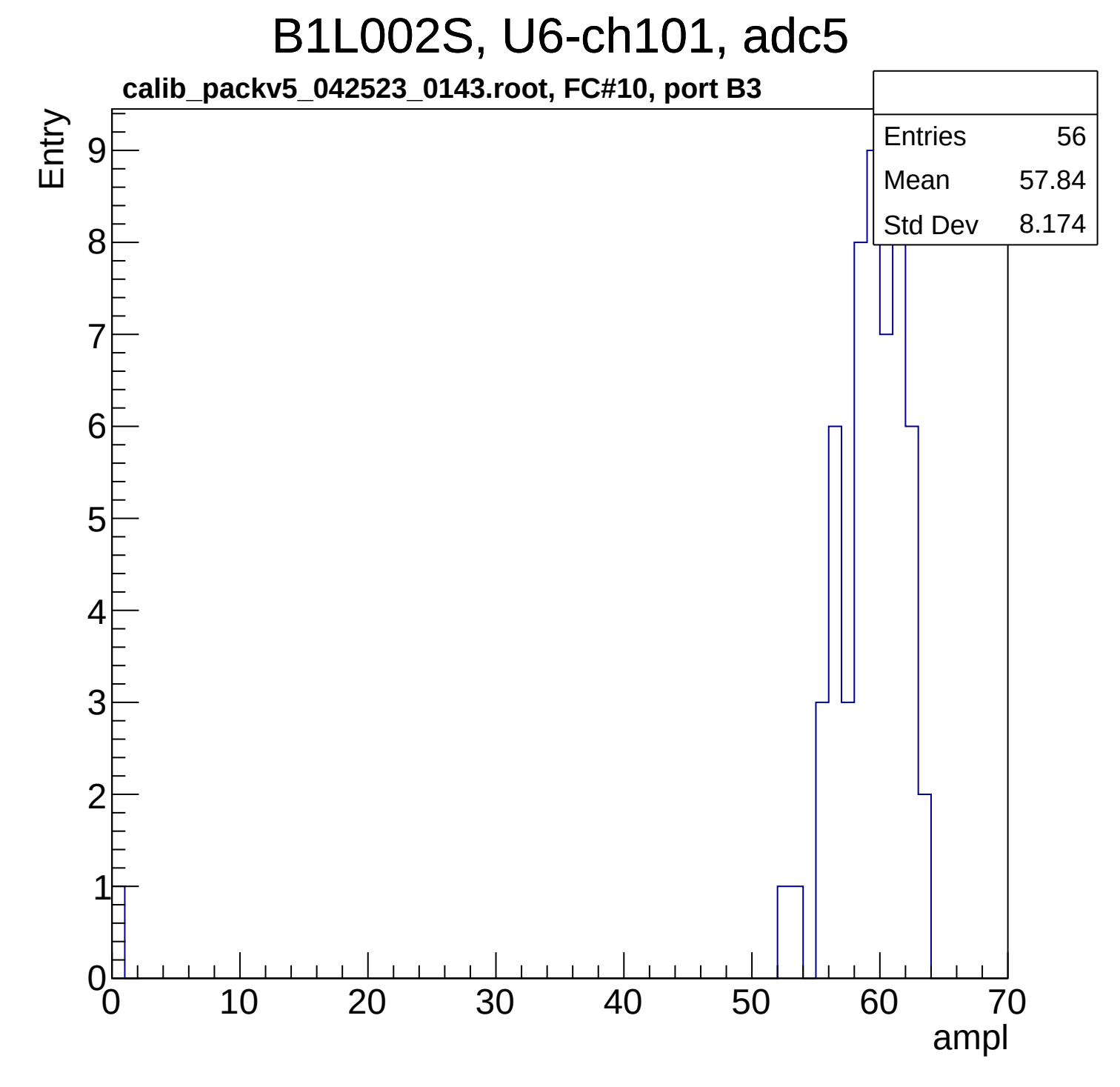

B1L002S, U6-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.14
Std Dev 5.92
Gaus mean : 28.7137
Gaus Width: 3.0825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

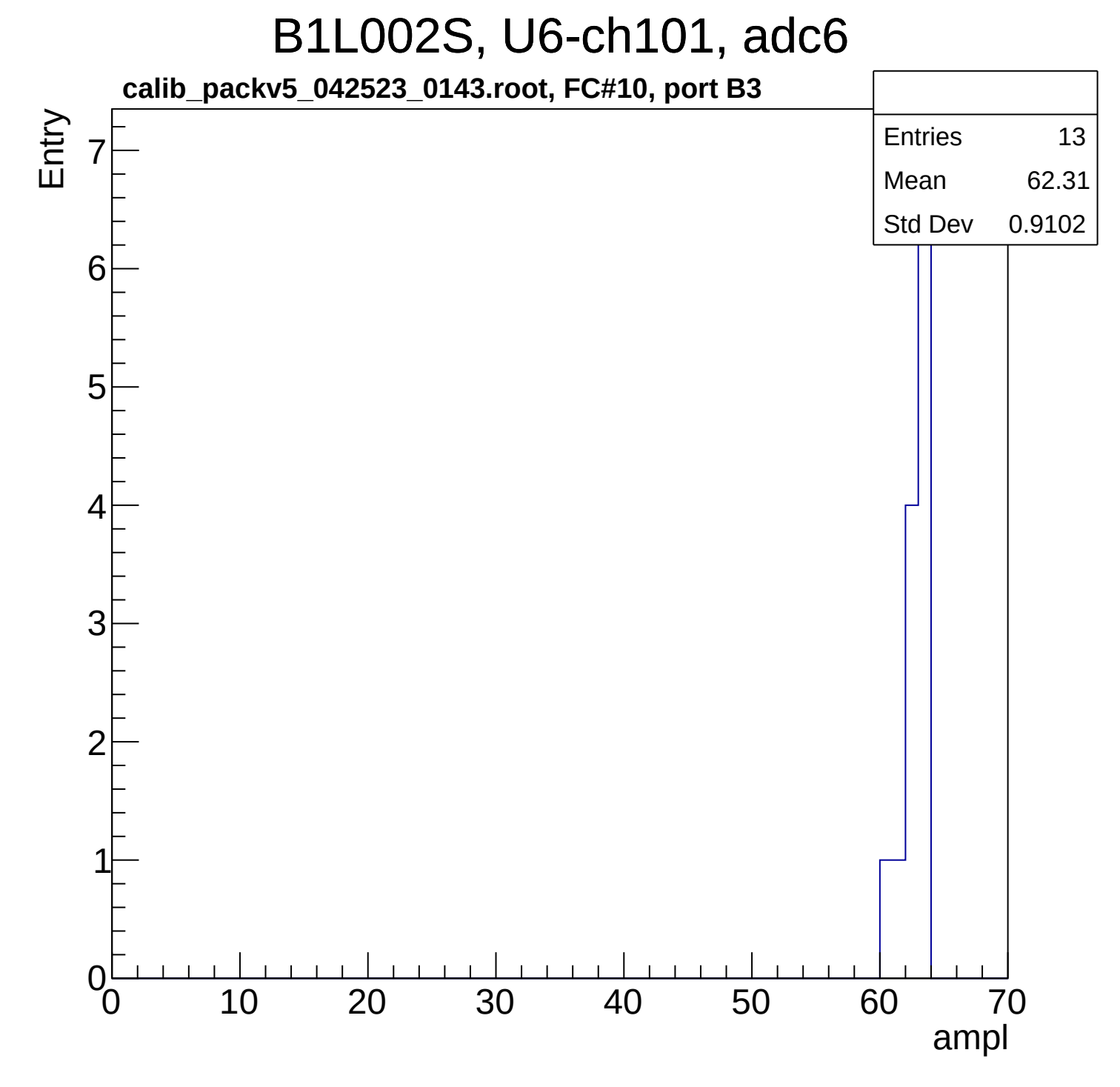

B1L002S, U6-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.19
Std Dev 3.216
Gaus mean : 34.4633
Gaus Width: 3.3544
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

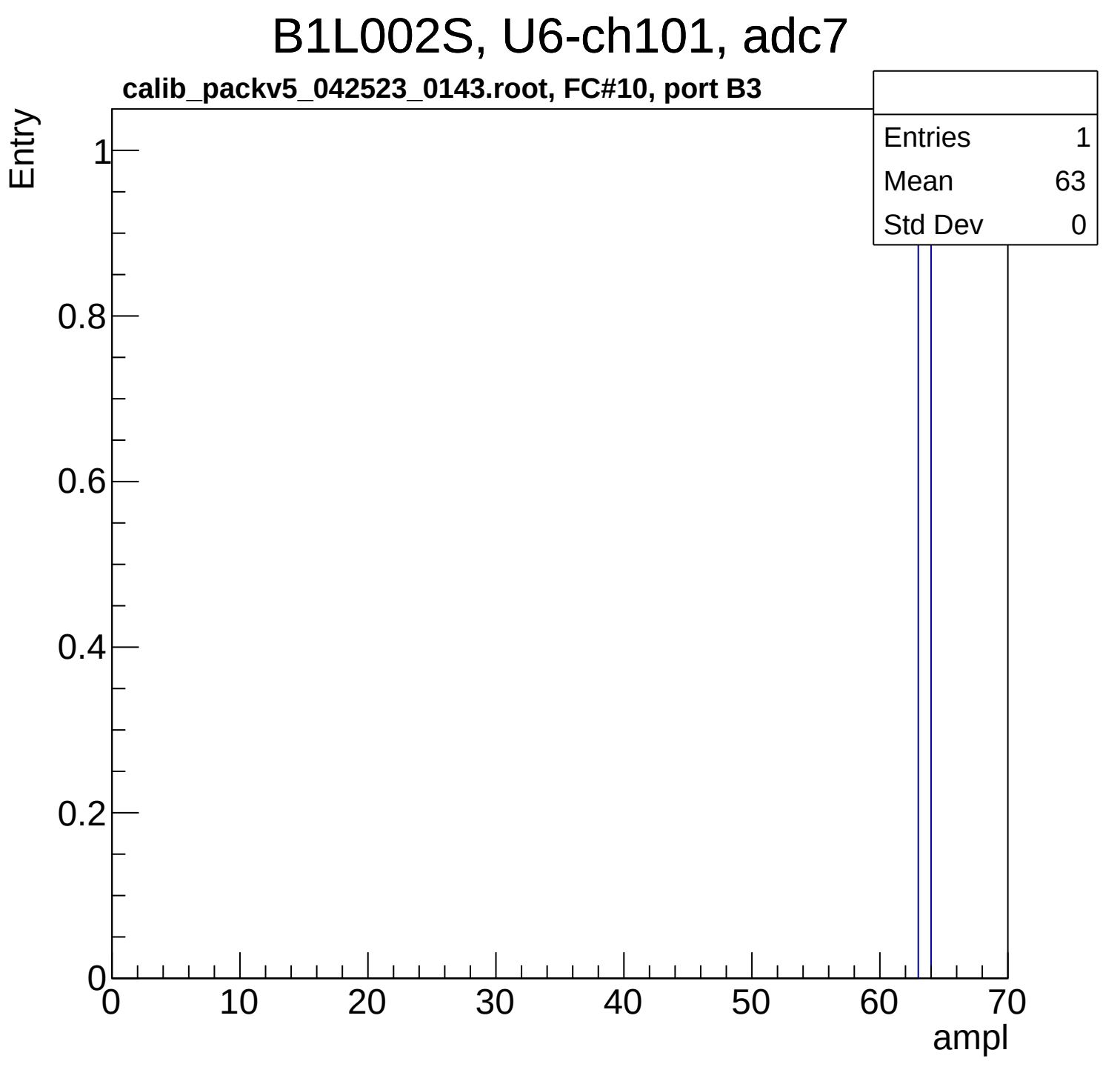

B1L002S, U6-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 40.68
Std Dev 3.451
Gaus mean : 41.3129
Gaus Width: 3.8454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

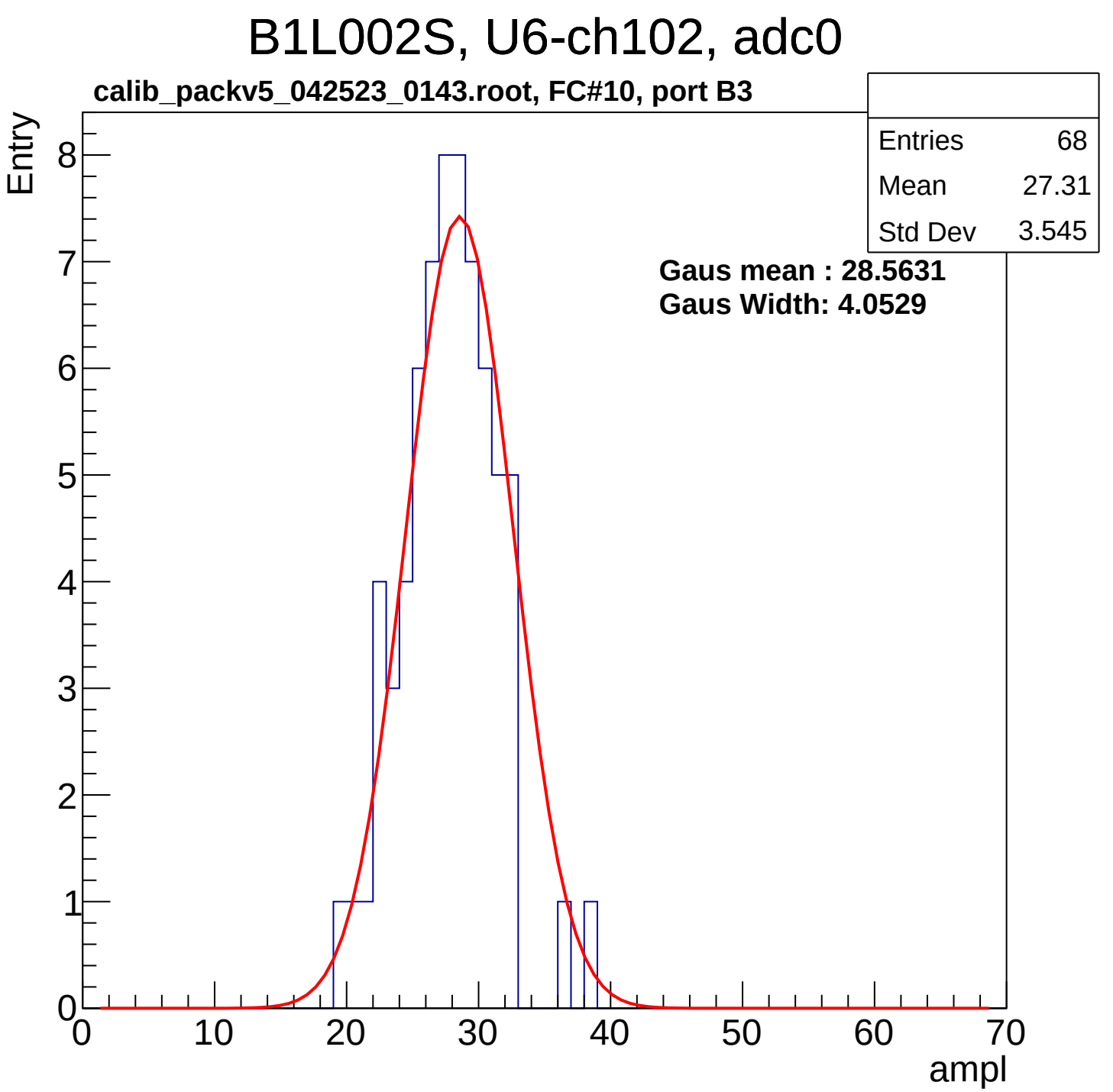

B1L002S, U6-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.88
Std Dev 3.044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

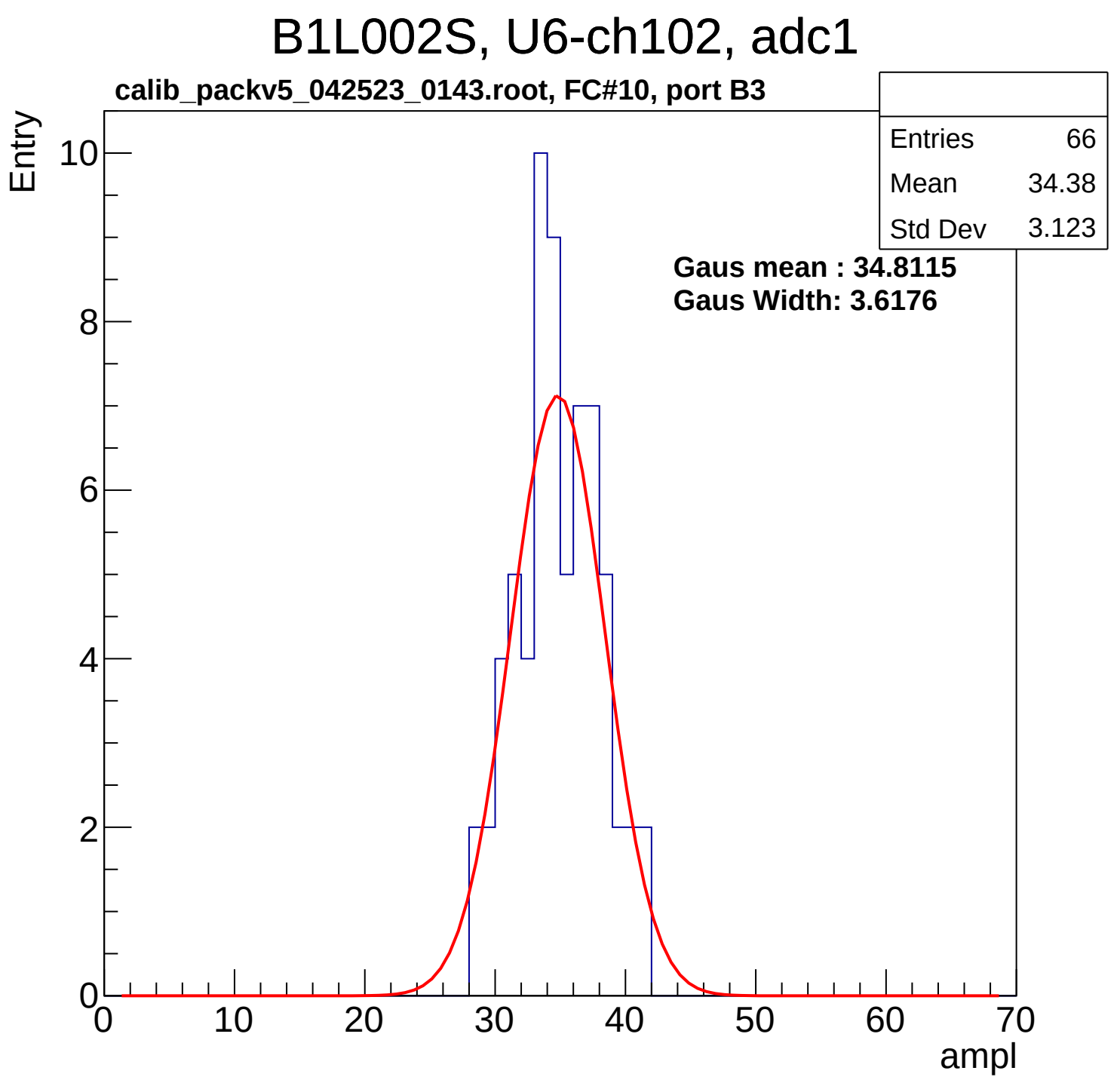

B1L002S, U6-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.38
Std Dev 3.147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

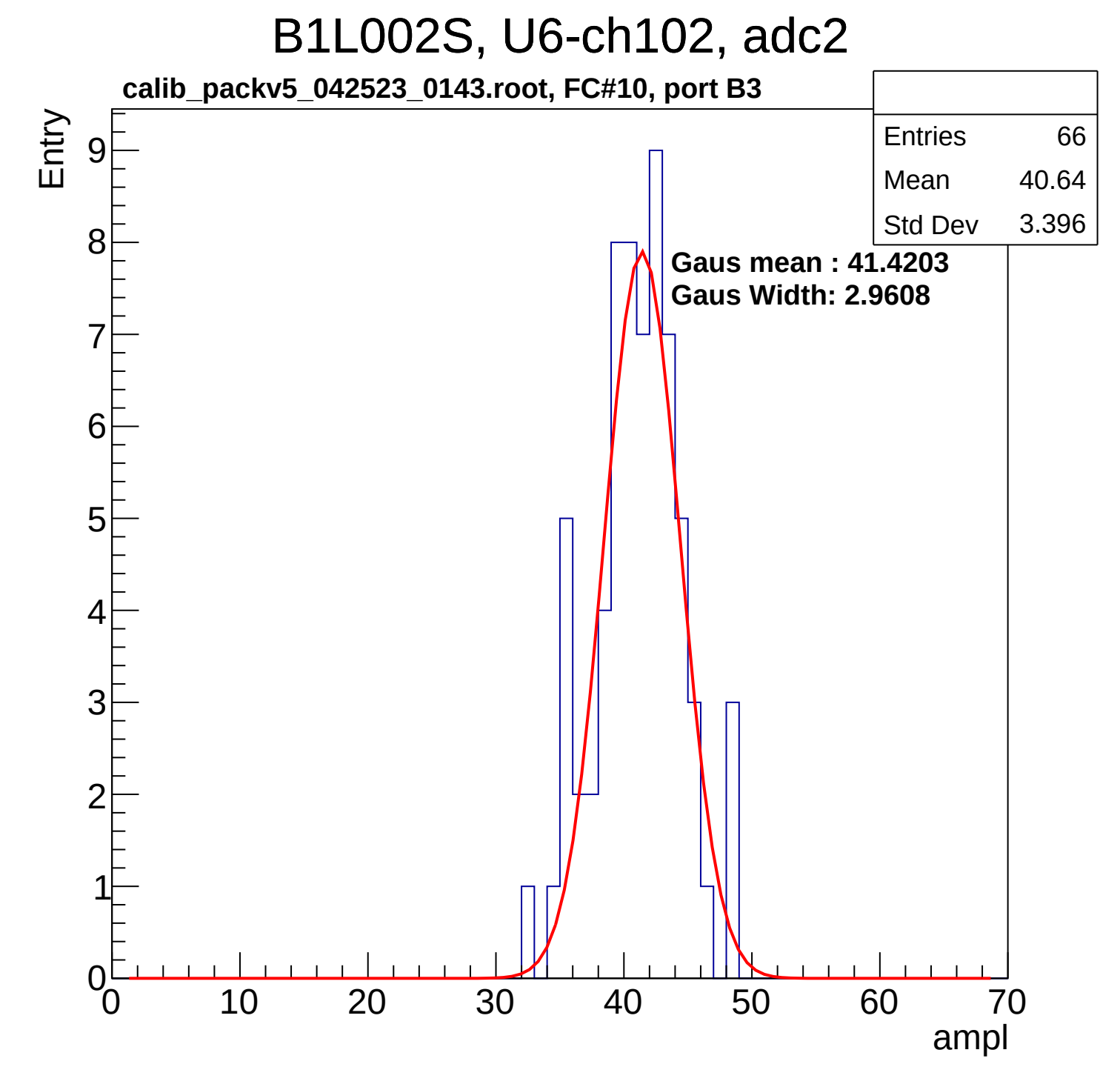

B1L002S, U6-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.84
Std Dev 8.174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

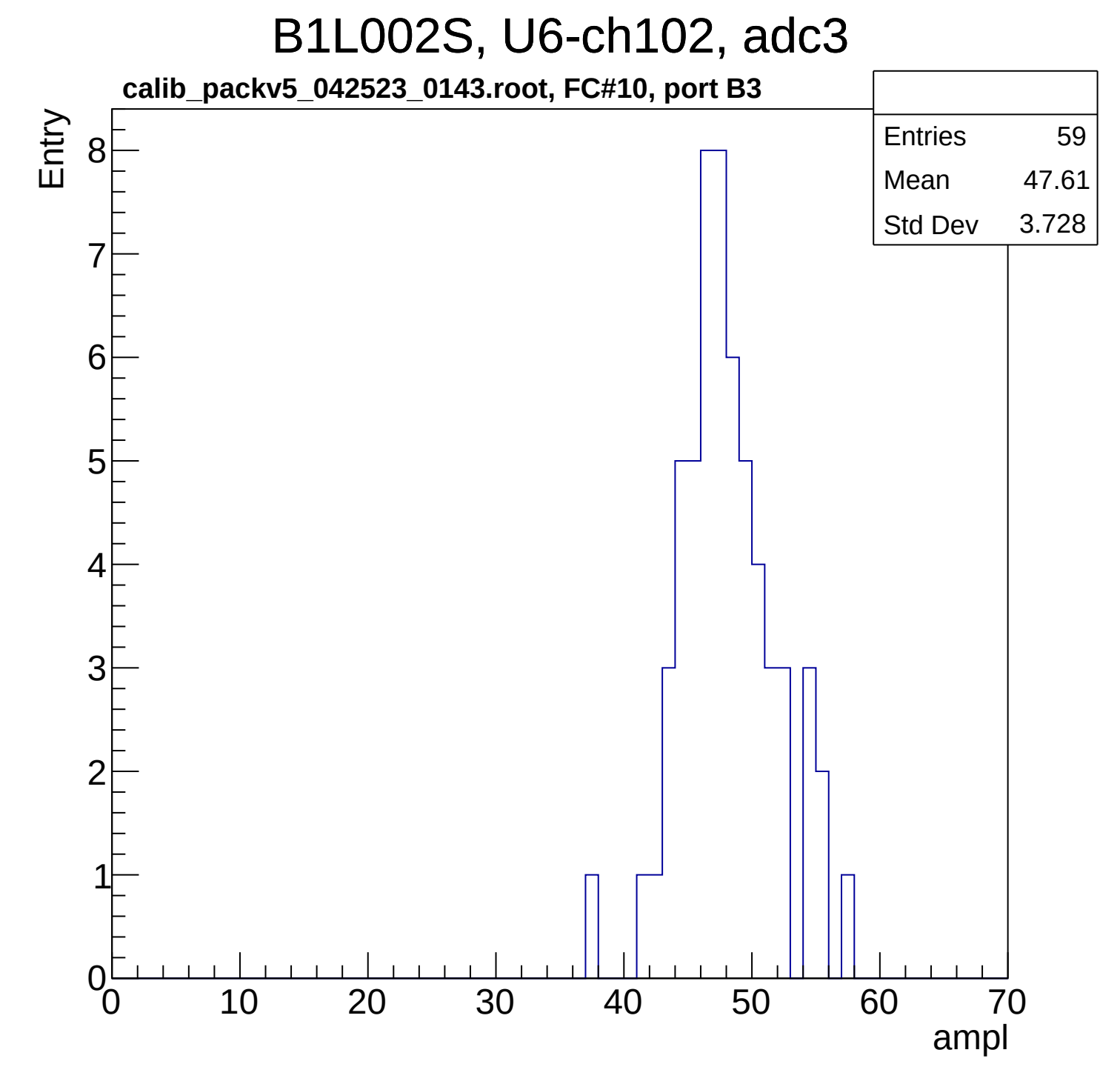

B1L002S, U6-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 62.31
Std Dev 0.9102
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

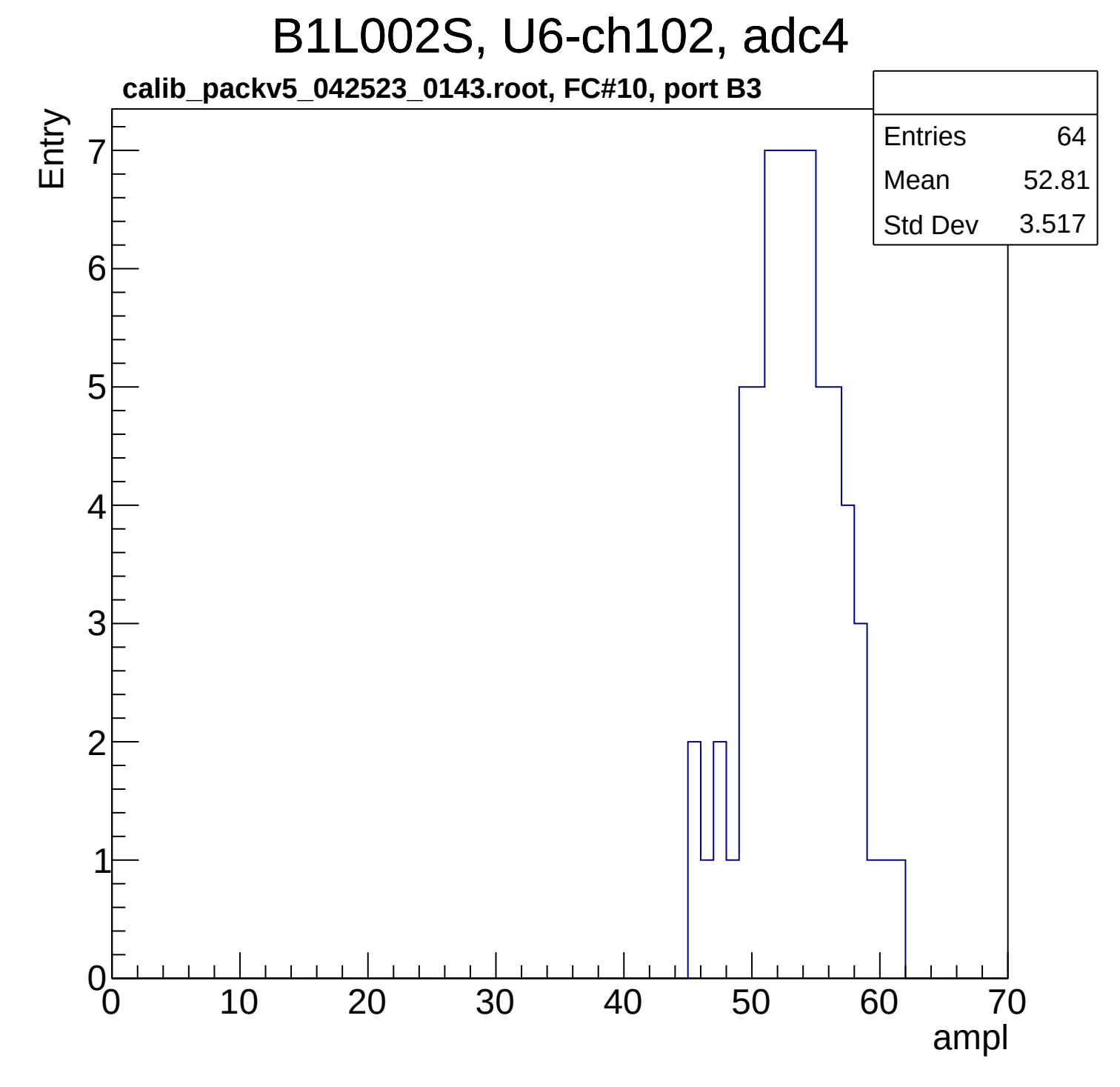

B1L002S, U6-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

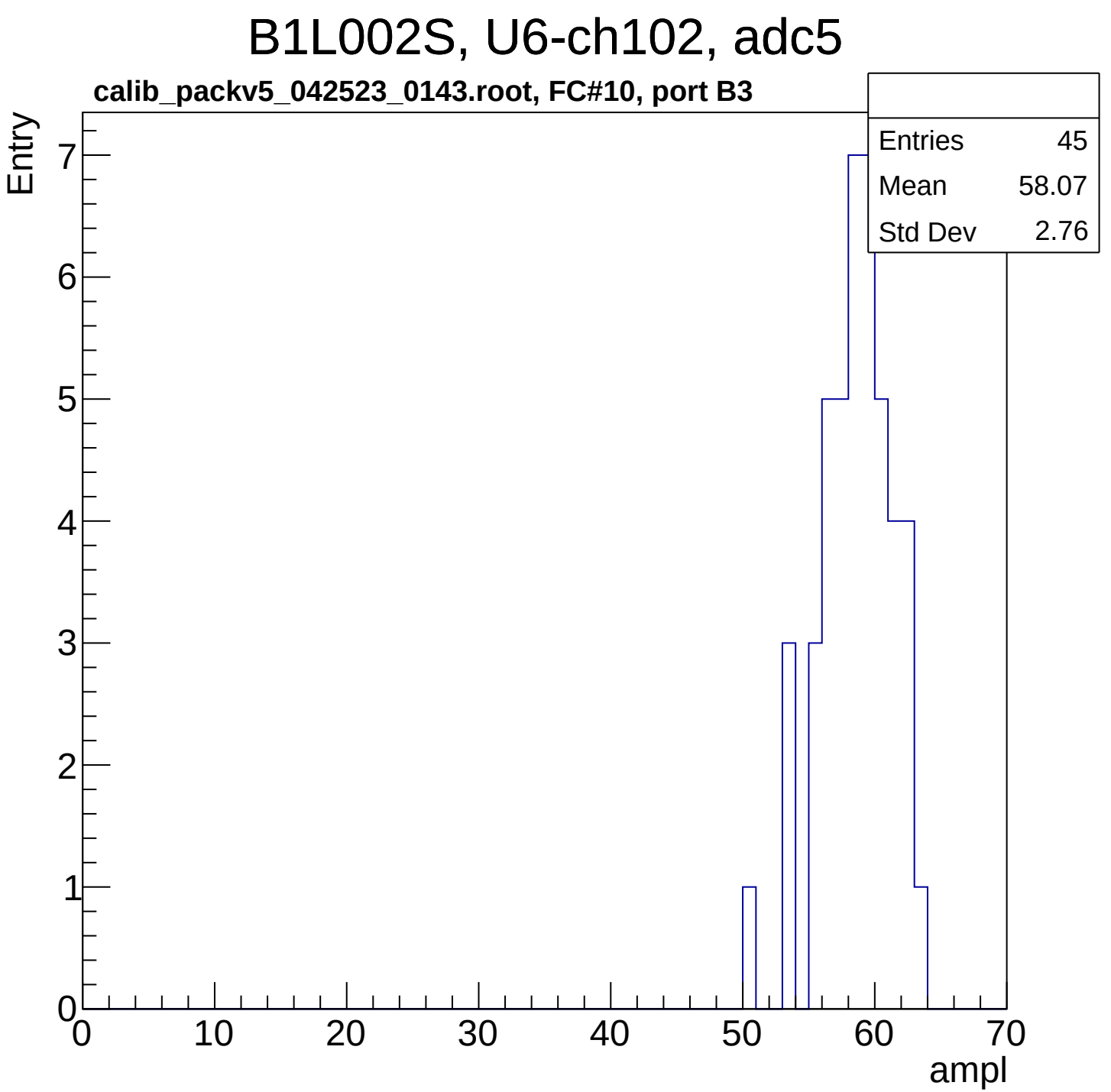

B1L002S, U6-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.31
Std Dev 3.545
Gaus mean : 28.5631
Gaus Width: 4.0529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

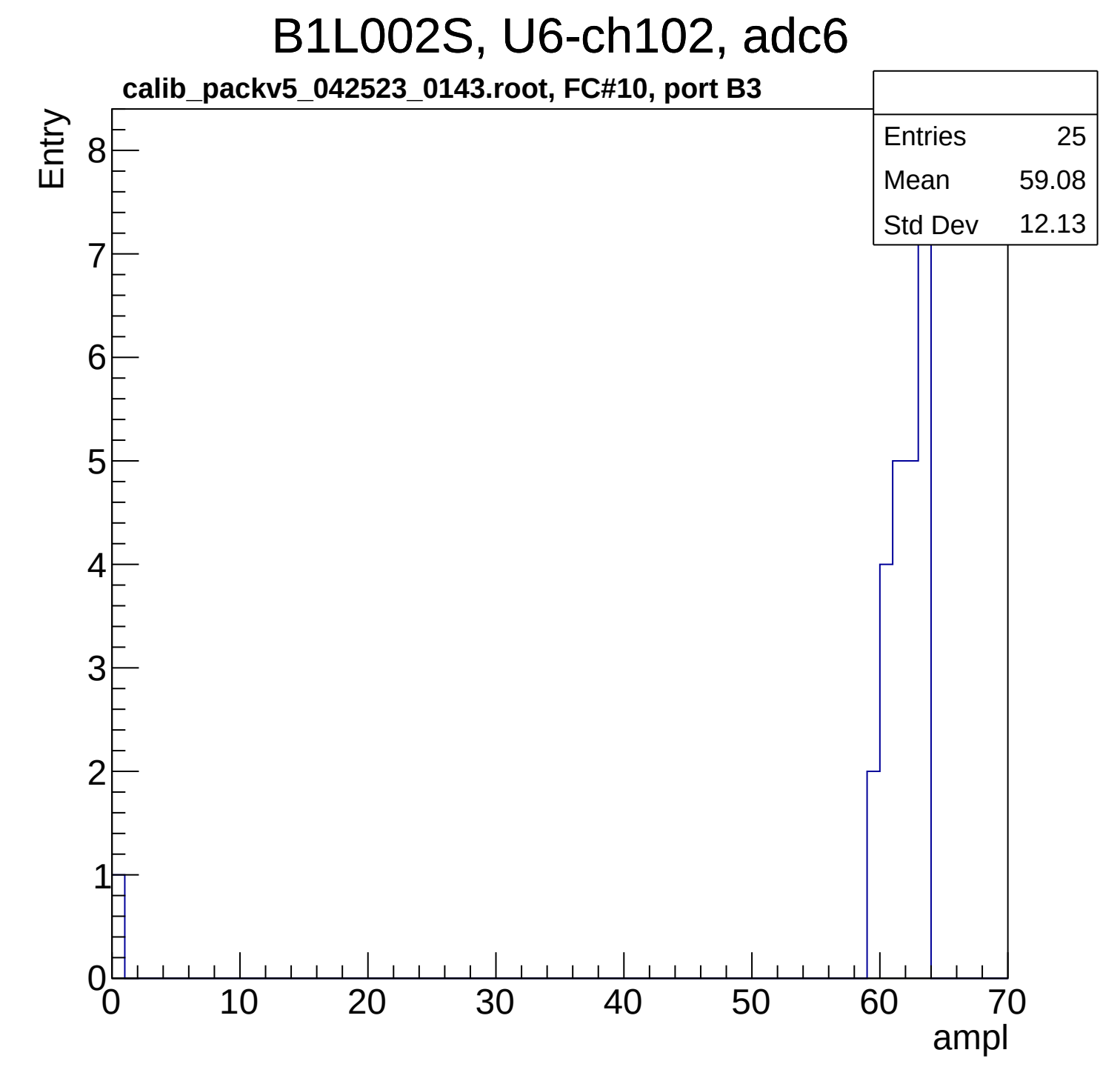

B1L002S, U6-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.38
Std Dev 3.123
Gaus mean : 34.8115
Gaus Width: 3.6176
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

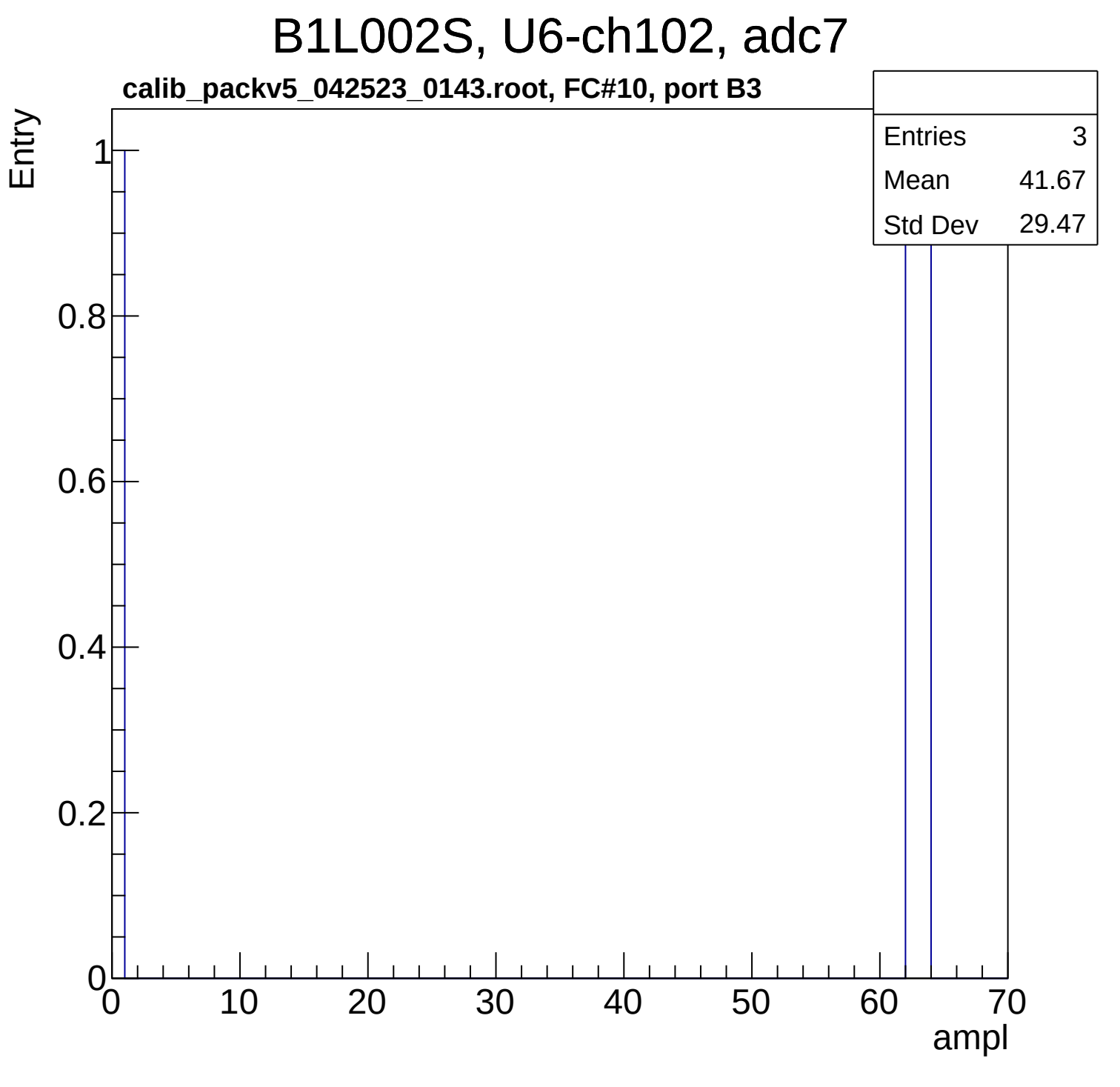

B1L002S, U6-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.64
Std Dev 3.396
Gaus mean : 41.4203
Gaus Width: 2.9608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

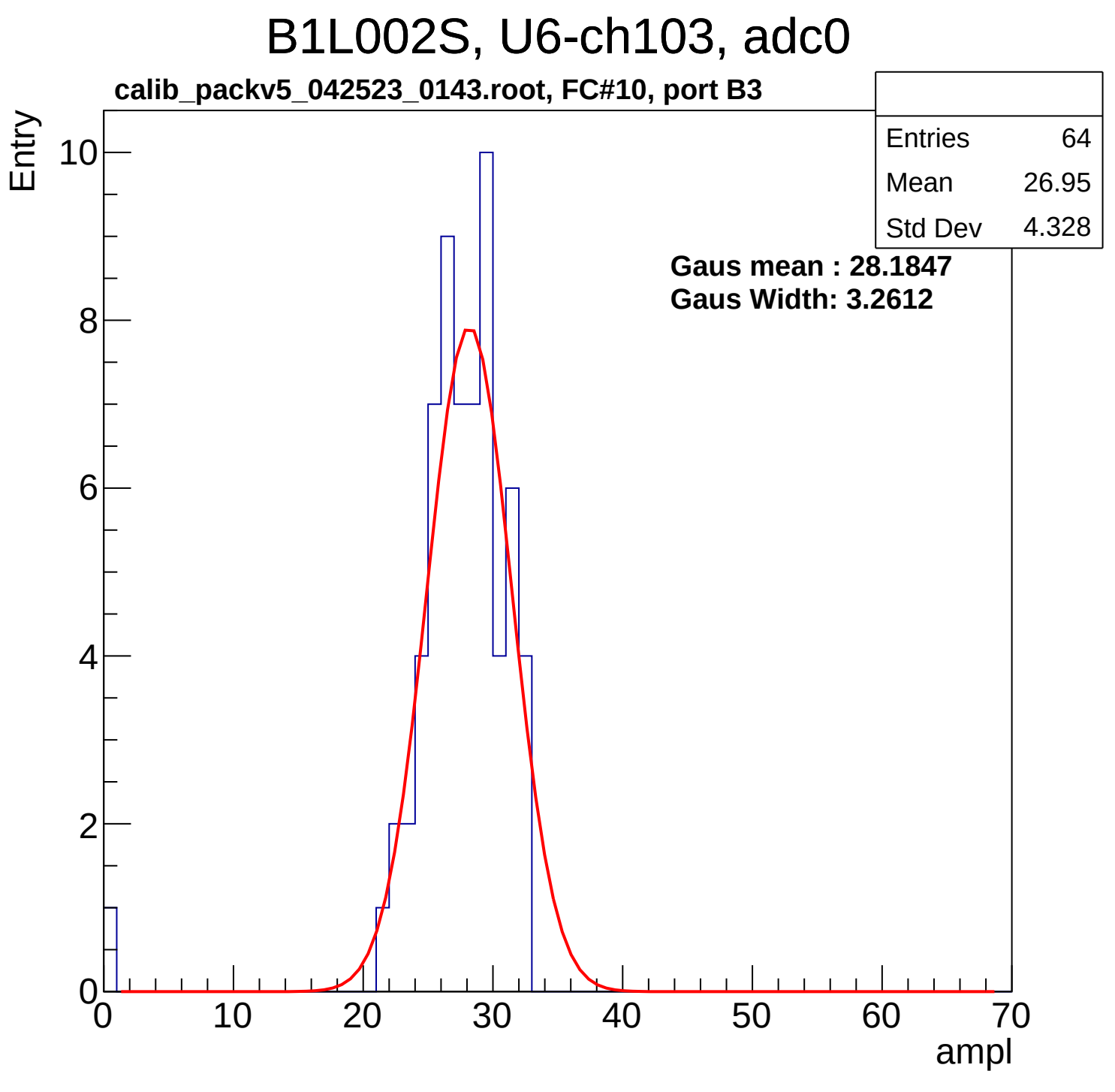

B1L002S, U6-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.61
Std Dev 3.728
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

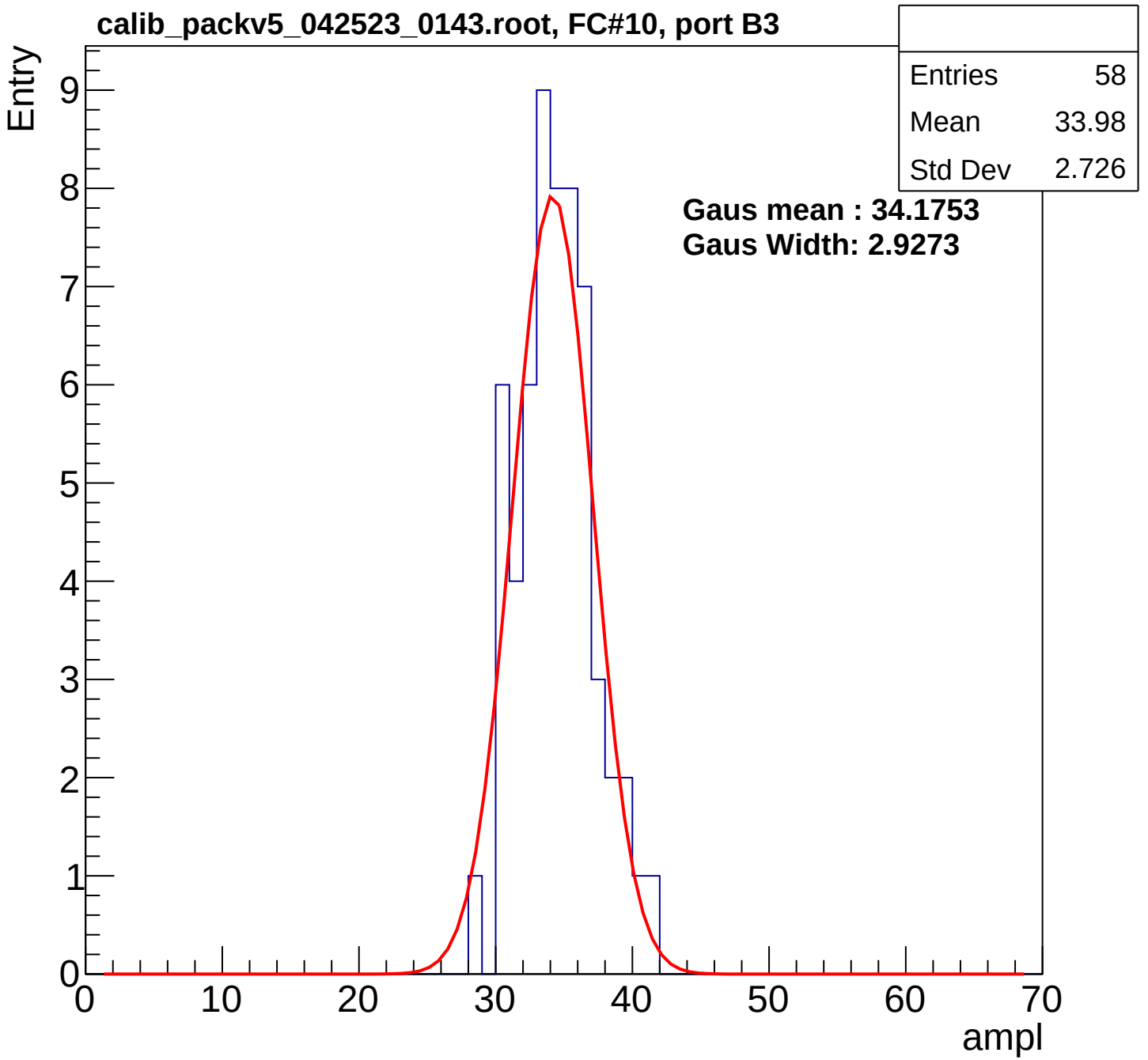

B1L002S, U6-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.81
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

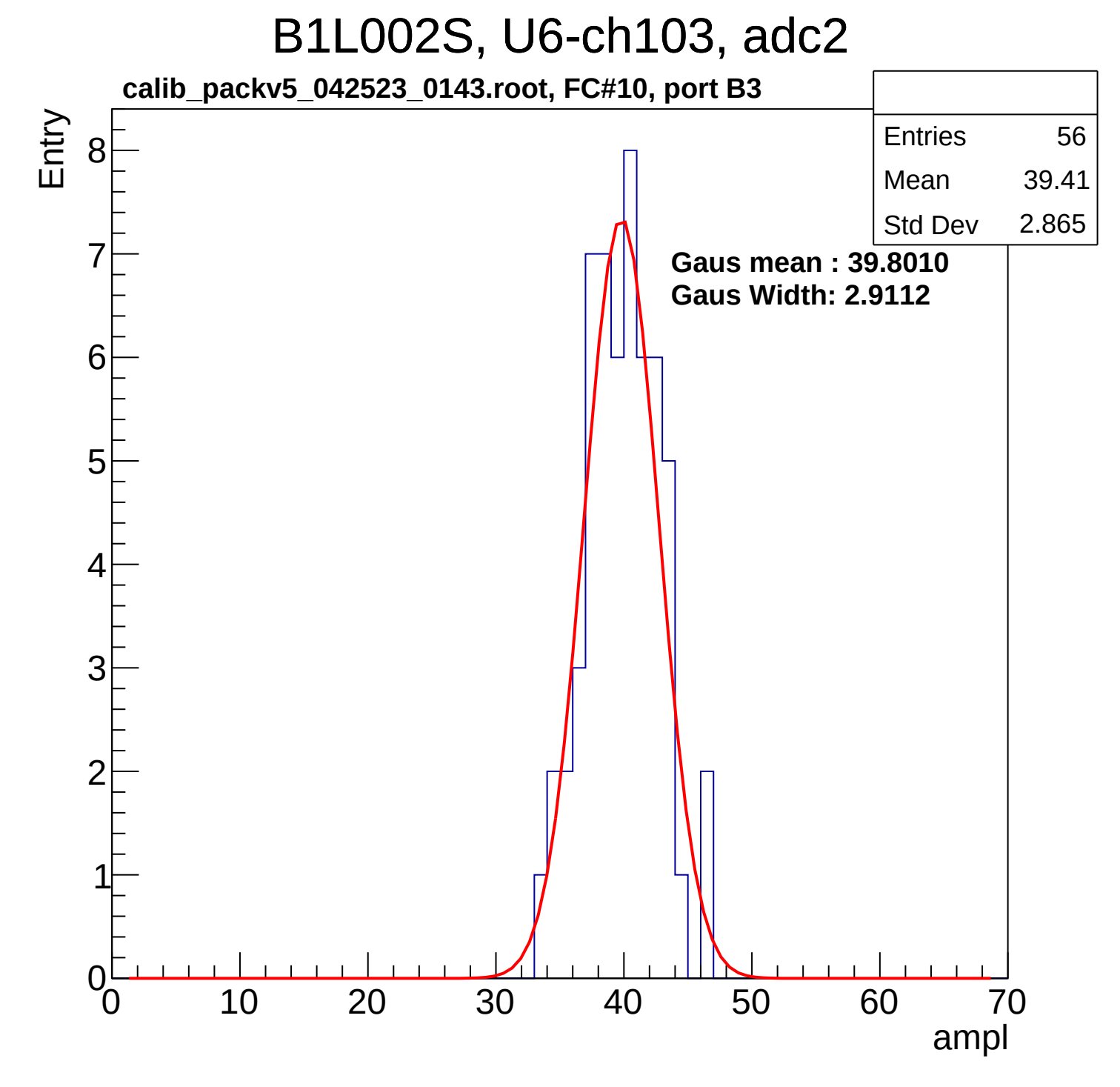

B1L002S, U6-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.07
Std Dev 2.76
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

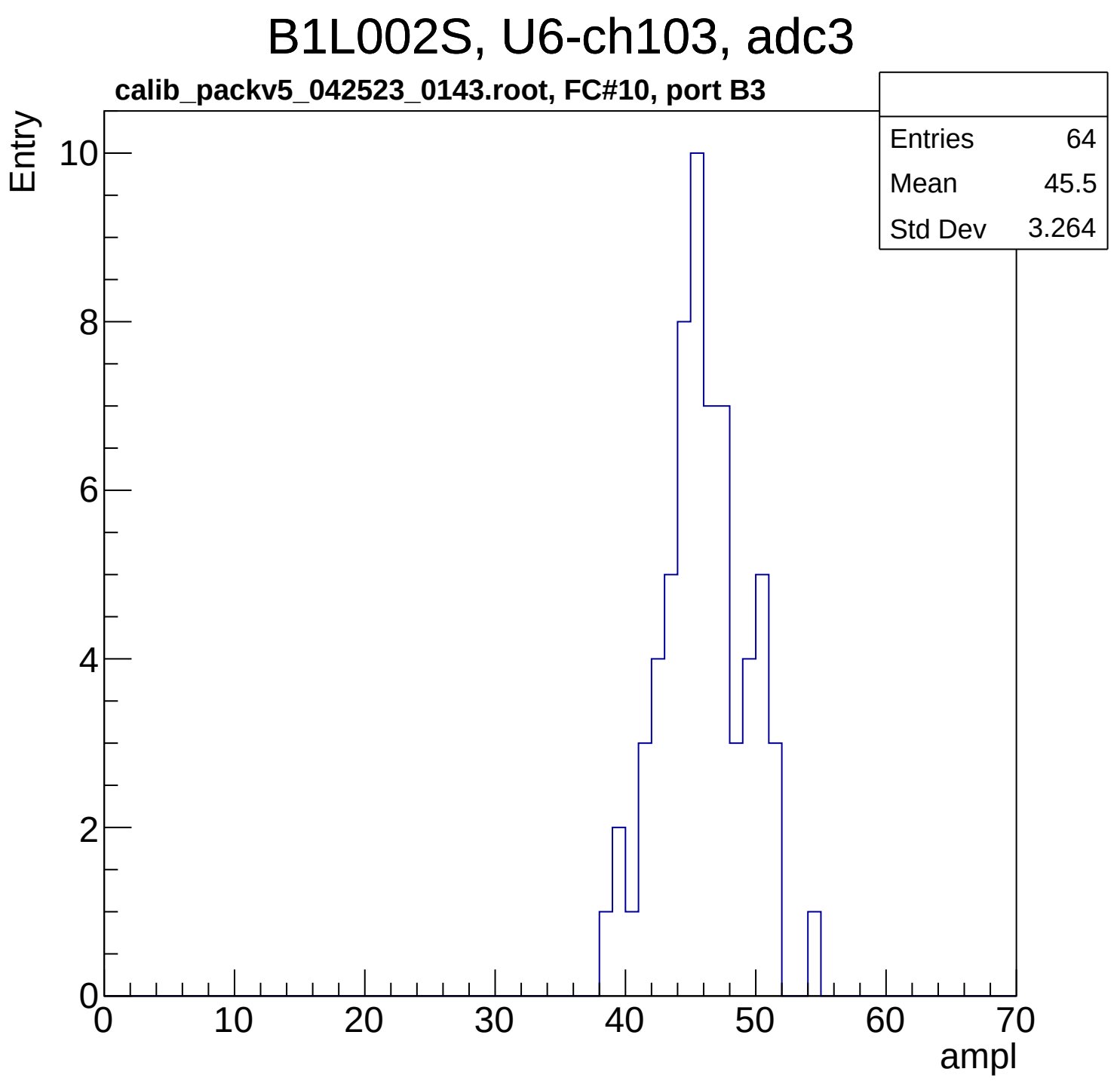

B1L002S, U6-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59.08
Std Dev 12.13
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

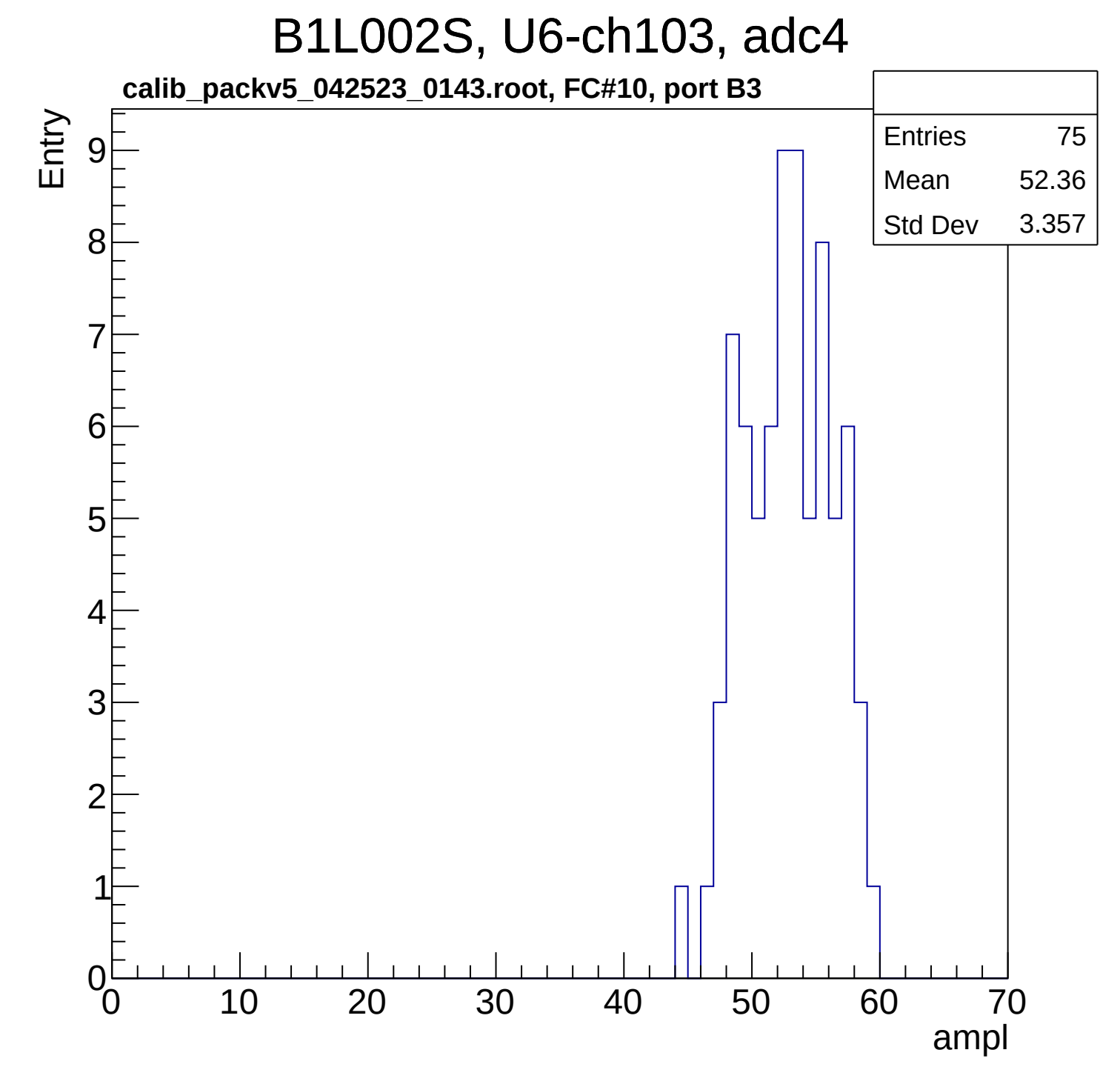

B1L002S, U6-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

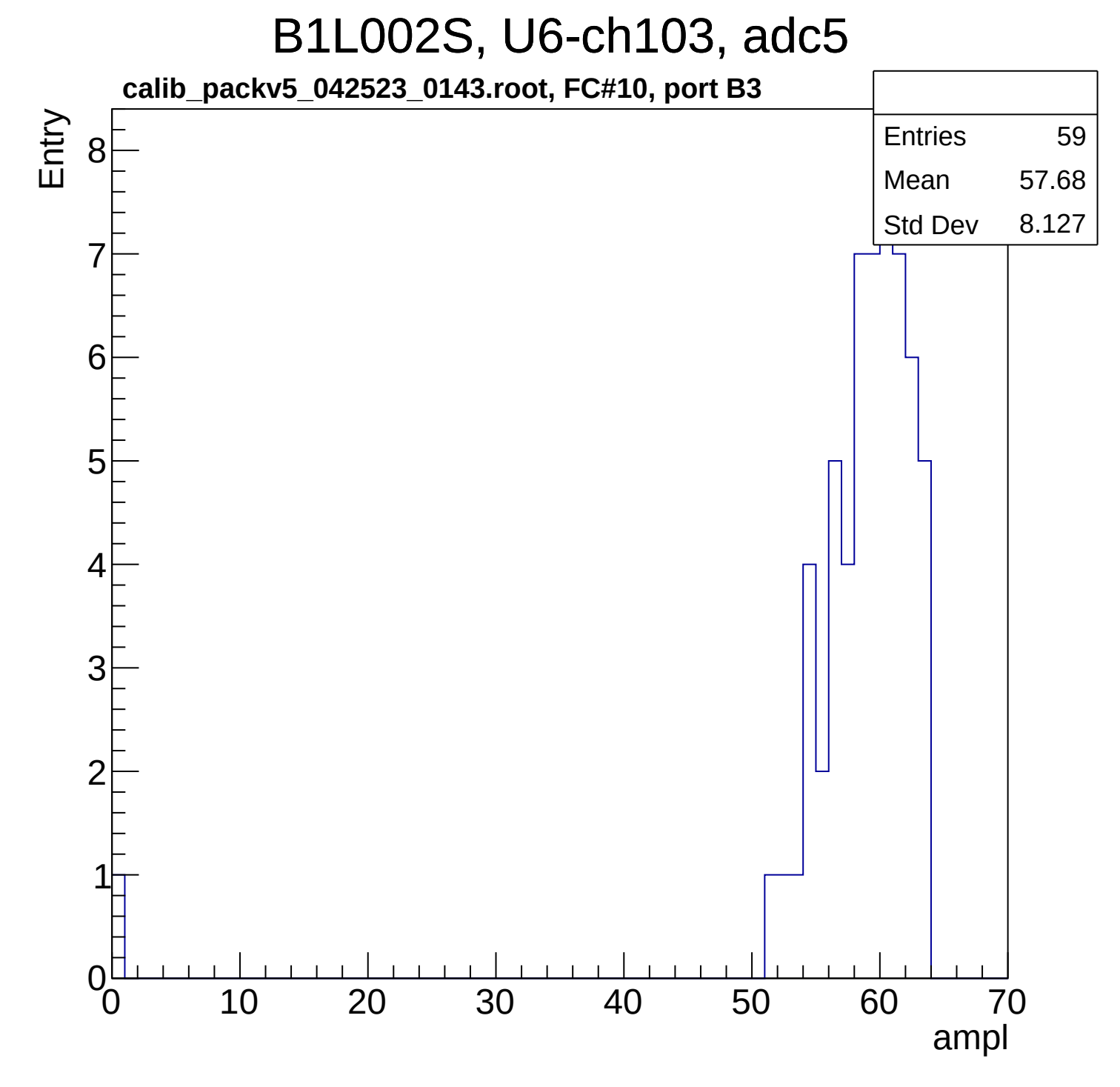

B1L002S, U6-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.95
Std Dev 4.328
Gaus mean : 28.1847
Gaus Width: 3.2612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

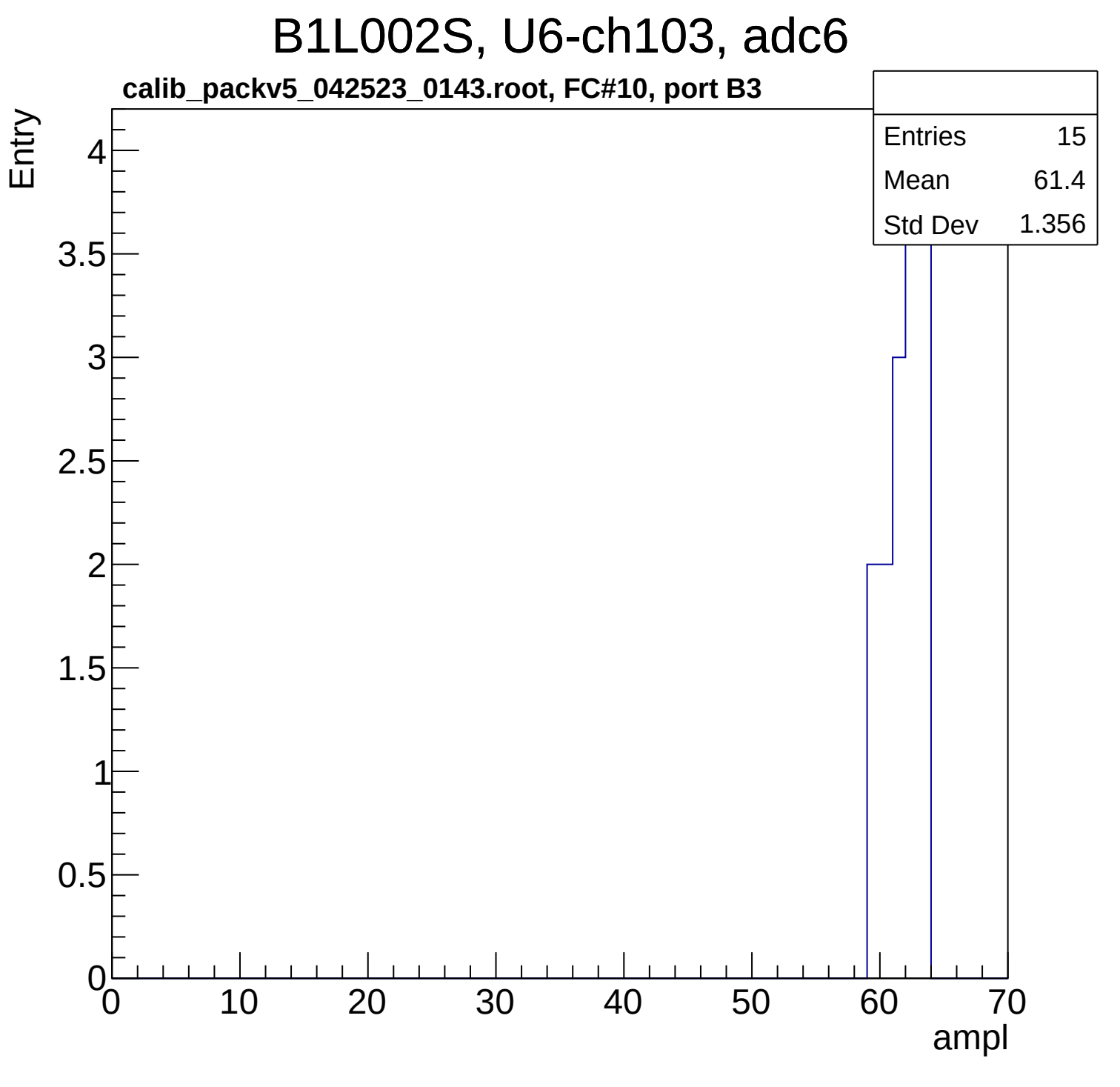

B1L002S, U6-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.98
Std Dev 2.726
Gaus mean : 34.1753
Gaus Width: 2.9273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

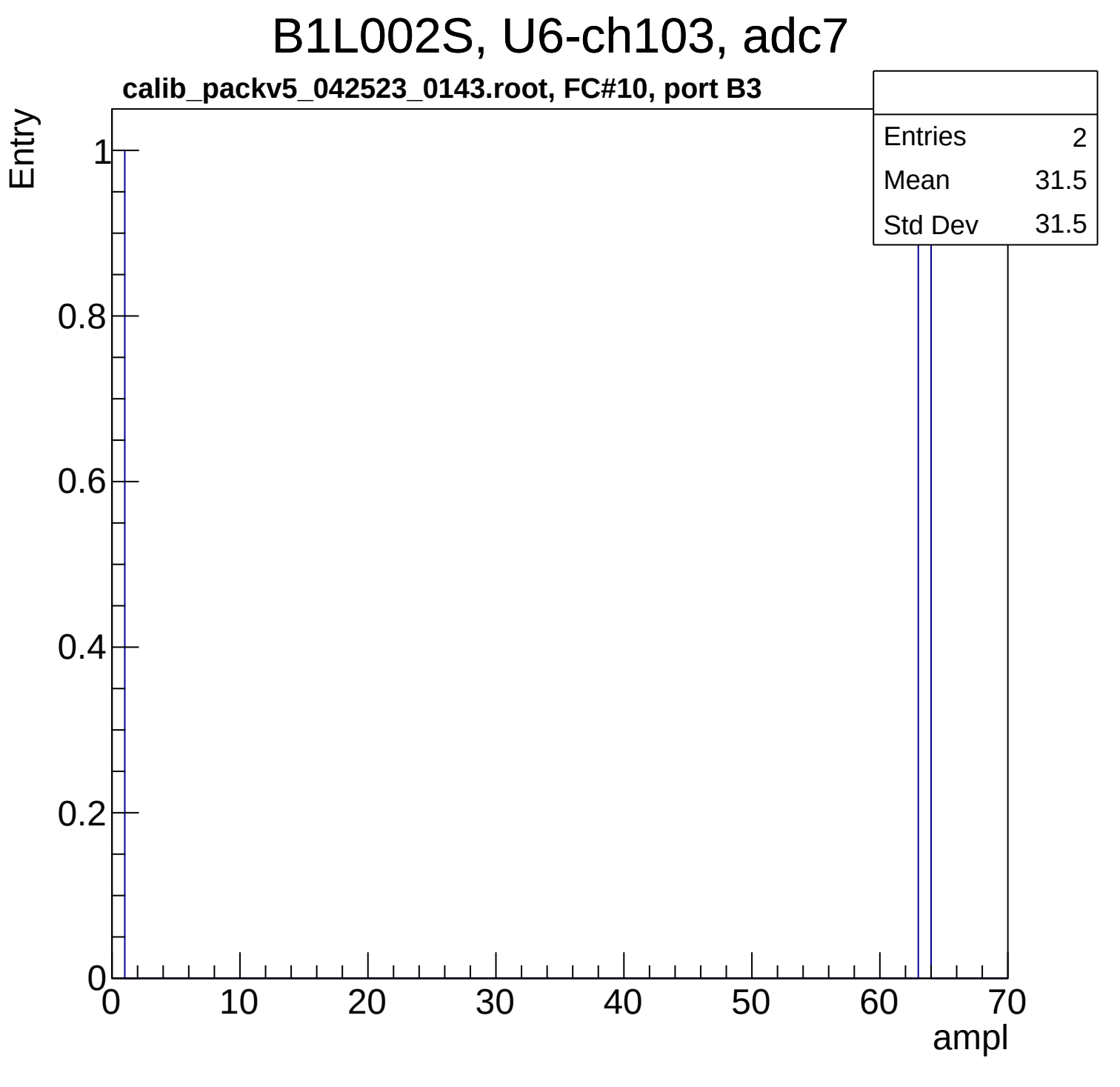

B1L002S, U6-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.41
Std Dev 2.865
Gaus mean : 39.8010
Gaus Width: 2.9112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

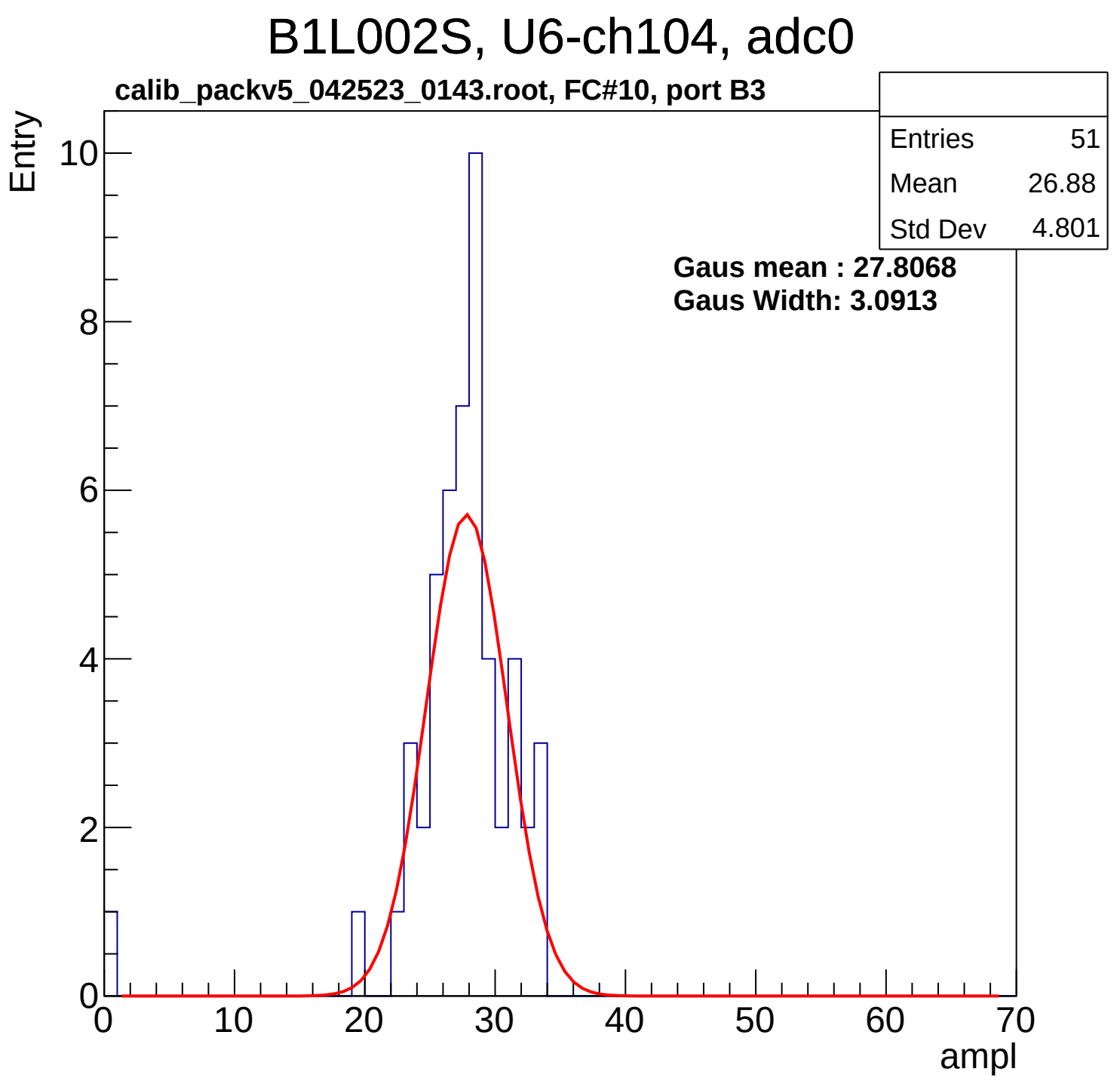

B1L002S, U6-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.5
Std Dev 3.264
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

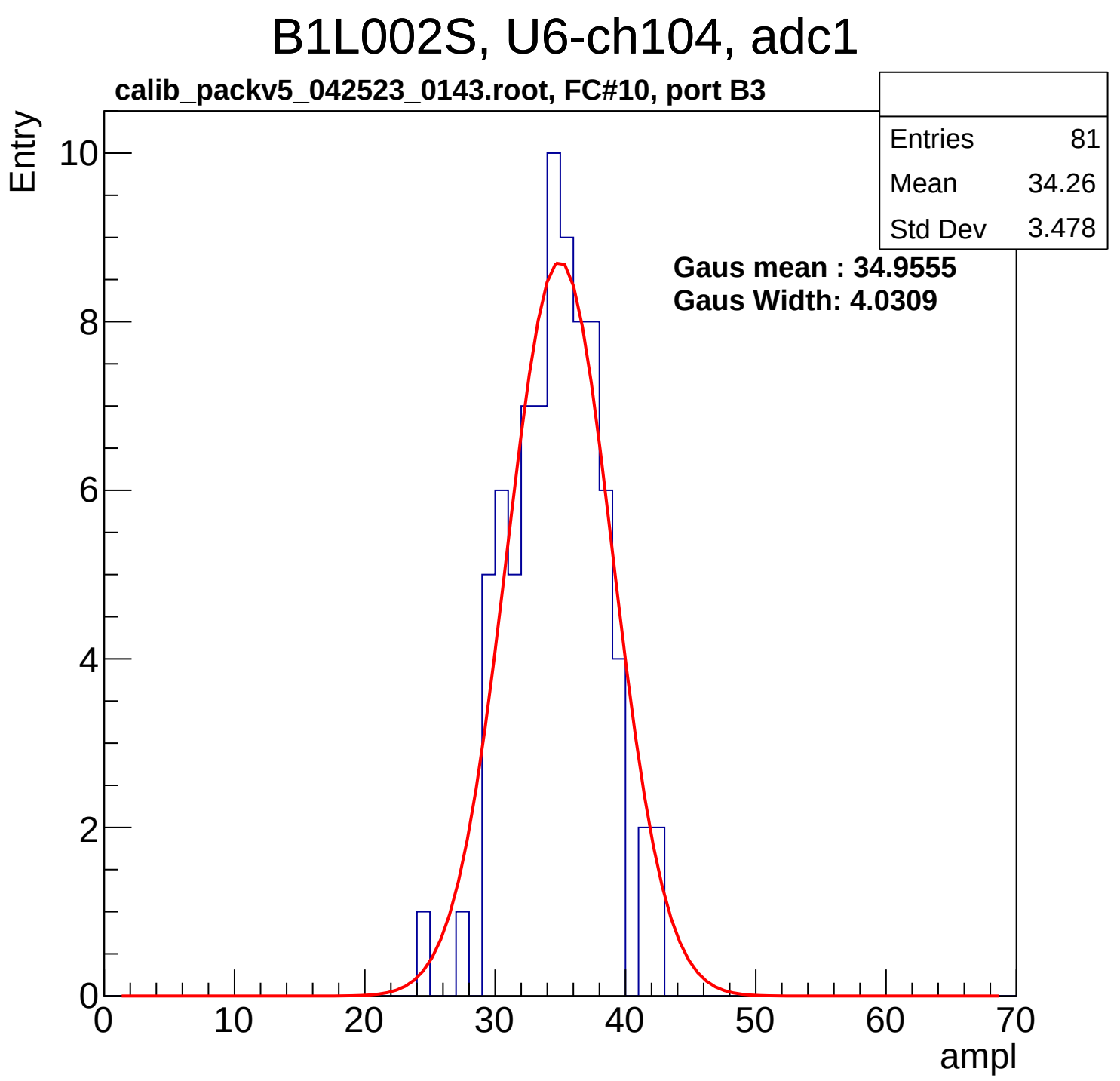

B1L002S, U6-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 52.36
Std Dev 3.357
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

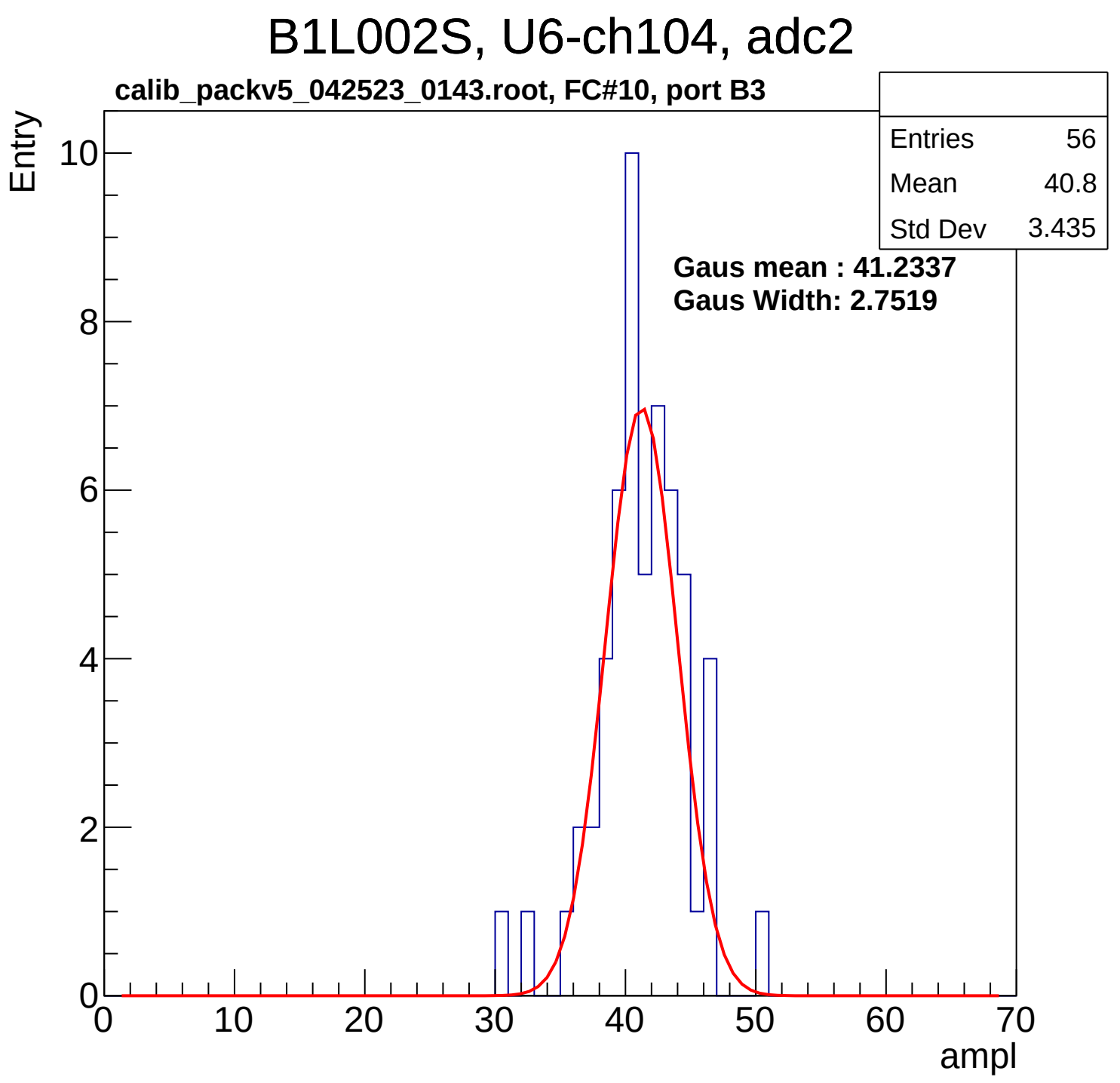

B1L002S, U6-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.68
Std Dev 8.127
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

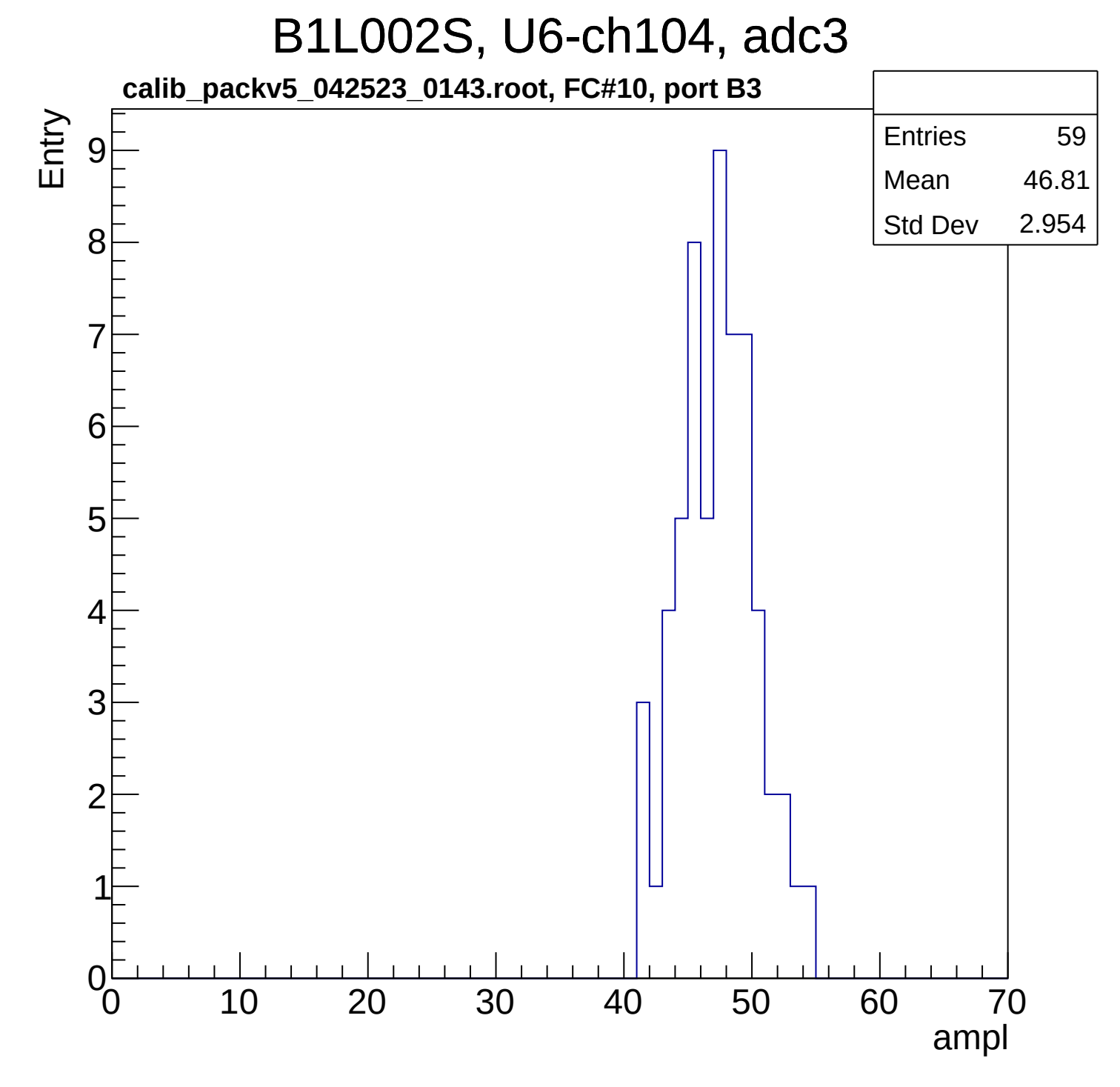

B1L002S, U6-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.4
Std Dev 1.356
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

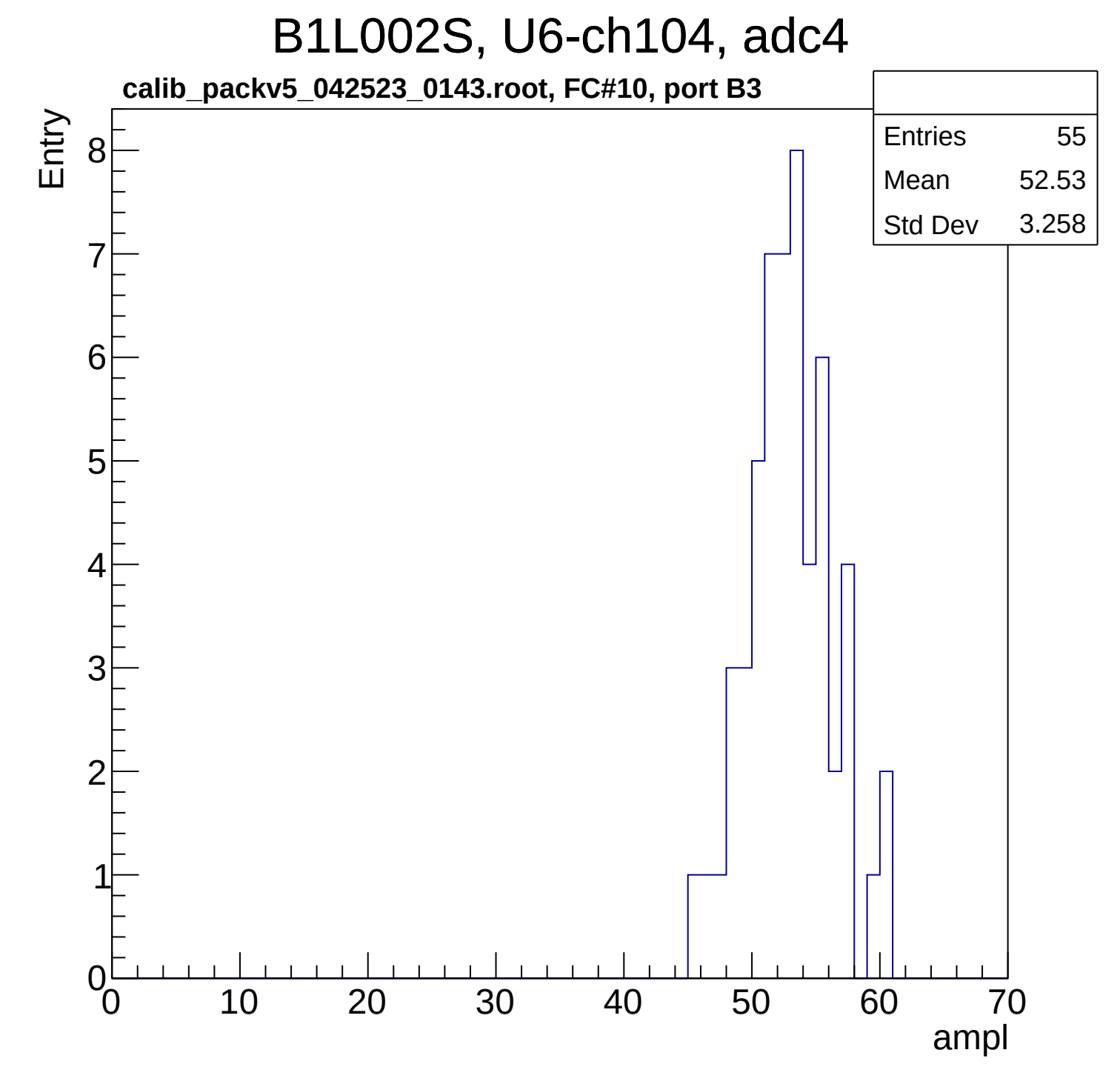

B1L002S, U6-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

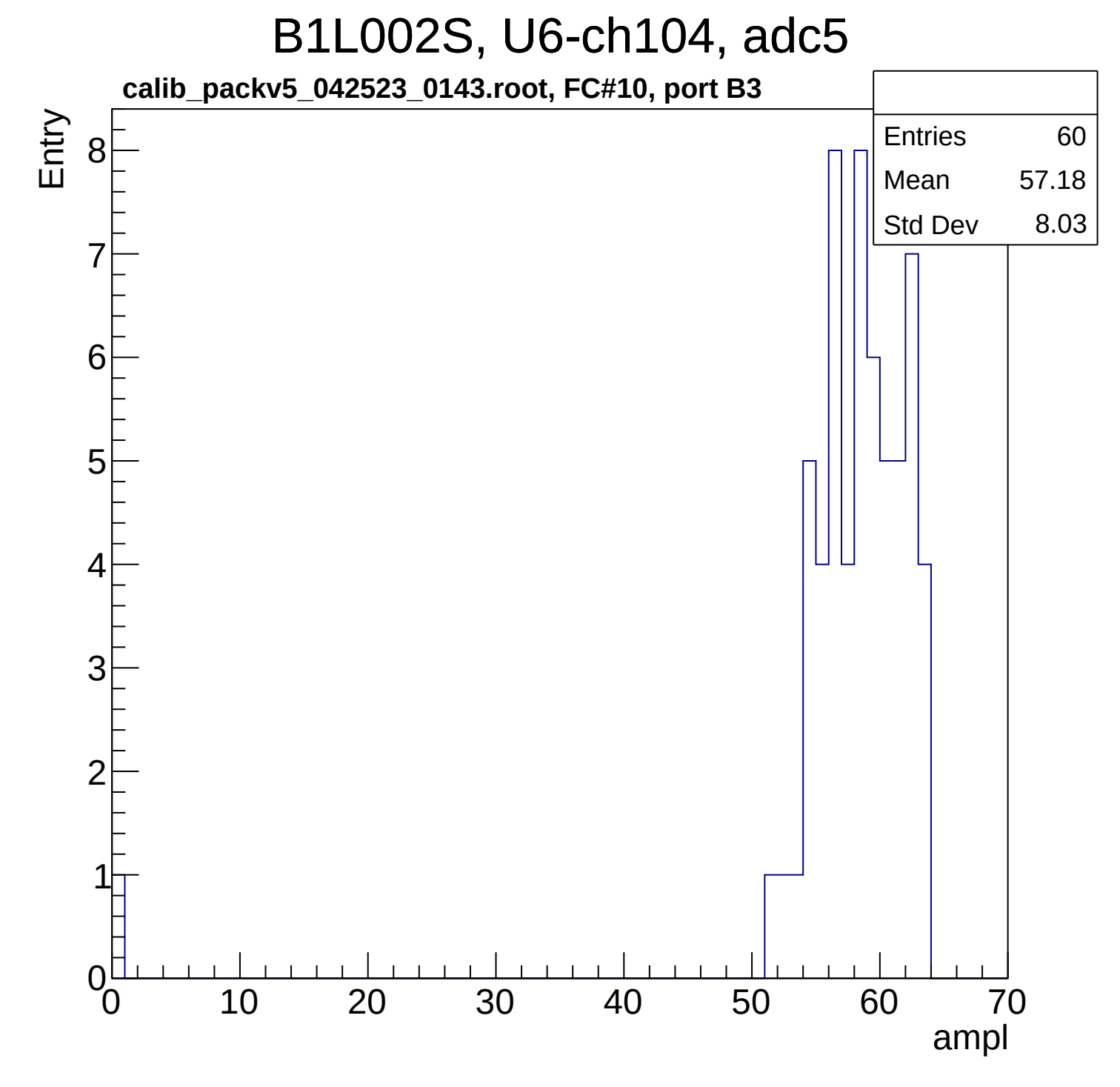

B1L002S, U6-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 26.88
Std Dev 4.801
Gaus mean : 27.8068
Gaus Width: 3.0913
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

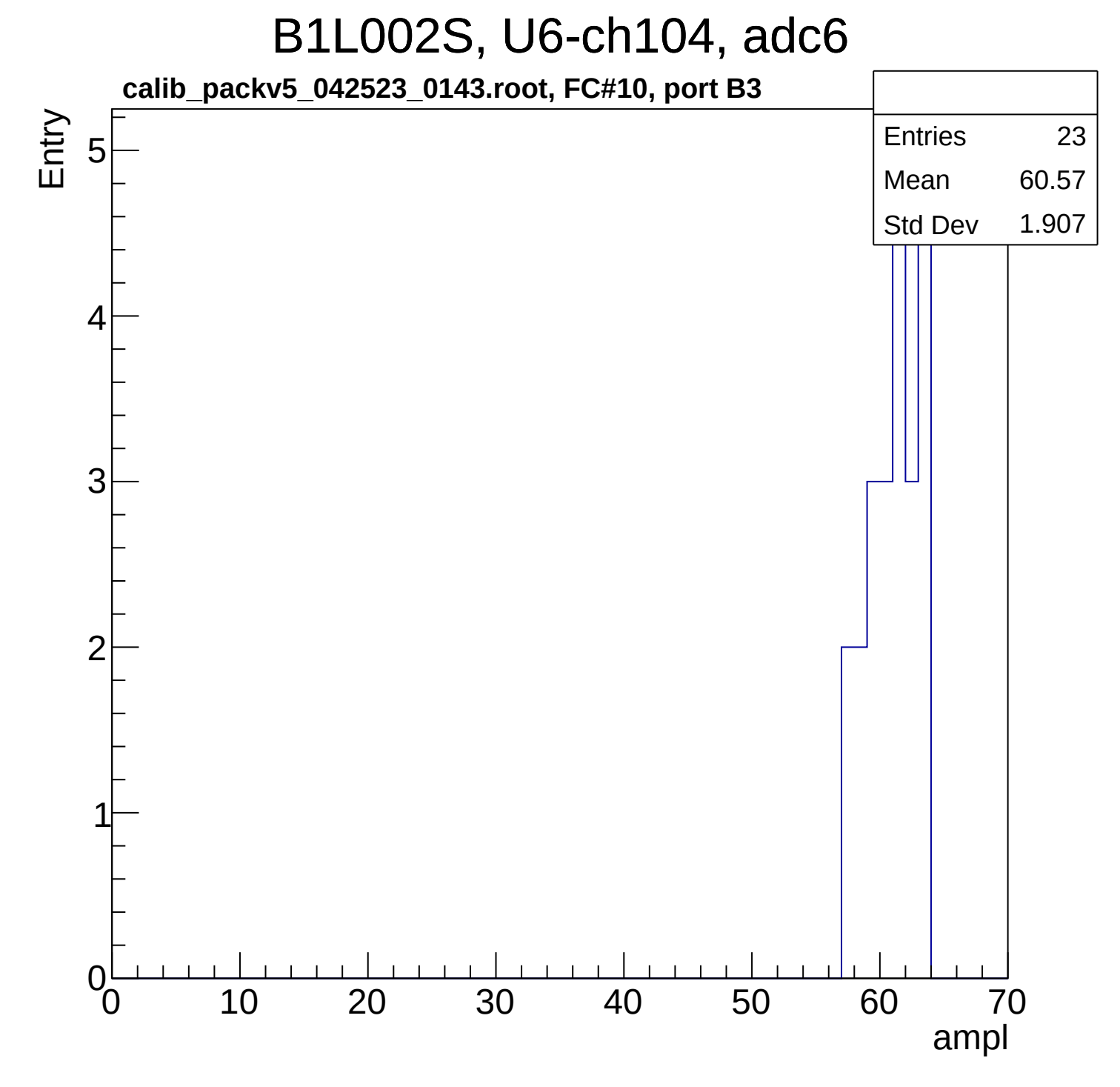

B1L002S, U6-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.26
Std Dev 3.478
Gaus mean : 34.9555
Gaus Width: 4.0309
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

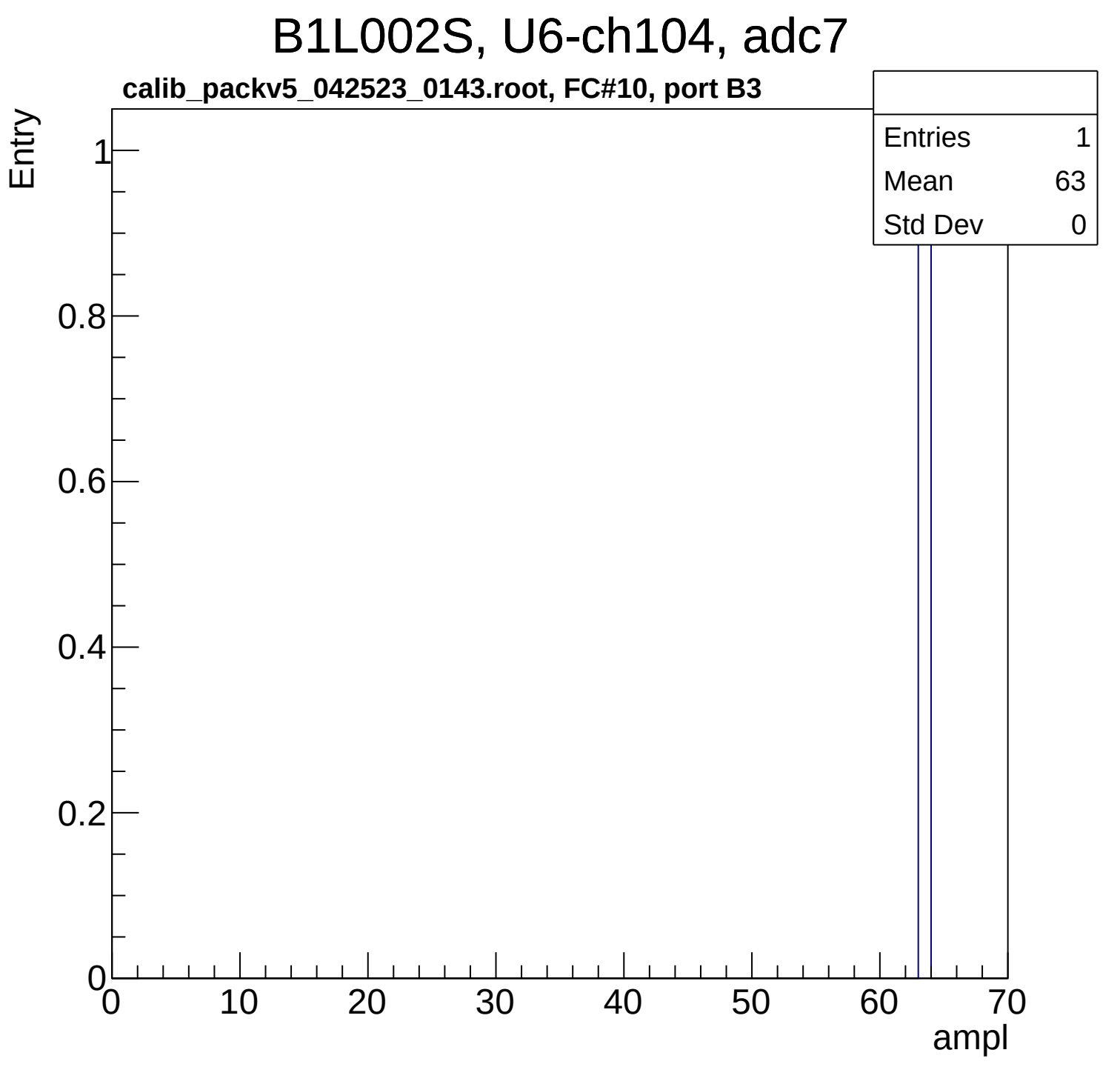

B1L002S, U6-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.8
Std Dev 3.435
Gaus mean : 41.2337
Gaus Width: 2.7519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

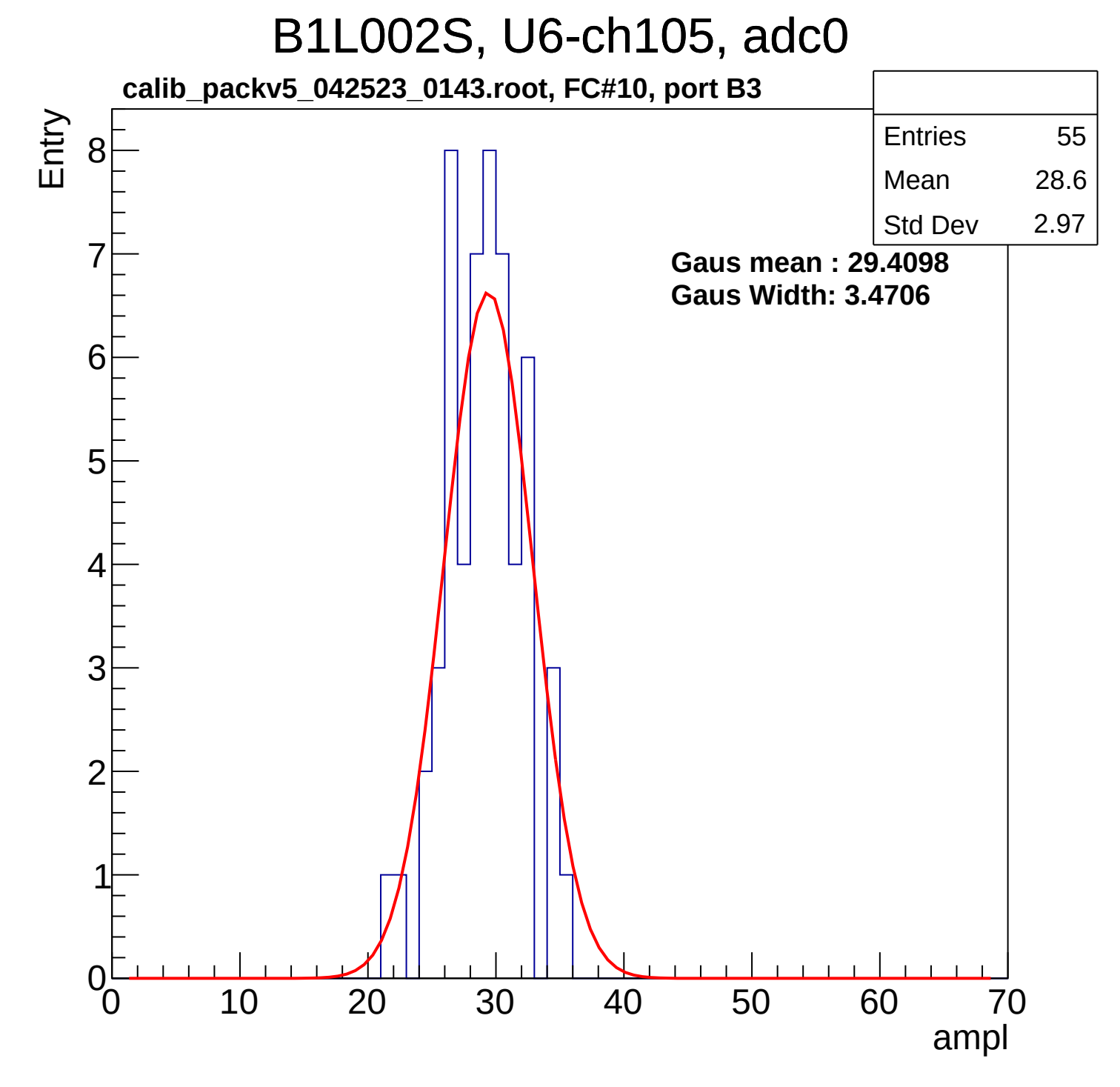

B1L002S, U6-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.81
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

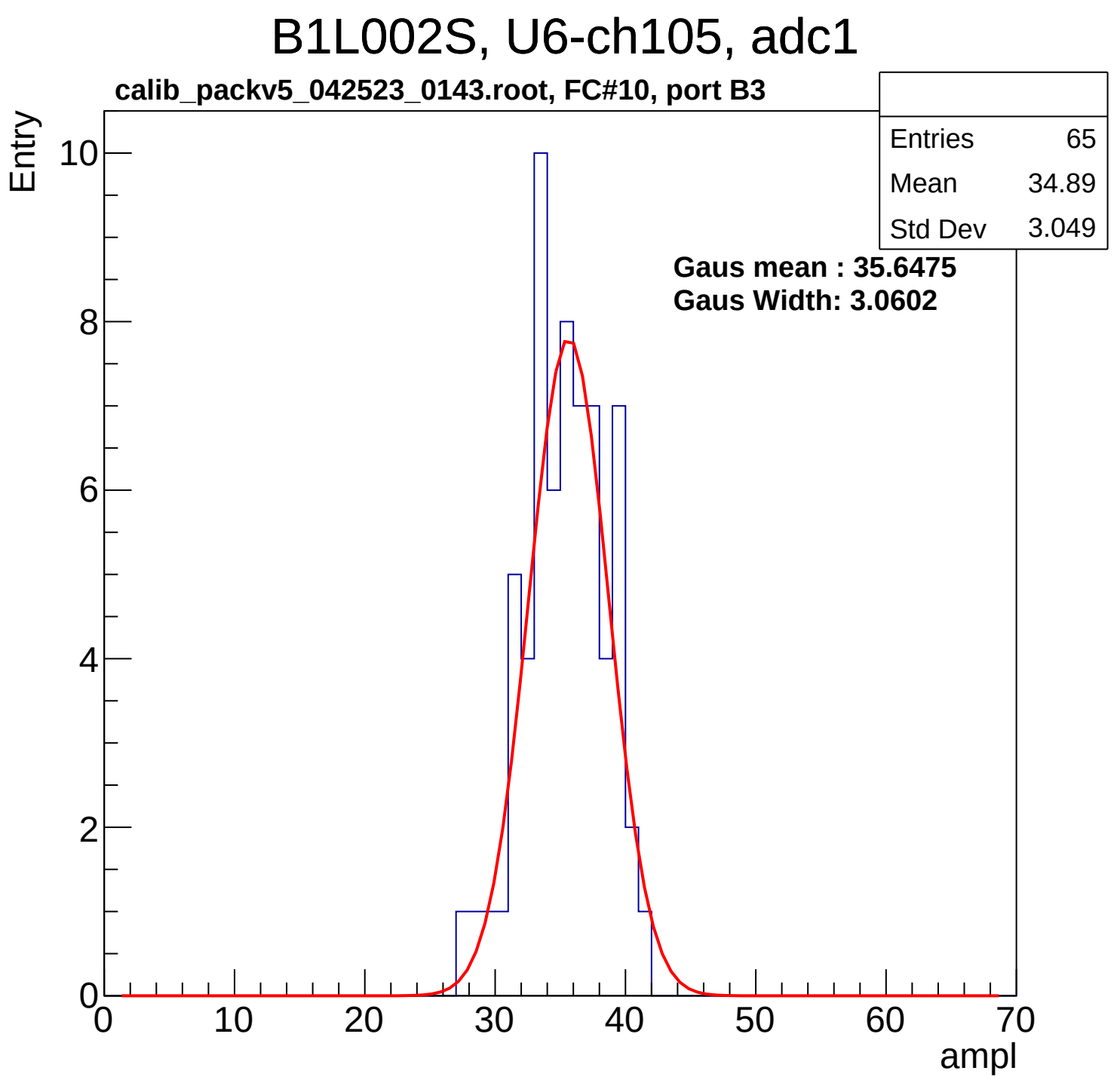

B1L002S, U6-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.53
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

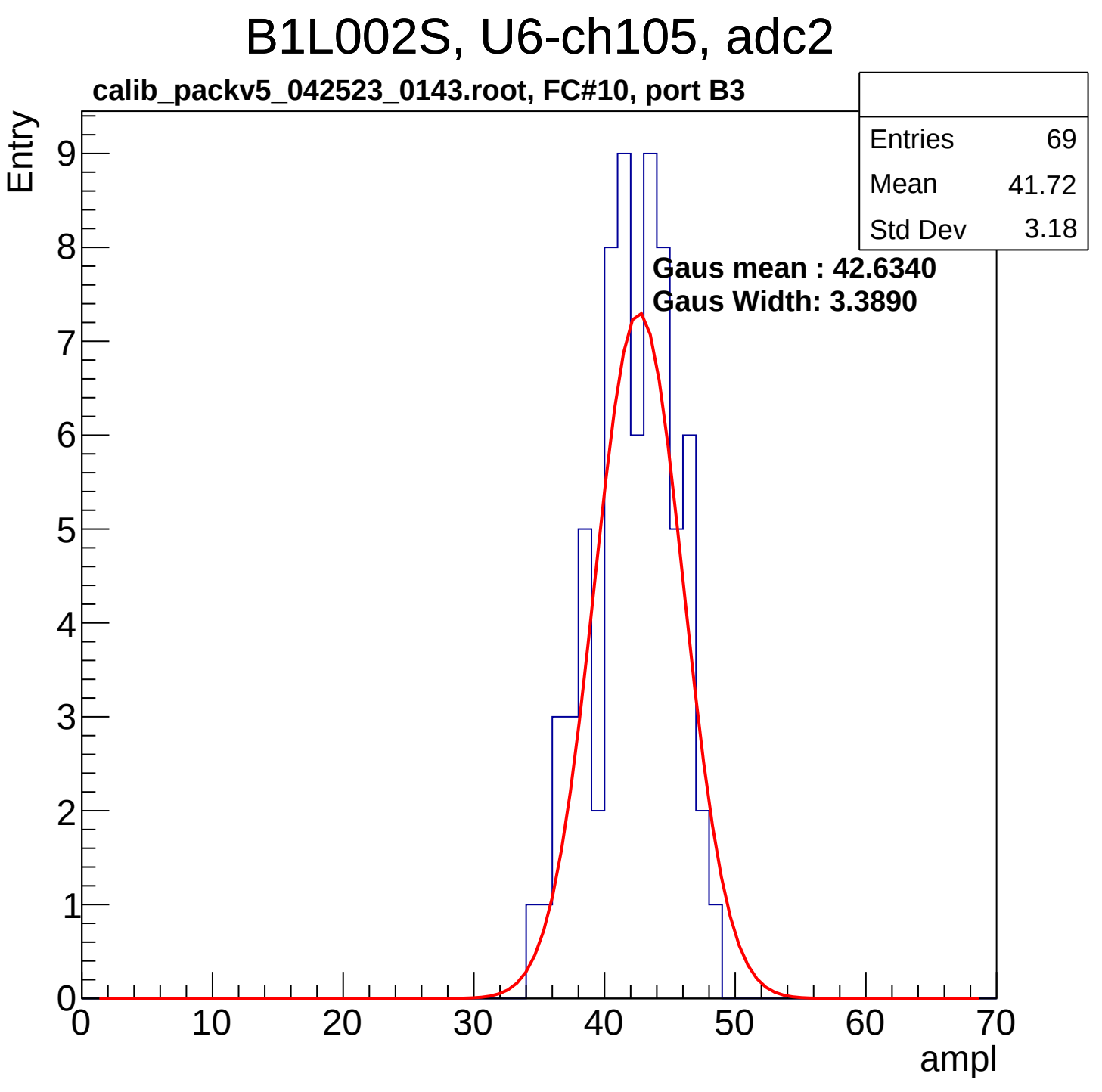

B1L002S, U6-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.18
Std Dev 8.03
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

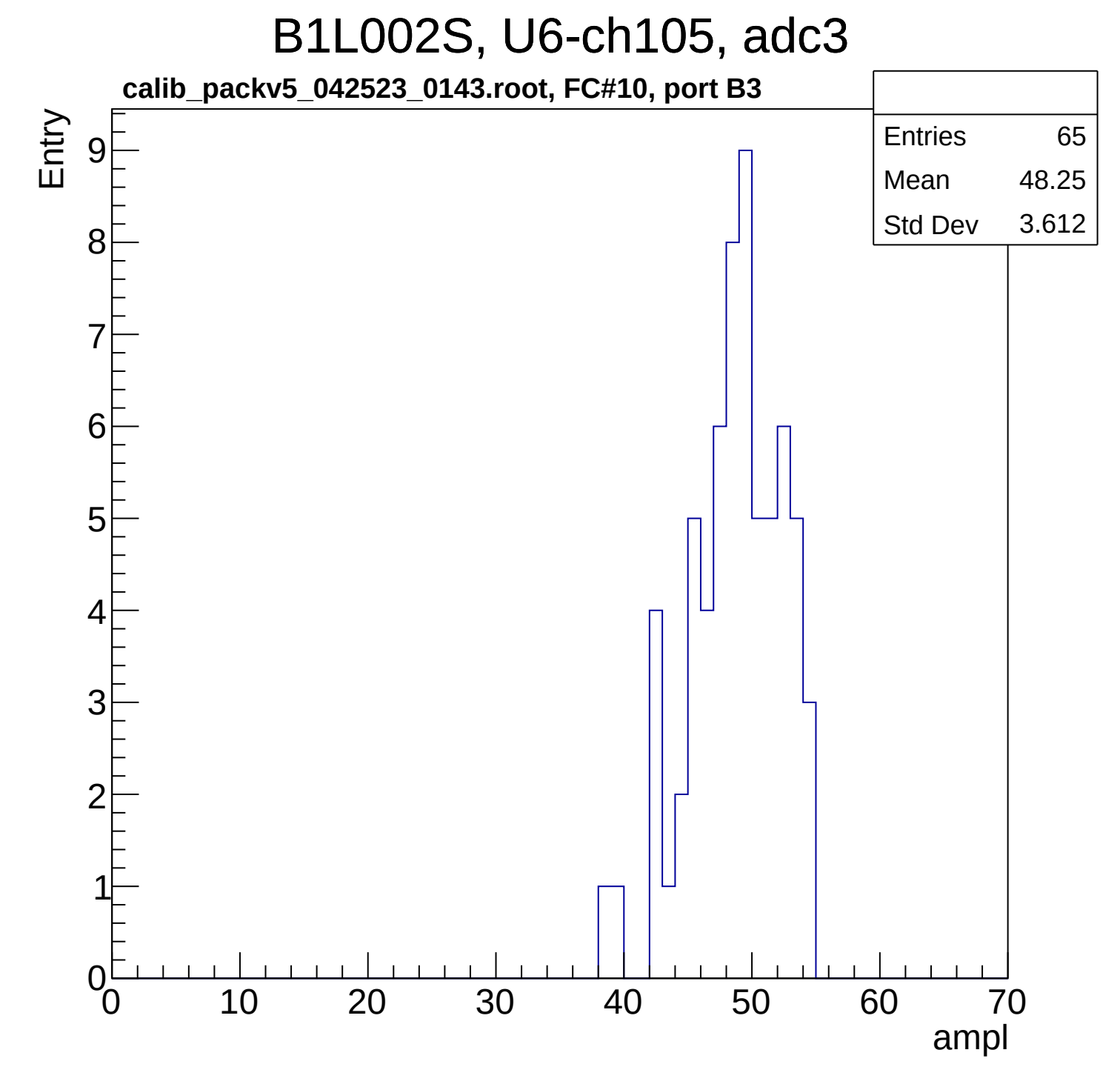

B1L002S, U6-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 60.57
Std Dev 1.907
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

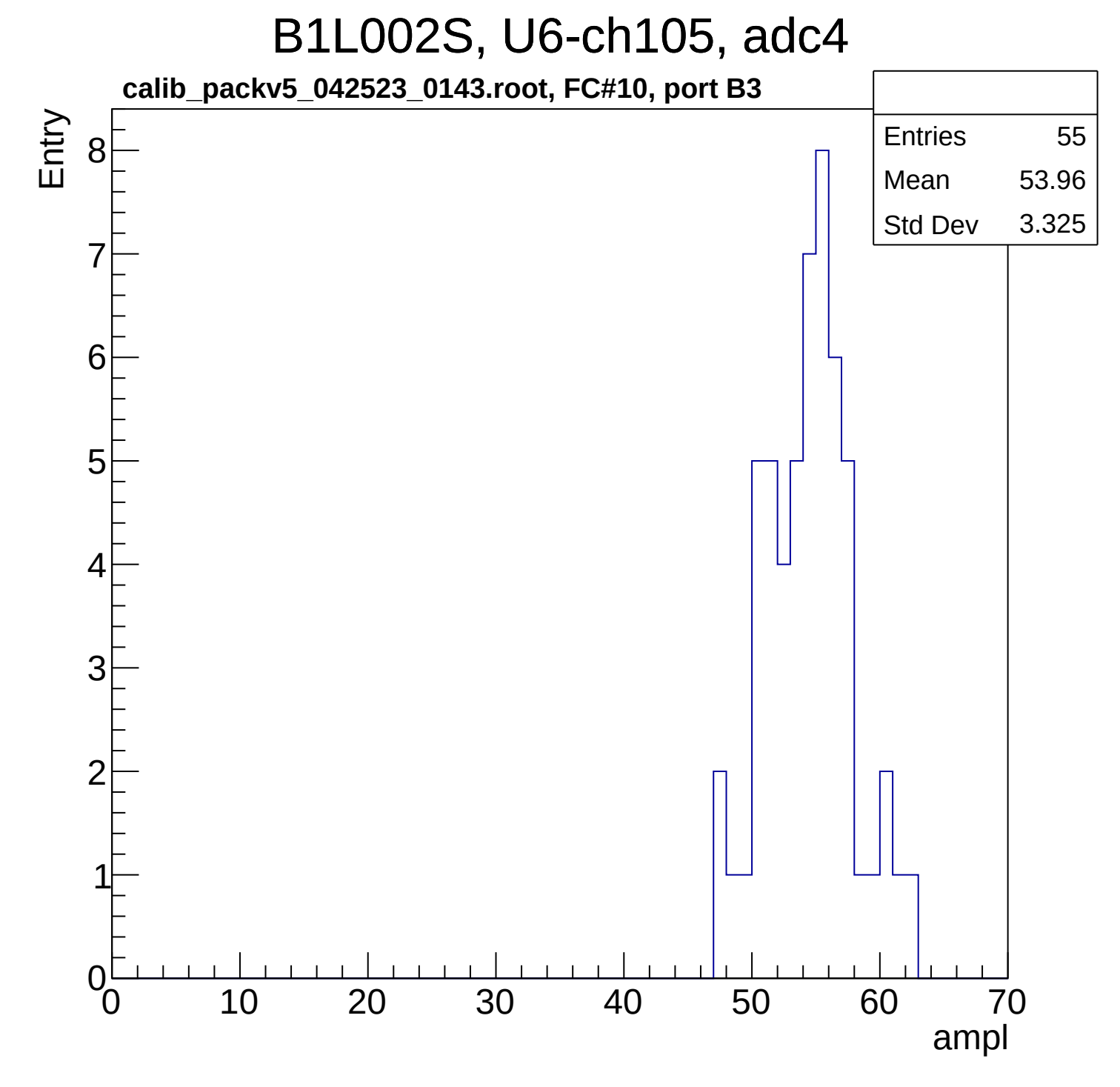

B1L002S, U6-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

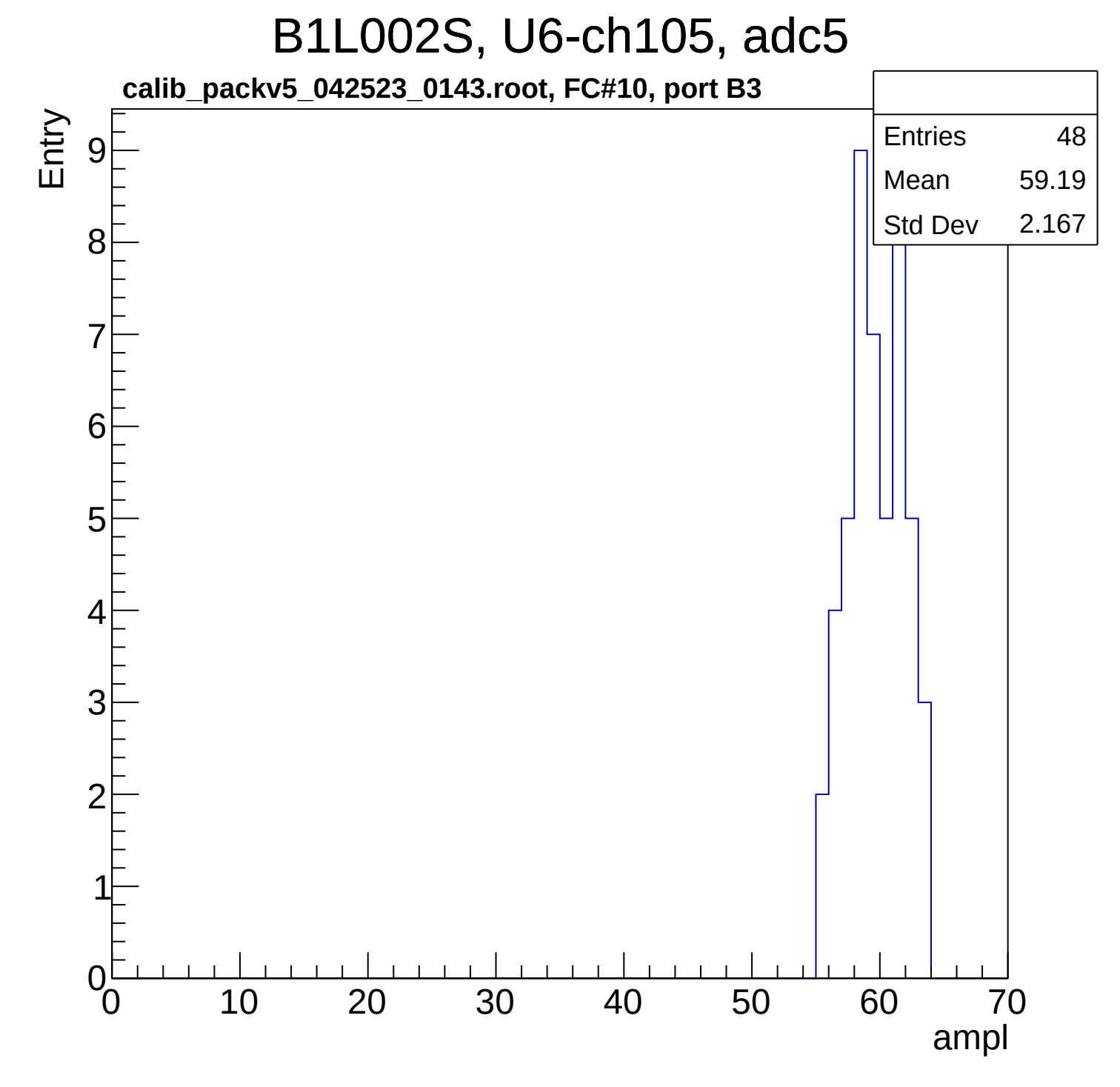

B1L002S, U6-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.6
Std Dev 2.97
Gaus mean : 29.4098
Gaus Width: 3.4706
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

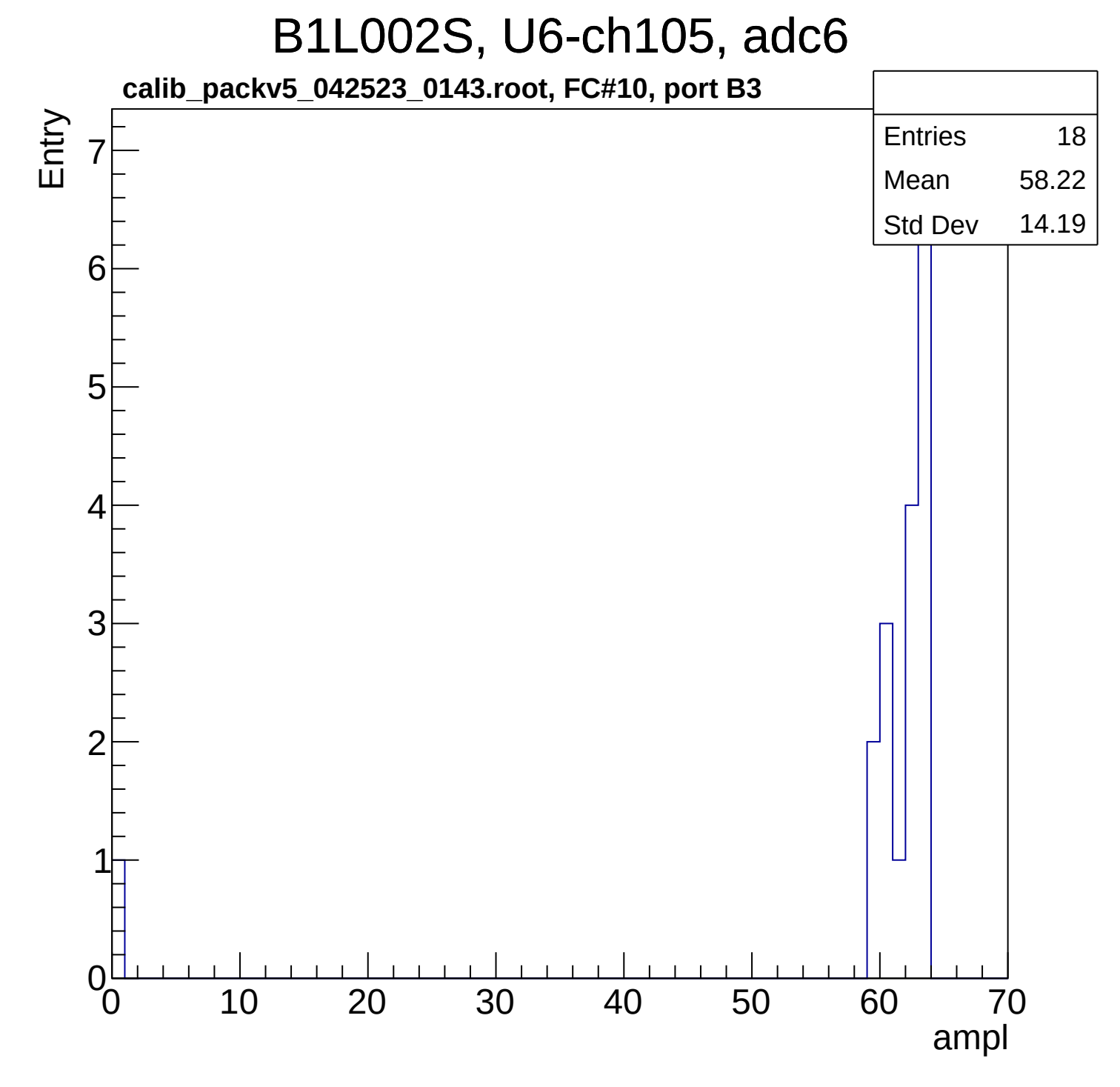

B1L002S, U6-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.89
Std Dev 3.049
Gaus mean : 35.6475
Gaus Width: 3.0602
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

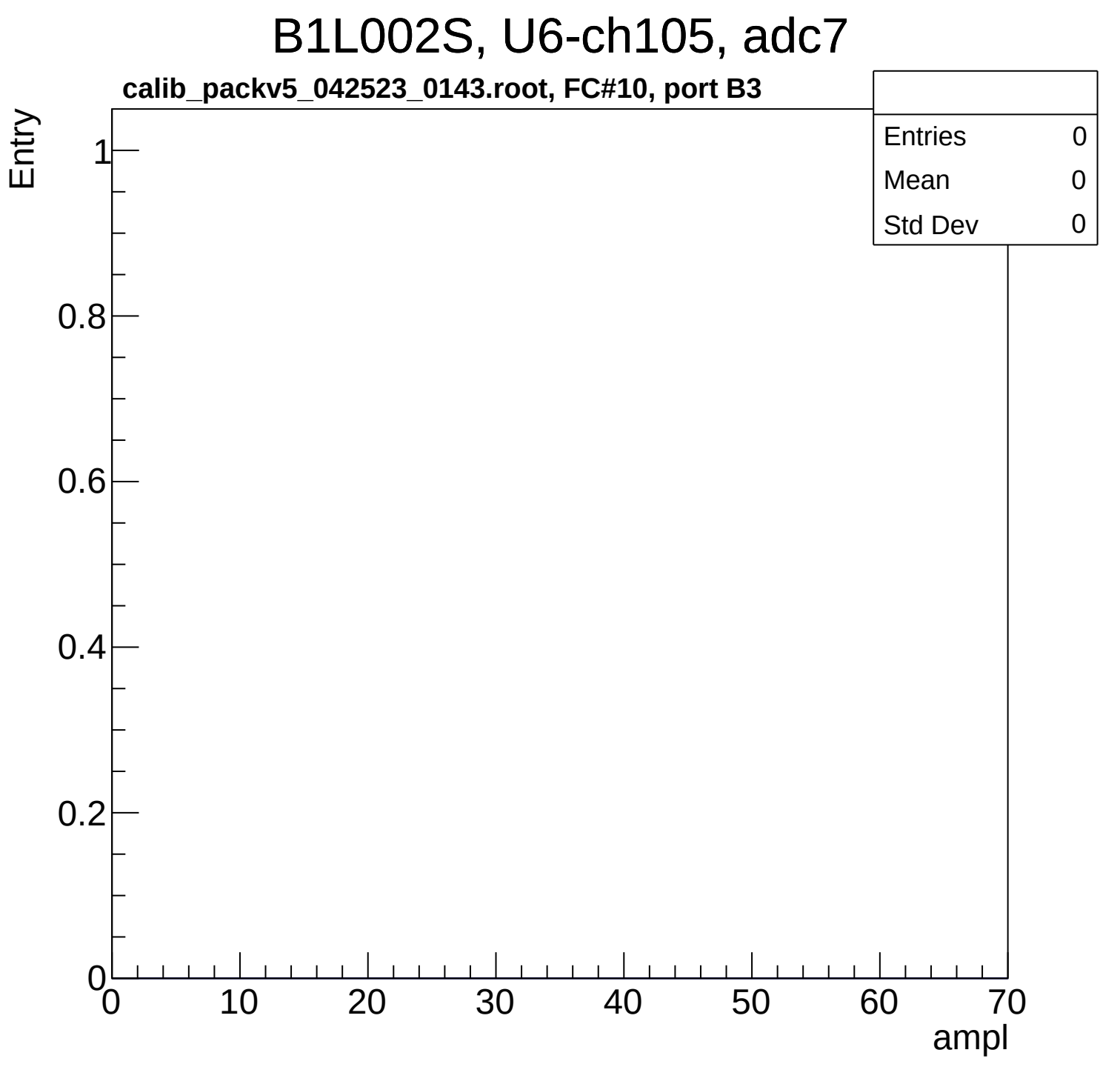

B1L002S, U6-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.72
Std Dev 3.18
Gaus mean : 42.6340
Gaus Width: 3.3890
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

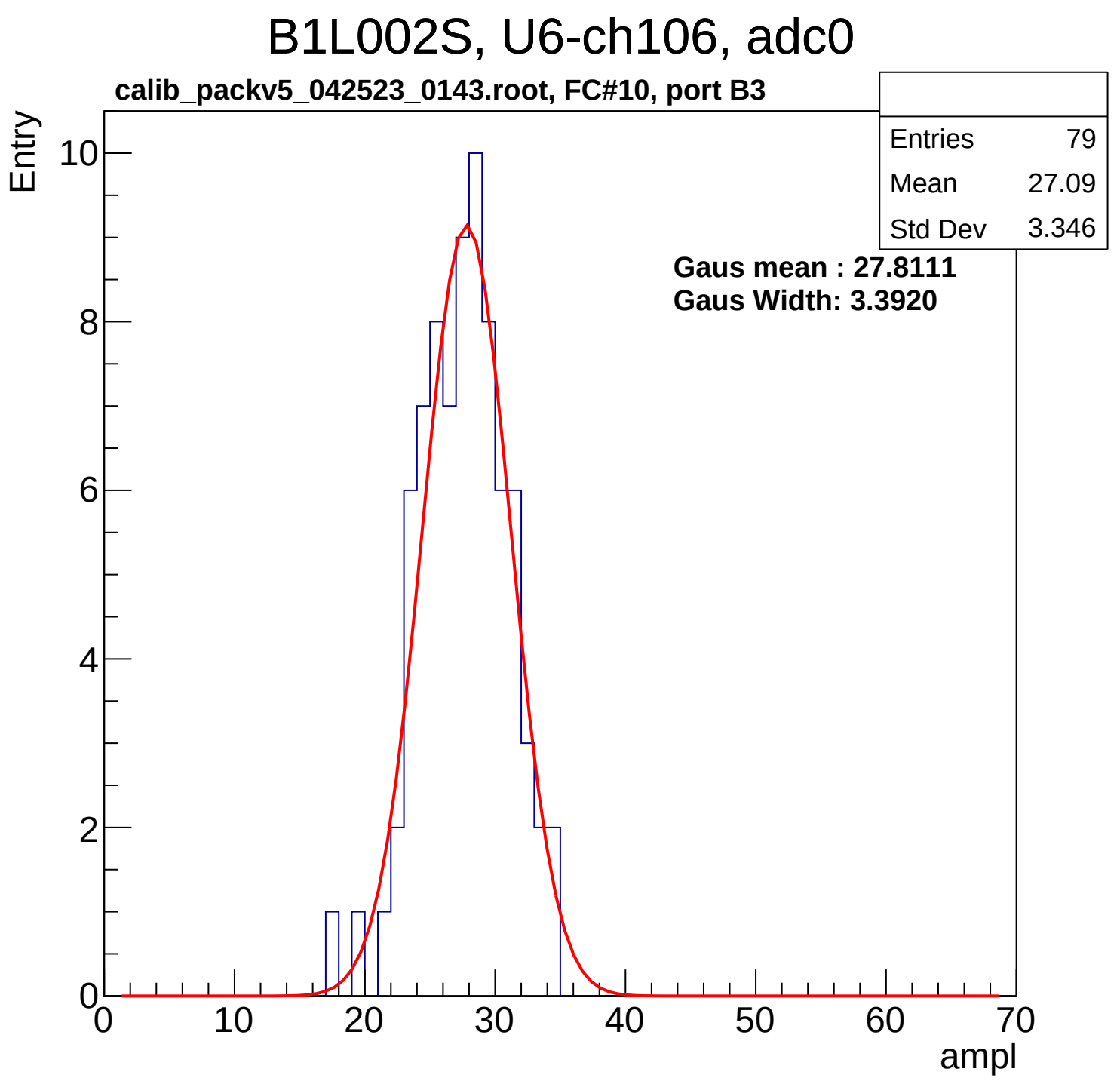

B1L002S, U6-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.25
Std Dev 3.612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

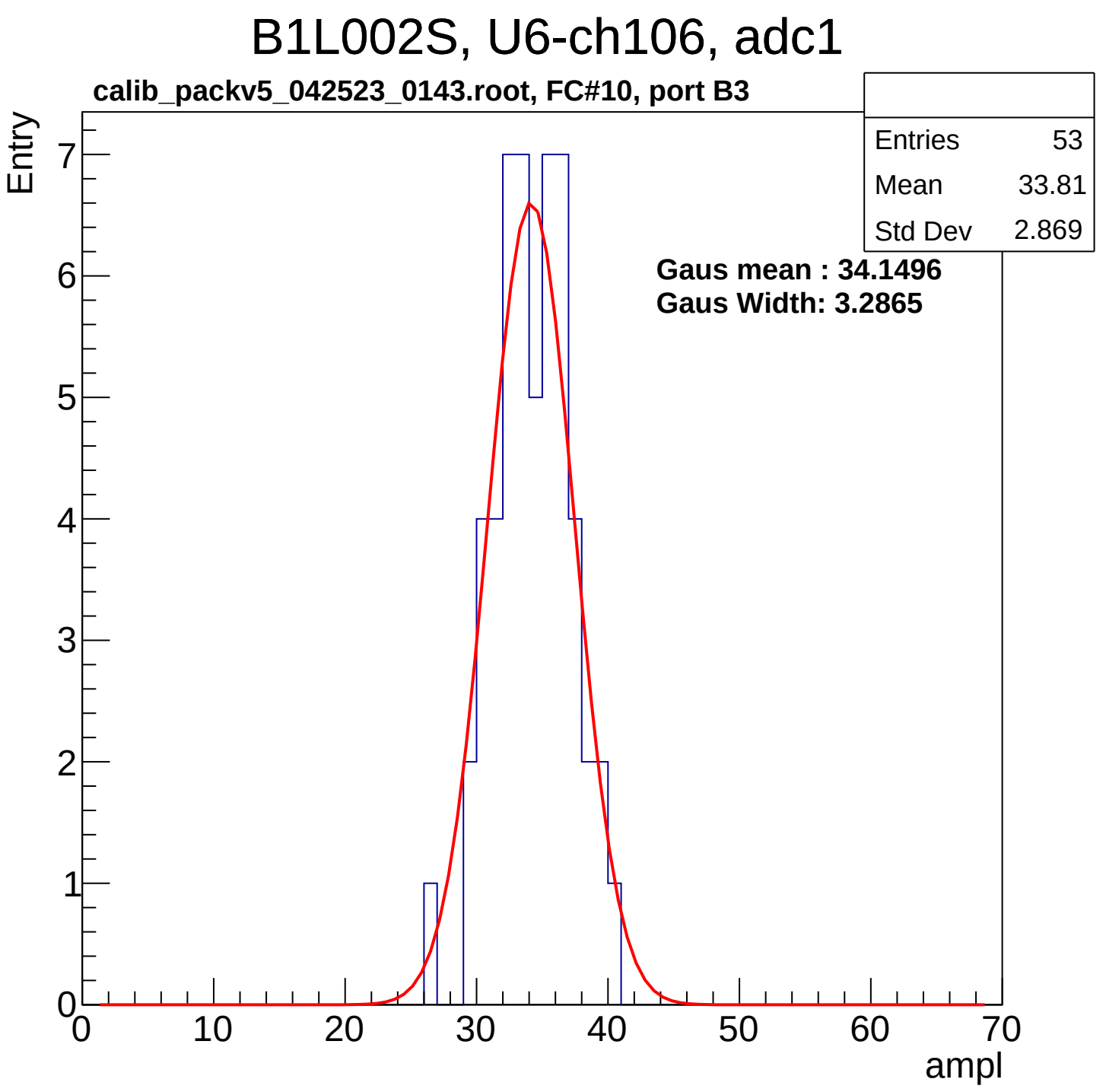

B1L002S, U6-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.96
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

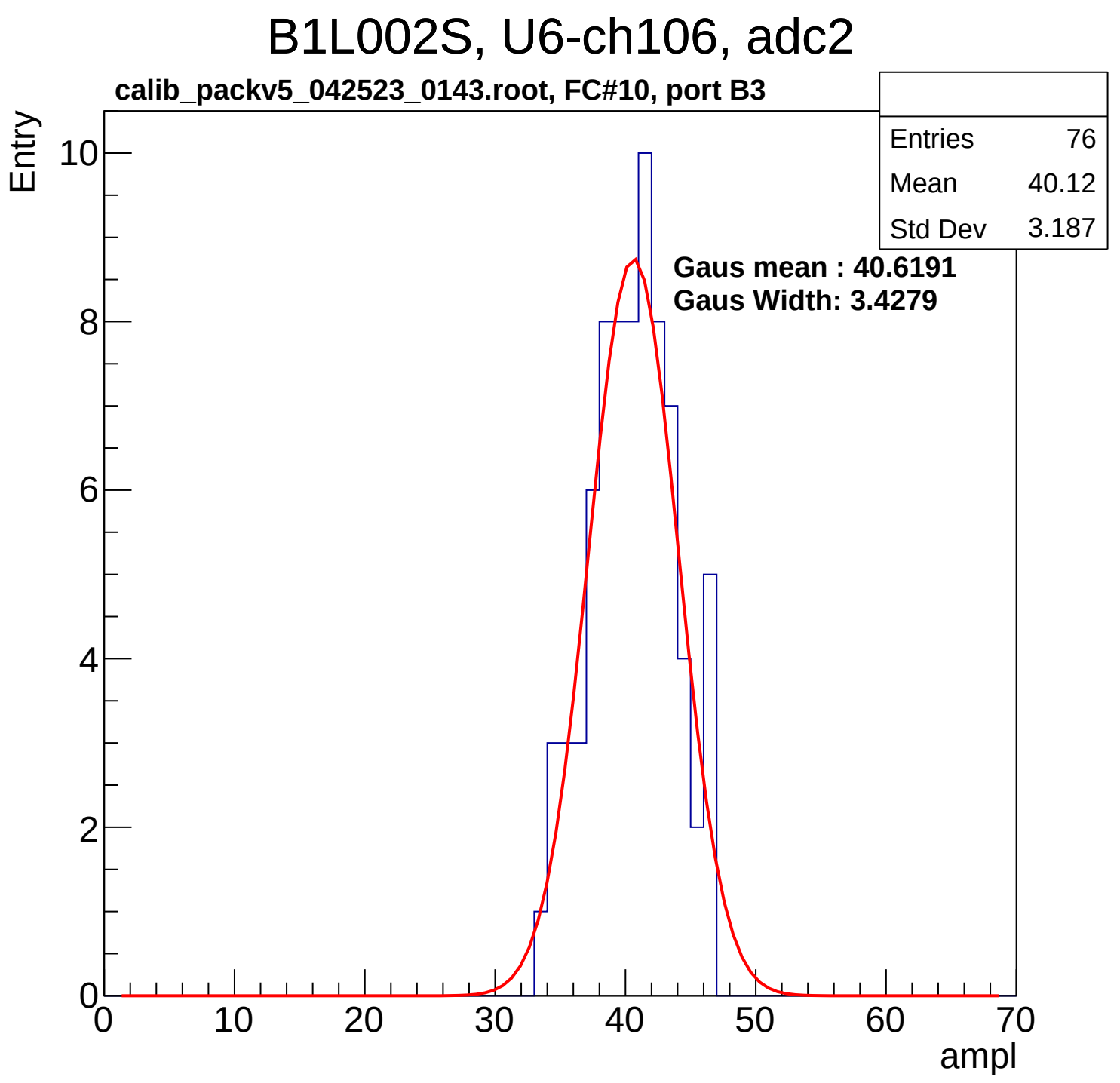

B1L002S, U6-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.19
Std Dev 2.167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

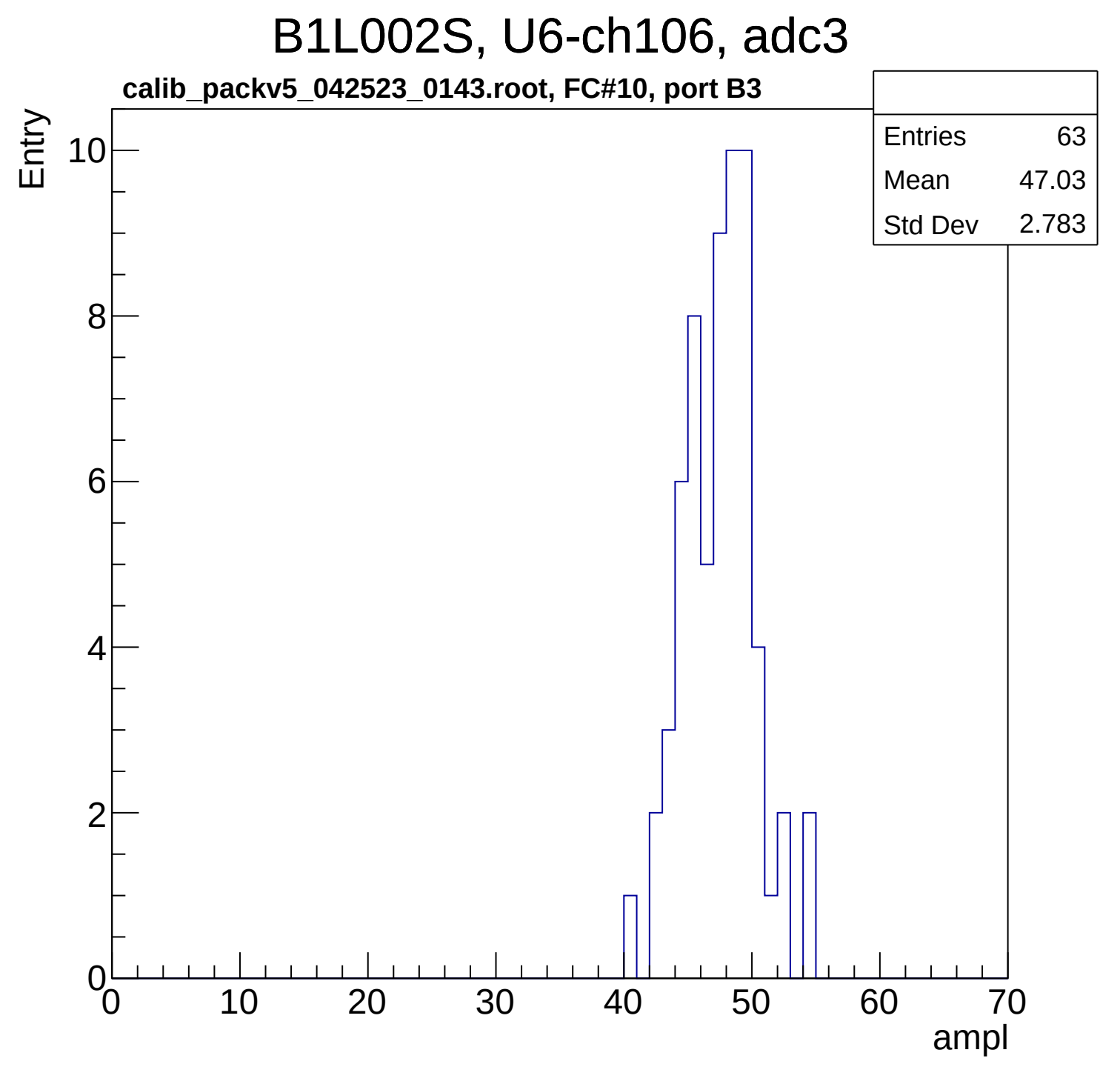

B1L002S, U6-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.22
Std Dev 14.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

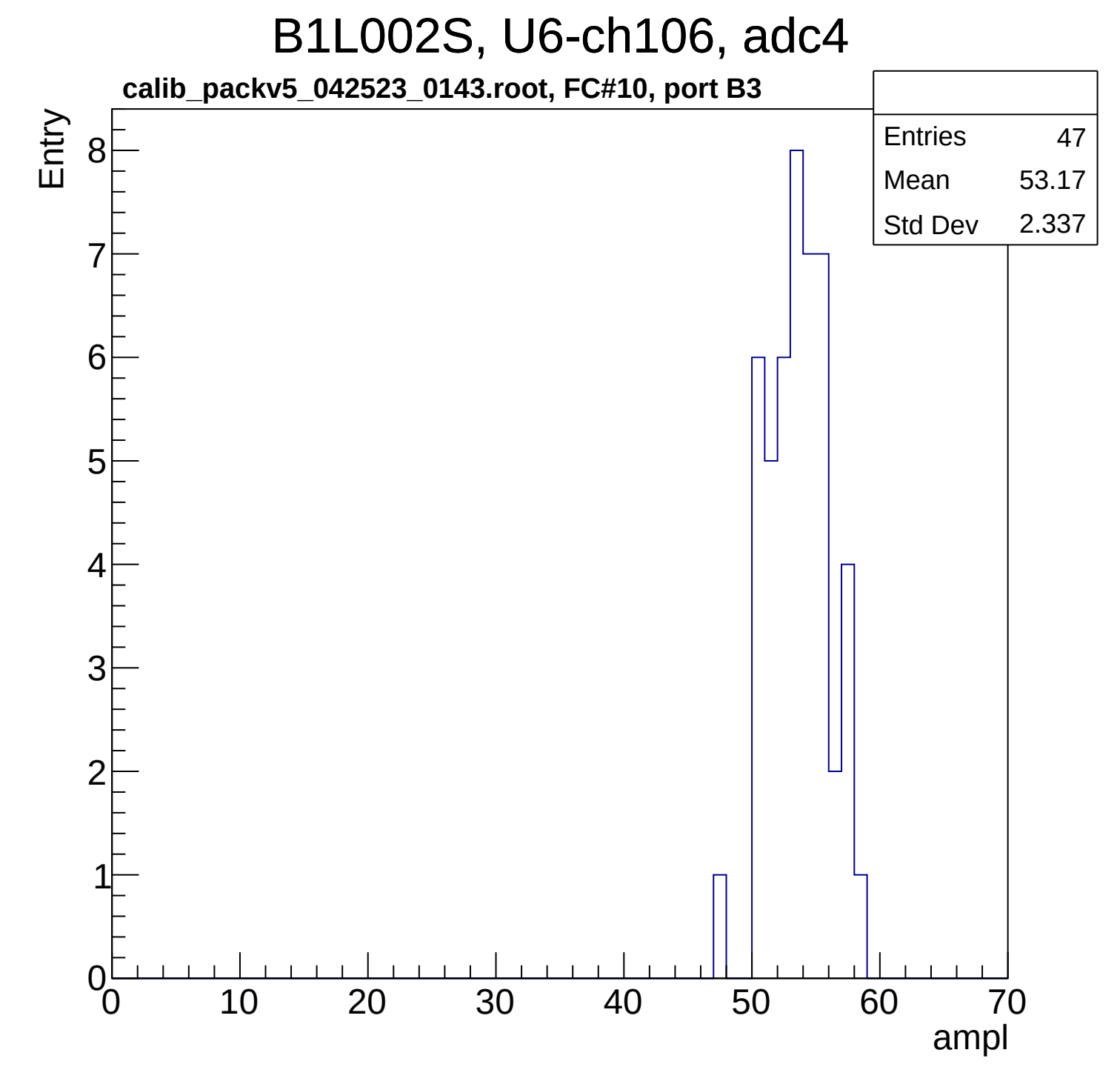

B1L002S, U6-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

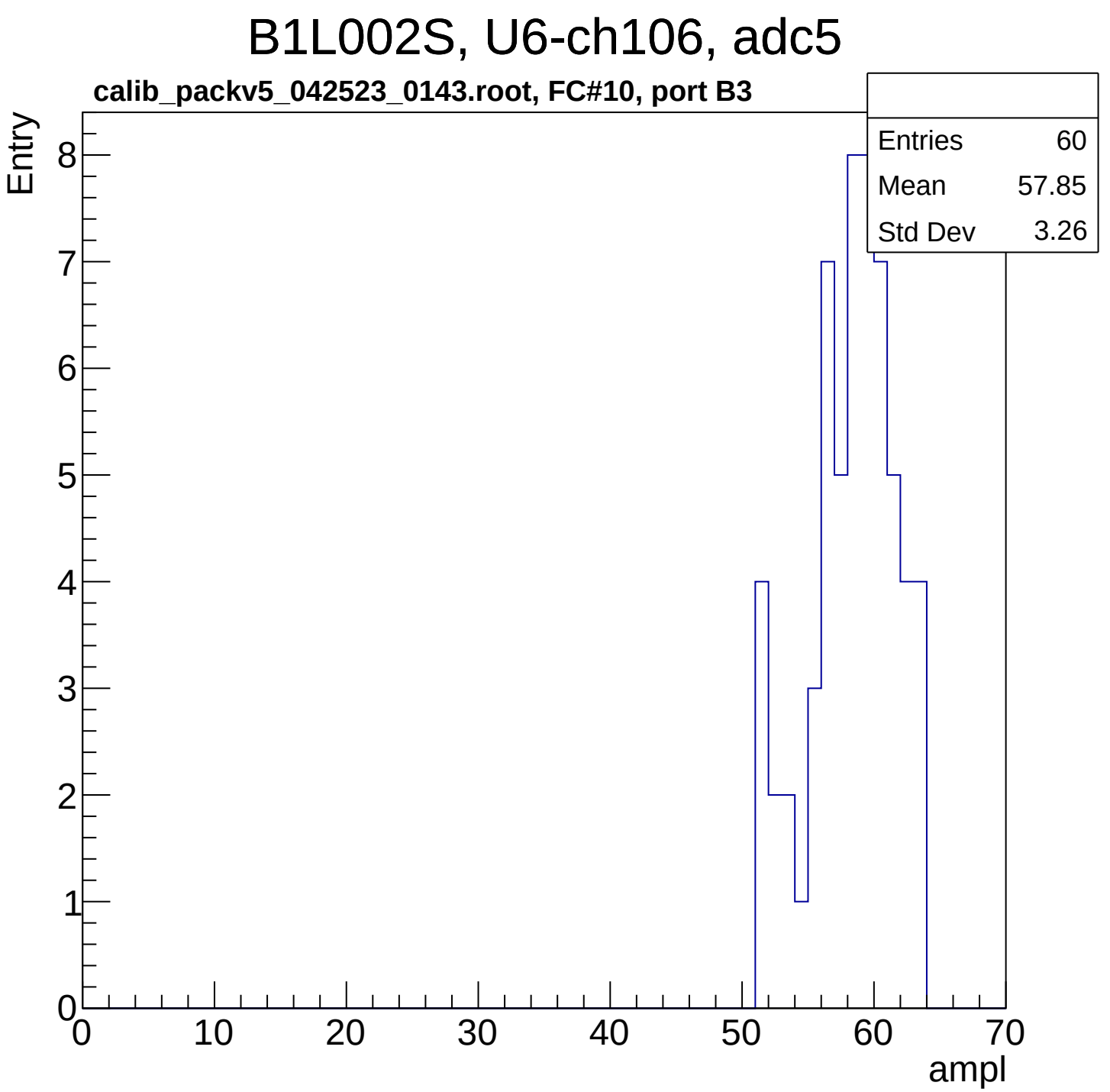

B1L002S, U6-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.09
Std Dev 3.346
Gaus mean : 27.8111
Gaus Width: 3.3920
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

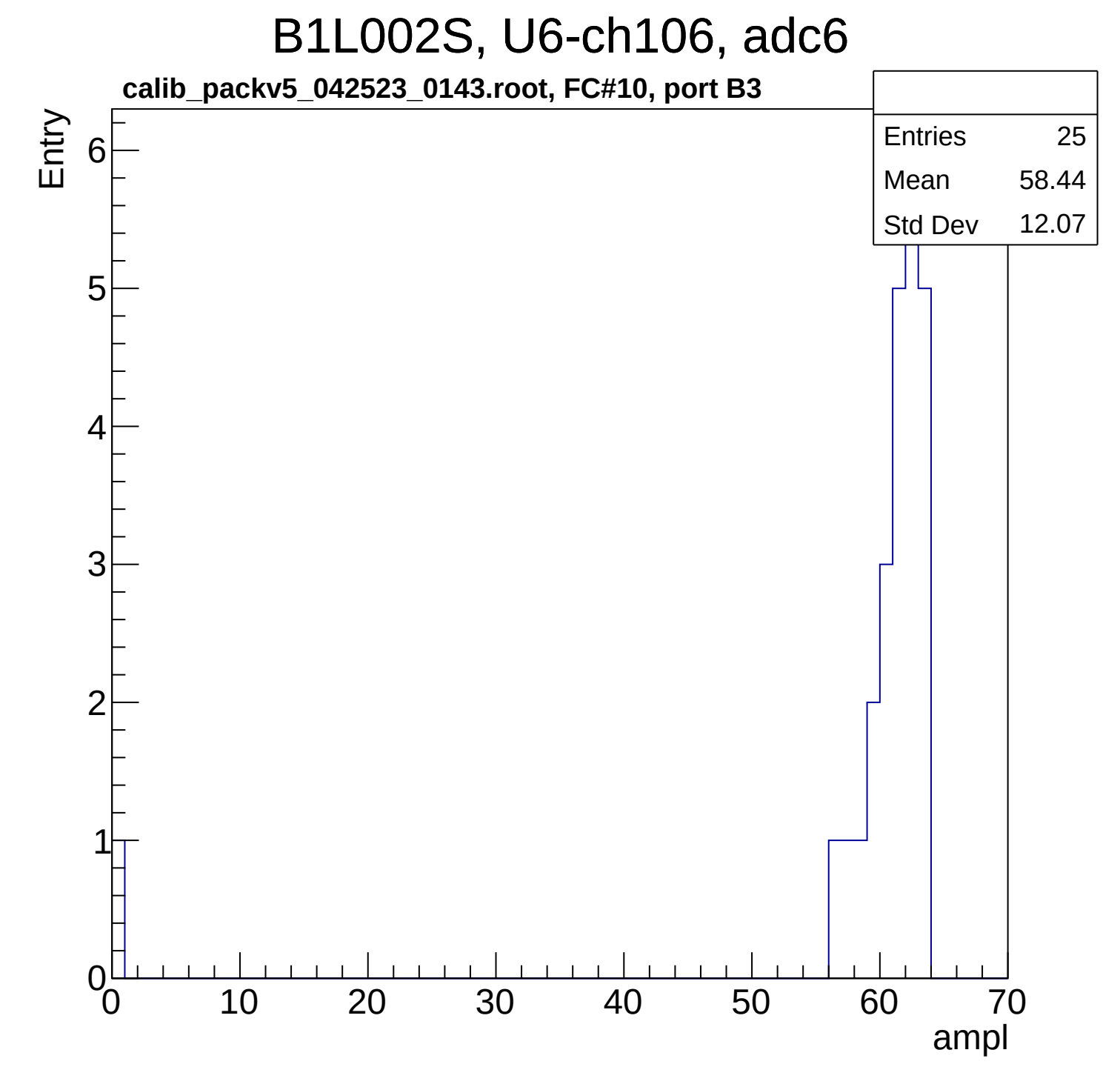

B1L002S, U6-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.81
Std Dev 2.869
Gaus mean : 34.1496
Gaus Width: 3.2865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

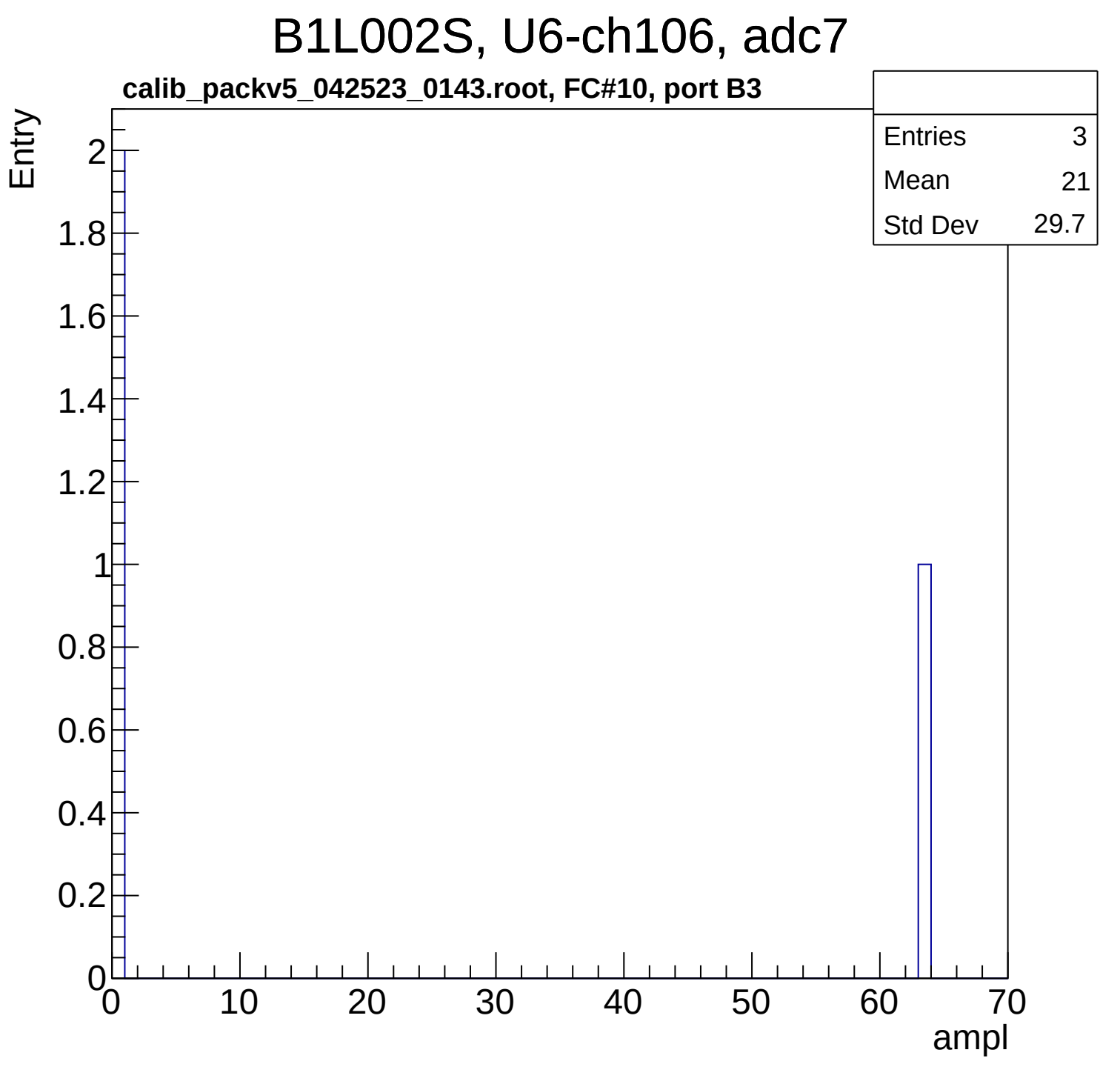

B1L002S, U6-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 40.12
Std Dev 3.187
Gaus mean : 40.6191
Gaus Width: 3.4279
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

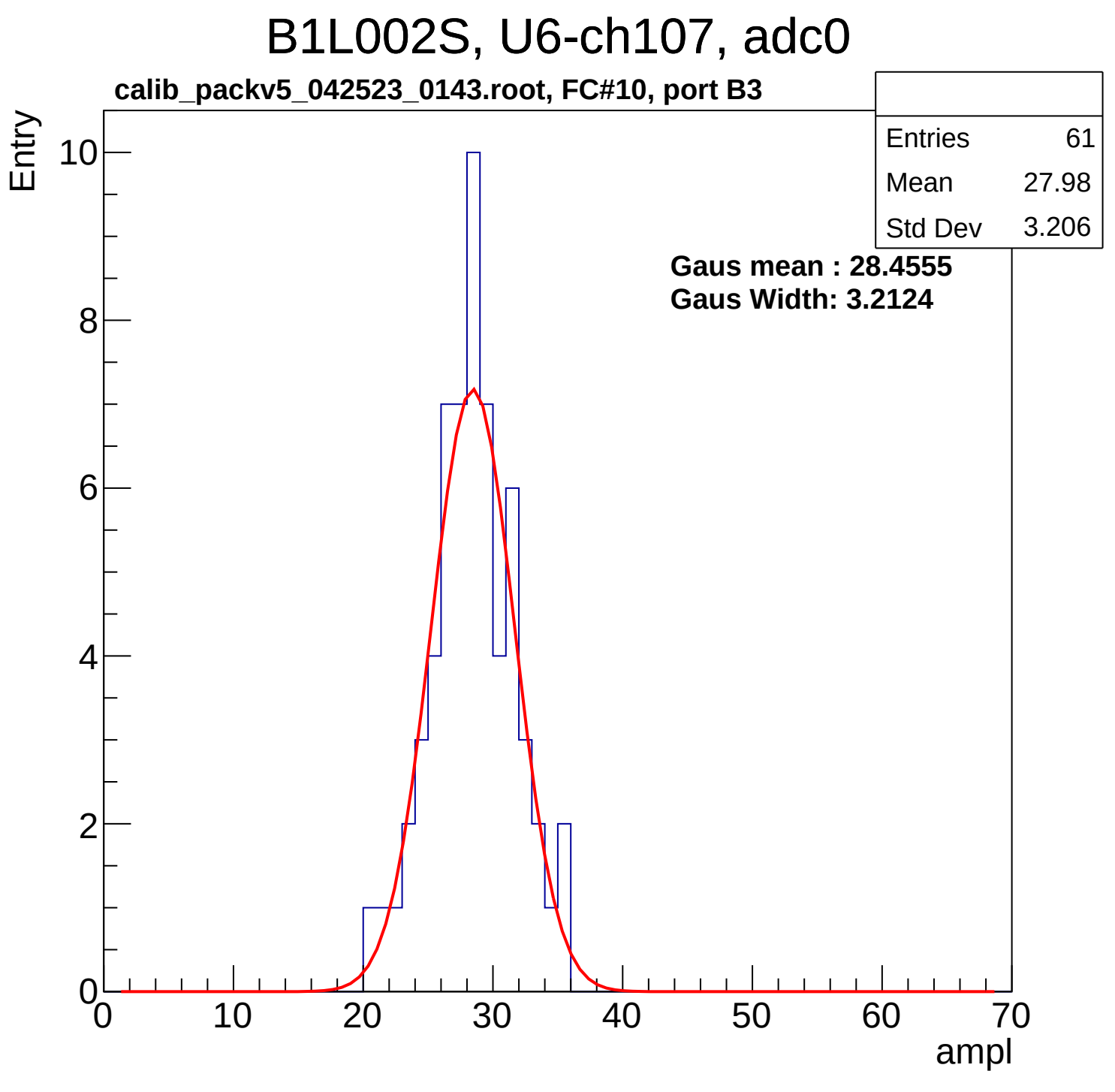

B1L002S, U6-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.03
Std Dev 2.783
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

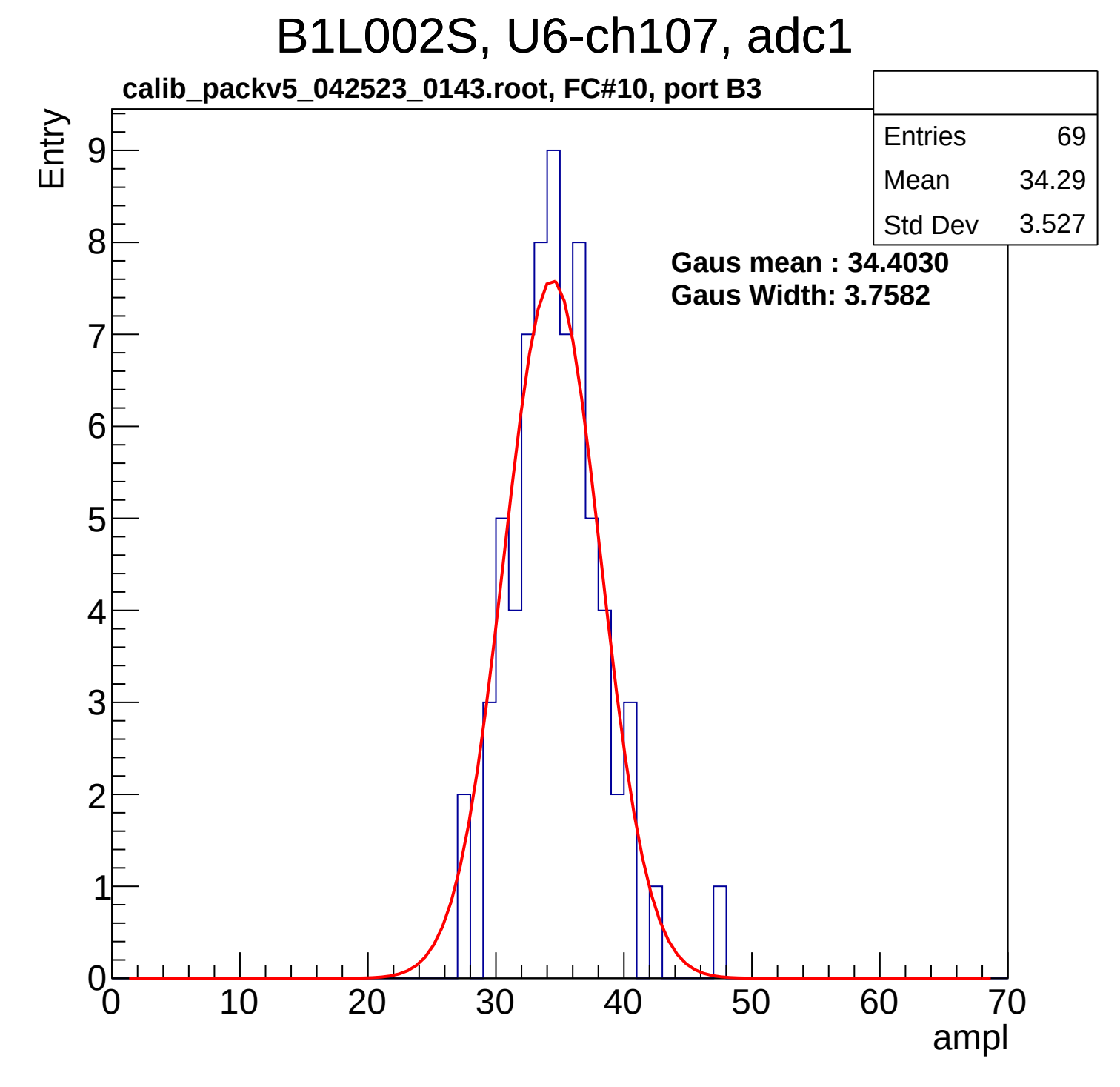

B1L002S, U6-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.17
Std Dev 2.337
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

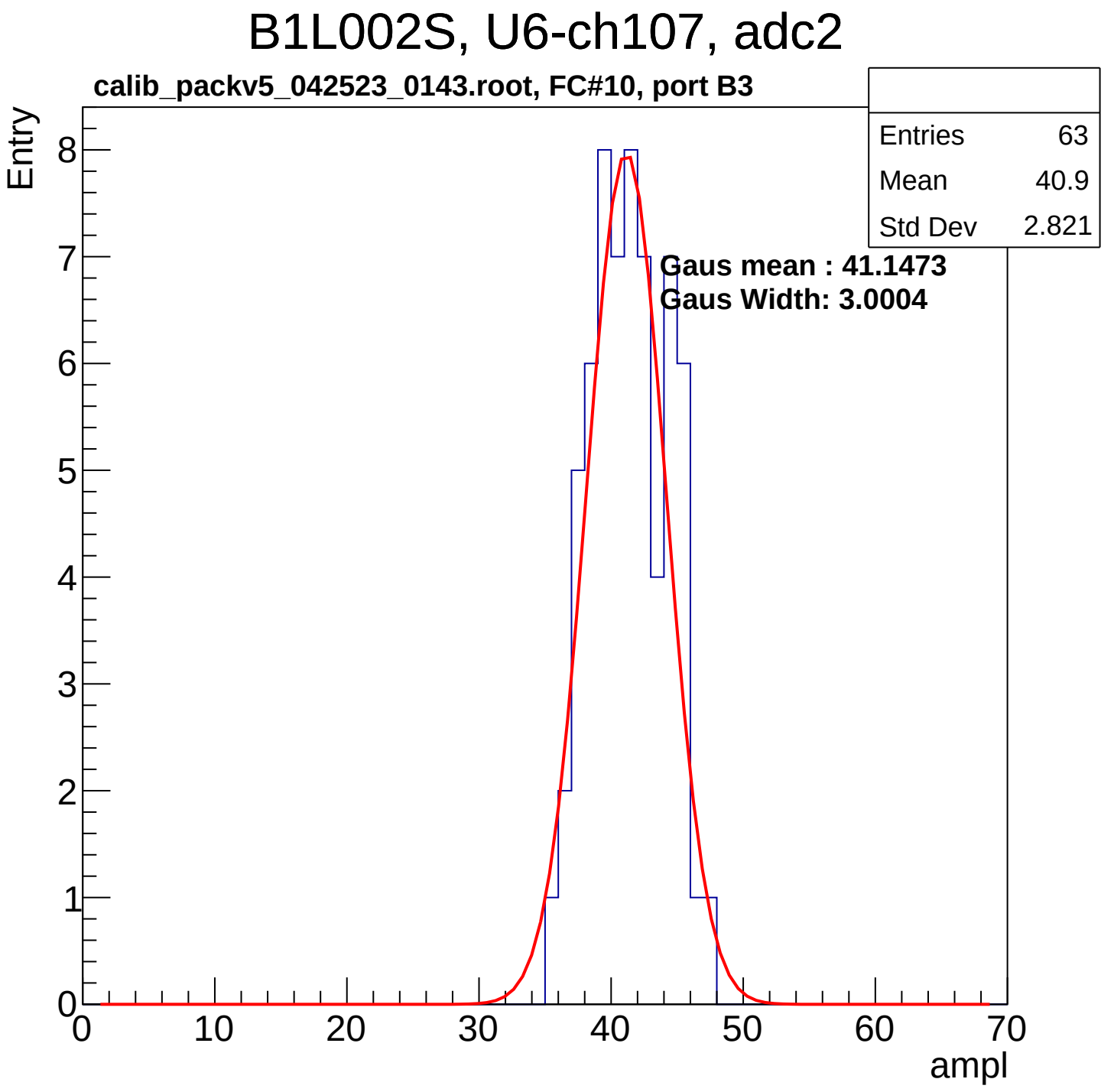

B1L002S, U6-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.85
Std Dev 3.26
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

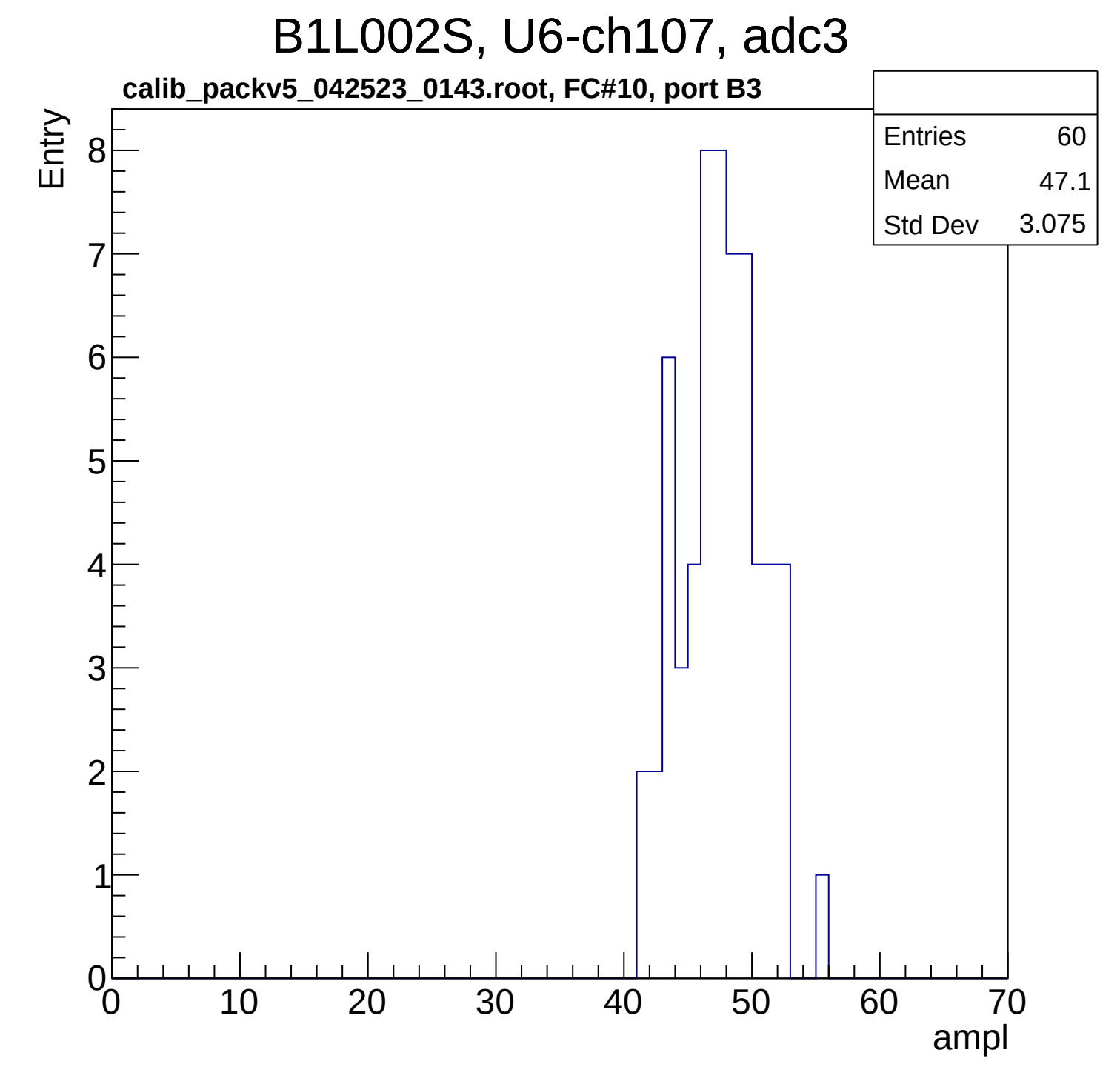

B1L002S, U6-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.44
Std Dev 12.07
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

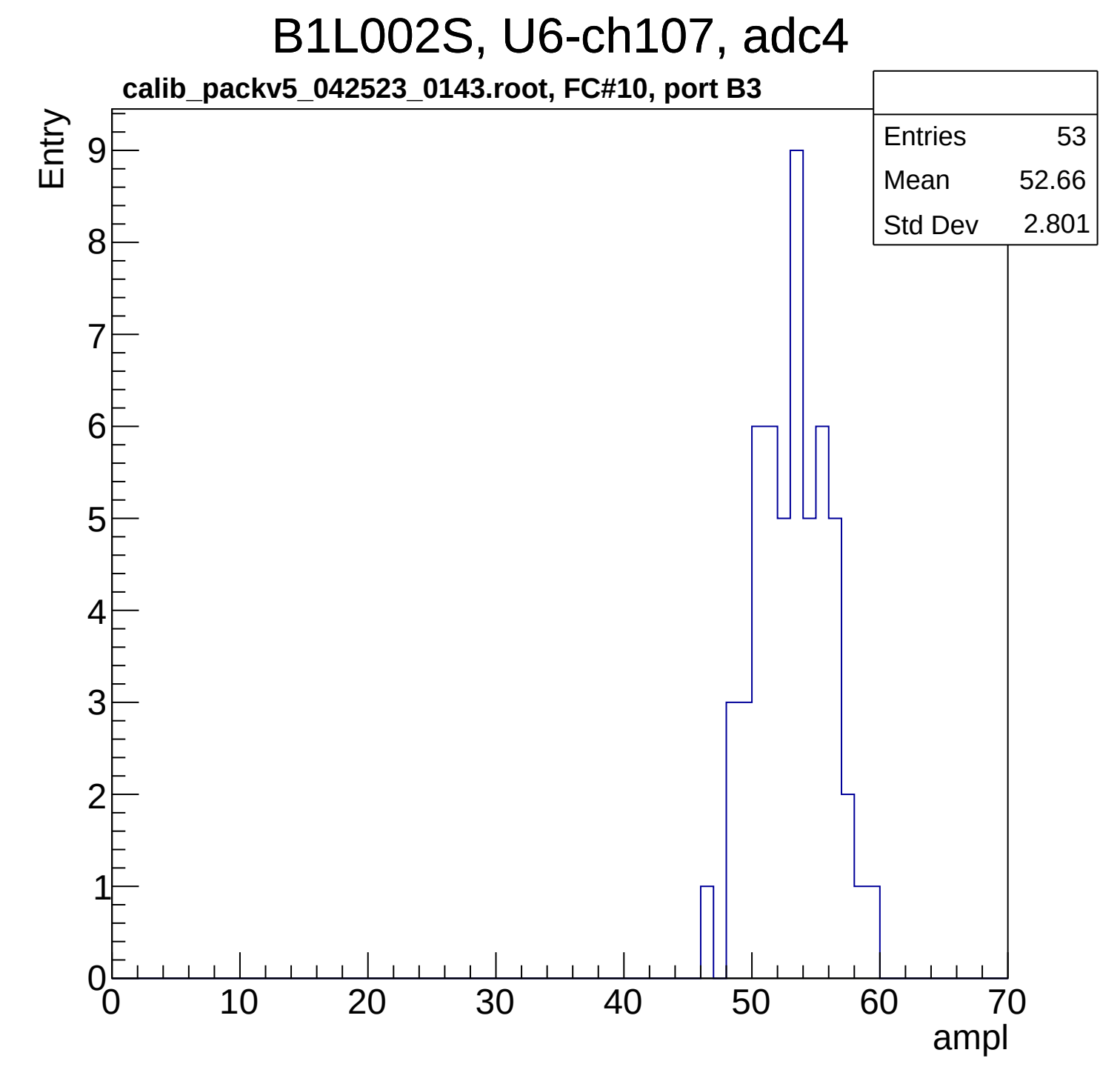

B1L002S, U6-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

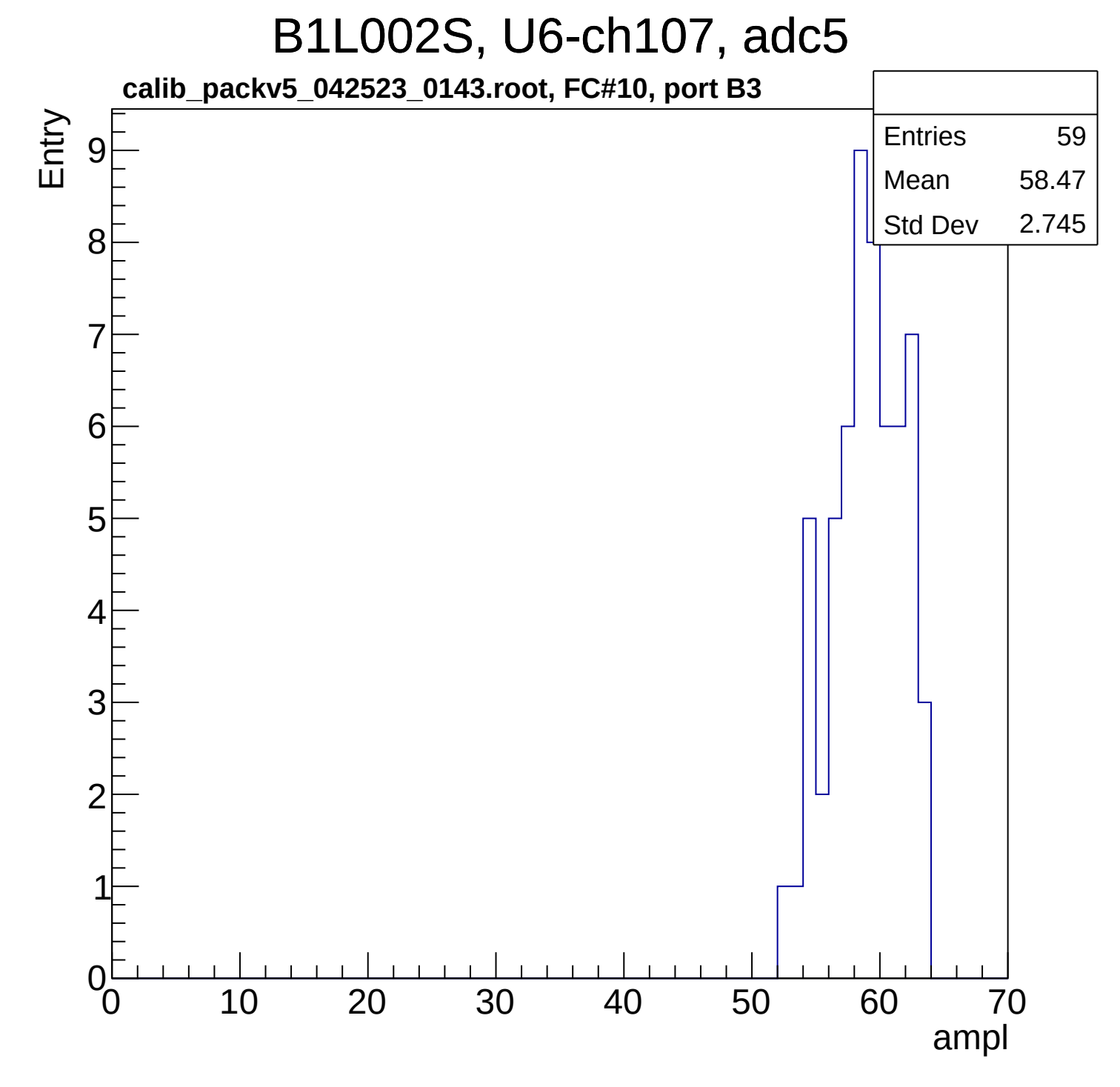

B1L002S, U6-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.98
Std Dev 3.206
Gaus mean : 28.4555
Gaus Width: 3.2124
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

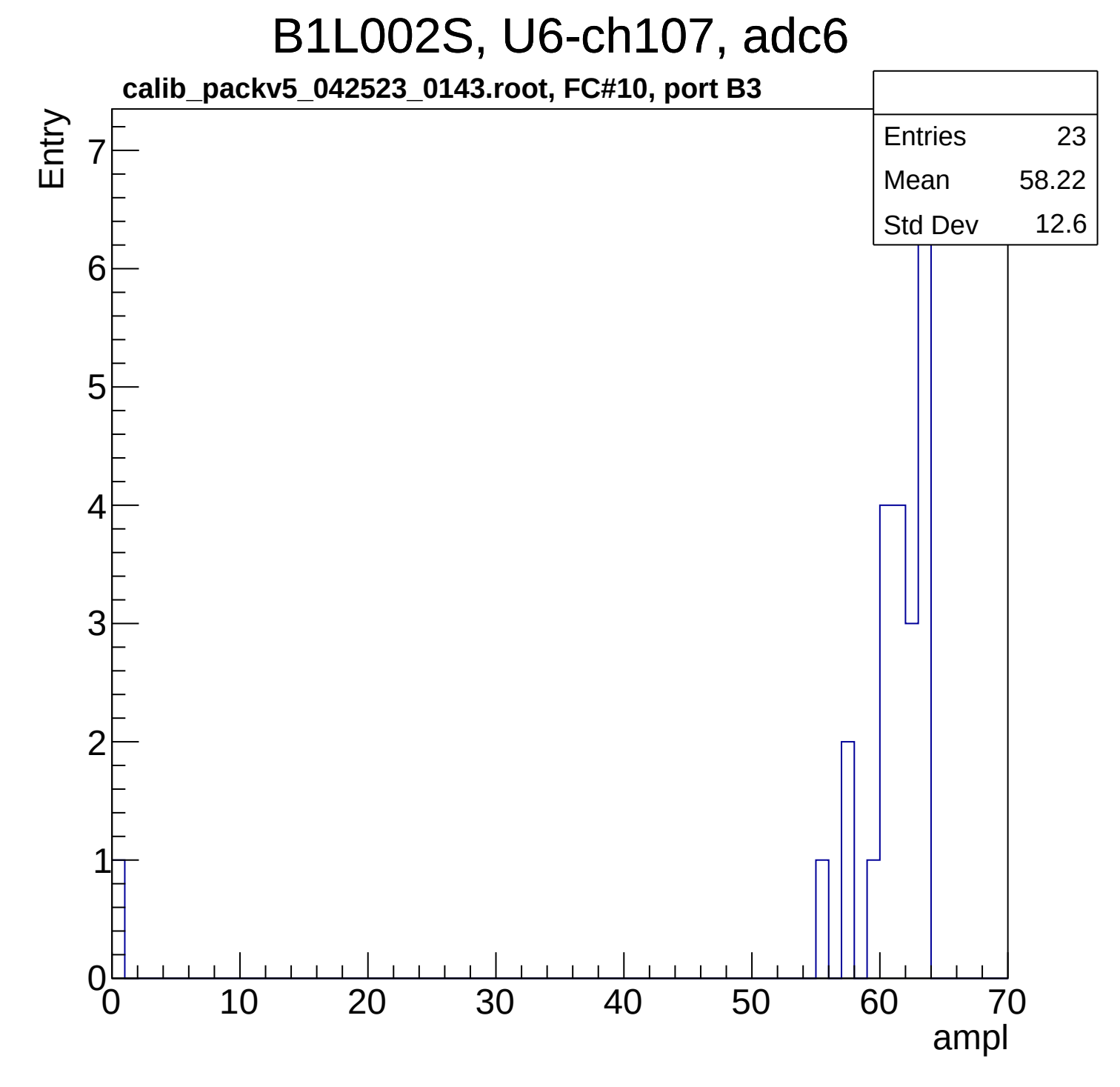

B1L002S, U6-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.29
Std Dev 3.527
Gaus mean : 34.4030
Gaus Width: 3.7582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

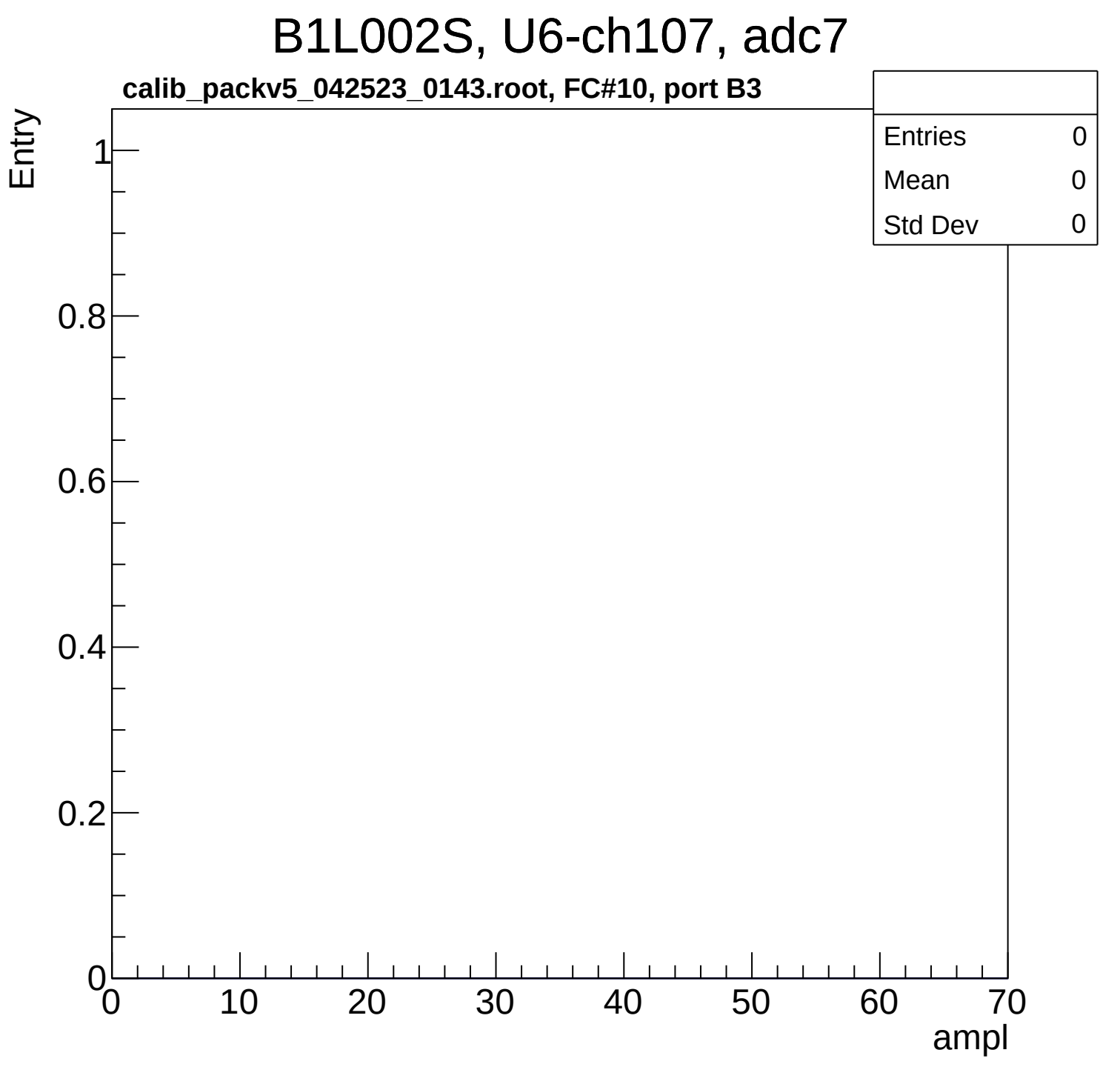

B1L002S, U6-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.9
Std Dev 2.821
Gaus mean : 41.1473
Gaus Width: 3.0004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

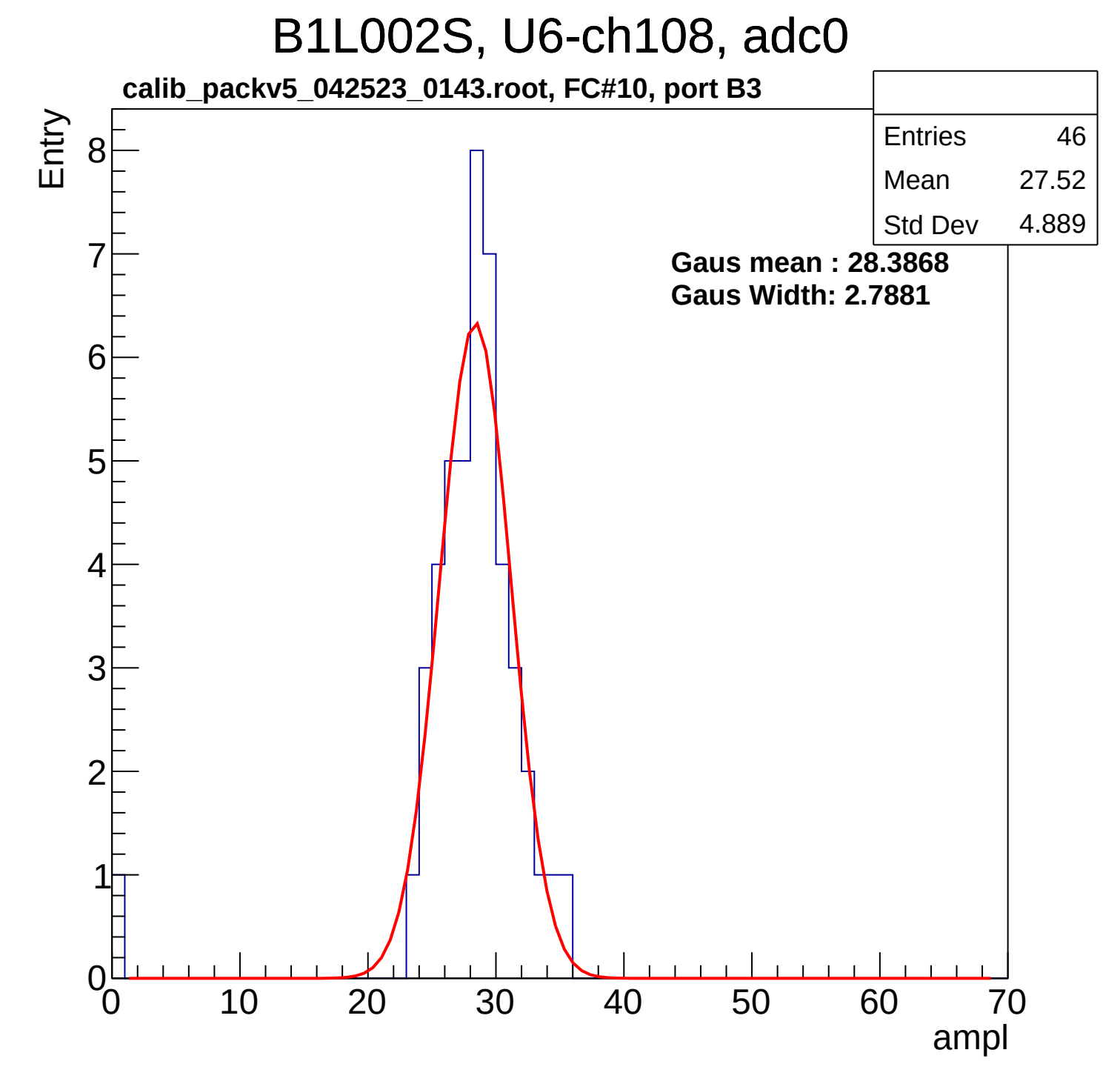

B1L002S, U6-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.1
Std Dev 3.075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

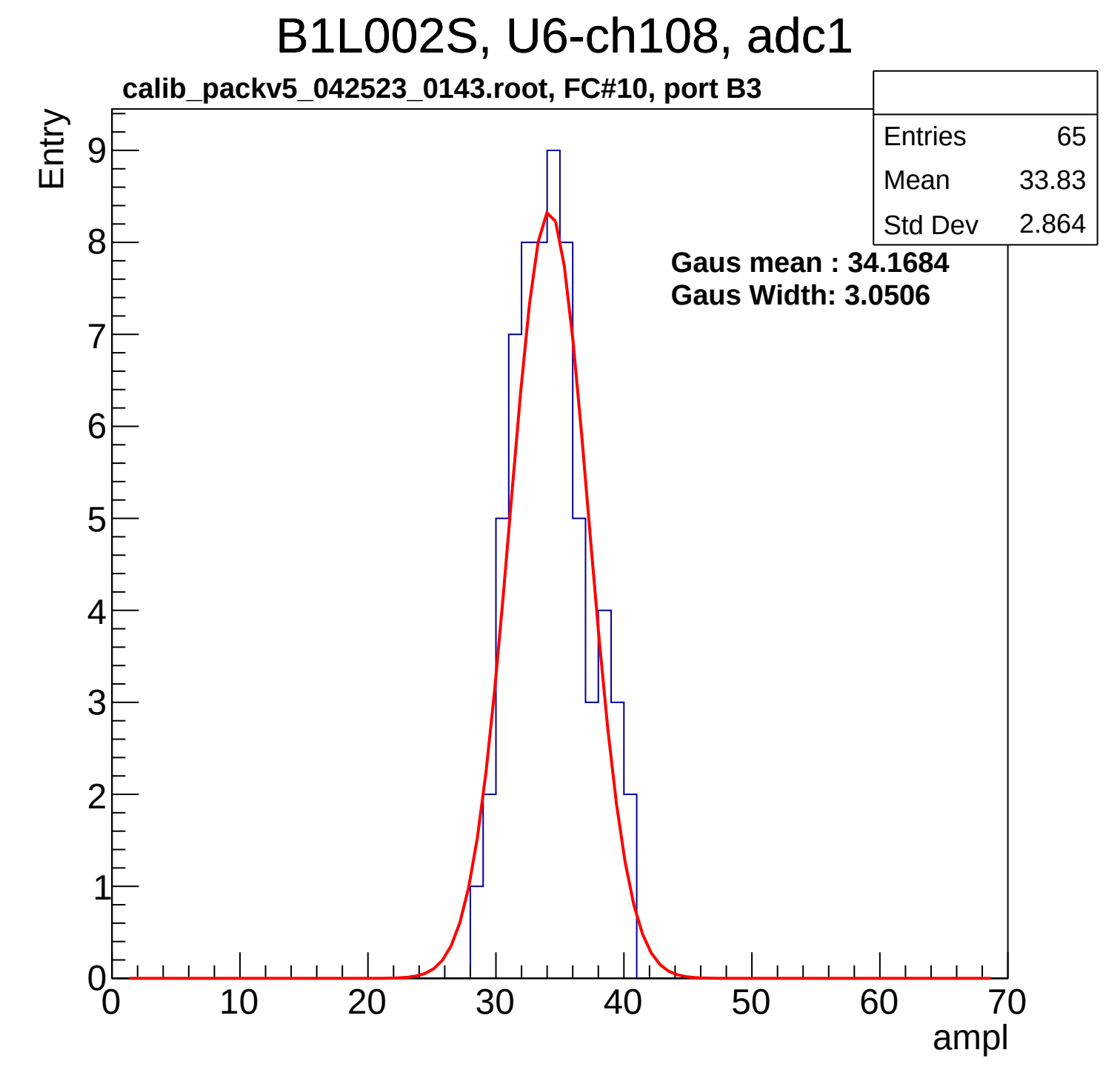

B1L002S, U6-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.66
Std Dev 2.801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

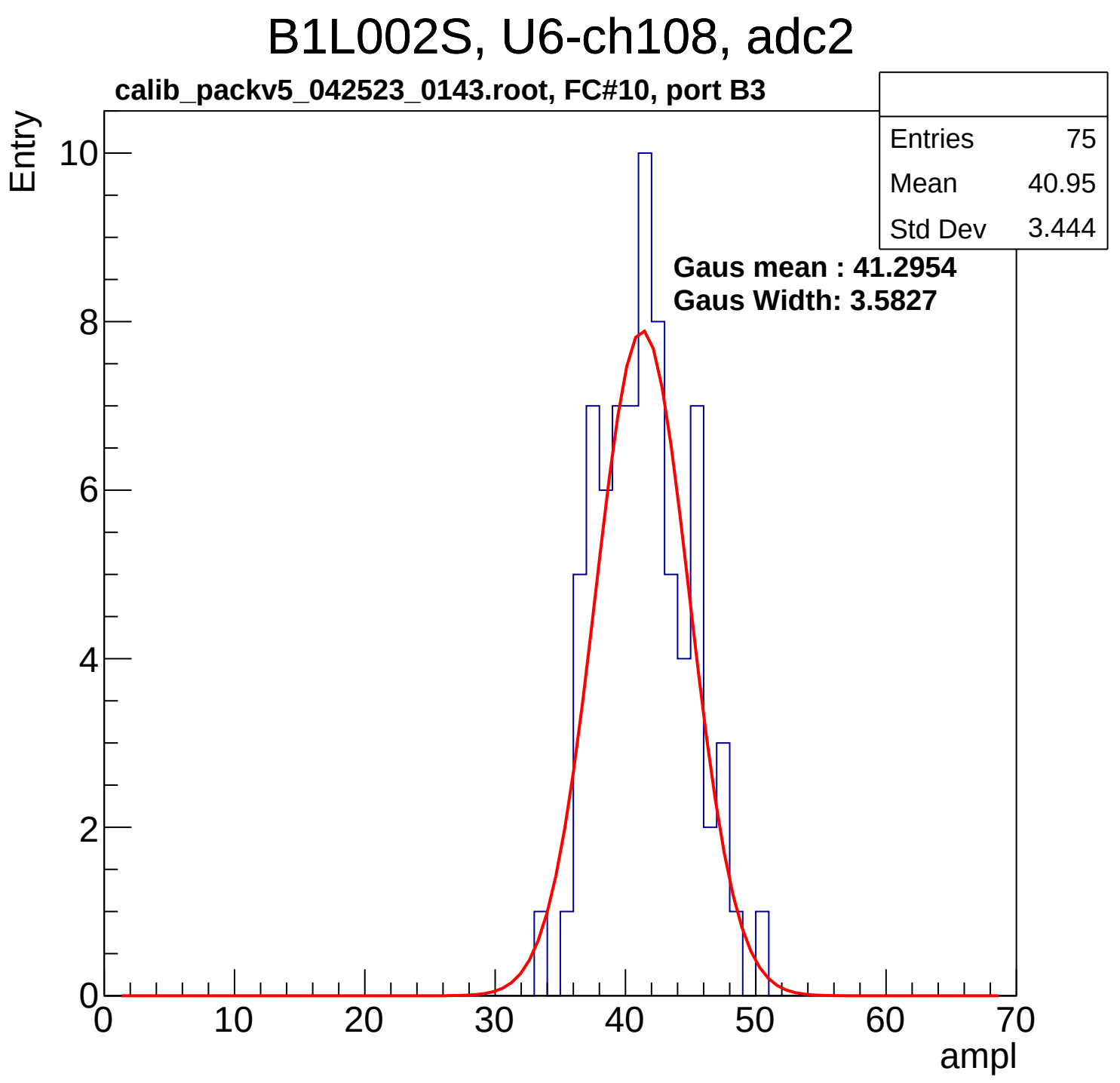

B1L002S, U6-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.47
Std Dev 2.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

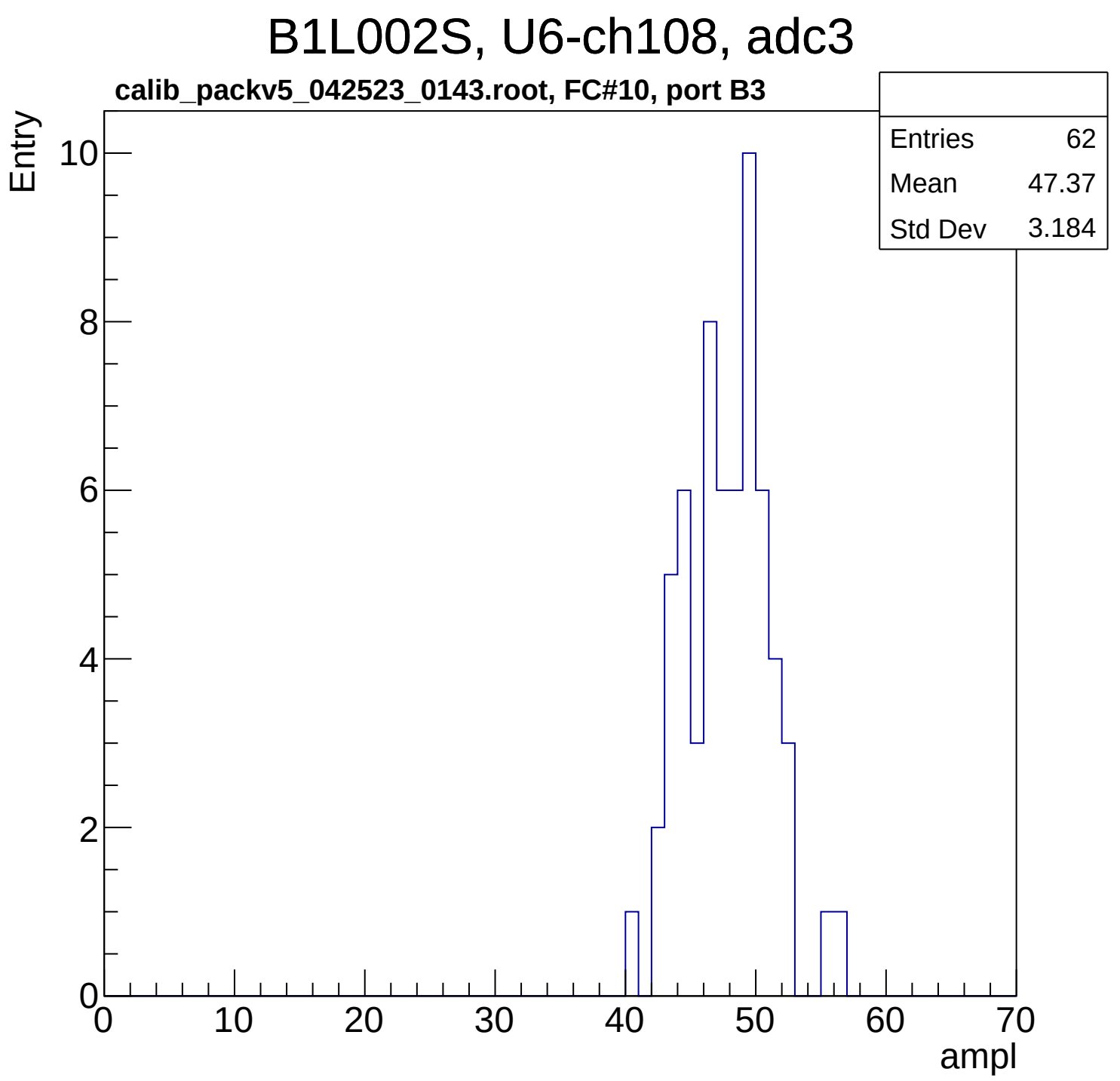

B1L002S, U6-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.22
Std Dev 12.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

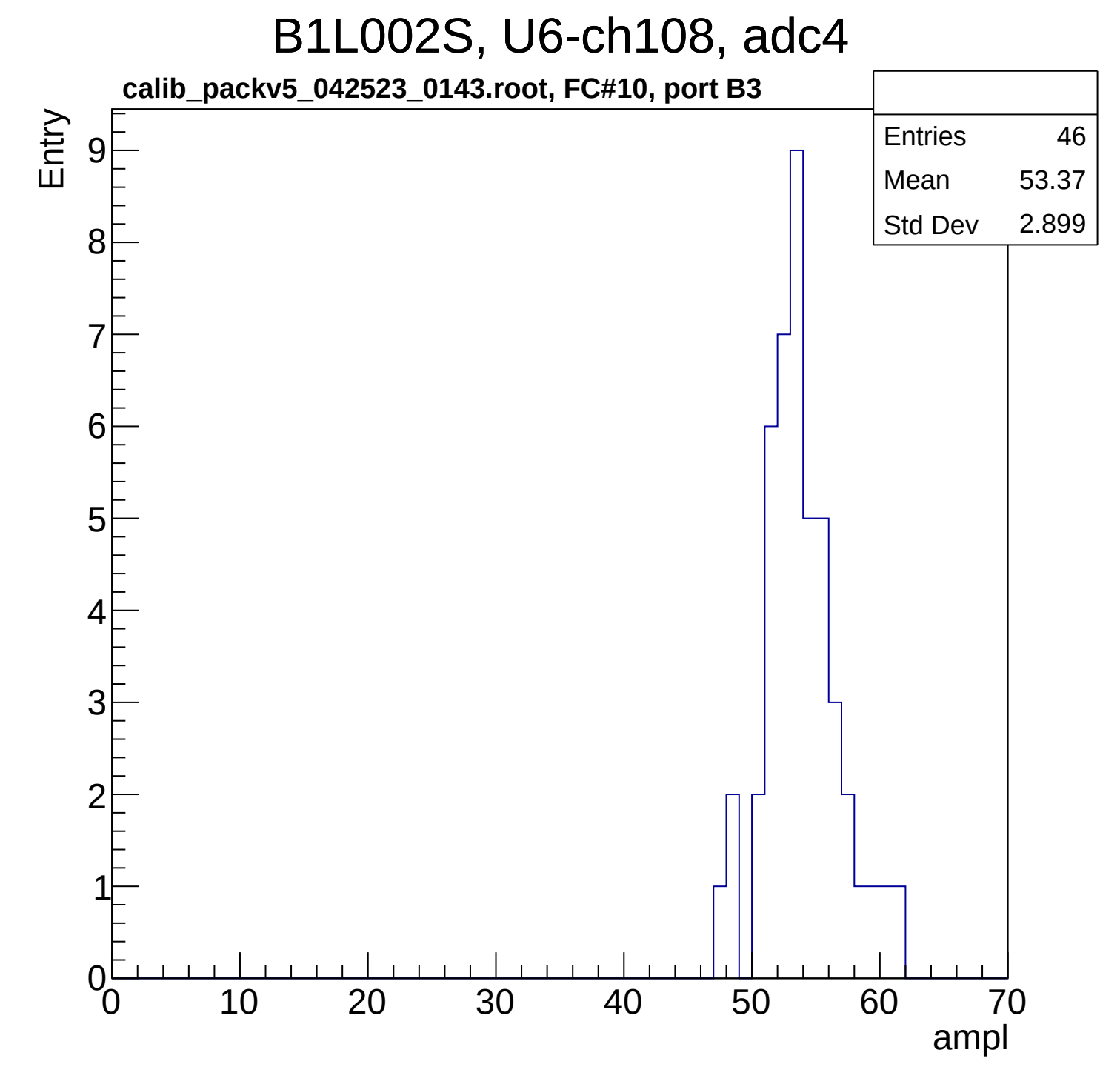

B1L002S, U6-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

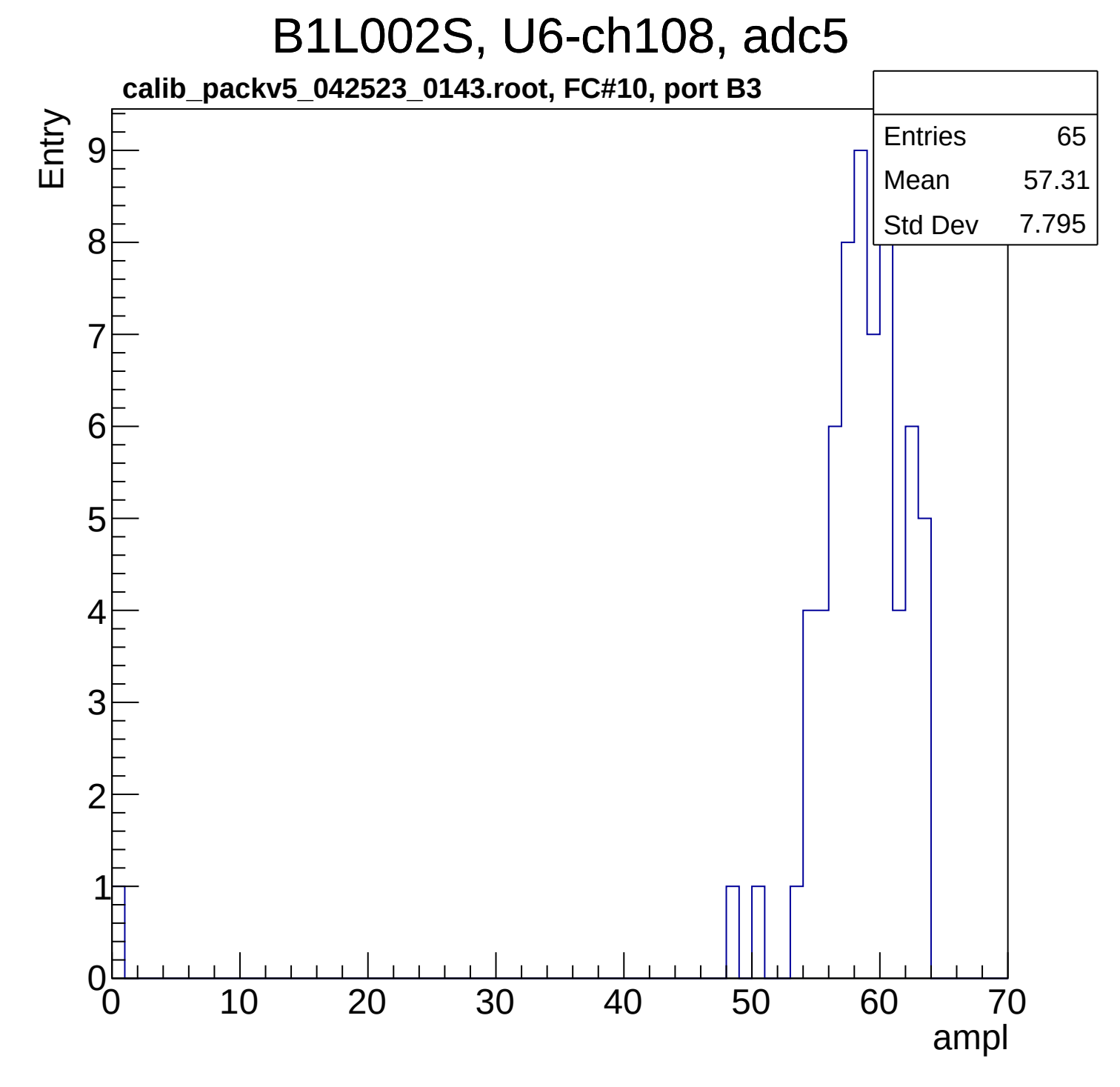

B1L002S, U6-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 27.52
Std Dev 4.889
Gaus mean : 28.3868
Gaus Width: 2.7881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

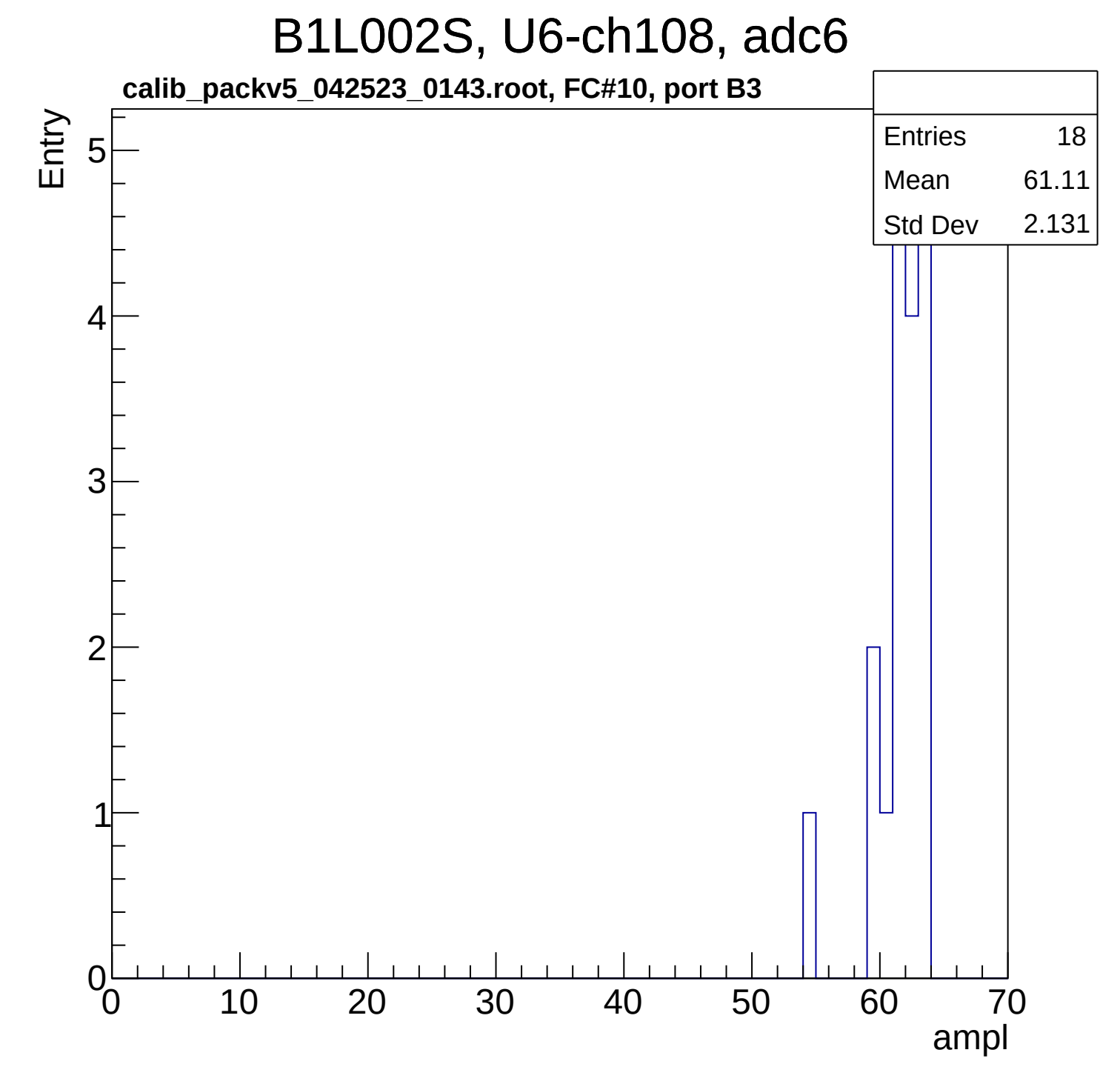

B1L002S, U6-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.83
Std Dev 2.864
Gaus mean : 34.1684
Gaus Width: 3.0506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

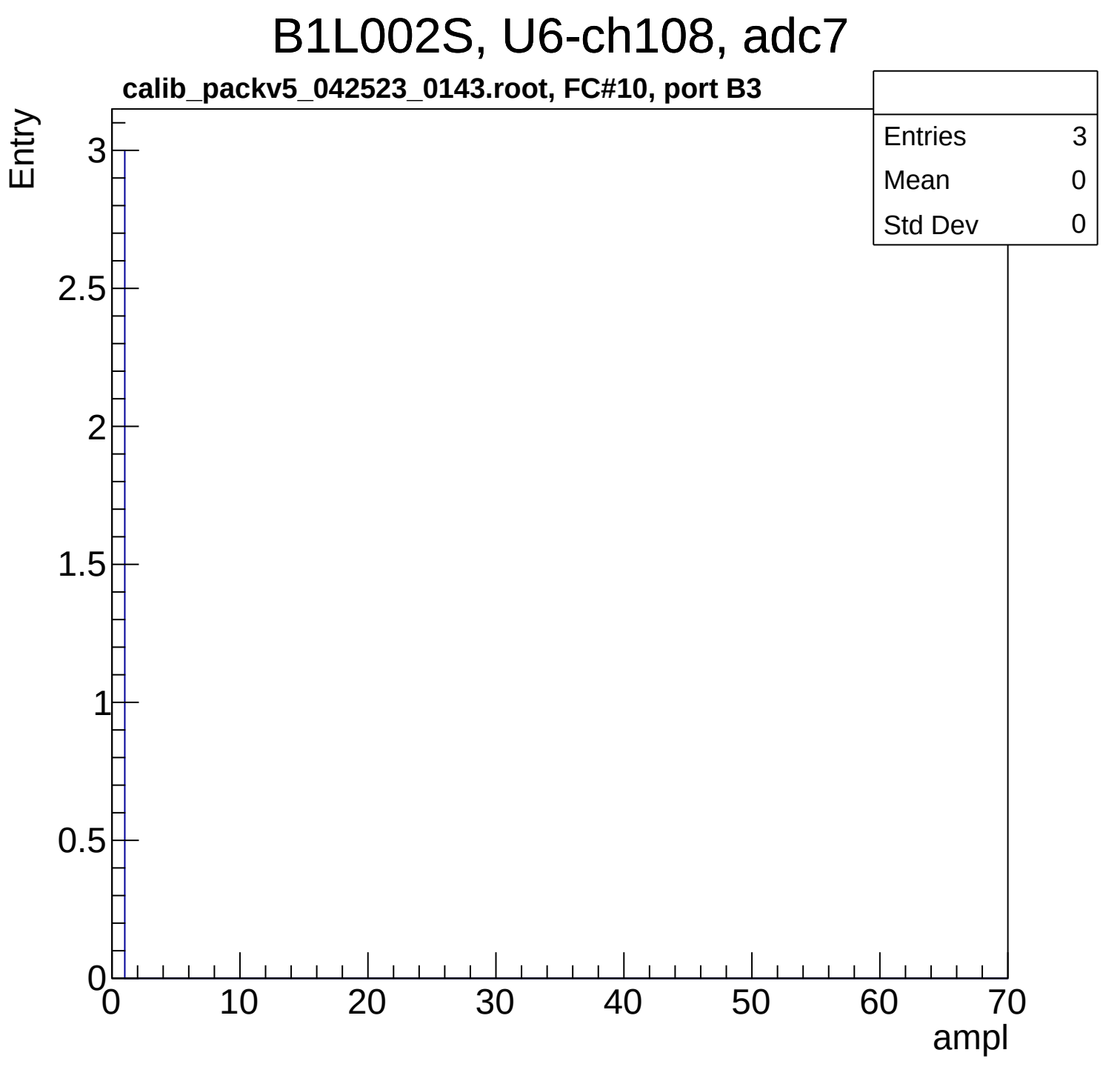

B1L002S, U6-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 40.95
Std Dev 3.444
Gaus mean : 41.2954
Gaus Width: 3.5827
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

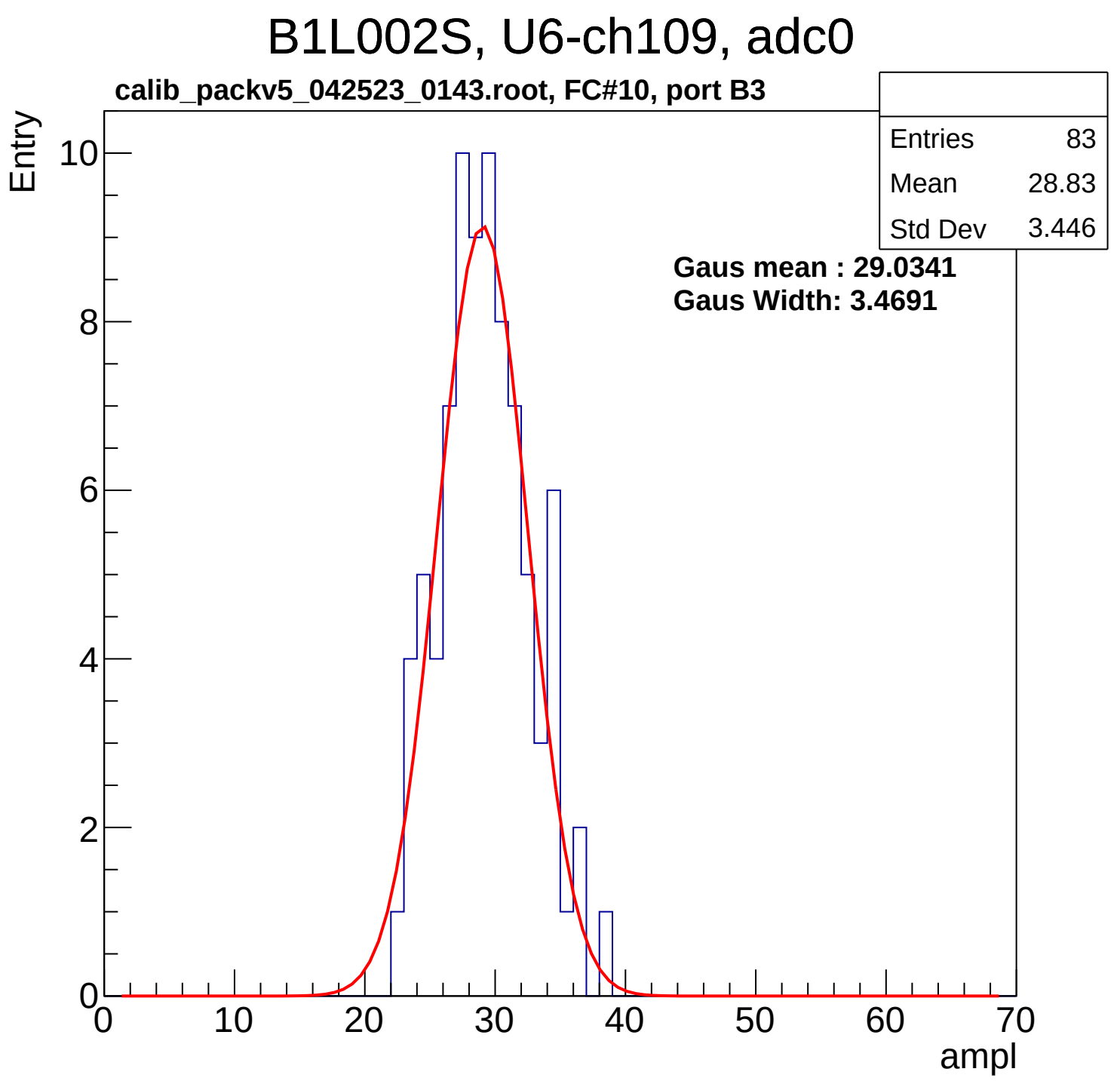

B1L002S, U6-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.37
Std Dev 3.184
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

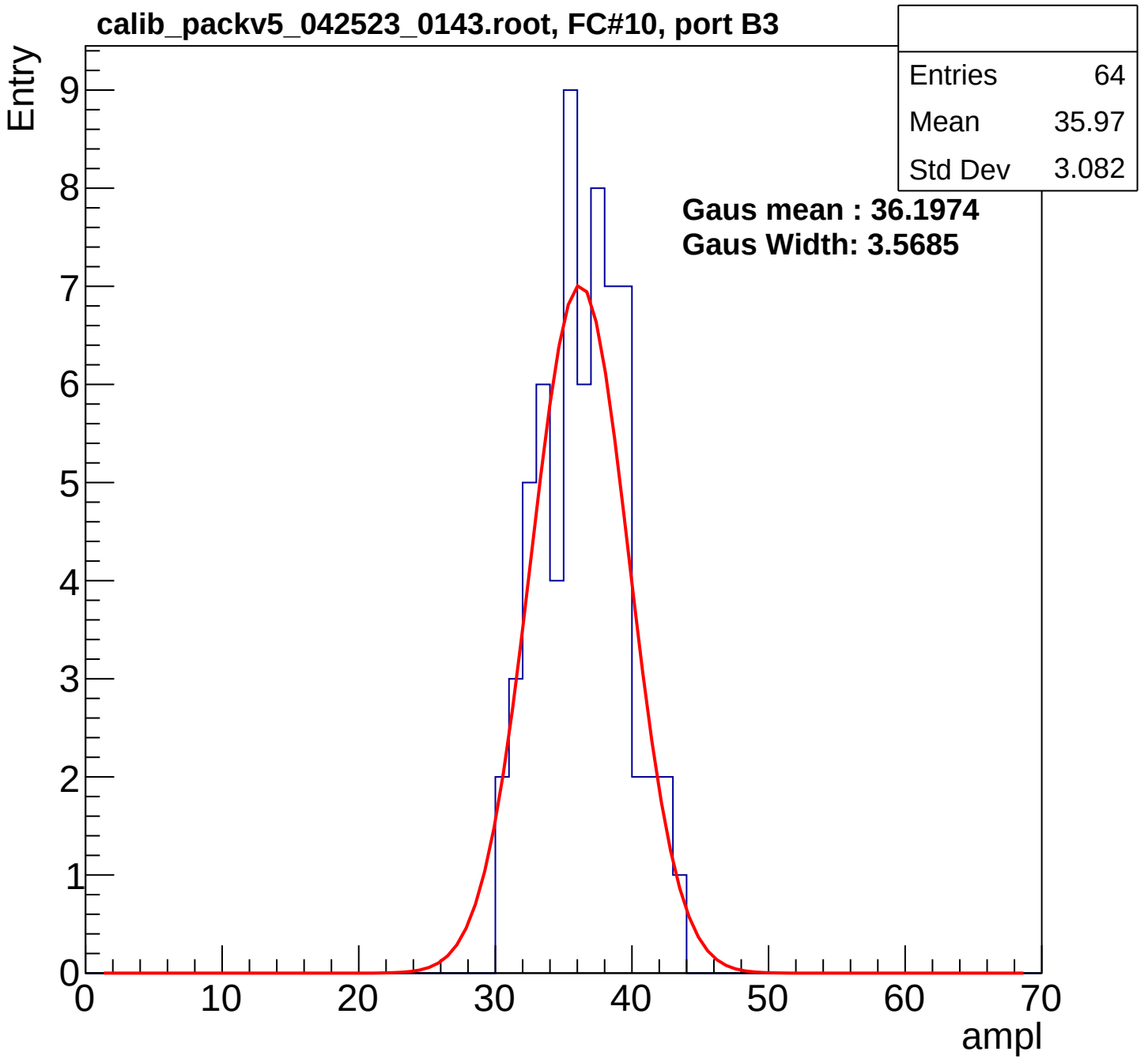

B1L002S, U6-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 53.37
Std Dev 2.899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

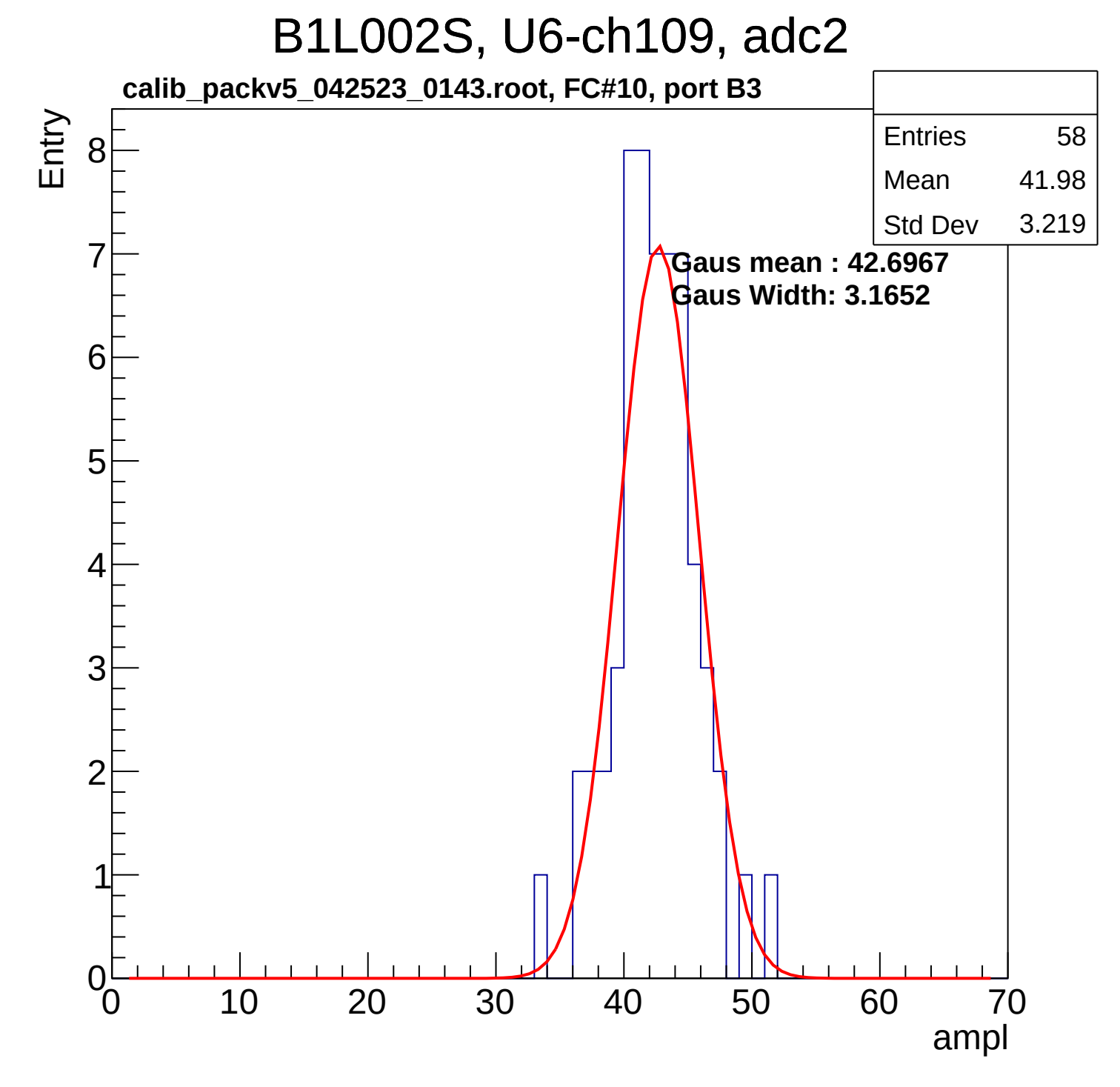

B1L002S, U6-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.31
Std Dev 7.795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

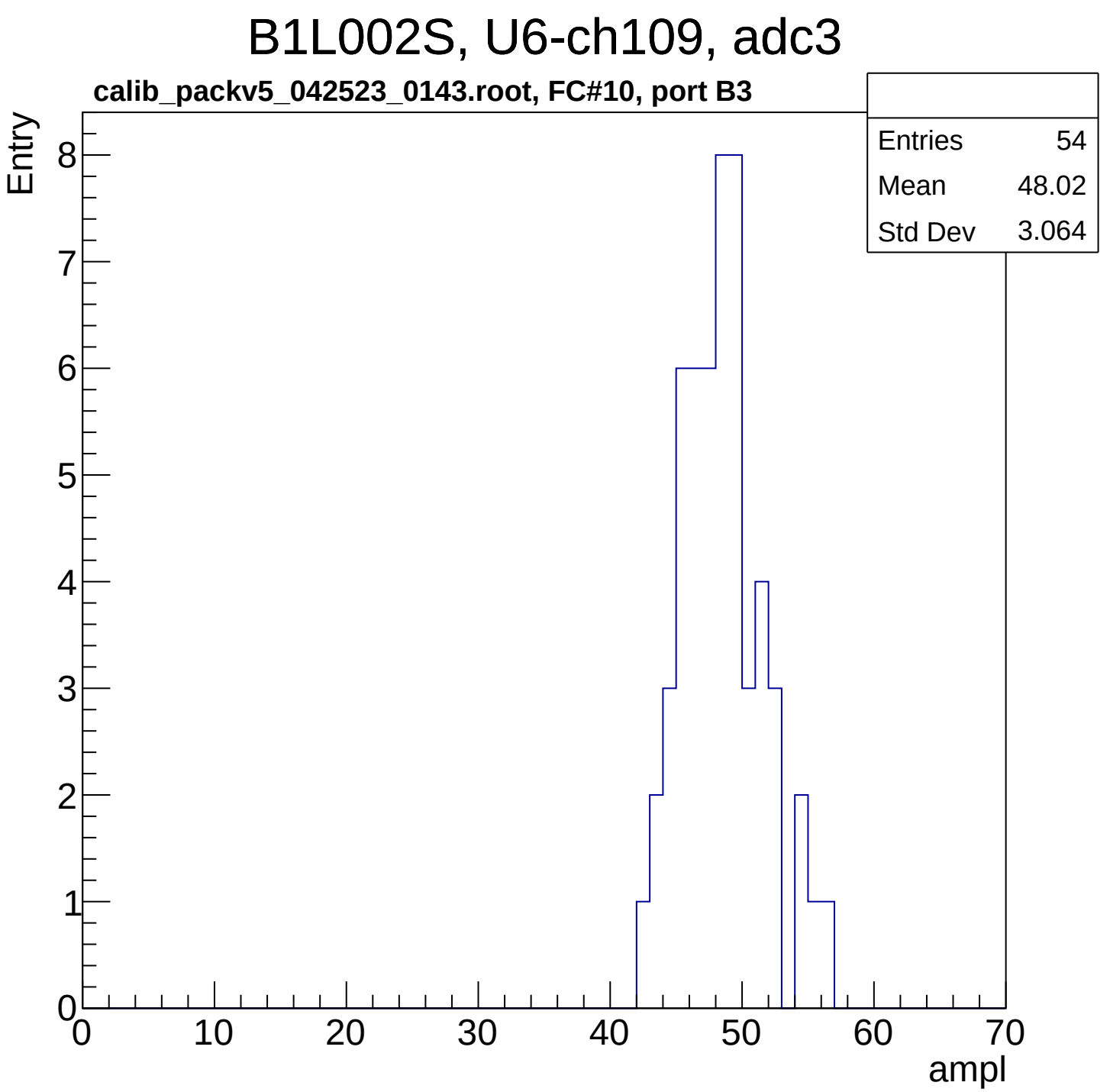

B1L002S, U6-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.11
Std Dev 2.131
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

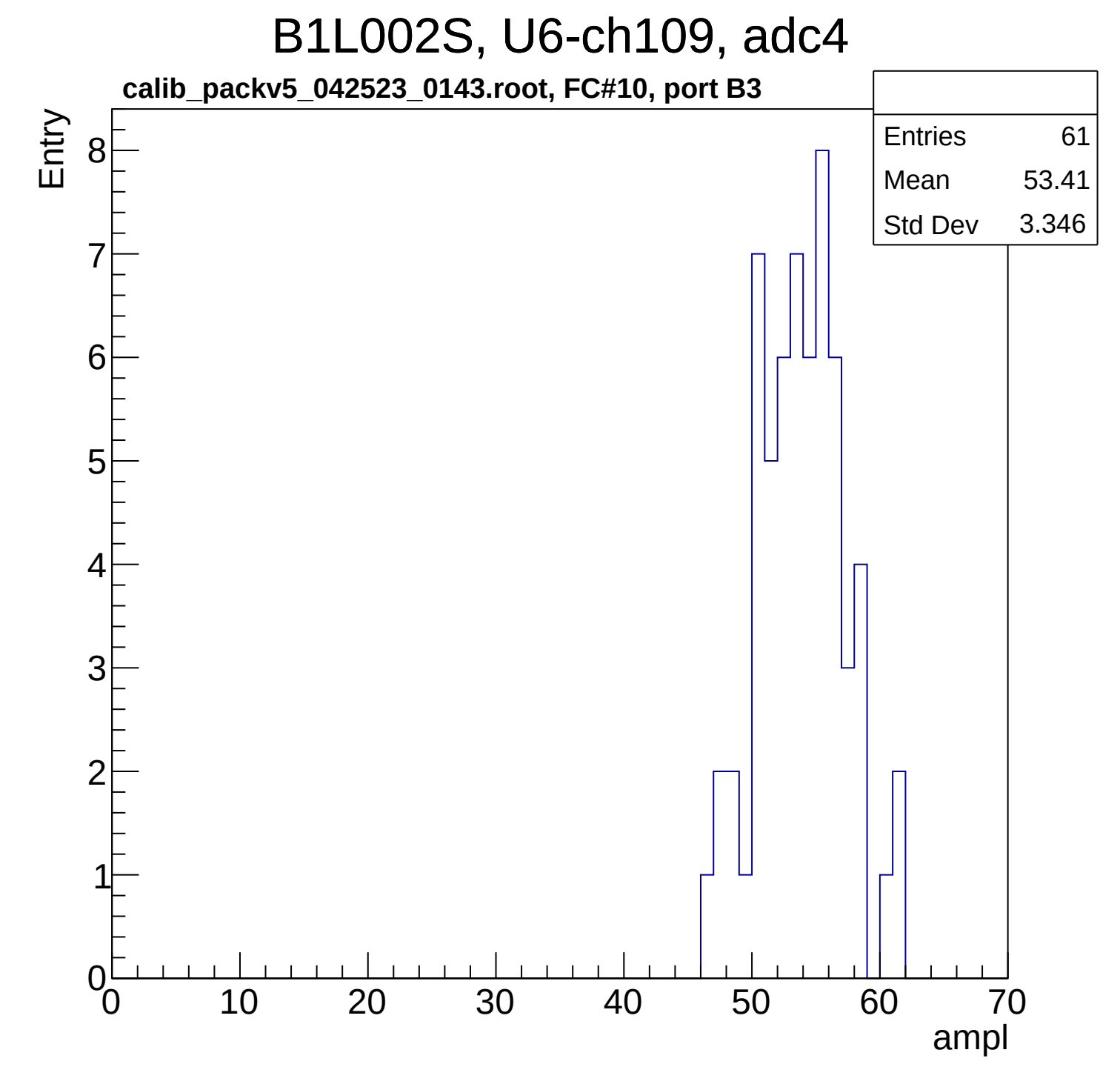

B1L002S, U6-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

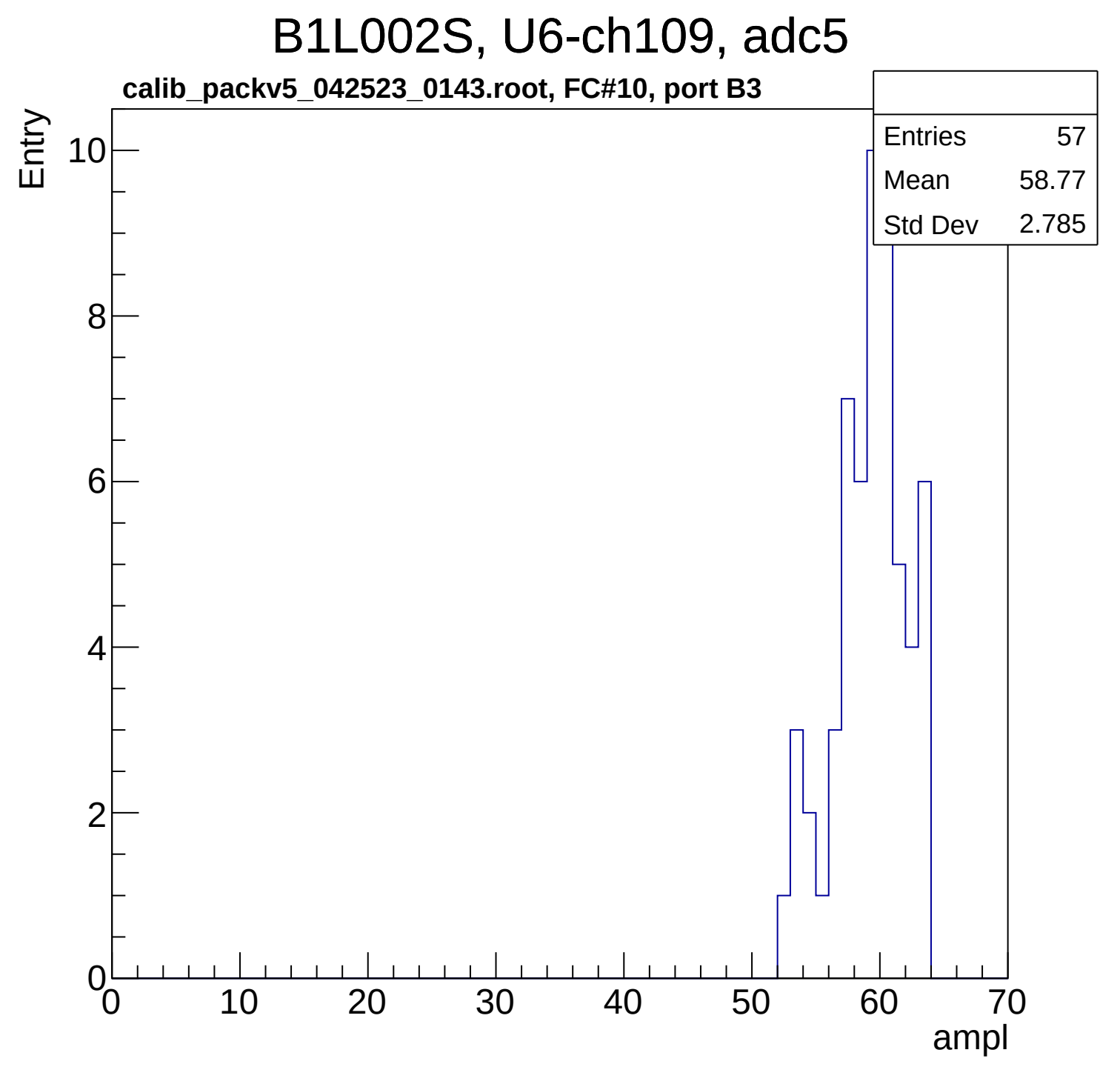

B1L002S, U6-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 28.83
Std Dev 3.446
Gaus mean : 29.0341
Gaus Width: 3.4691
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

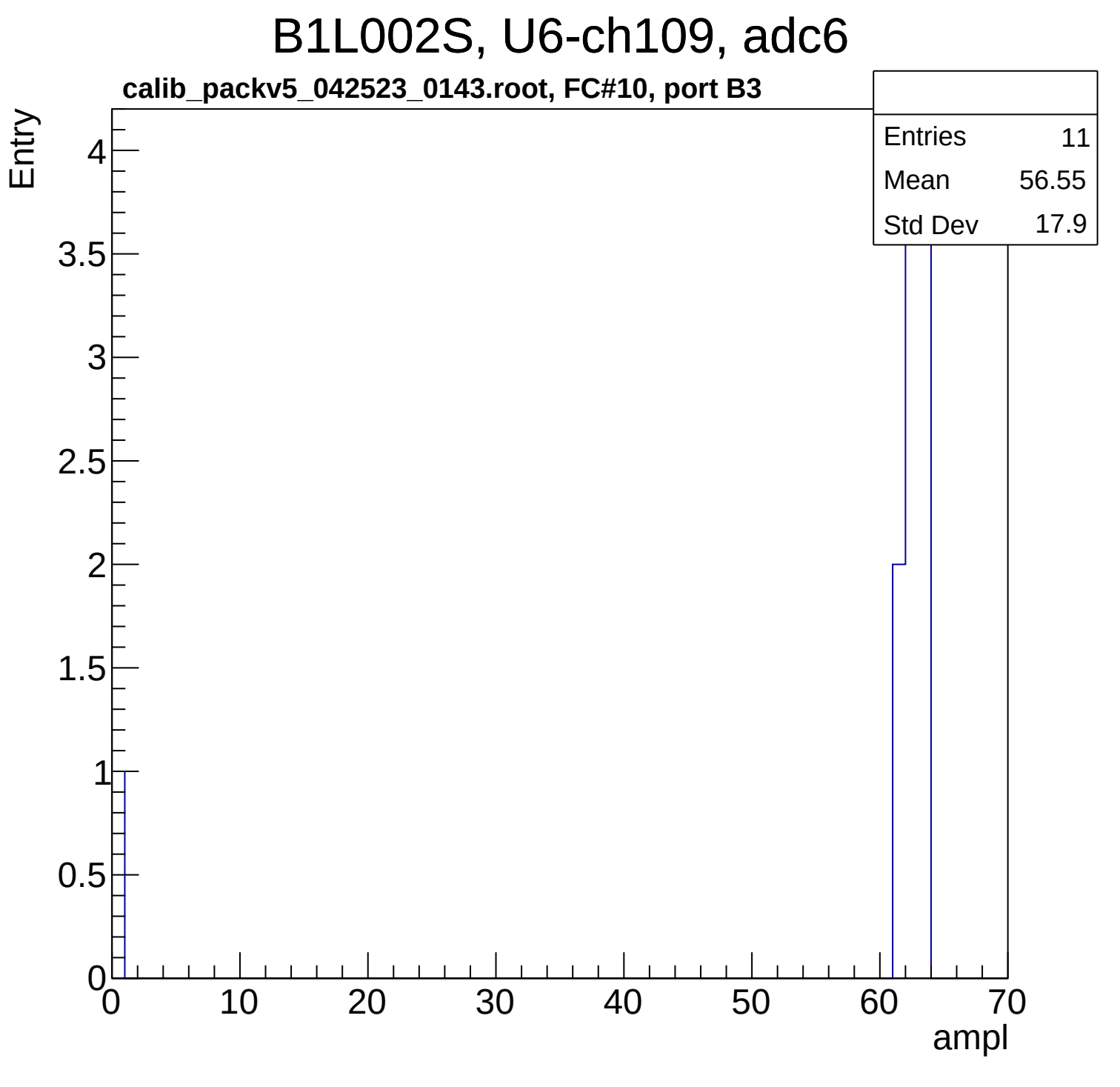

B1L002S, U6-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.97
Std Dev 3.082
Gaus mean : 36.1974
Gaus Width: 3.5685
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

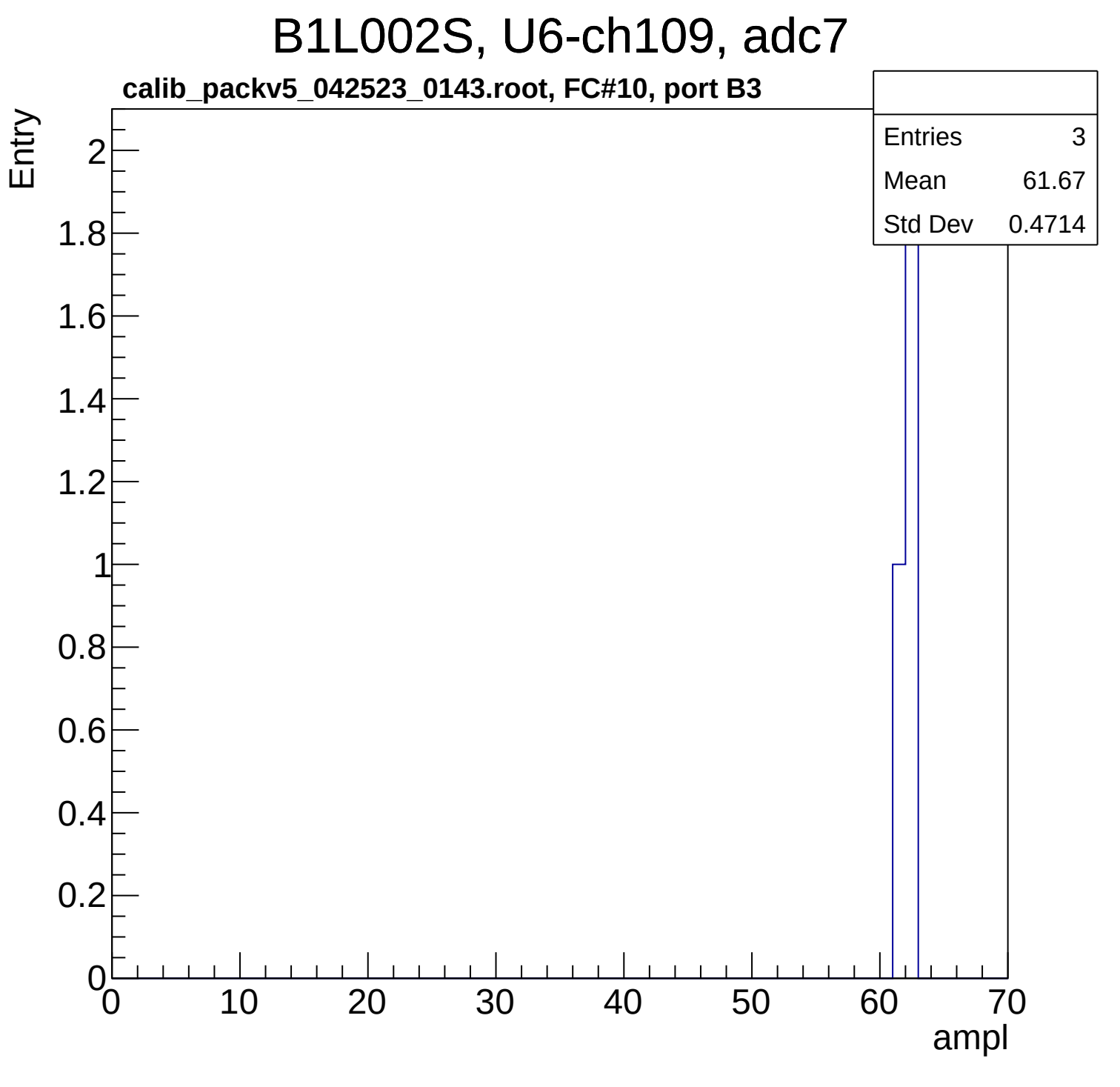

B1L002S, U6-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.98
Std Dev 3.219
Gaus mean : 42.6967
Gaus Width: 3.1652
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

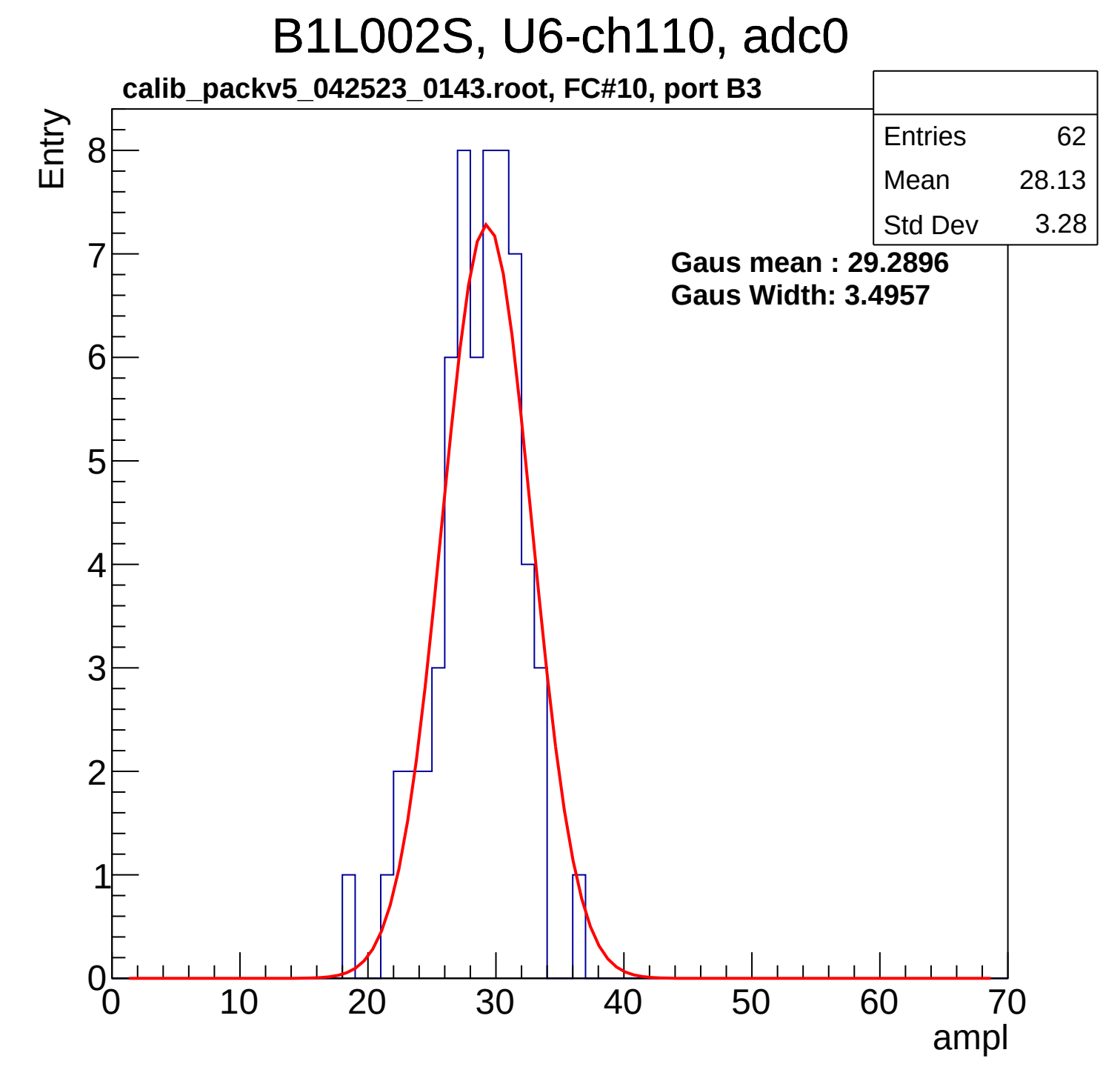

B1L002S, U6-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.02
Std Dev 3.064
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

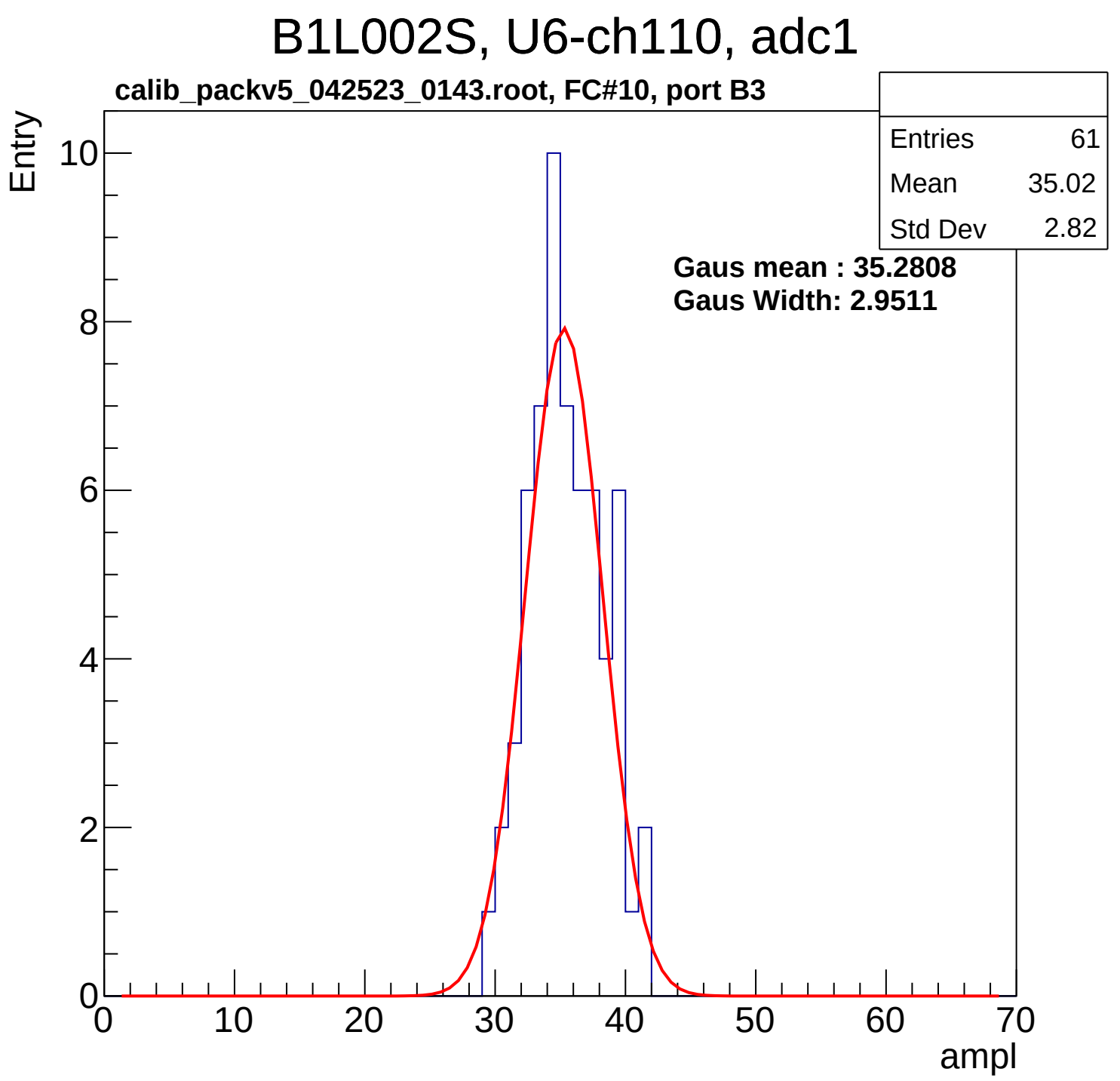

B1L002S, U6-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.41
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

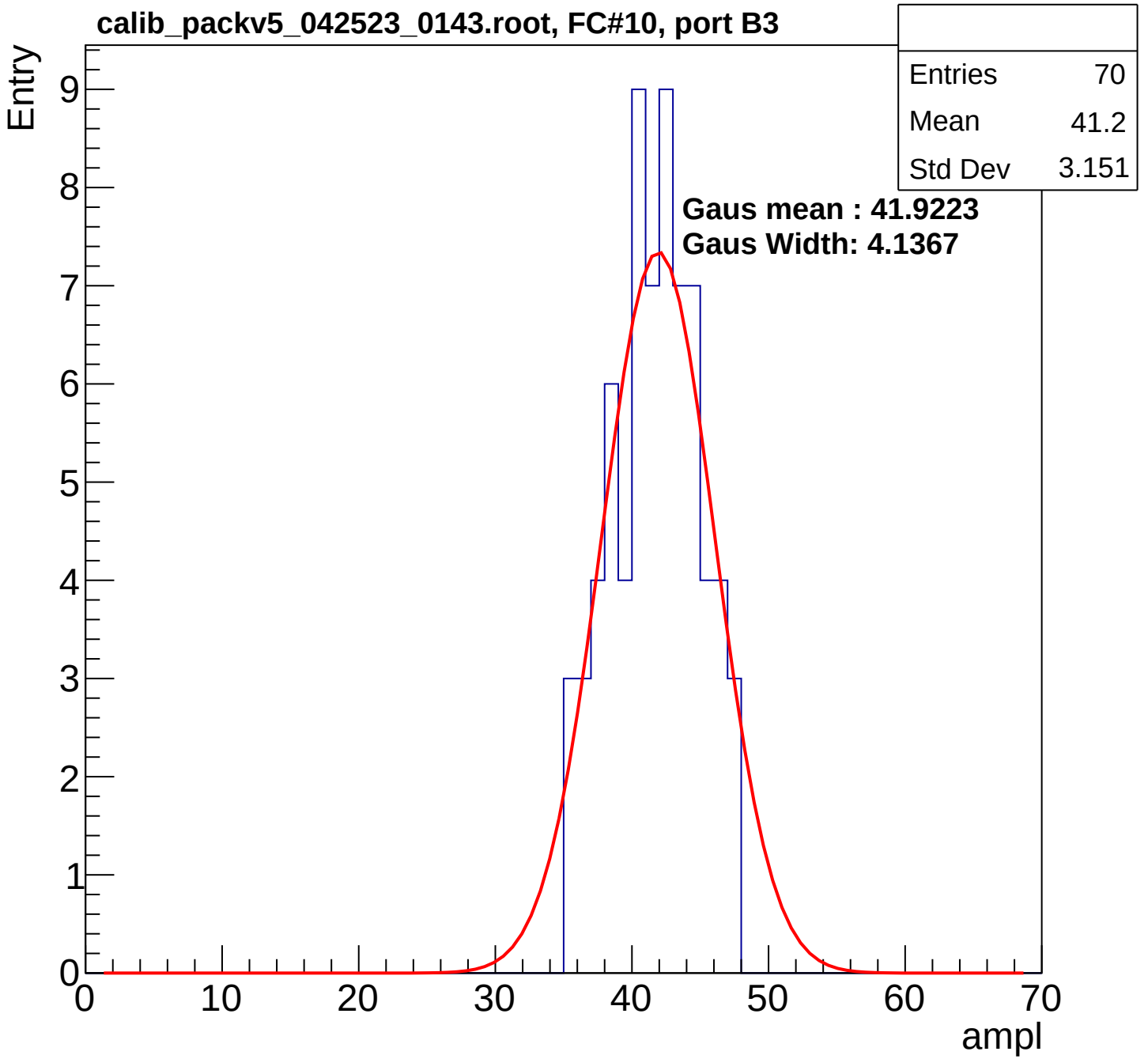

B1L002S, U6-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.77
Std Dev 2.785
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

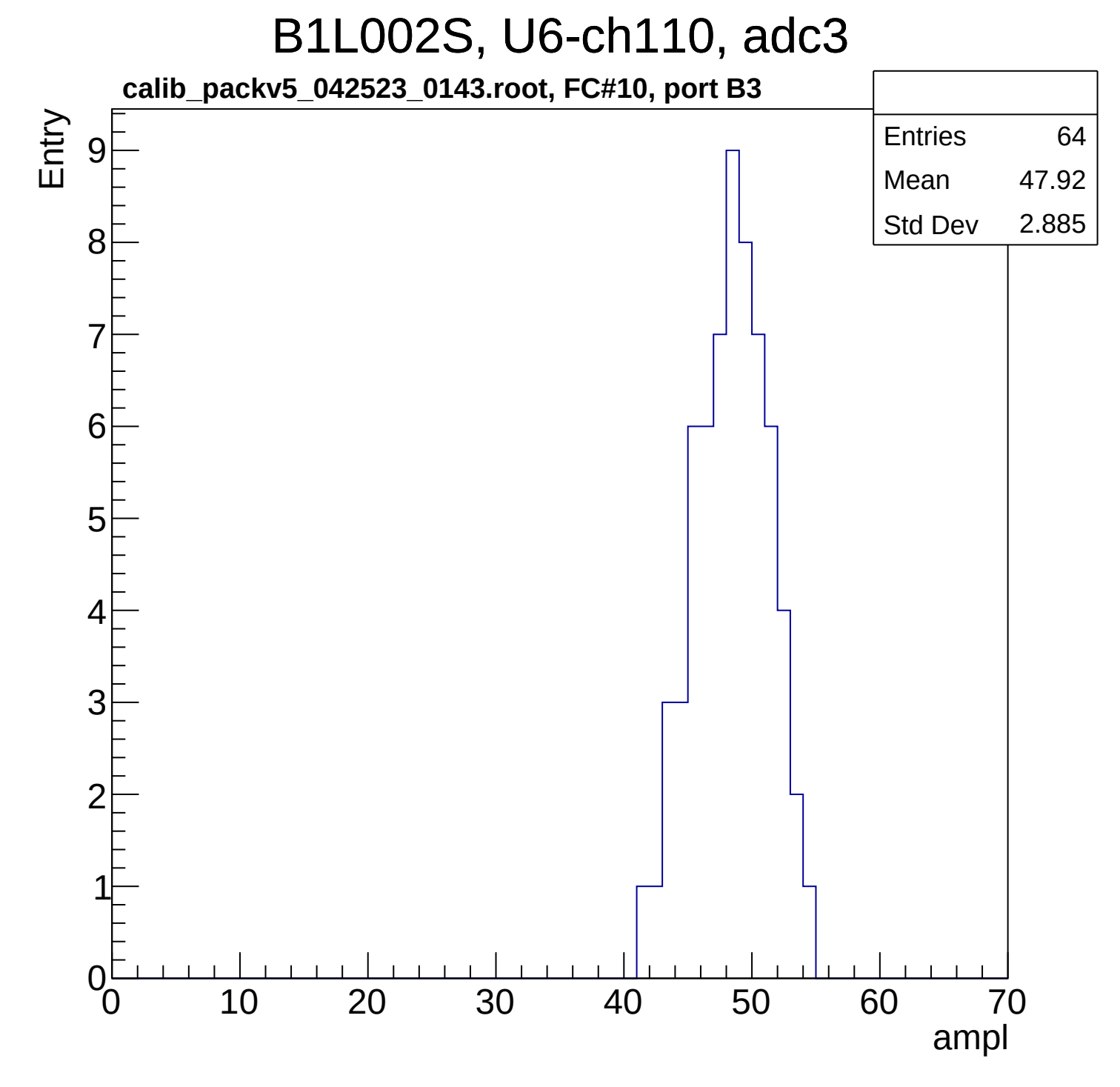

B1L002S, U6-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.55
Std Dev 17.9
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

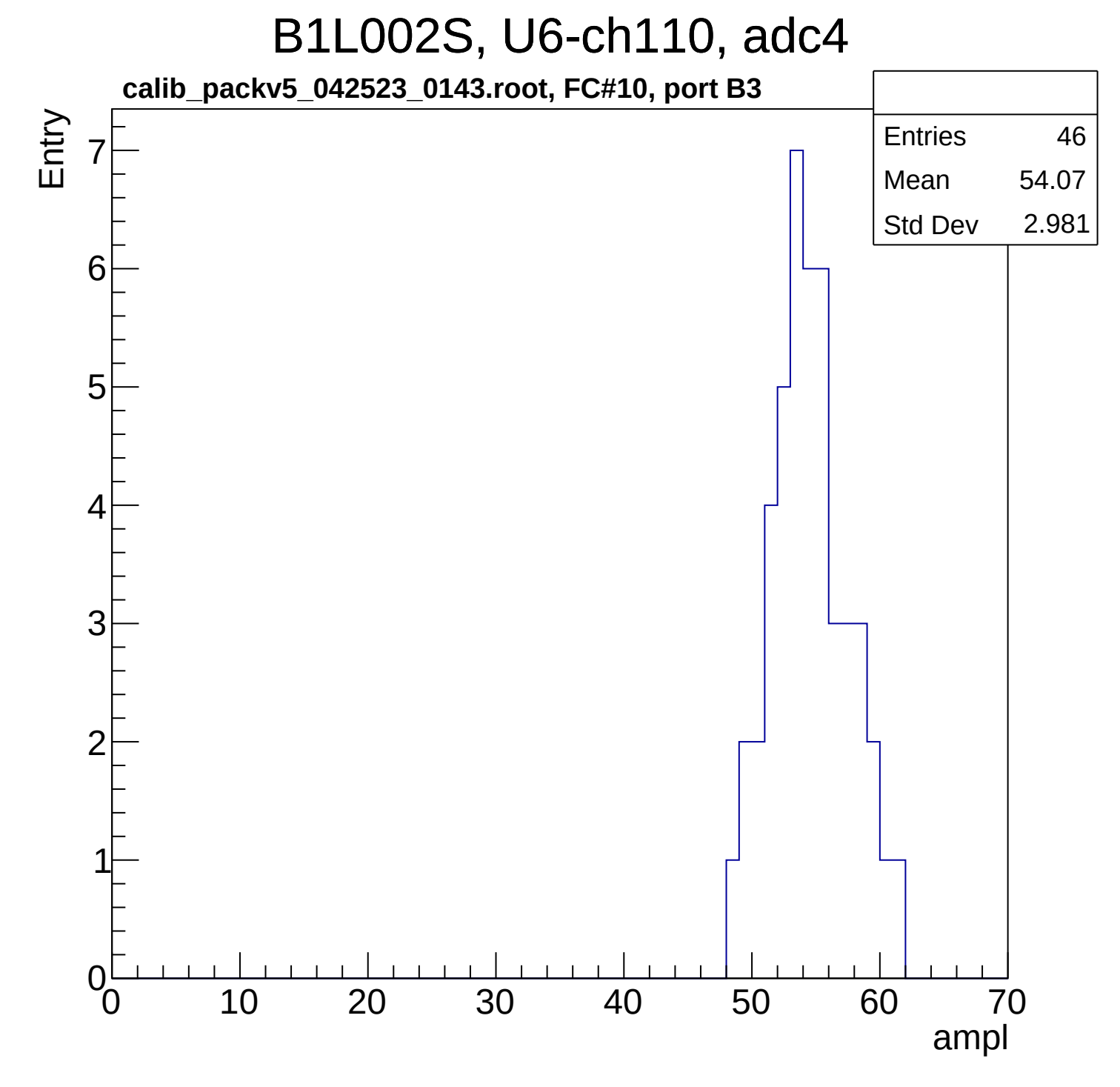

B1L002S, U6-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

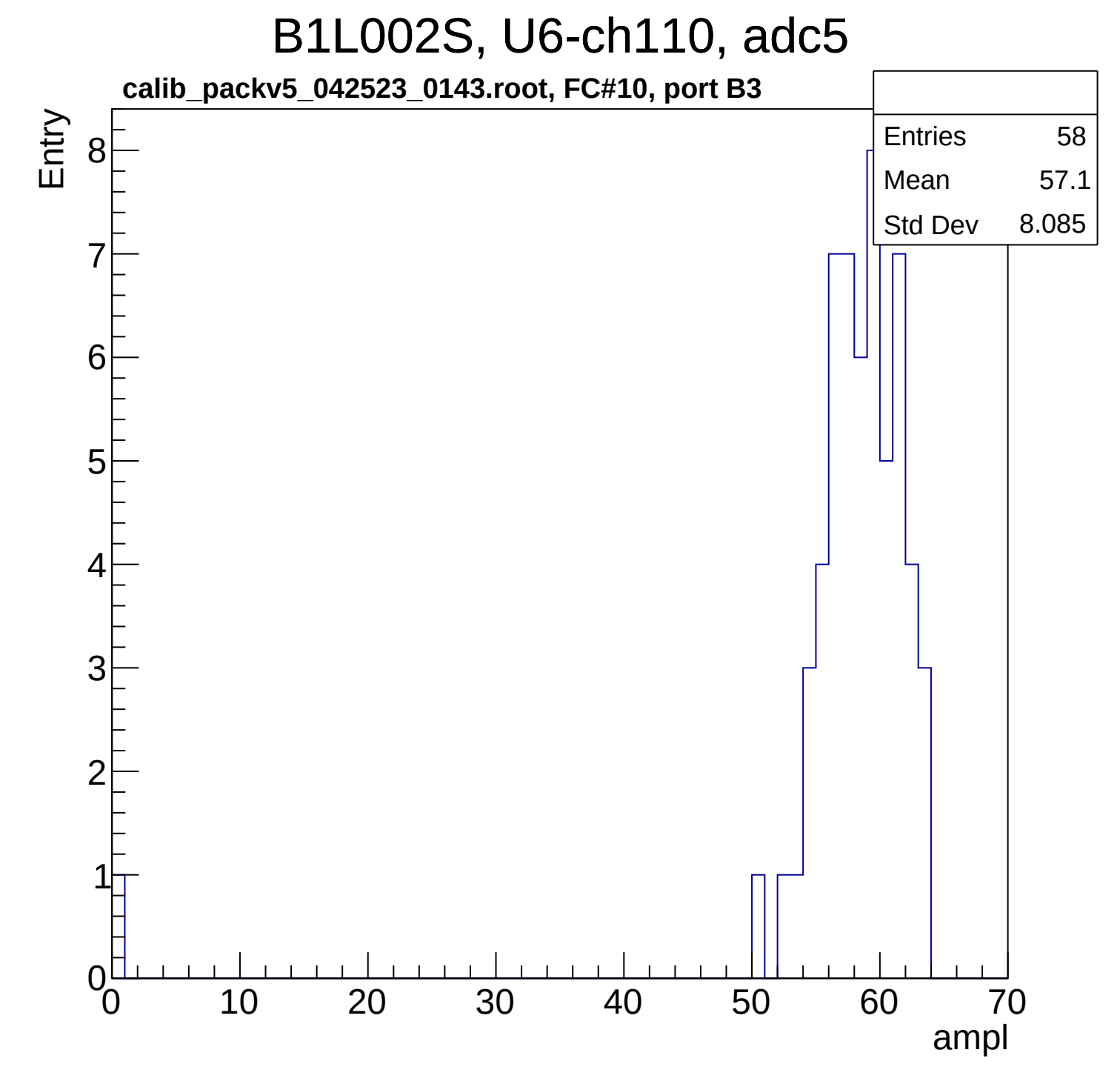

B1L002S, U6-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.13
Std Dev 3.28
Gaus mean : 29.2896
Gaus Width: 3.4957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

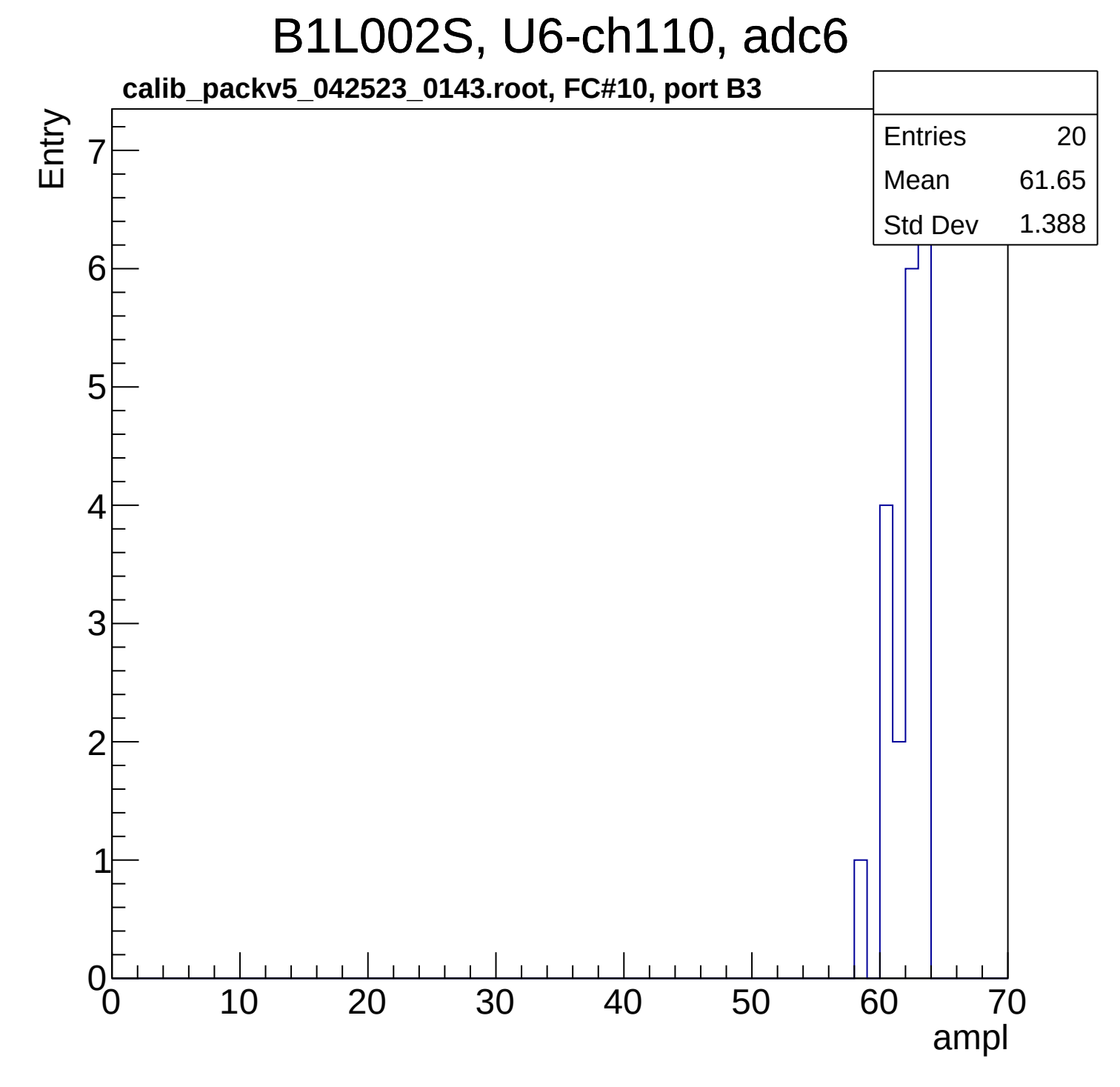

B1L002S, U6-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.02
Std Dev 2.82
Gaus mean : 35.2808
Gaus Width: 2.9511
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

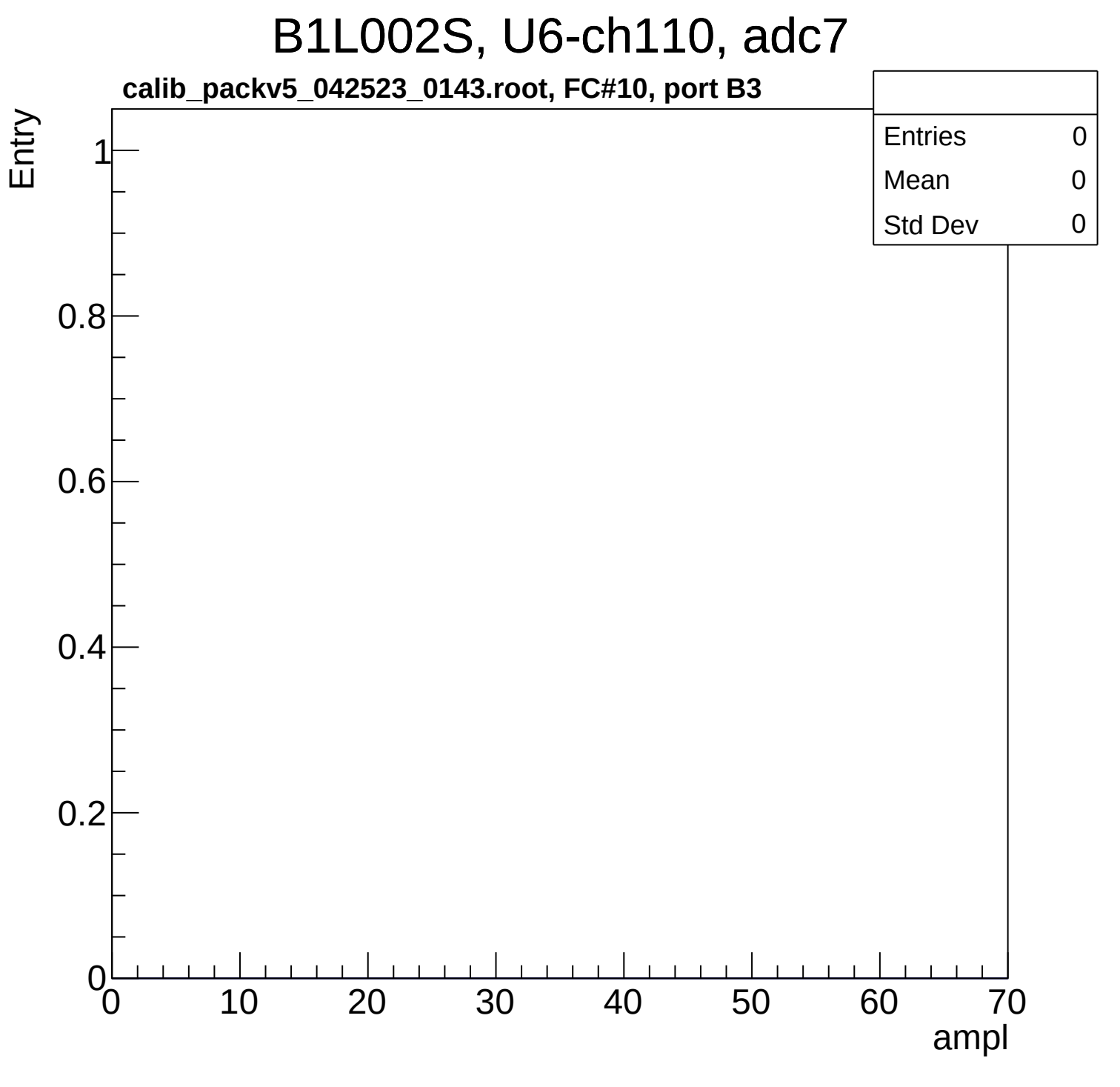

B1L002S, U6-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.2
Std Dev 3.151
Gaus mean : 41.9223
Gaus Width: 4.1367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

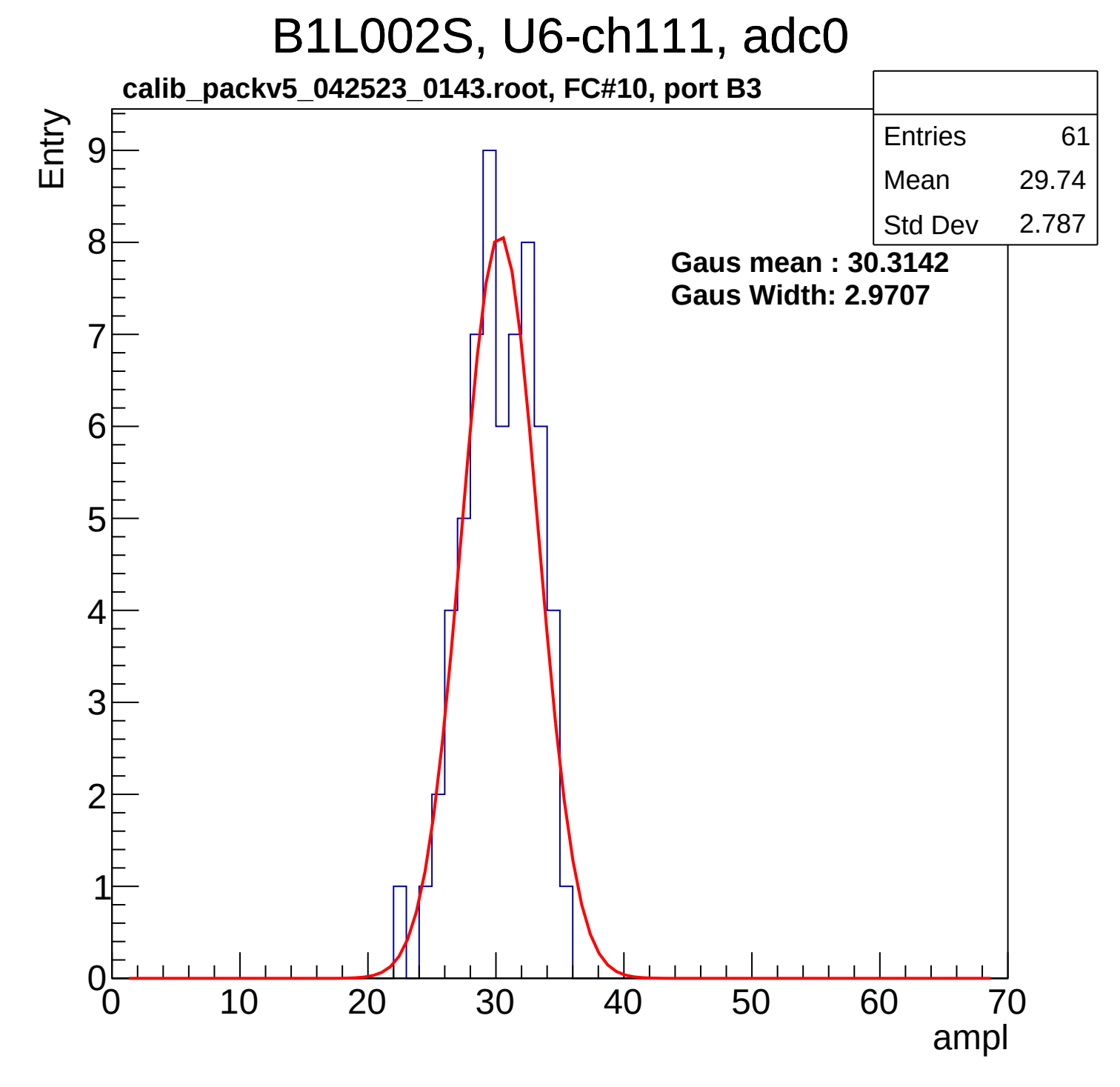

B1L002S, U6-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.92
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

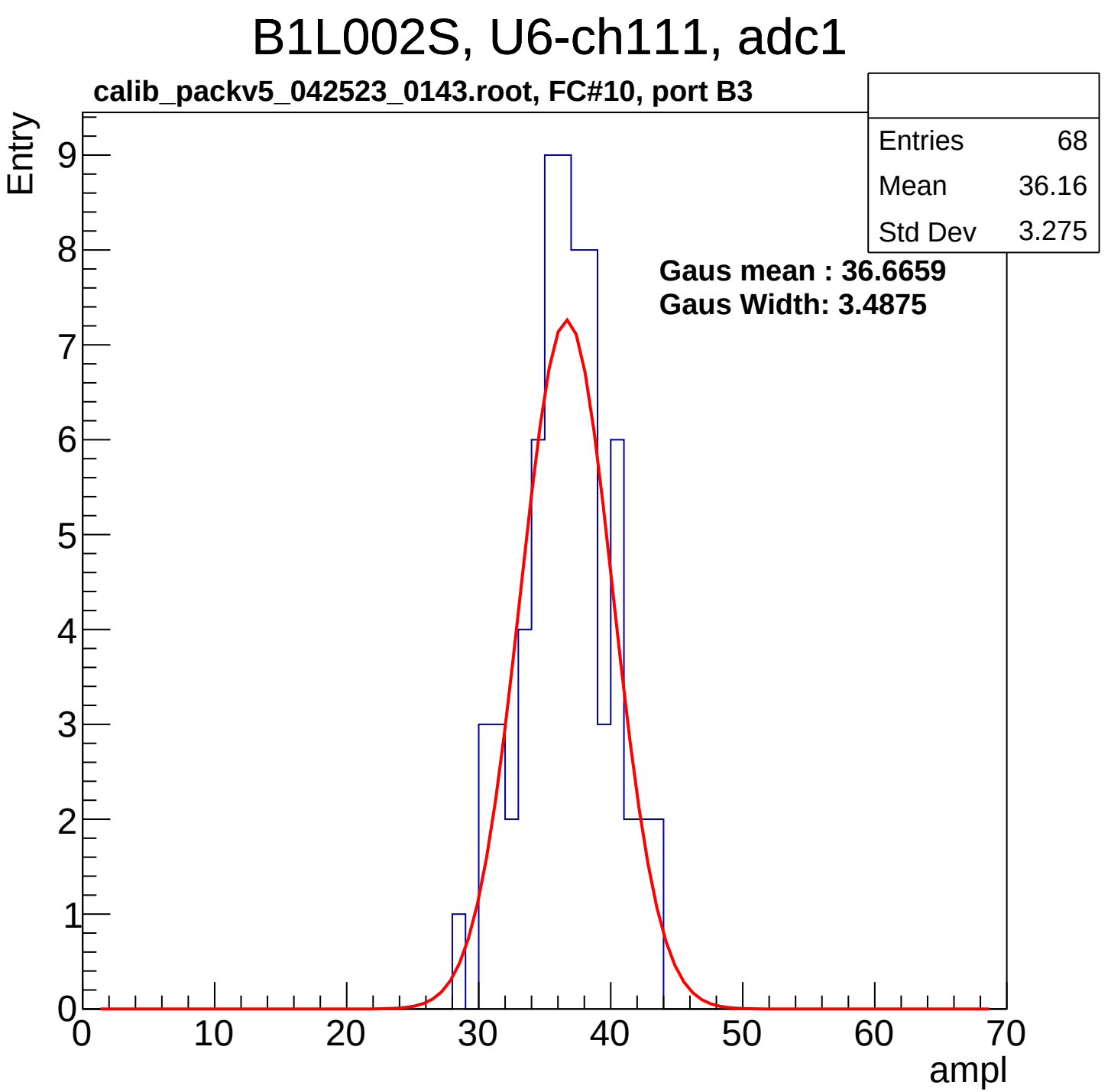

B1L002S, U6-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 54.07
Std Dev 2.981
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

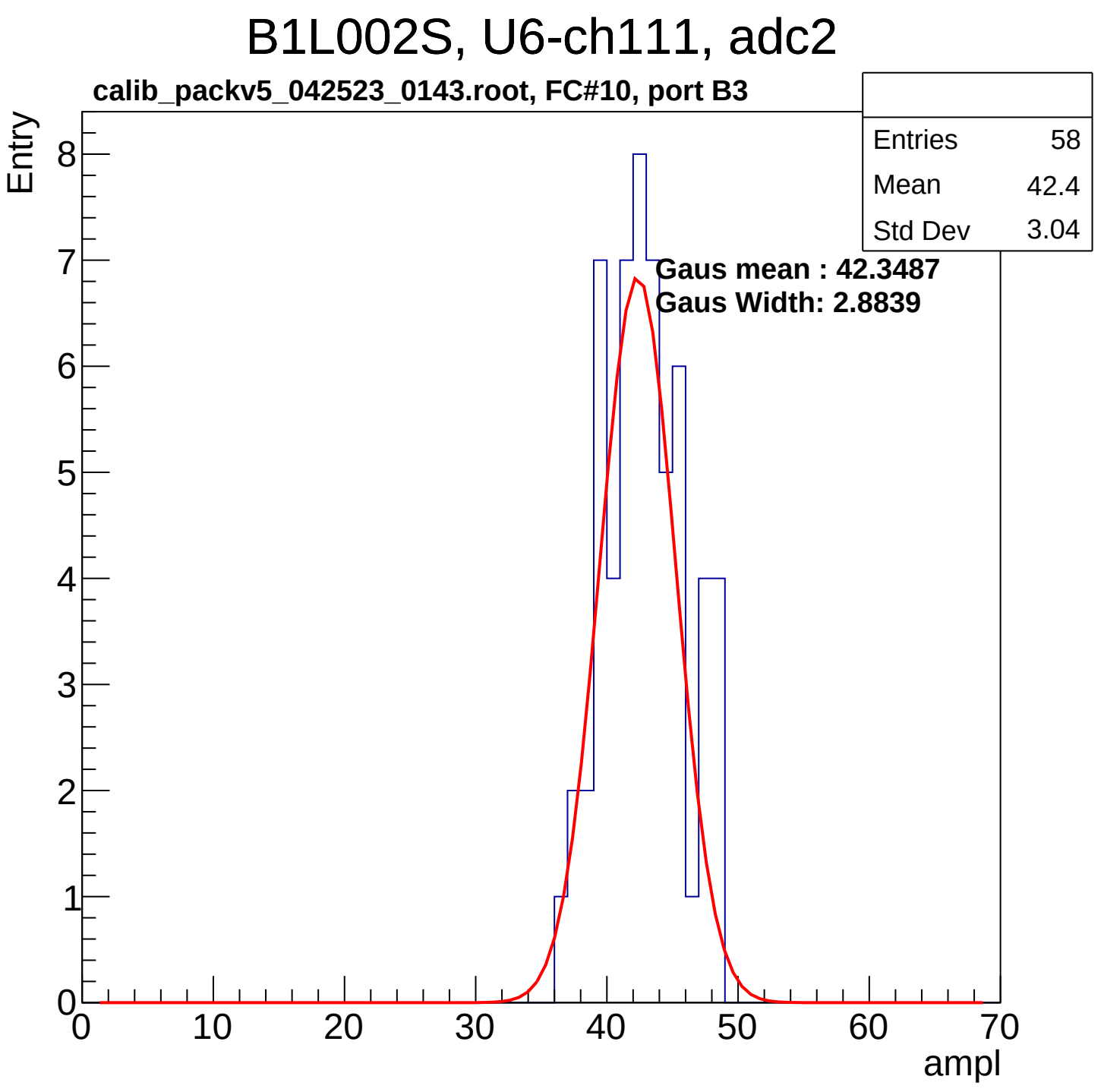

B1L002S, U6-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.1
Std Dev 8.085
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

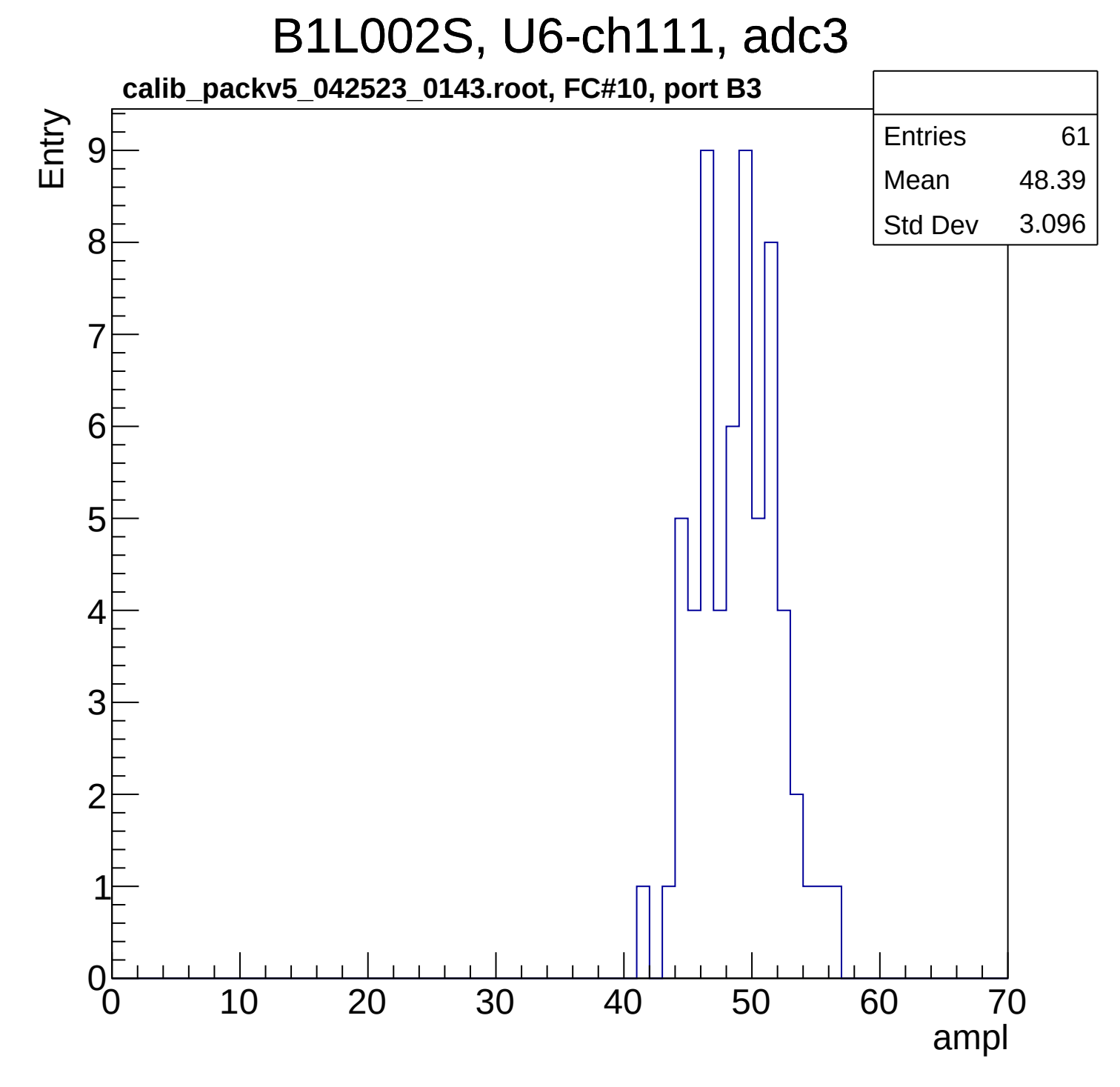

B1L002S, U6-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.65
Std Dev 1.388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

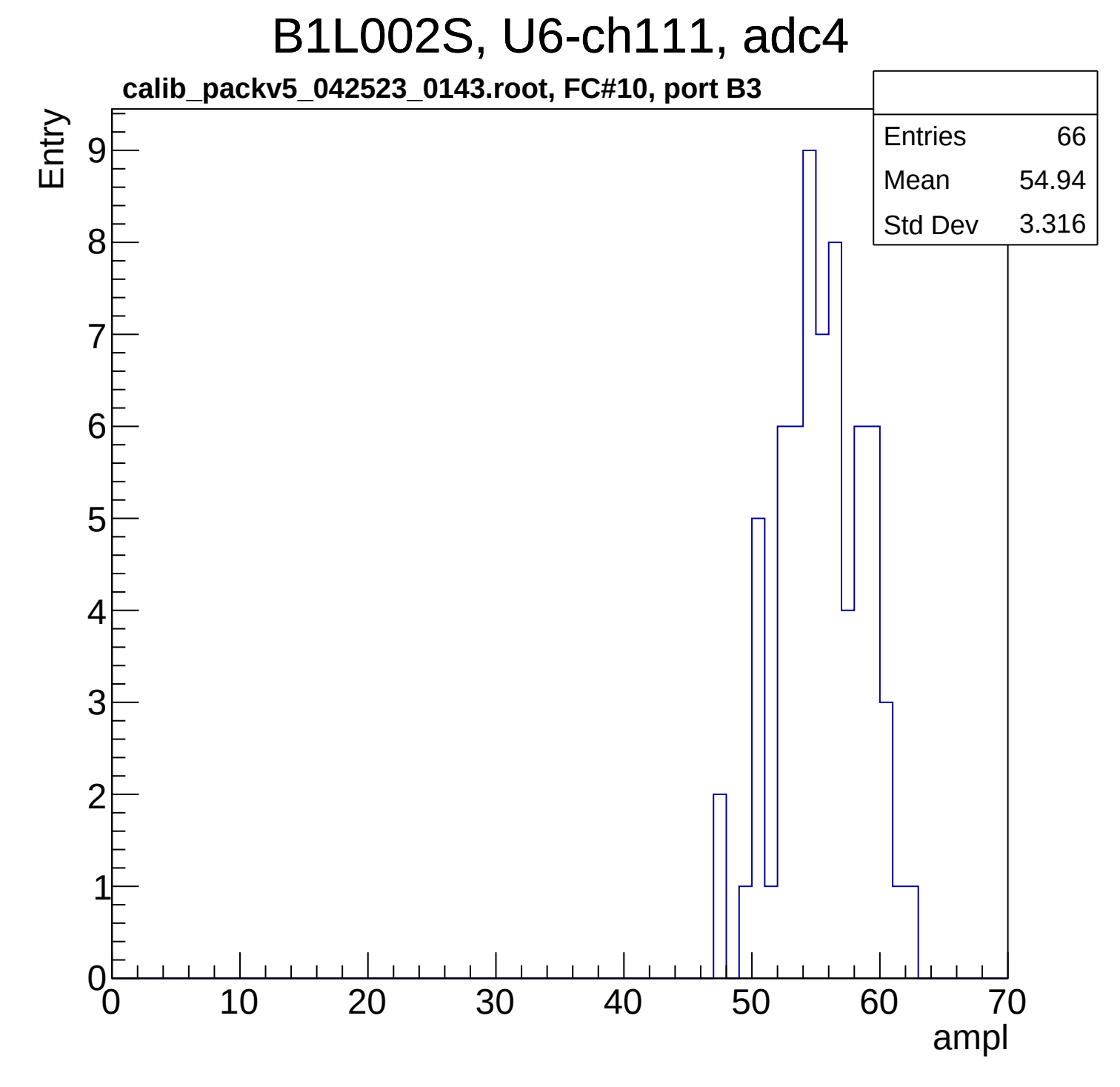

B1L002S, U6-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

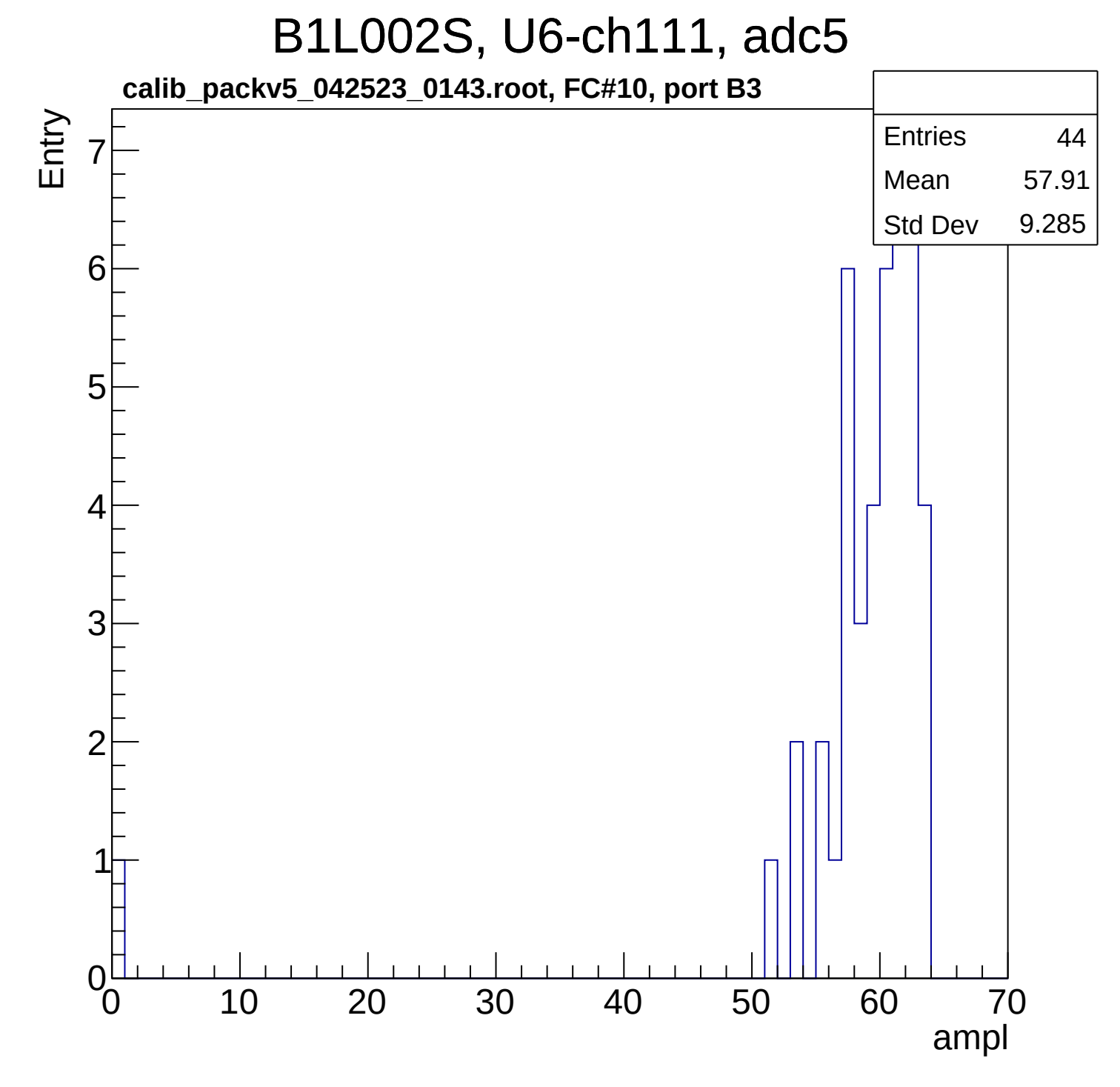

B1L002S, U6-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.74
Std Dev 2.787
Gaus mean : 30.3142
Gaus Width: 2.9707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

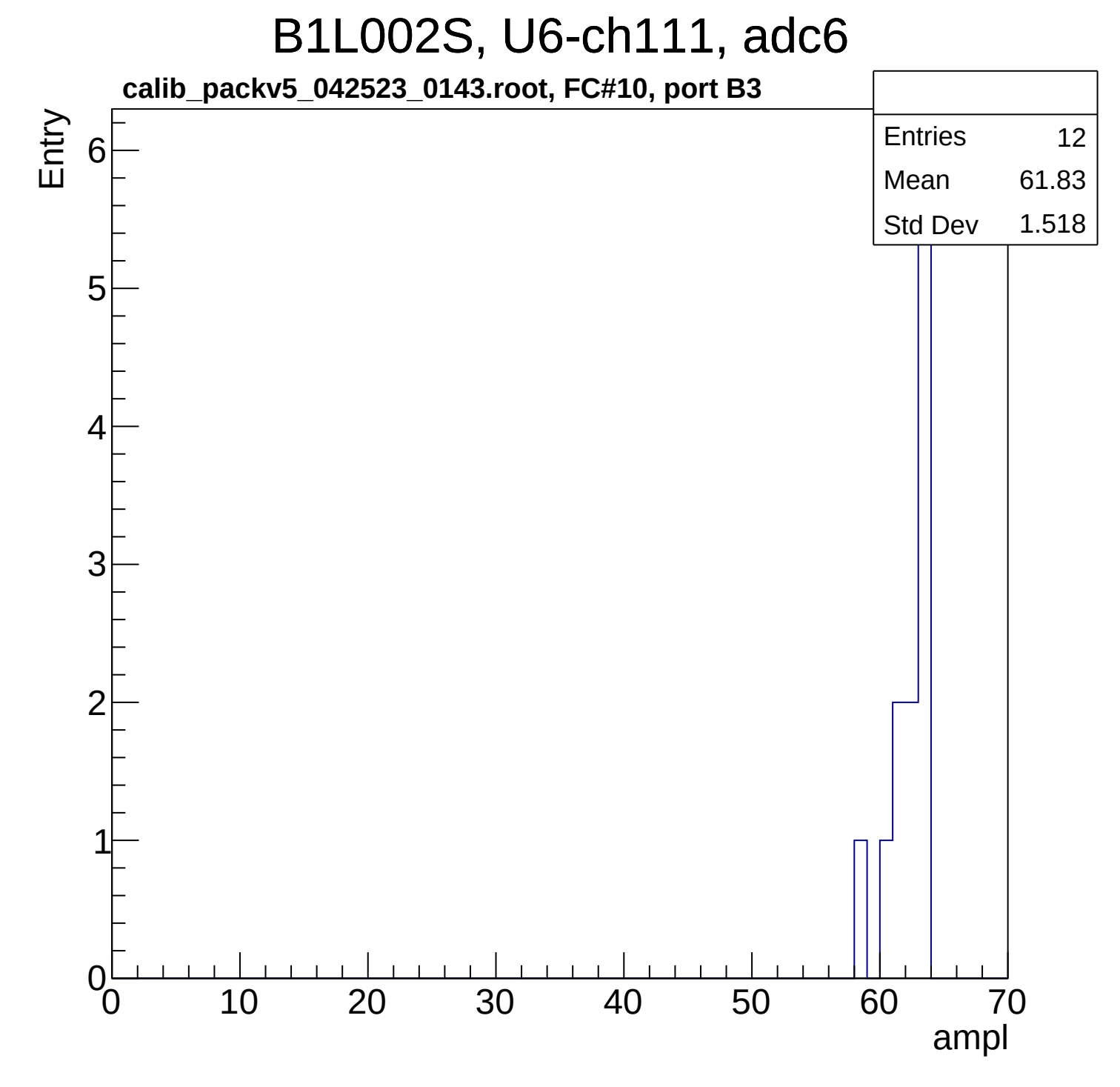

B1L002S, U6-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.16
Std Dev 3.275
Gaus mean : 36.6659
Gaus Width: 3.4875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

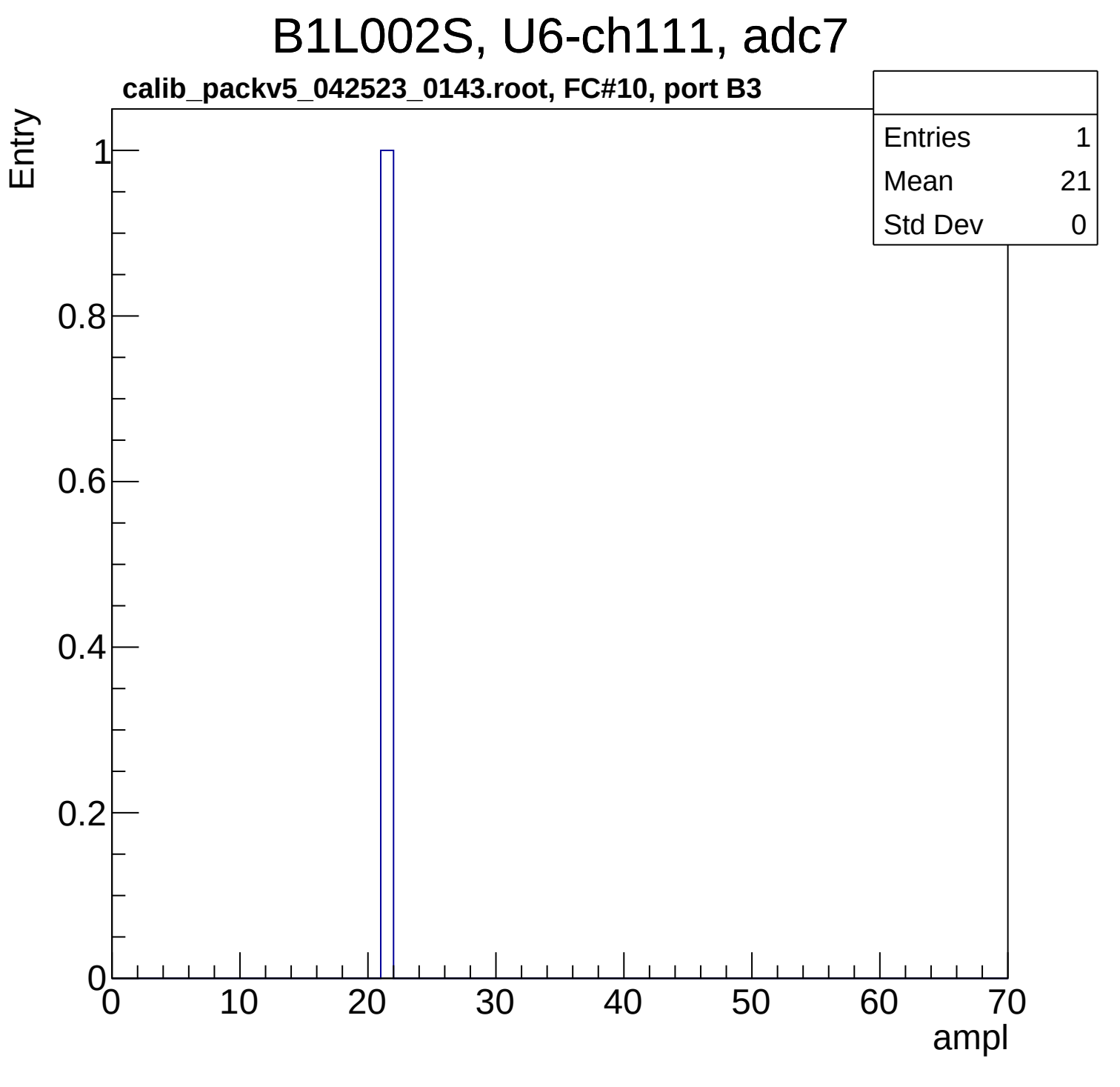

B1L002S, U6-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.4
Std Dev 3.04
Gaus mean : 42.3487
Gaus Width: 2.8839
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

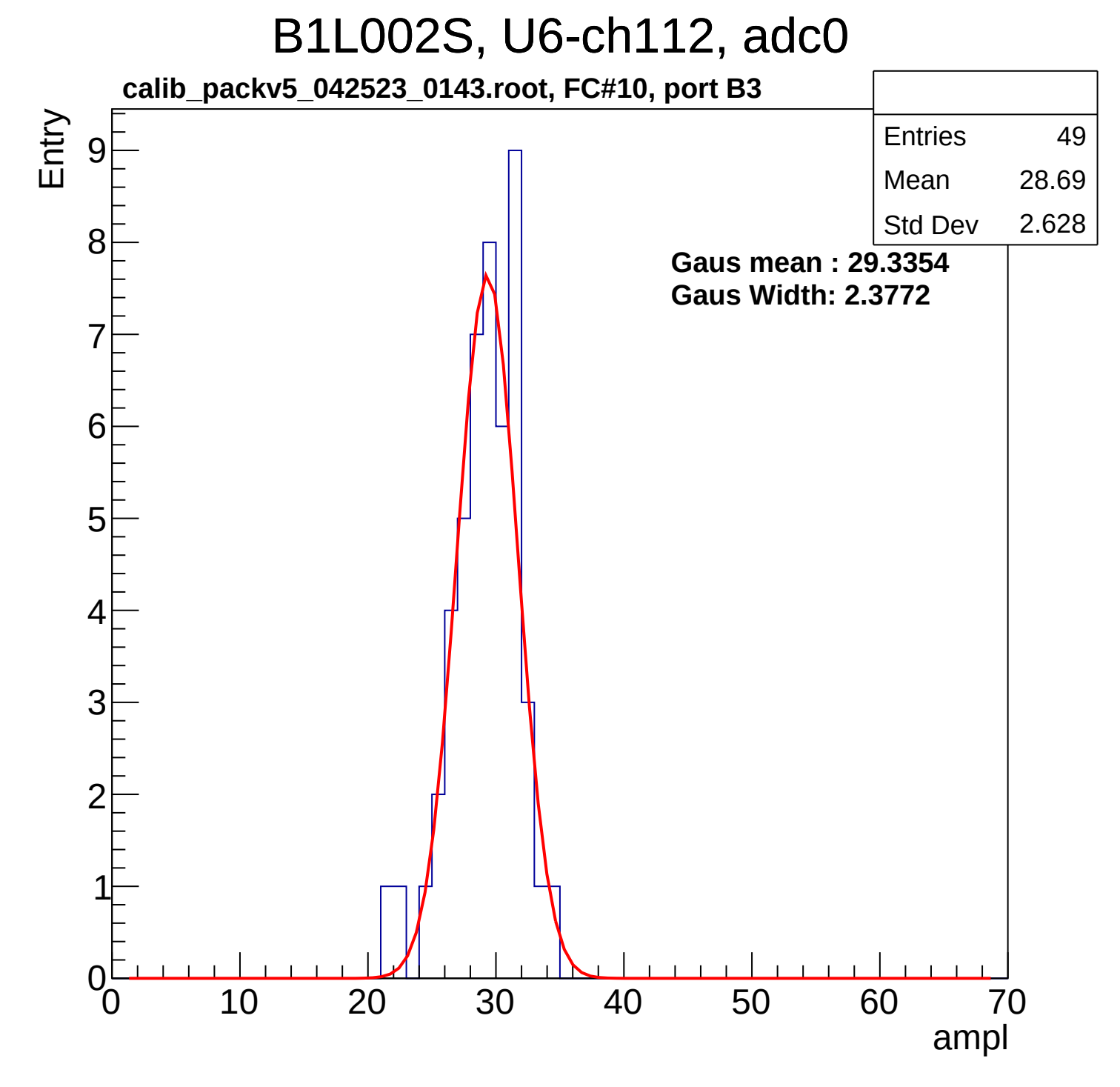

B1L002S, U6-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.39
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

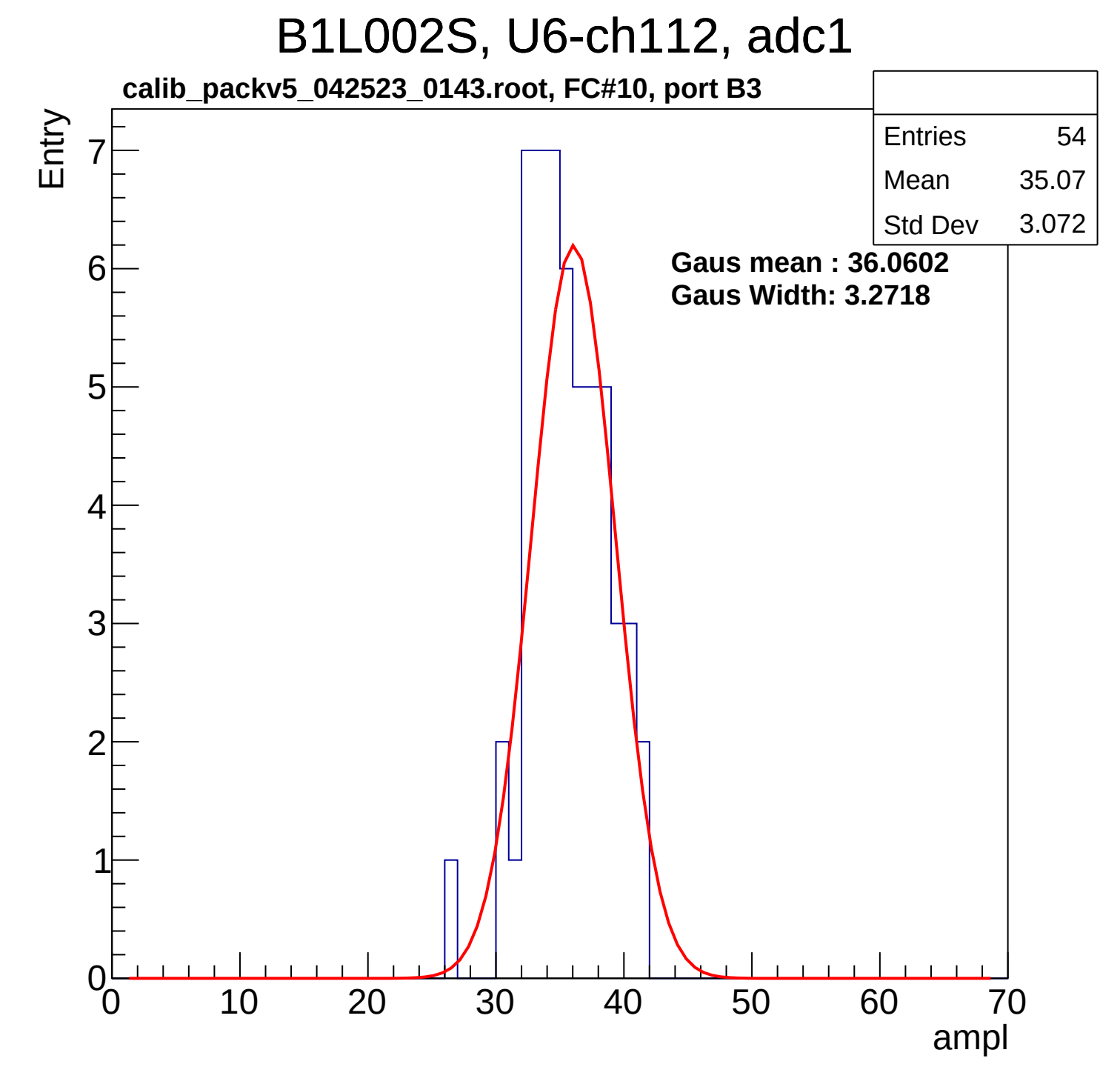

B1L002S, U6-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.94
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

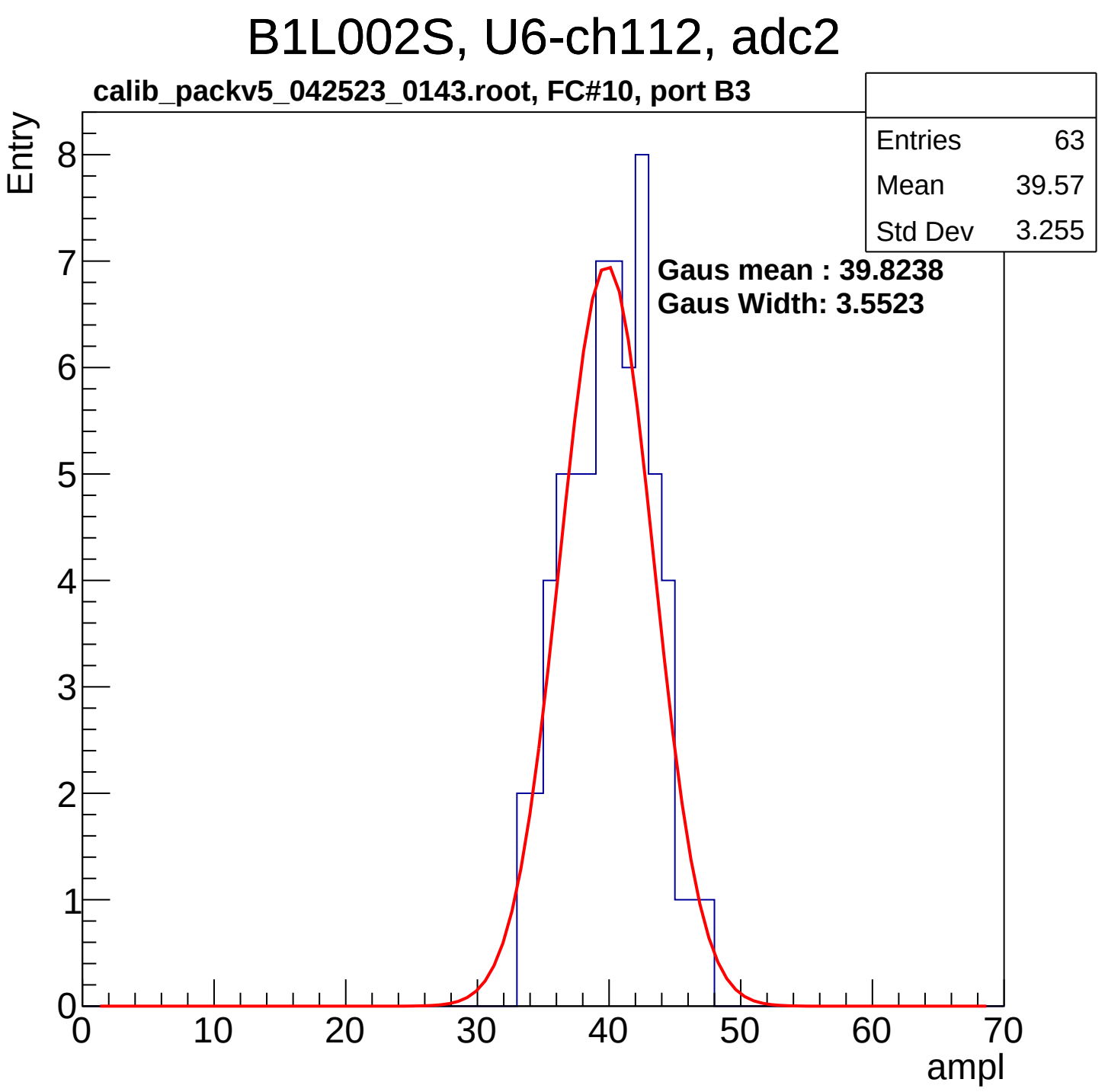

B1L002S, U6-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.91
Std Dev 9.285
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

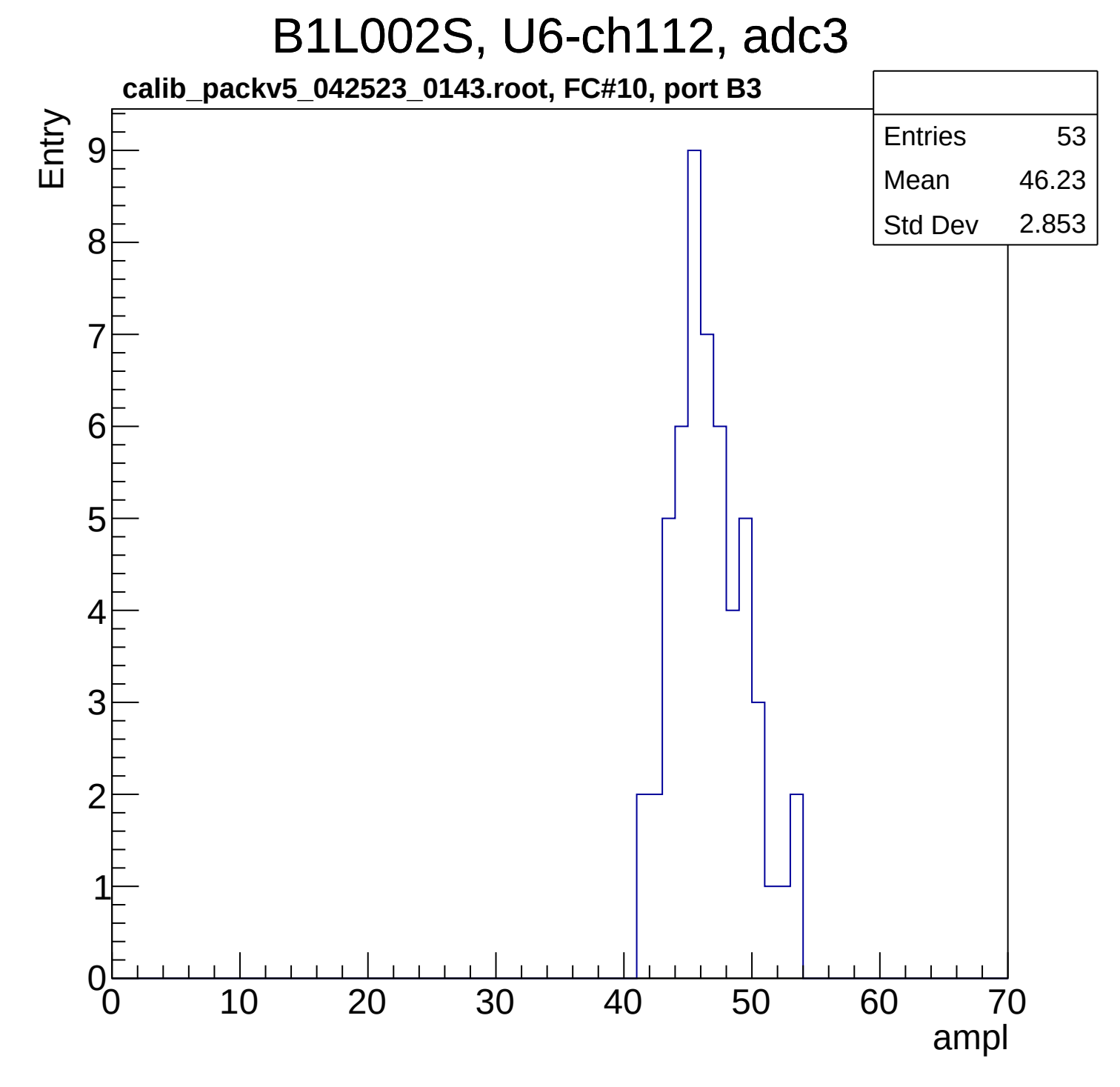

B1L002S, U6-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.83
Std Dev 1.518
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

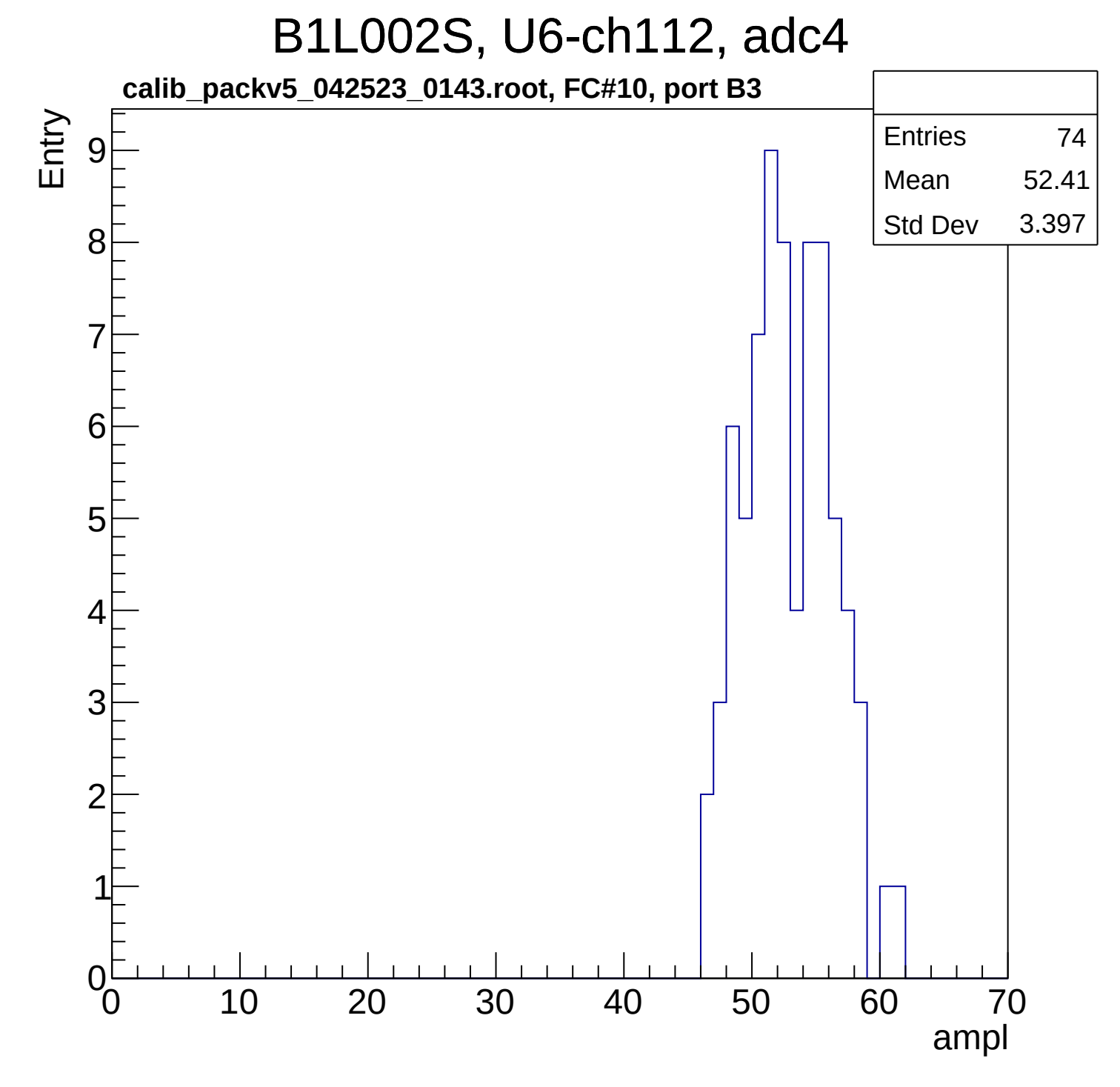

B1L002S, U6-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

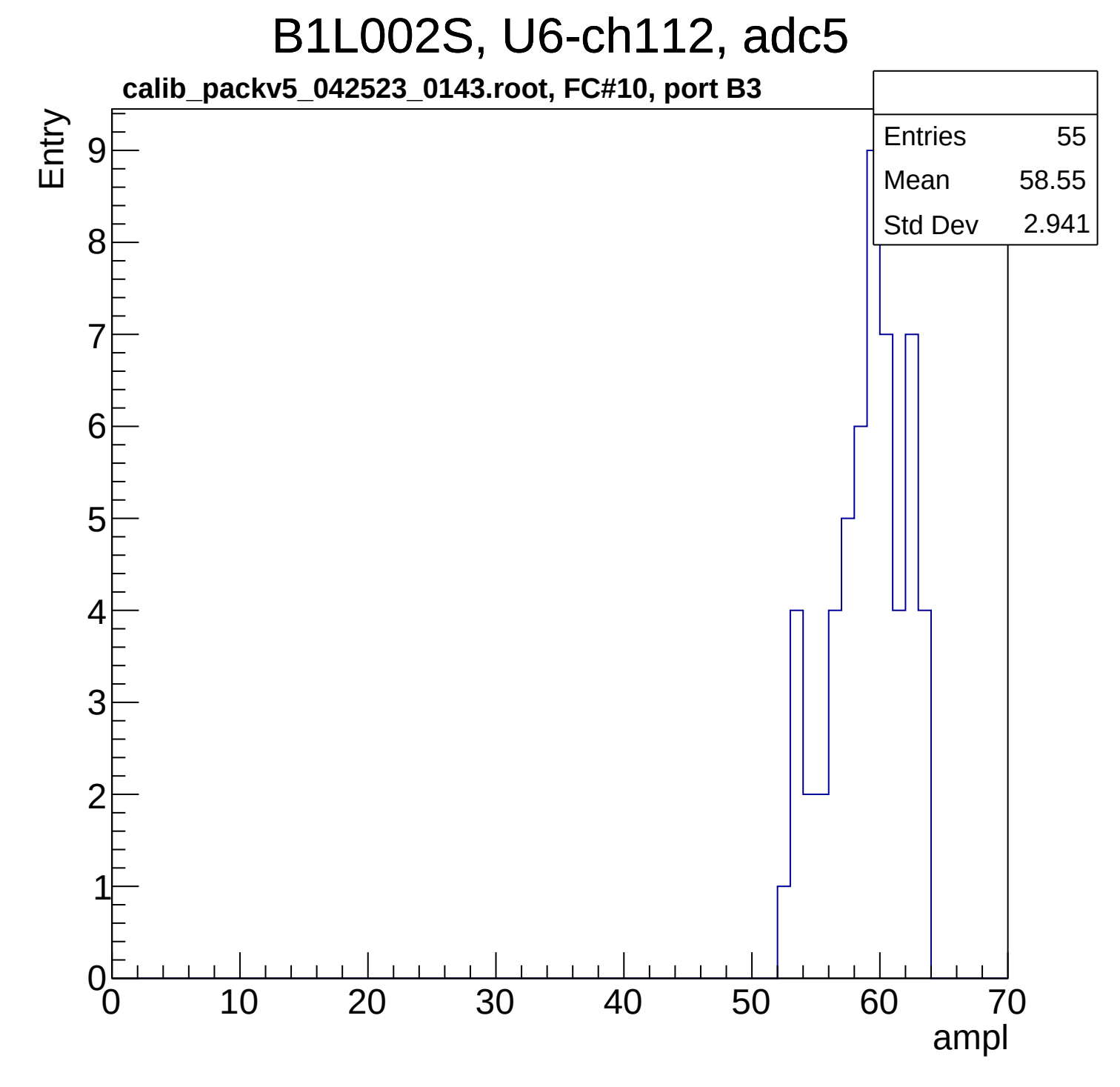

B1L002S, U6-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 28.69
Std Dev 2.628
Gaus mean : 29.3354
Gaus Width: 2.3772
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

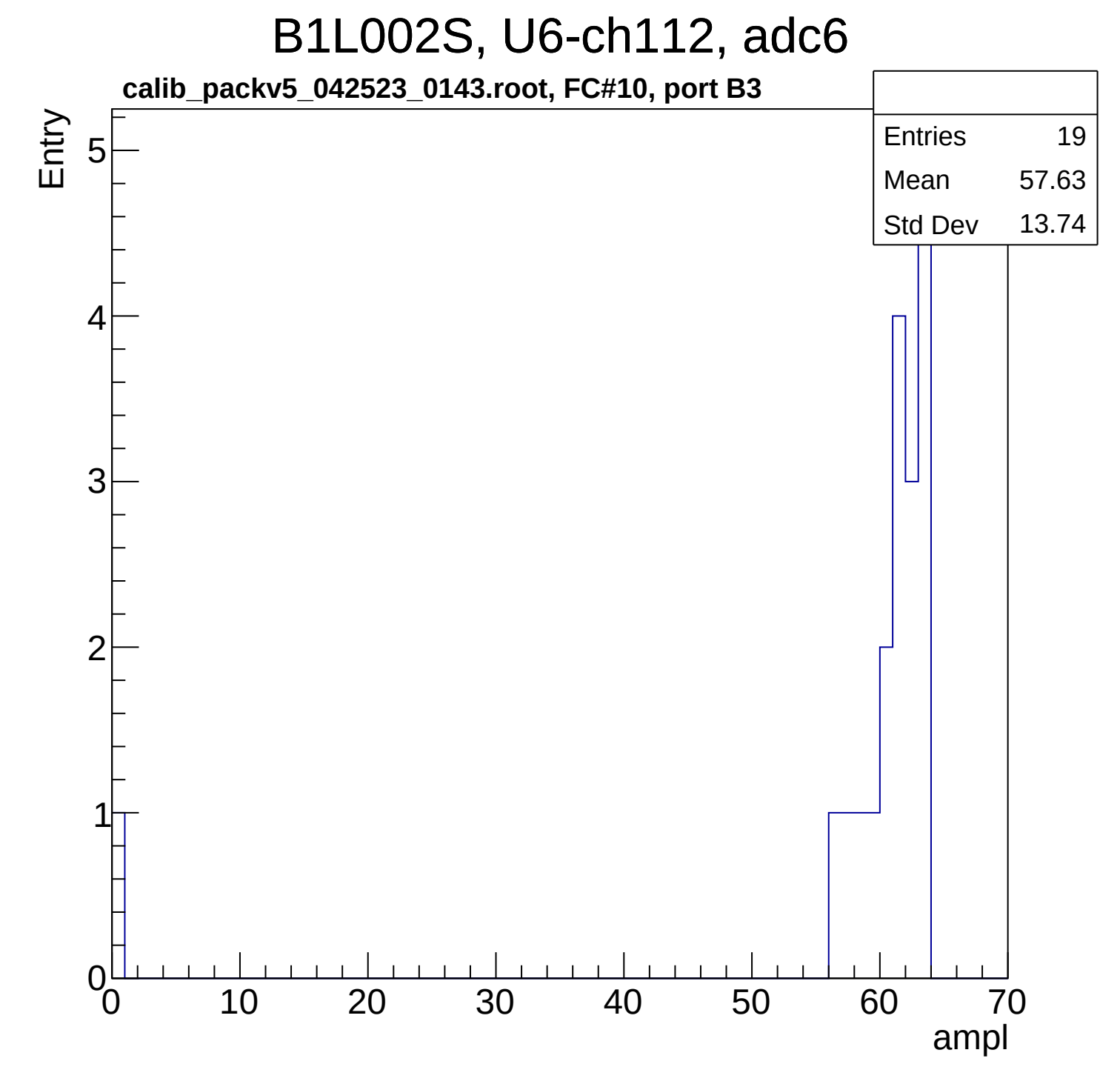

B1L002S, U6-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.07
Std Dev 3.072
Gaus mean : 36.0602
Gaus Width: 3.2718
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

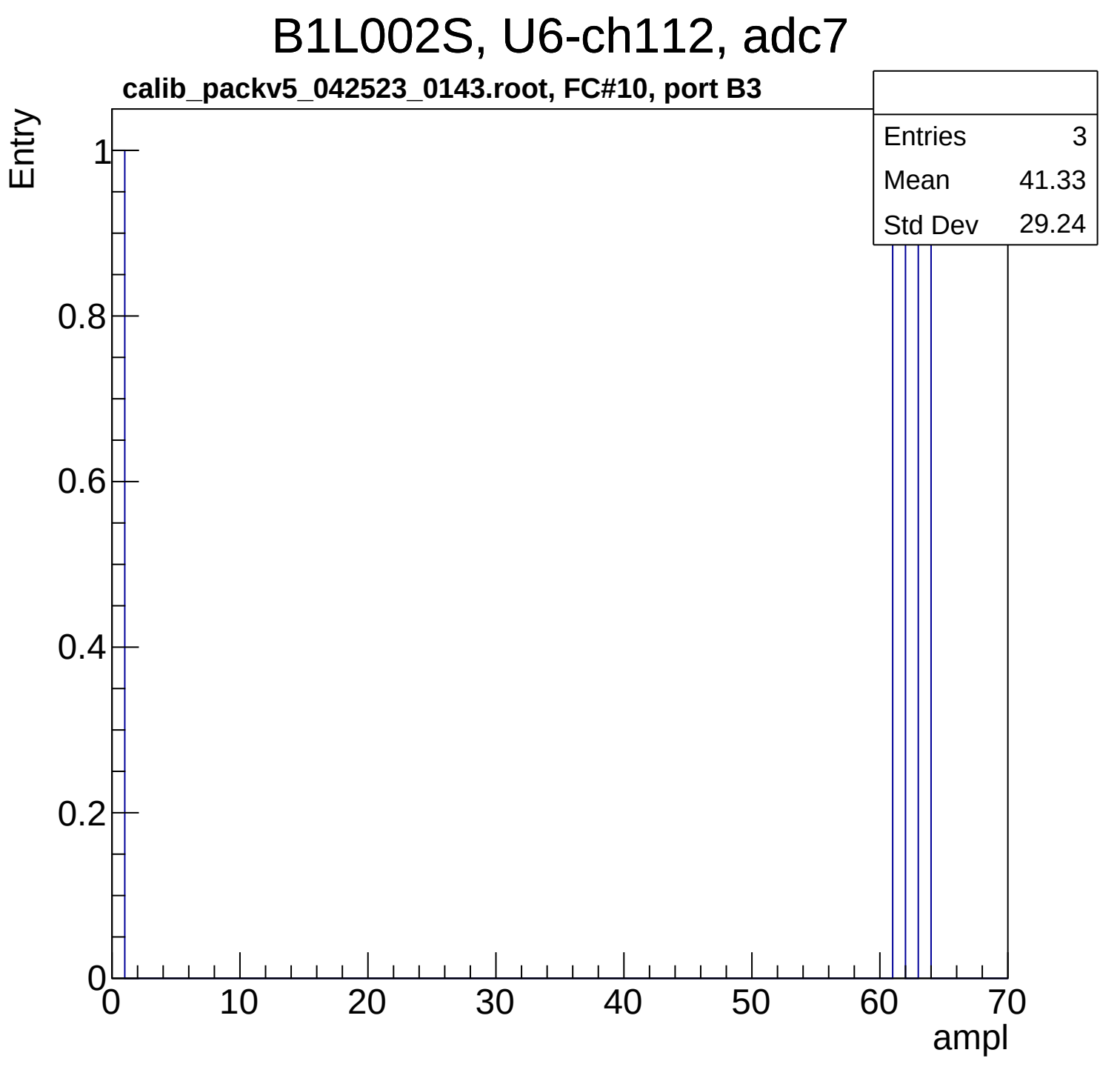

B1L002S, U6-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.57
Std Dev 3.255
Gaus mean : 39.8238
Gaus Width: 3.5523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

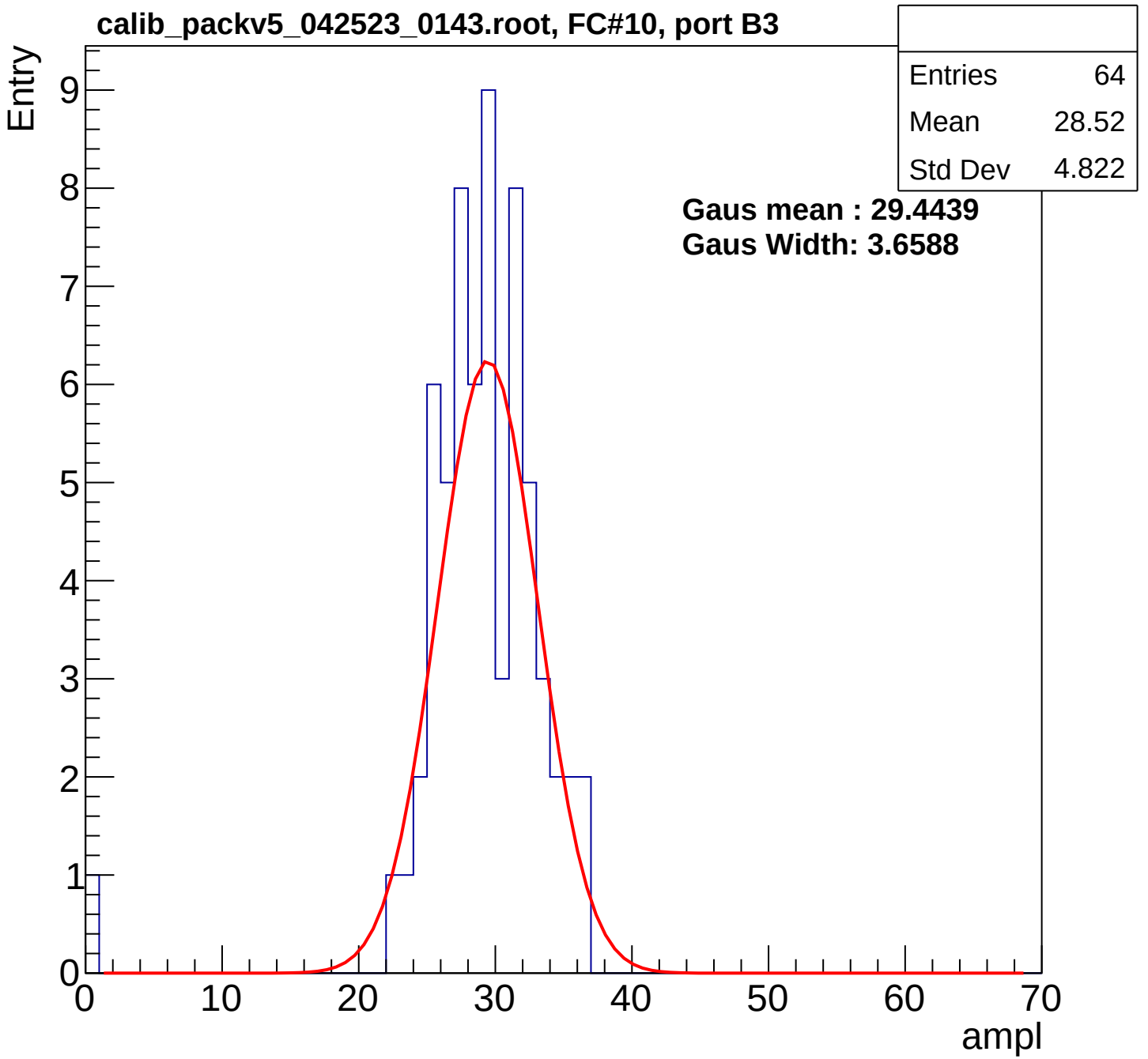

B1L002S, U6-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 46.23
Std Dev 2.853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

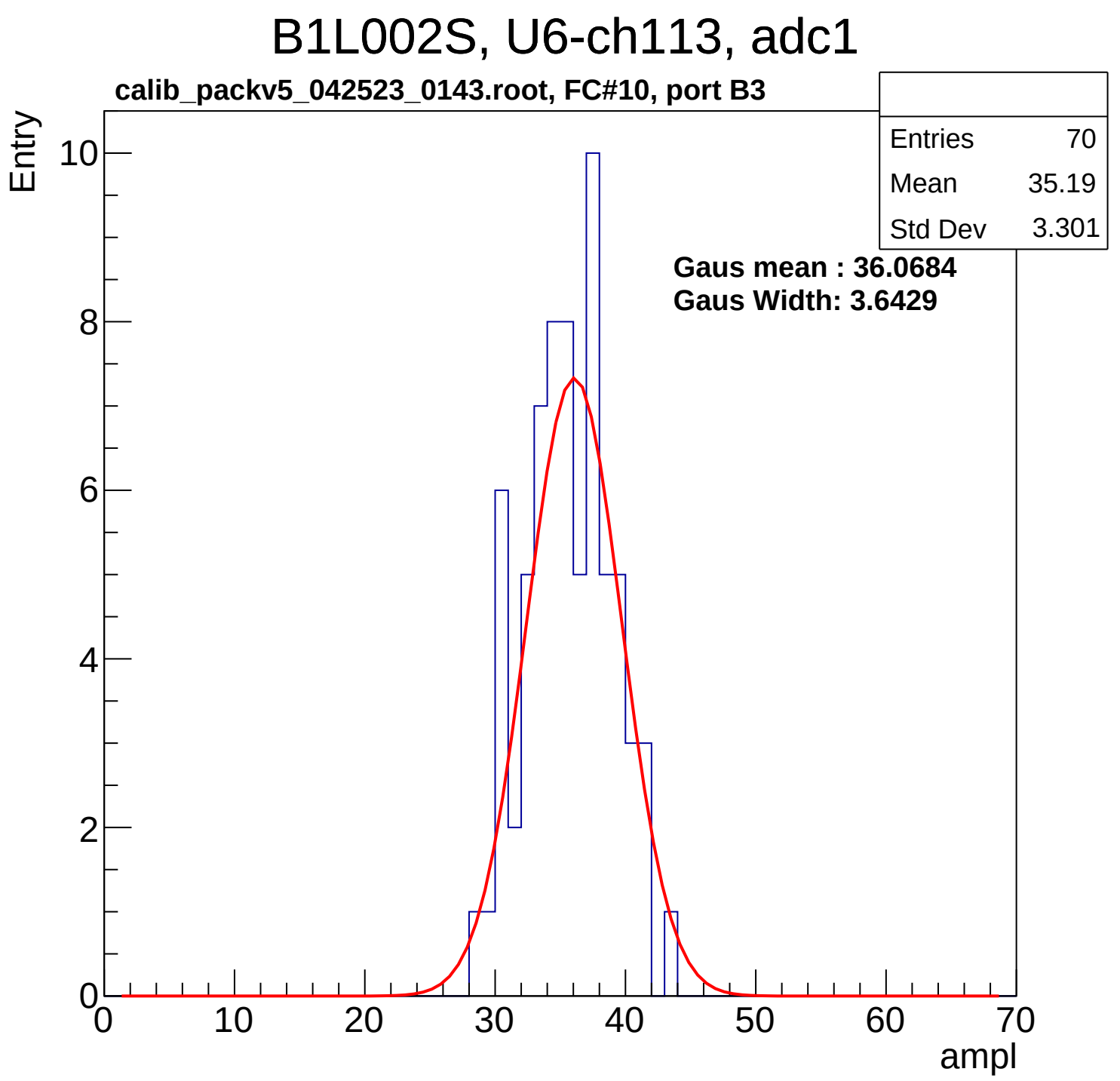

B1L002S, U6-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 52.41
Std Dev 3.397
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

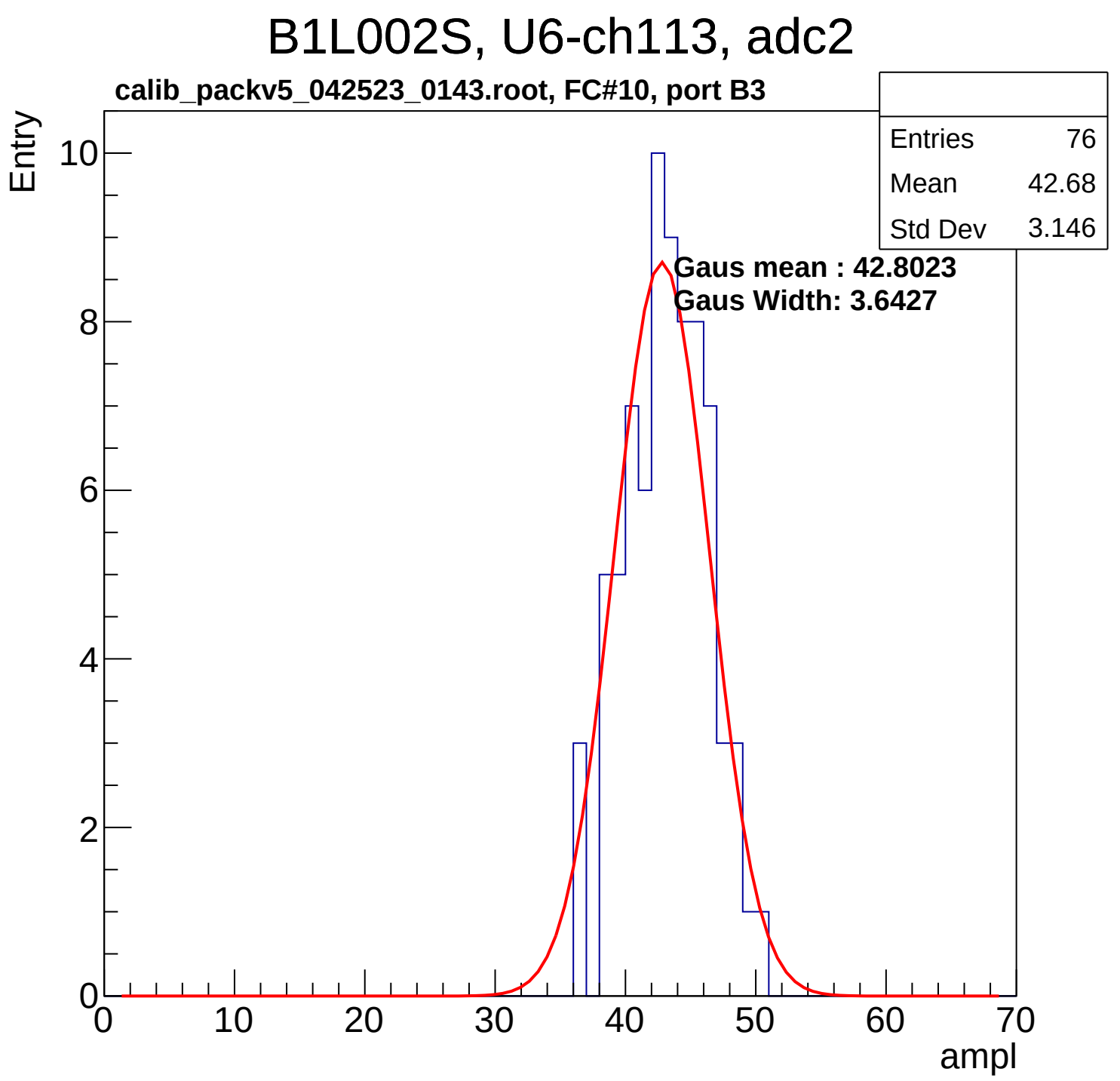

B1L002S, U6-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.55
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

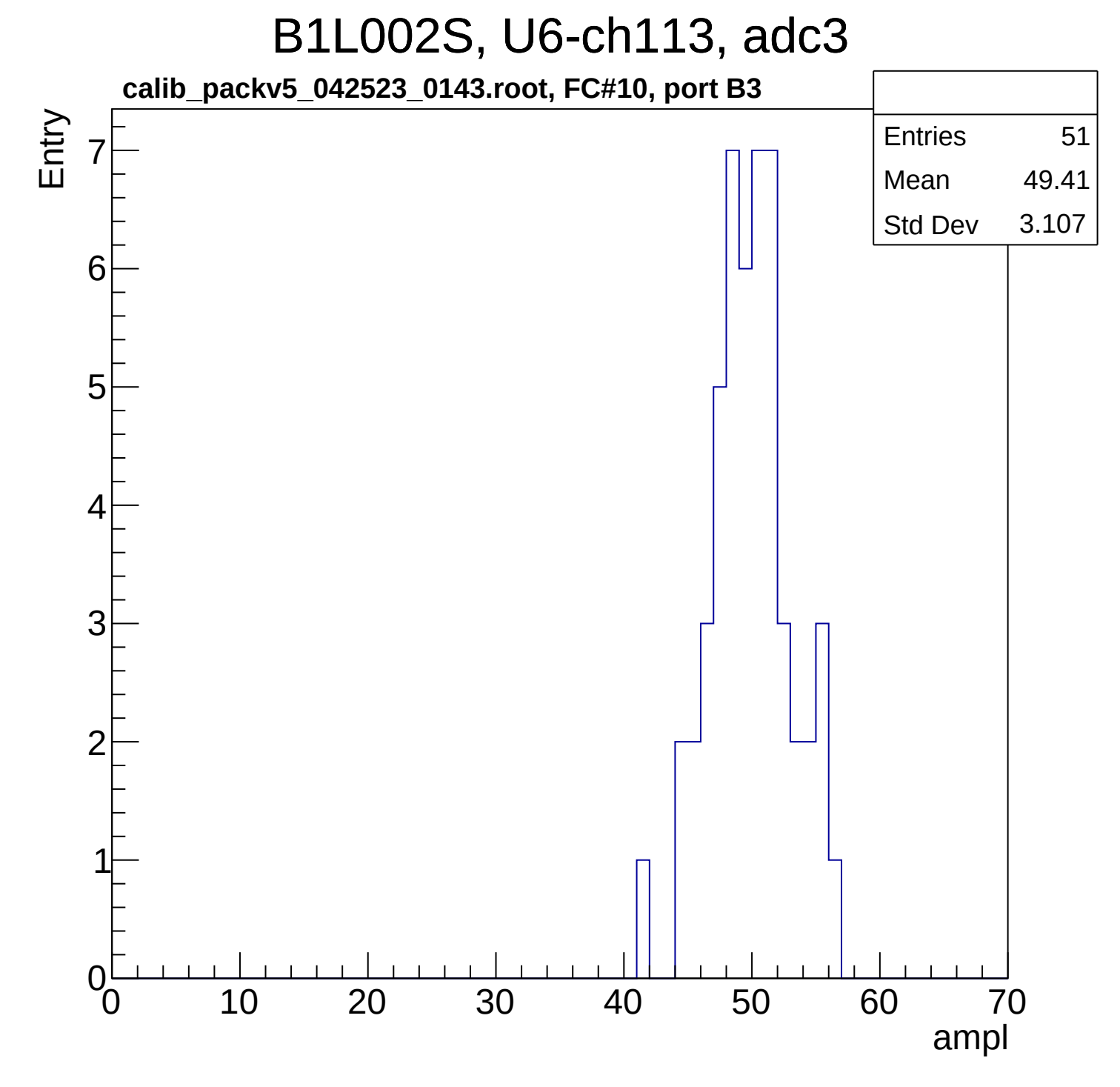

B1L002S, U6-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.63
Std Dev 13.74
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

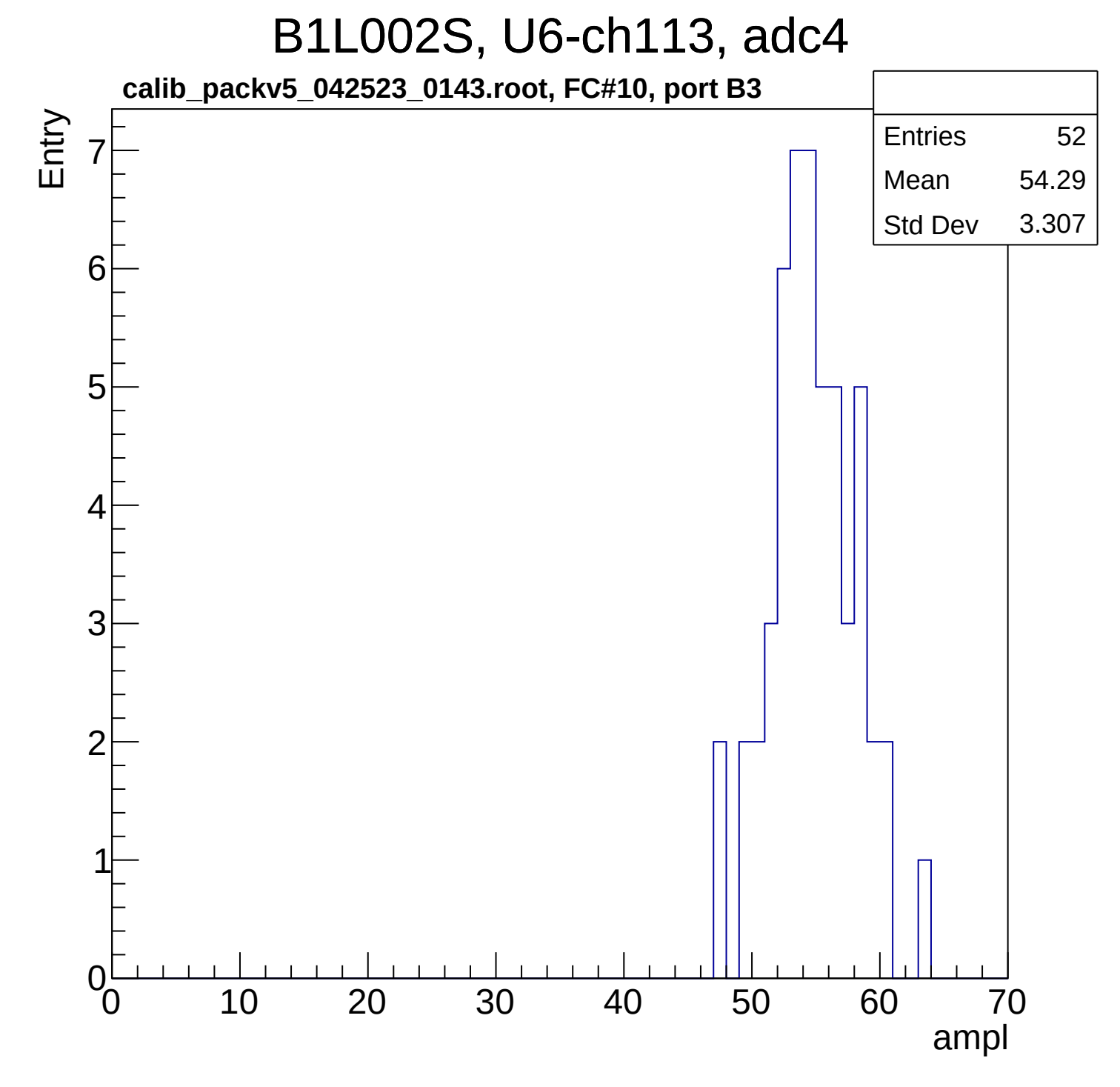

B1L002S, U6-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

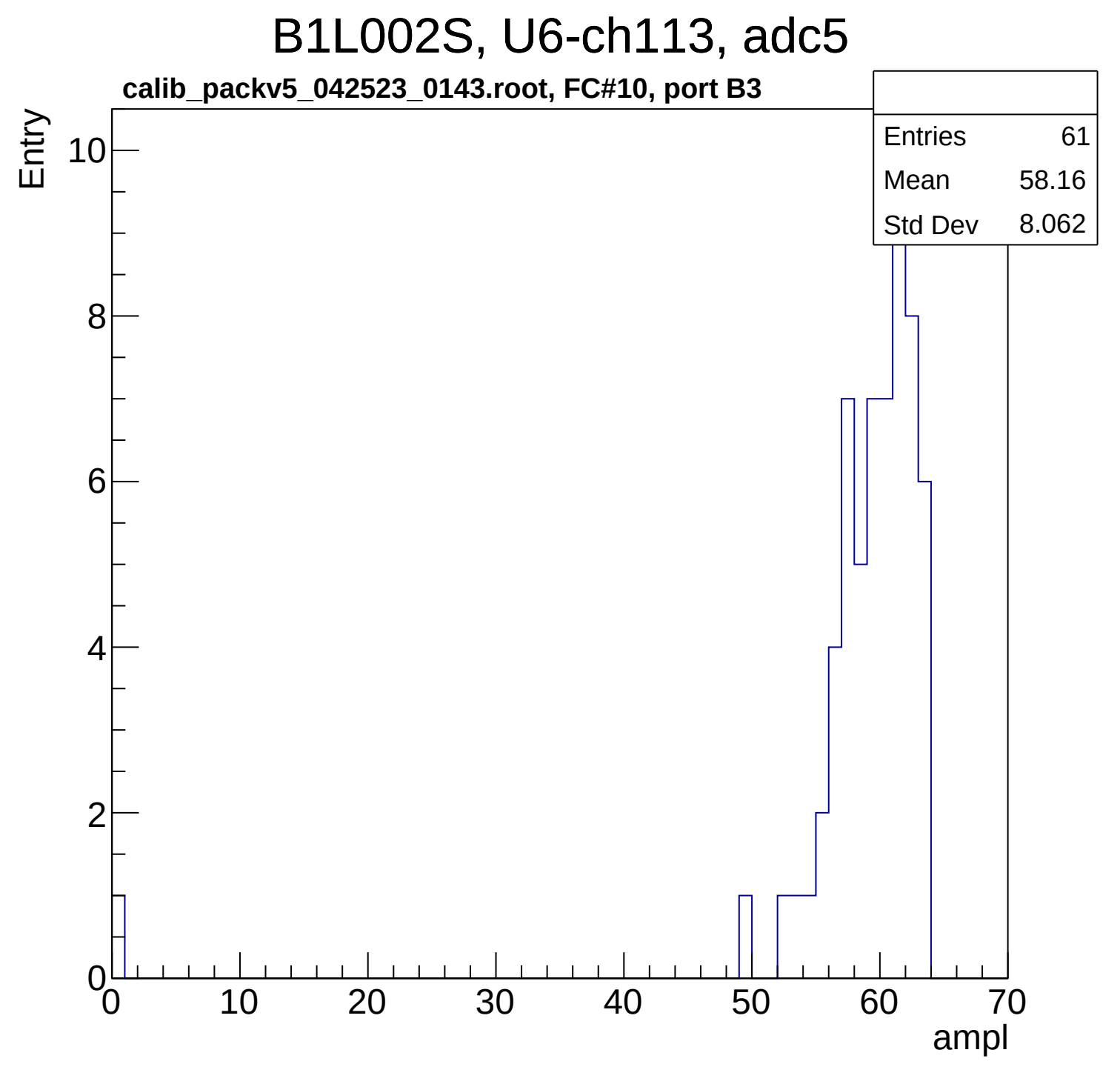

B1L002S, U6-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.52
Std Dev 4.822
Gaus mean : 29.4439
Gaus Width: 3.6588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

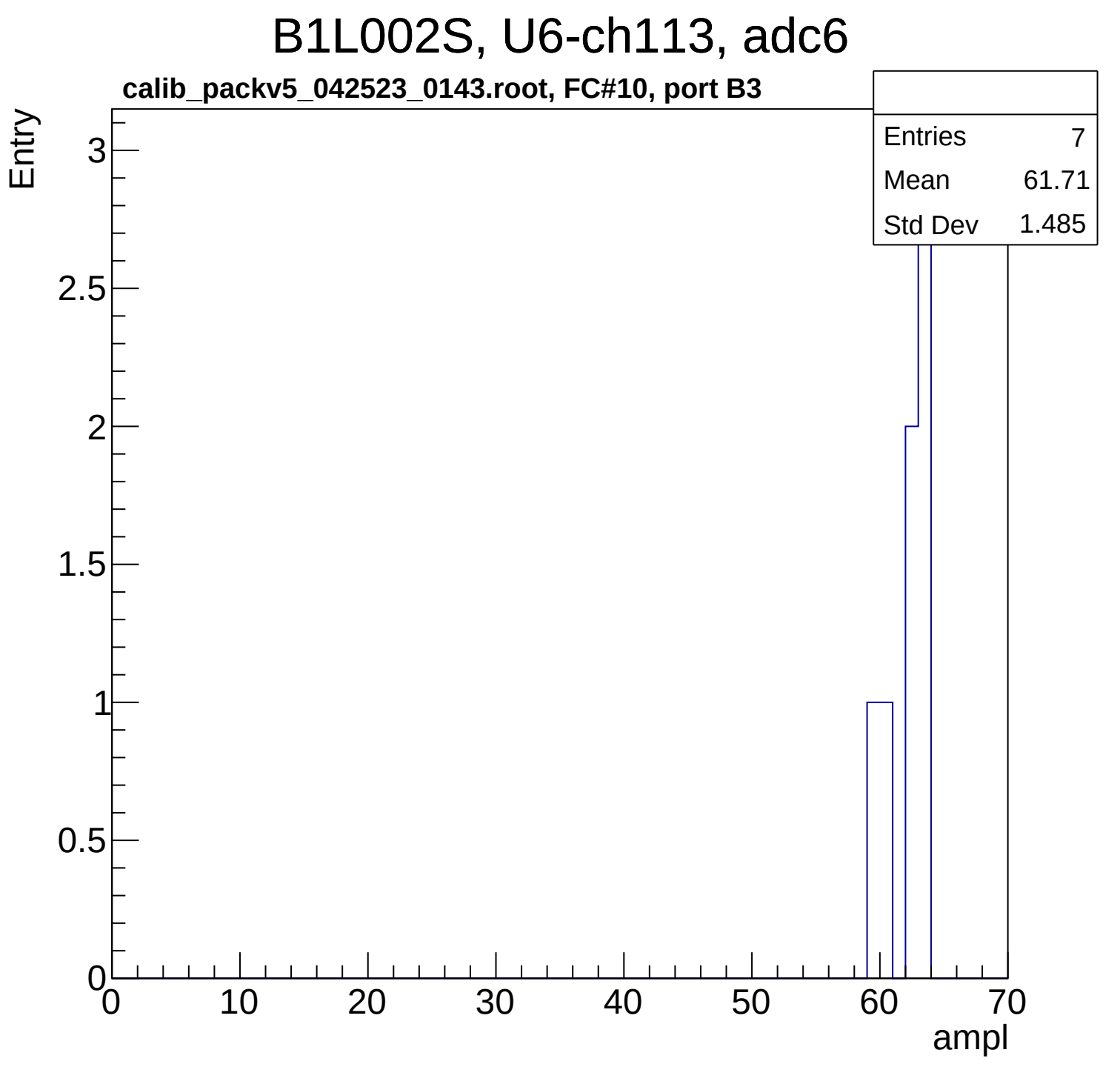

B1L002S, U6-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.19
Std Dev 3.301
Gaus mean : 36.0684
Gaus Width: 3.6429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

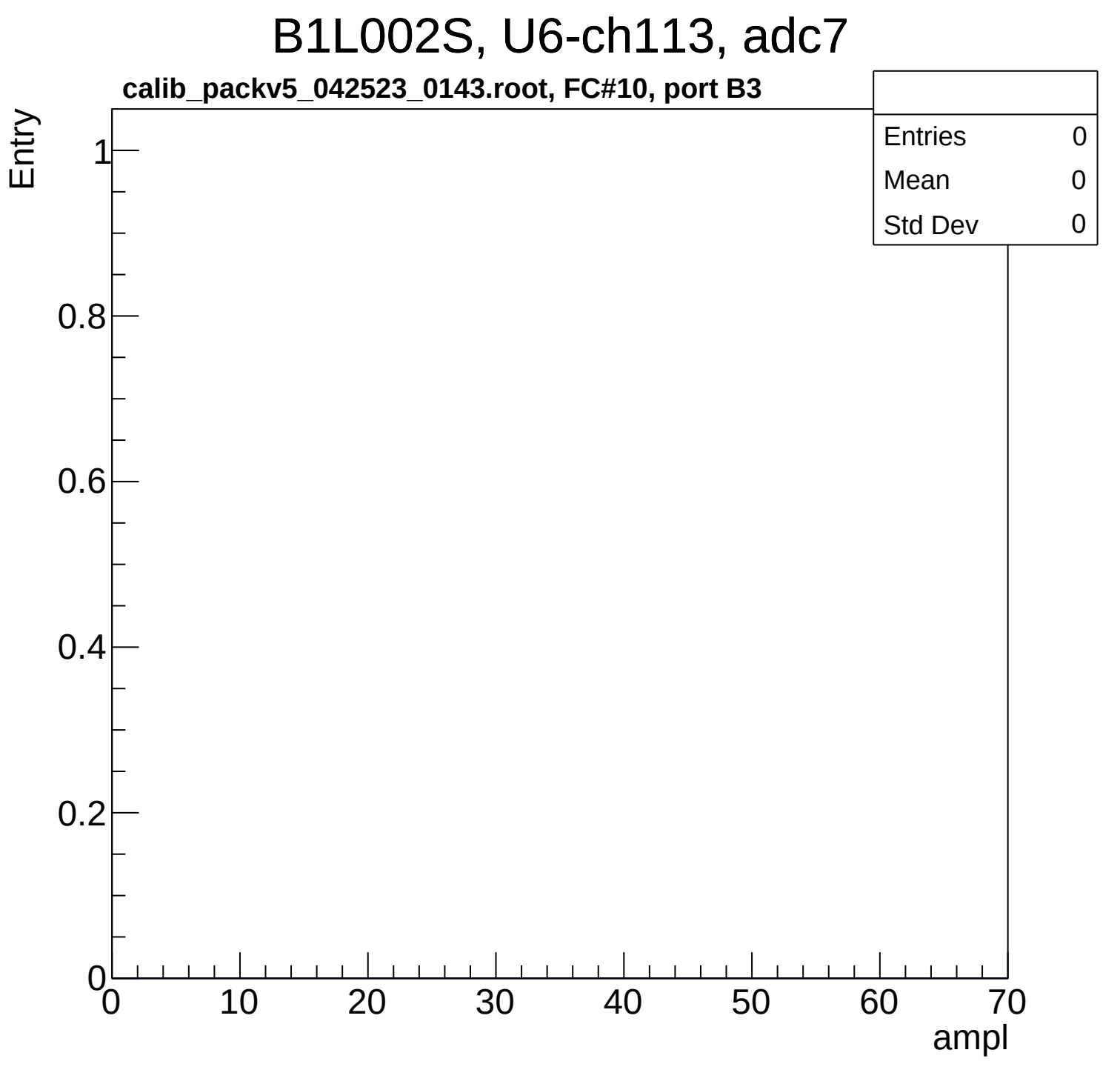

B1L002S, U6-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 42.68
Std Dev 3.146
Gaus mean : 42.8023
Gaus Width: 3.6427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

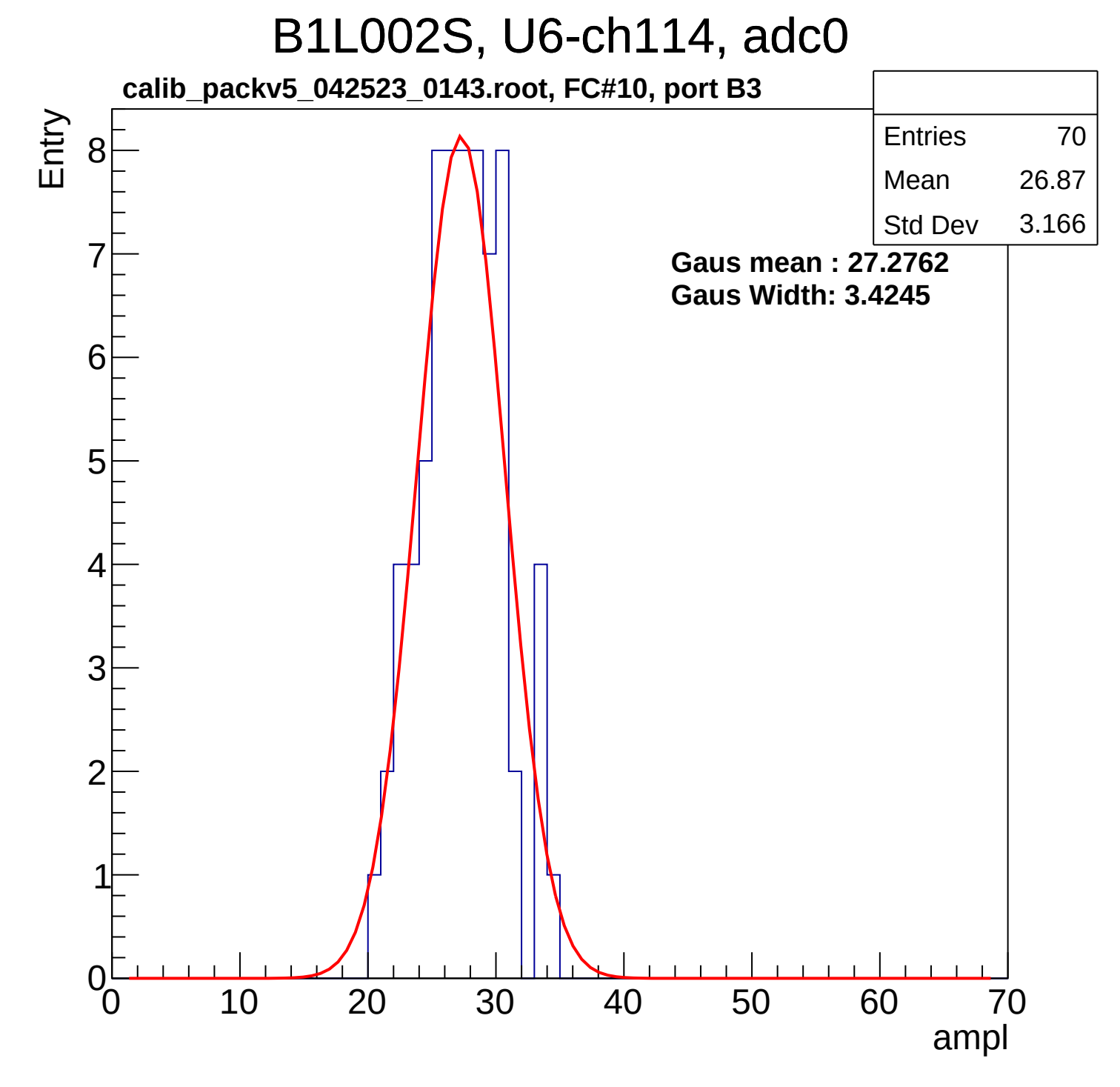

B1L002S, U6-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 49.41
Std Dev 3.107
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

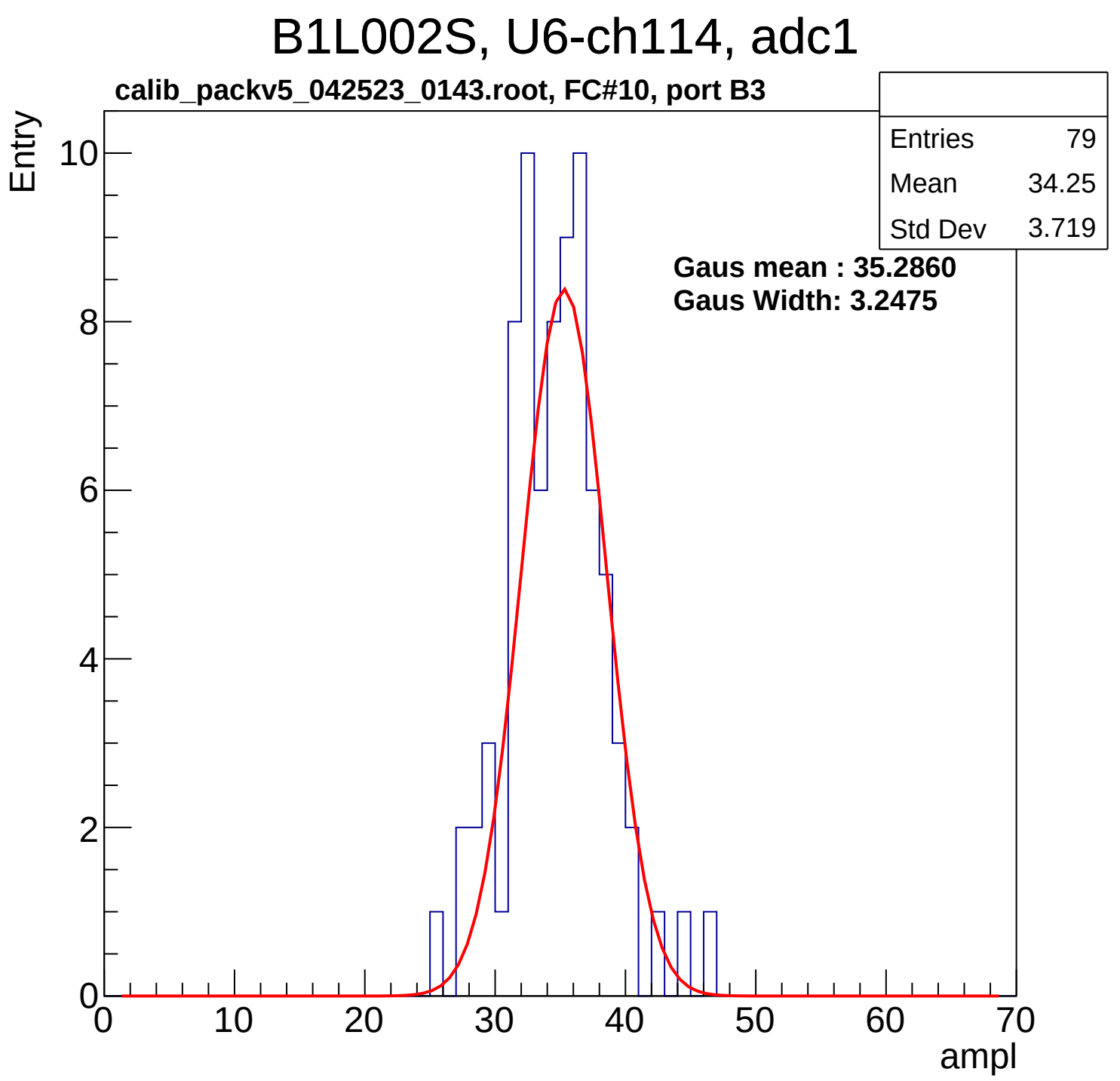

B1L002S, U6-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 54.29
Std Dev 3.307
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

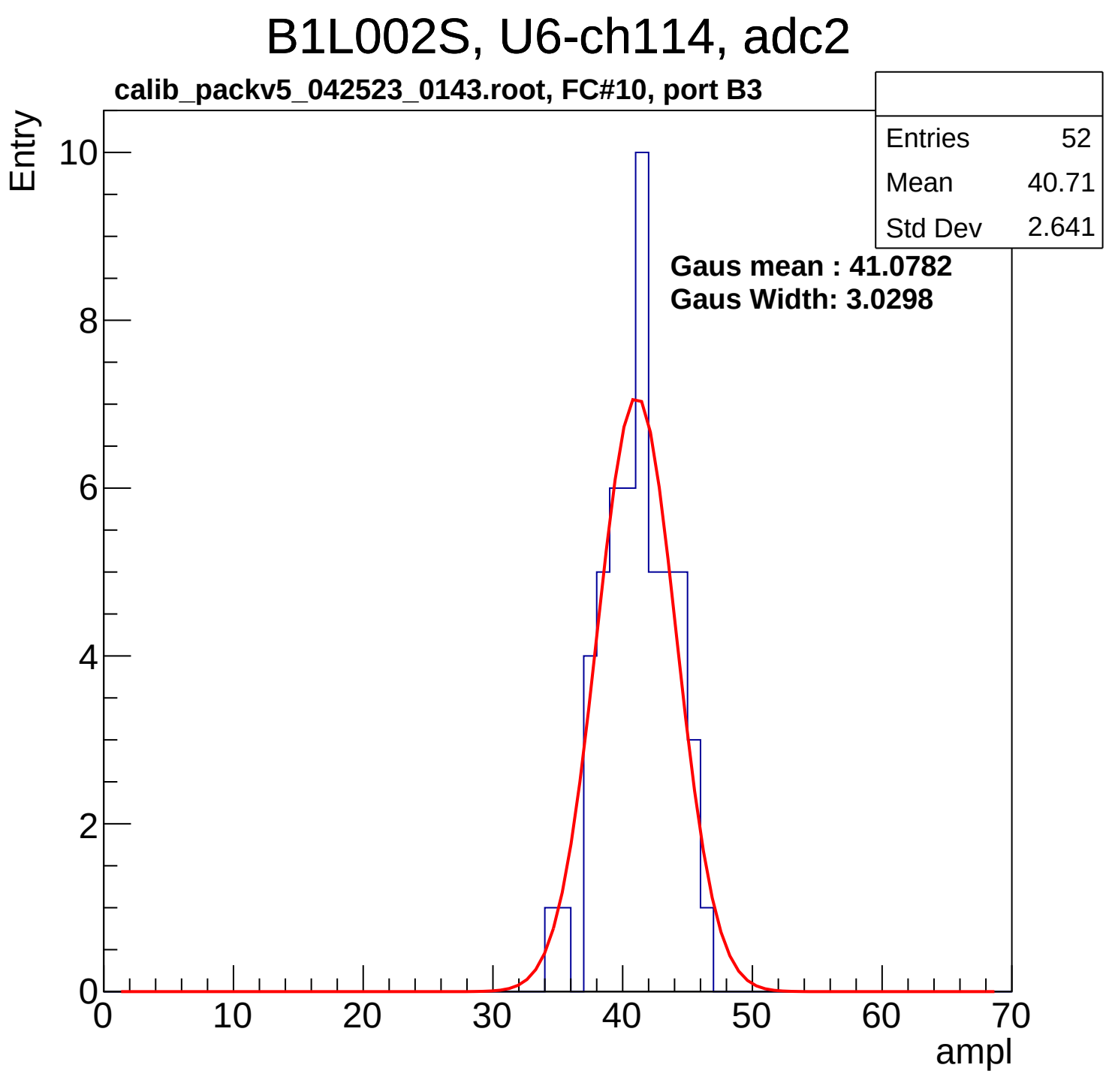

B1L002S, U6-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.16
Std Dev 8.062
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

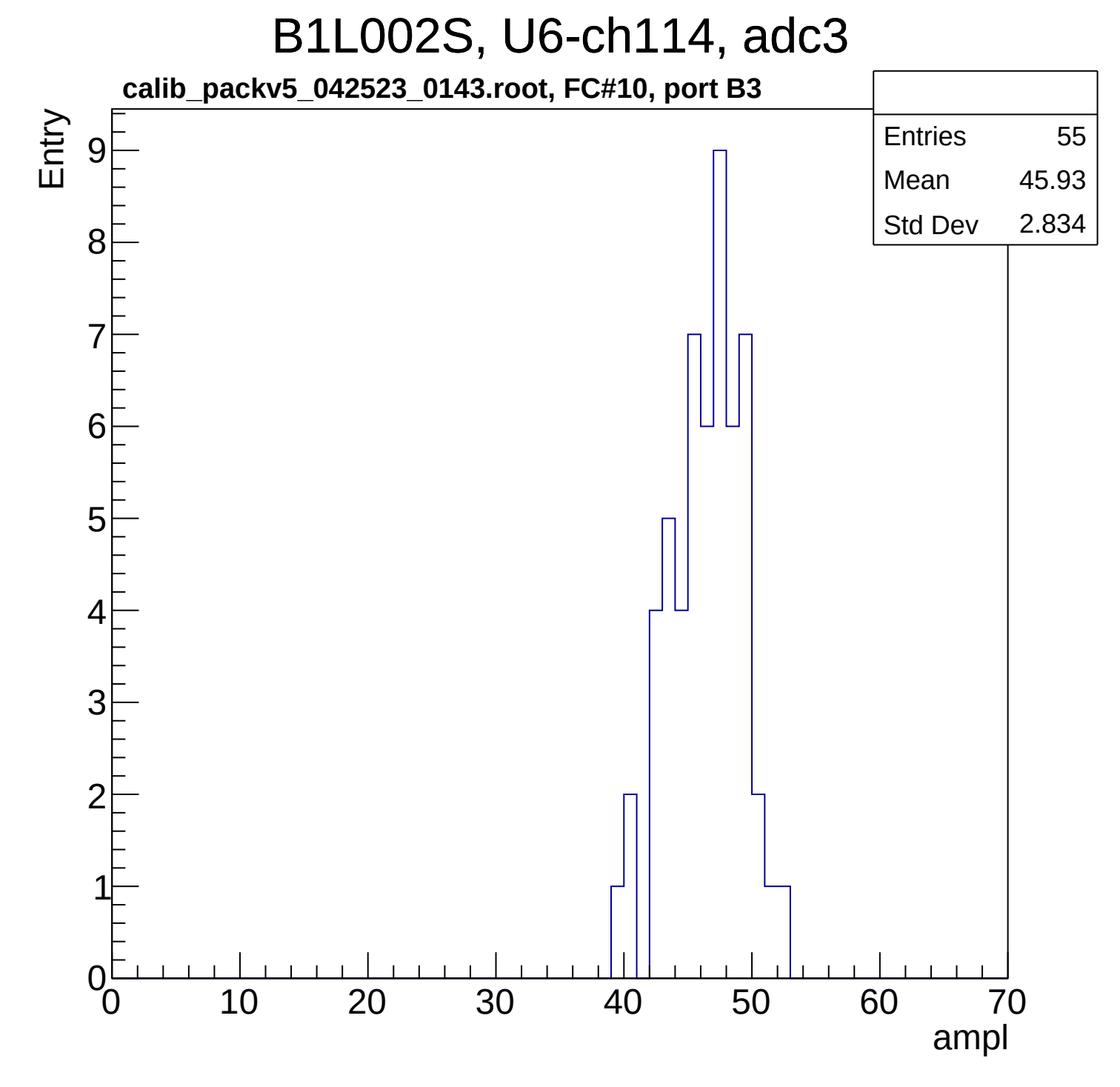

B1L002S, U6-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.71
Std Dev 1.485
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

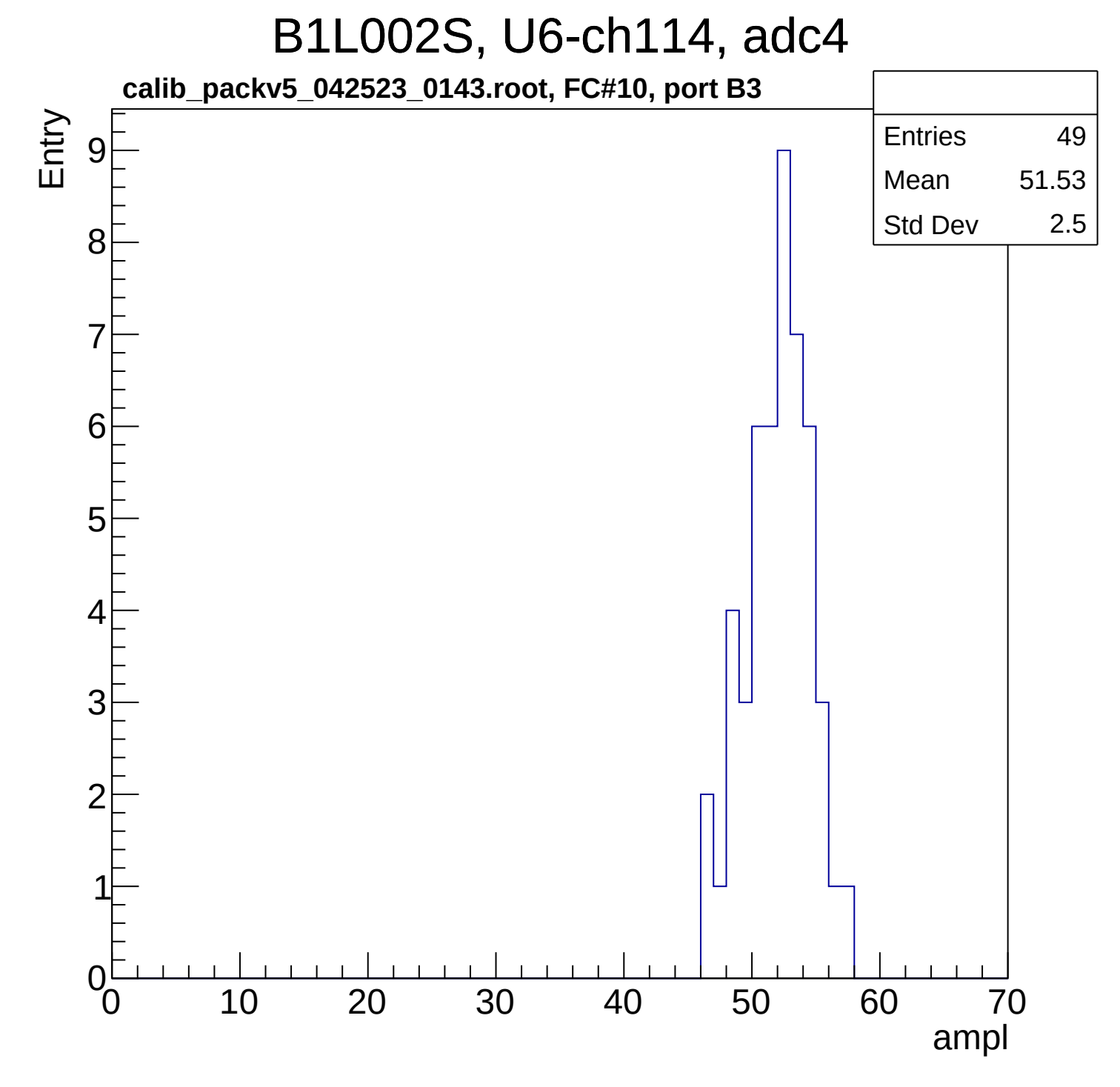

B1L002S, U6-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

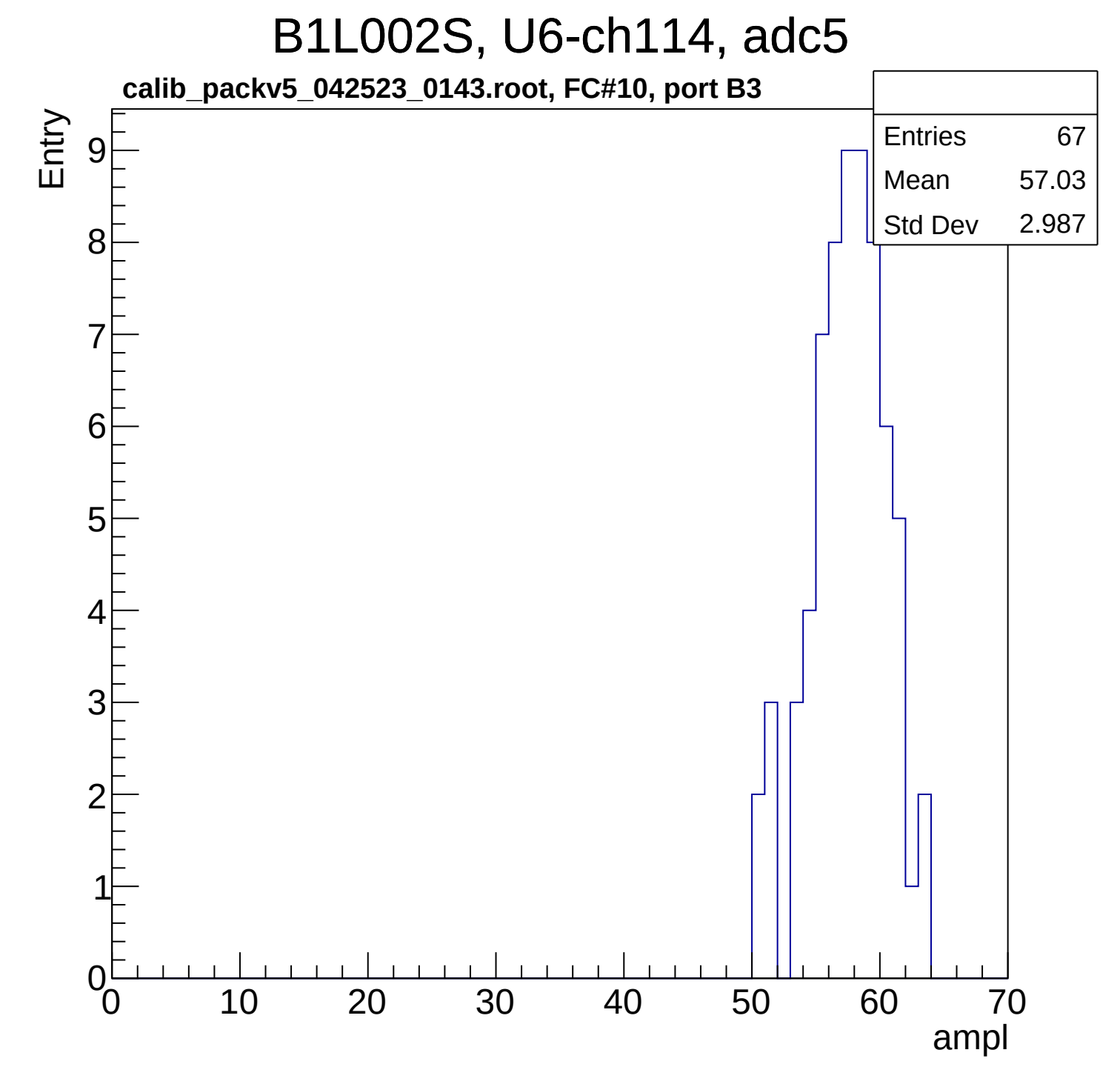

B1L002S, U6-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.87
Std Dev 3.166
Gaus mean : 27.2762
Gaus Width: 3.4245
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

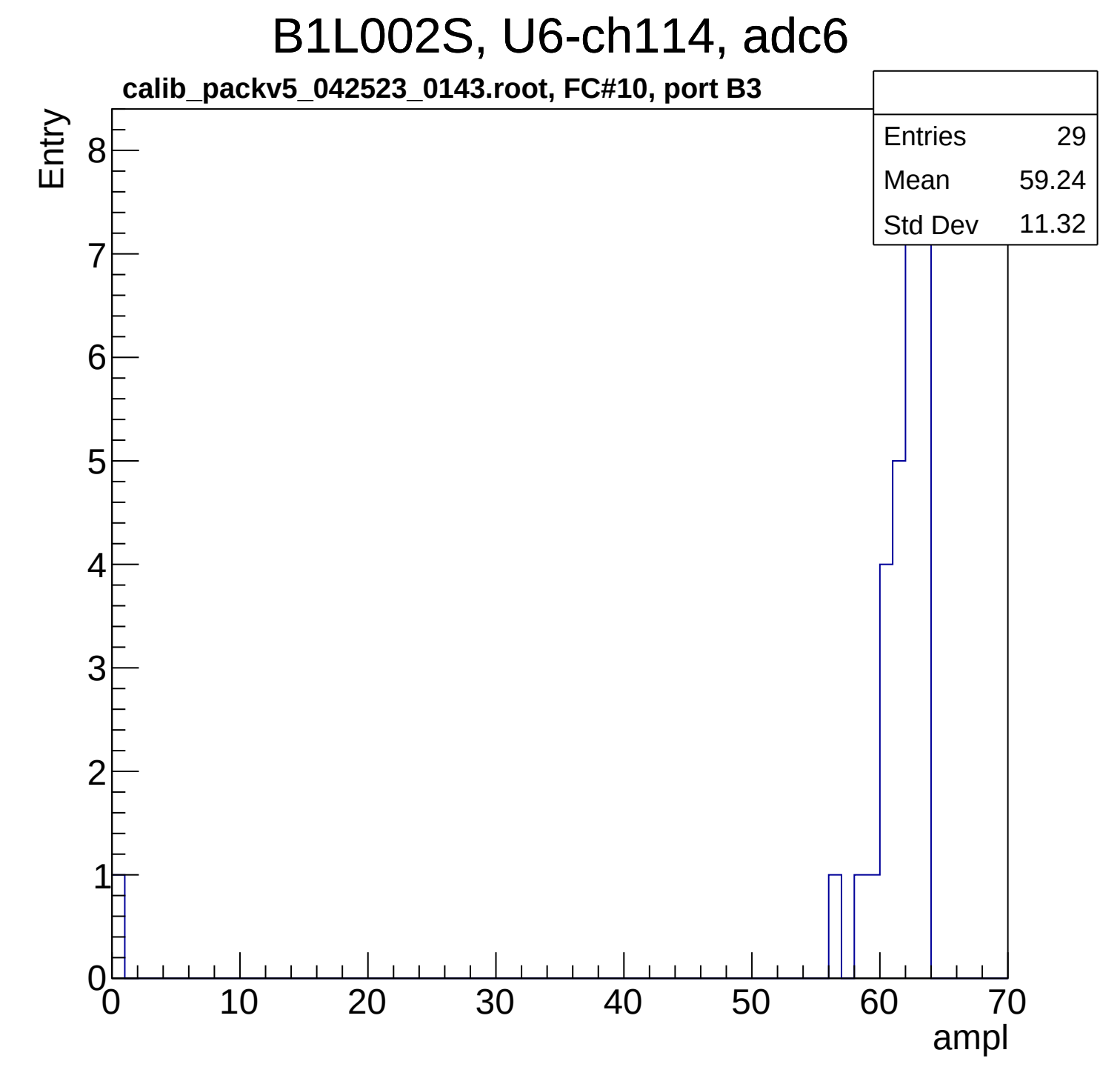

B1L002S, U6-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.25
Std Dev 3.719
Gaus mean : 35.2860
Gaus Width: 3.2475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

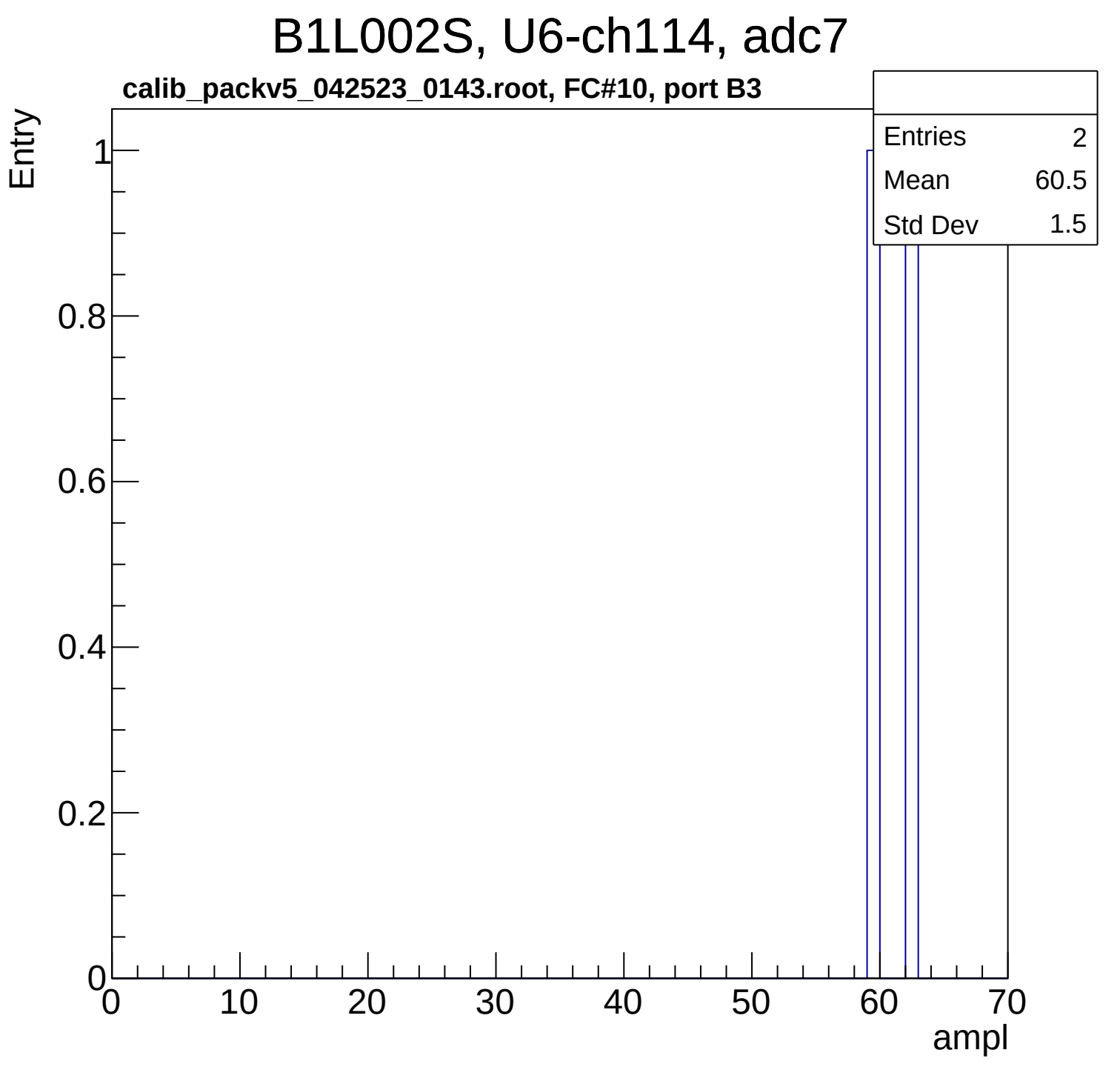

B1L002S, U6-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.71
Std Dev 2.641
Gaus mean : 41.0782
Gaus Width: 3.0298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

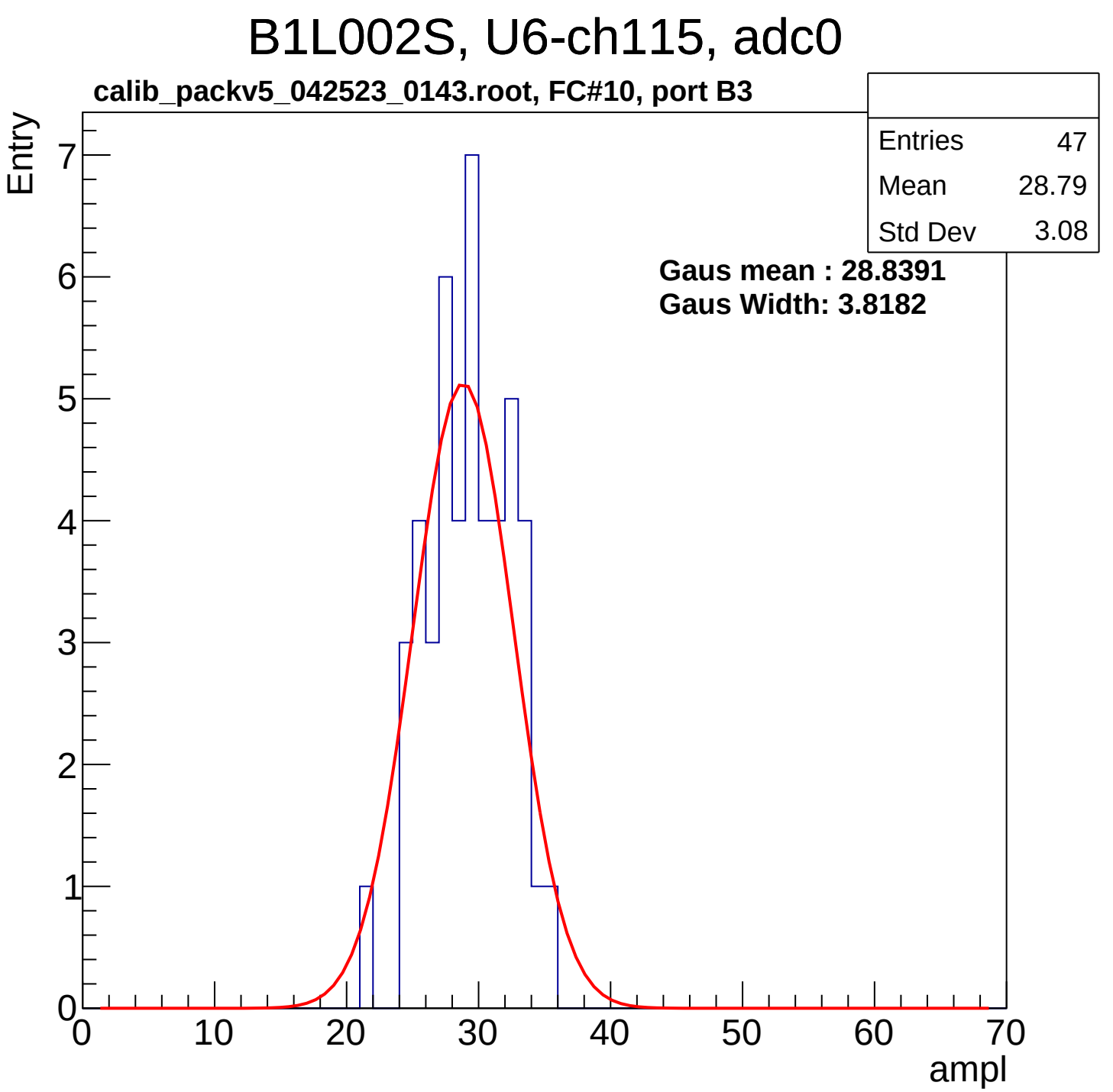

B1L002S, U6-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 45.93
Std Dev 2.834
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

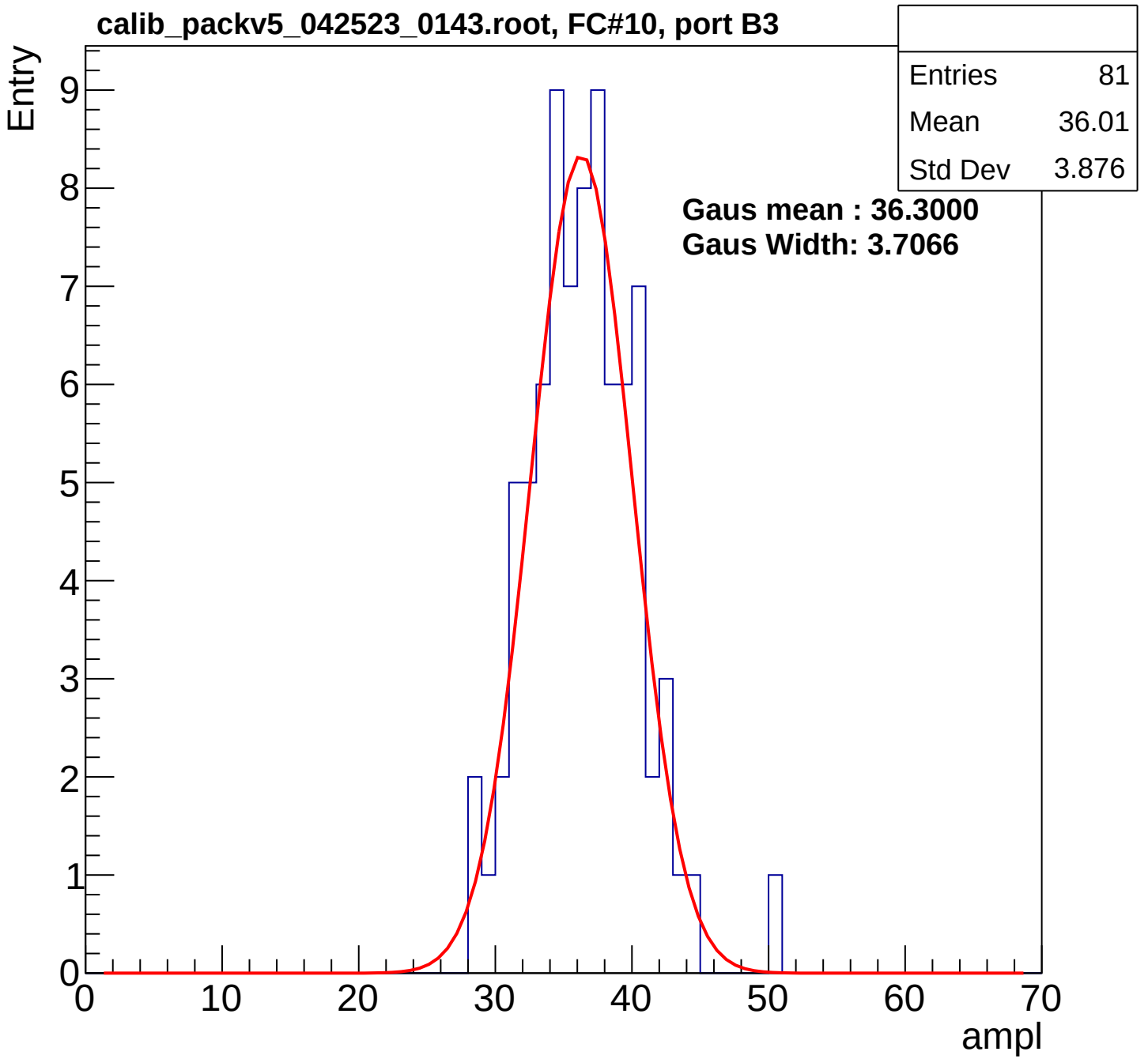

B1L002S, U6-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.53
Std Dev 2.5
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

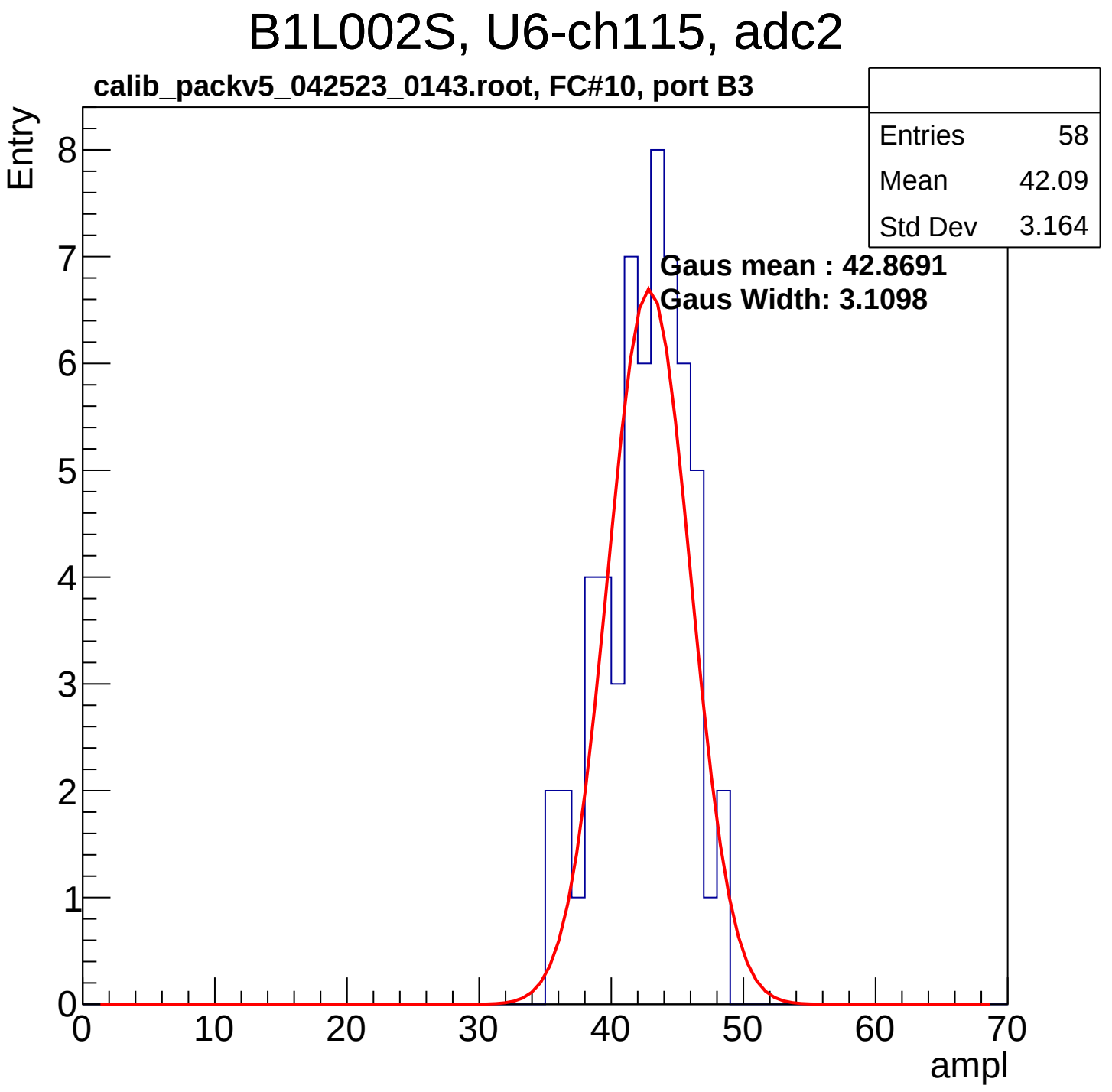

B1L002S, U6-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.03
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

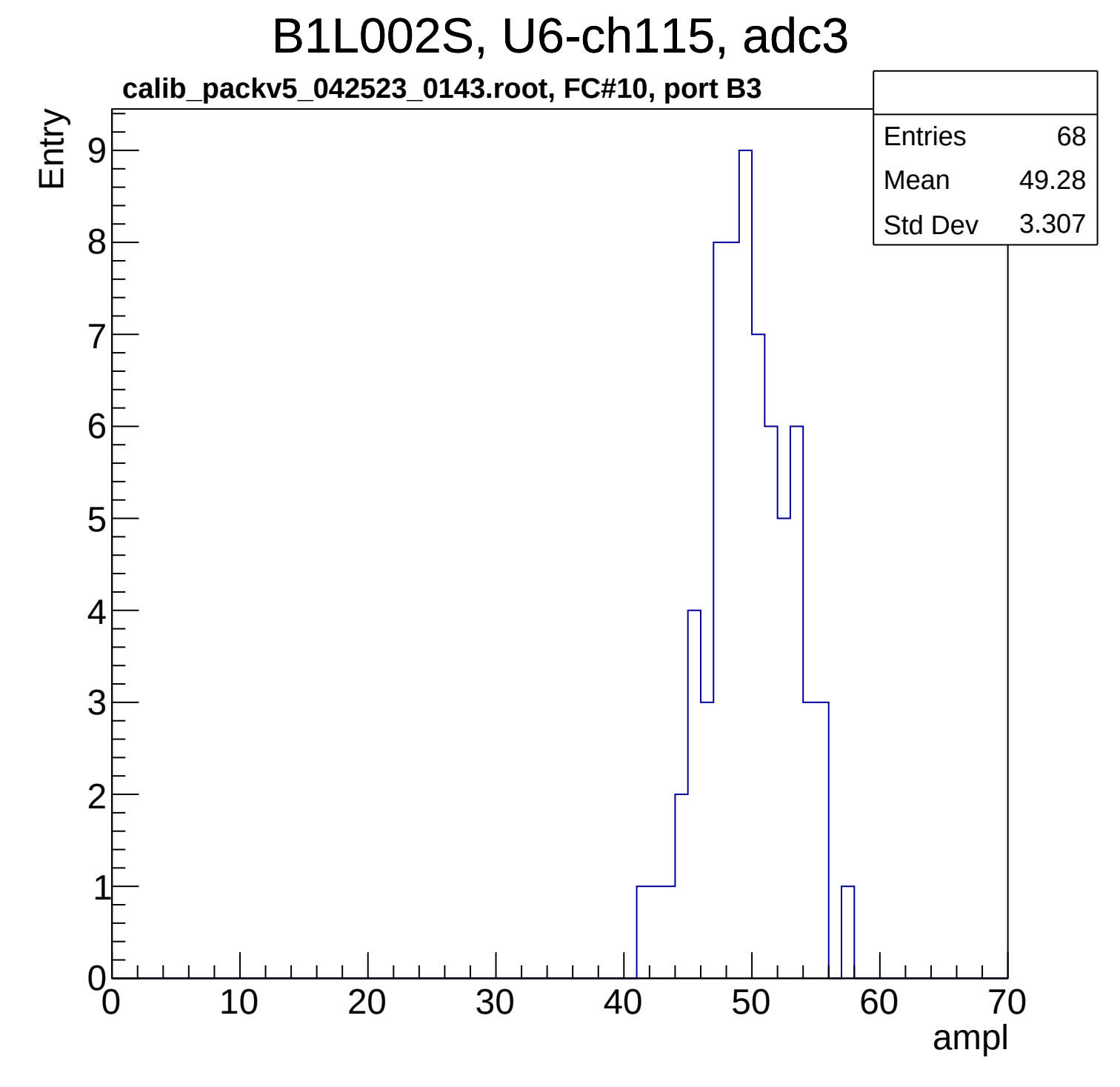

B1L002S, U6-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.24
Std Dev 11.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

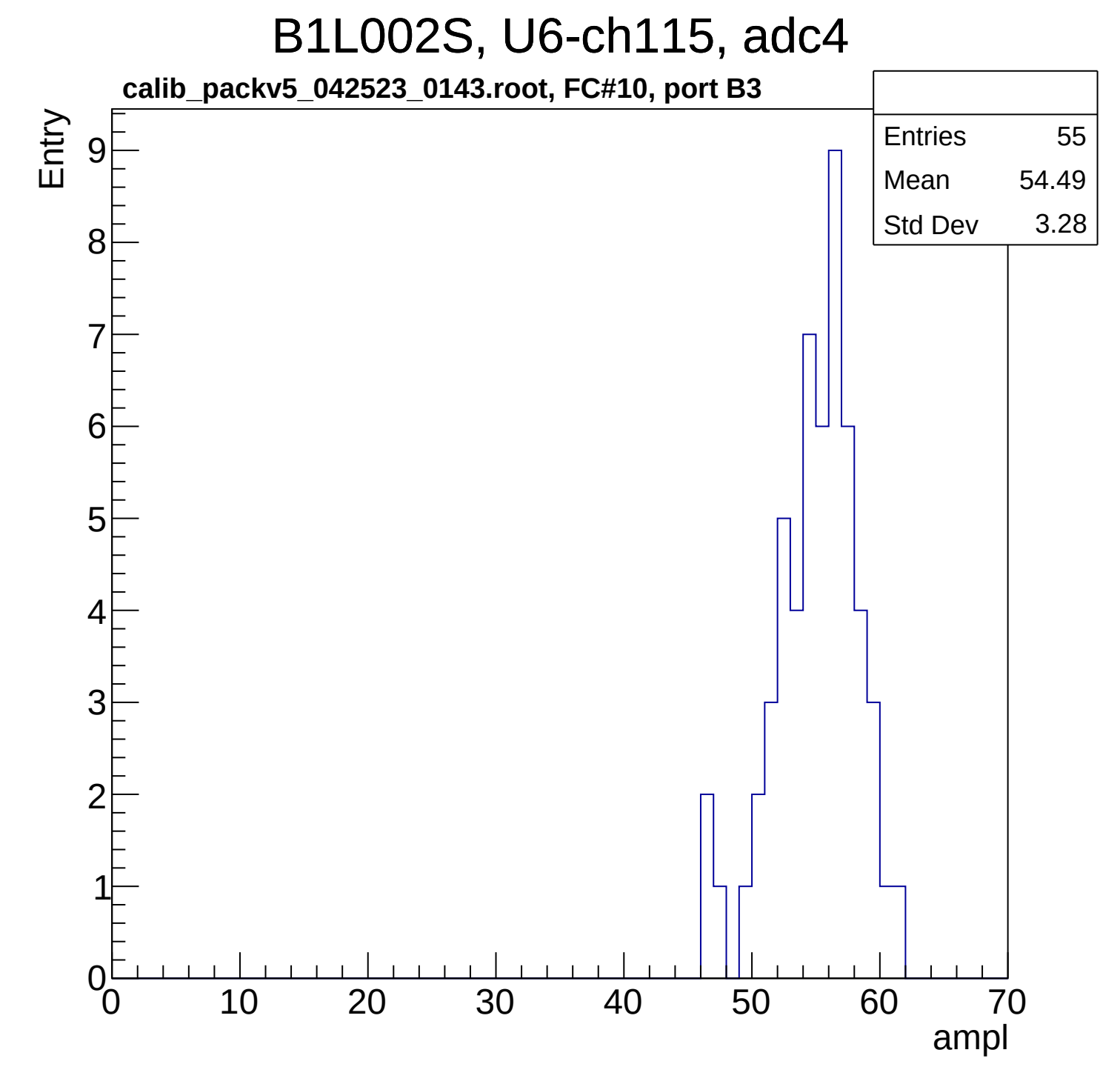

B1L002S, U6-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 60.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

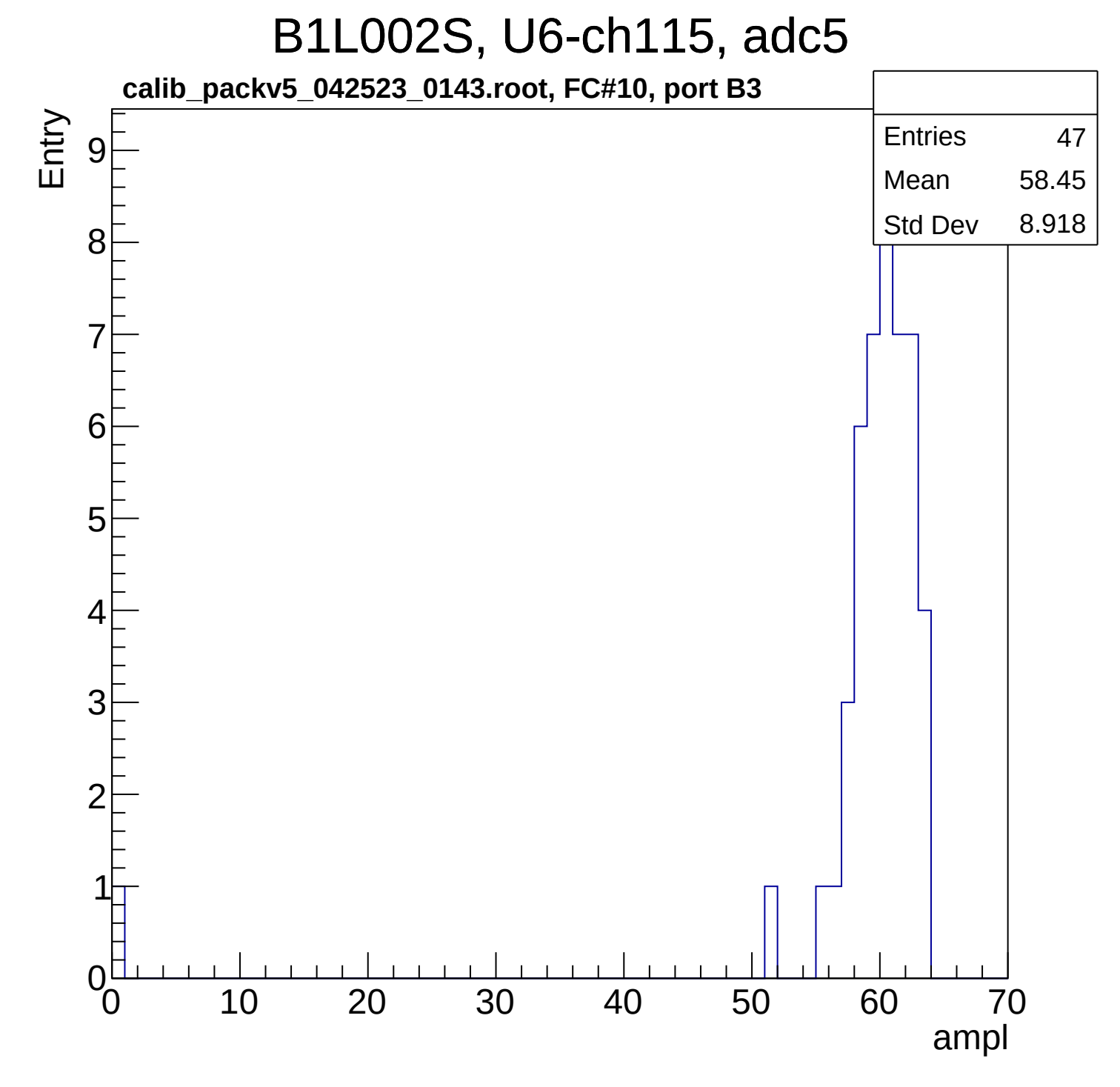

B1L002S, U6-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 28.79
Std Dev 3.08
Gaus mean : 28.8391
Gaus Width: 3.8182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

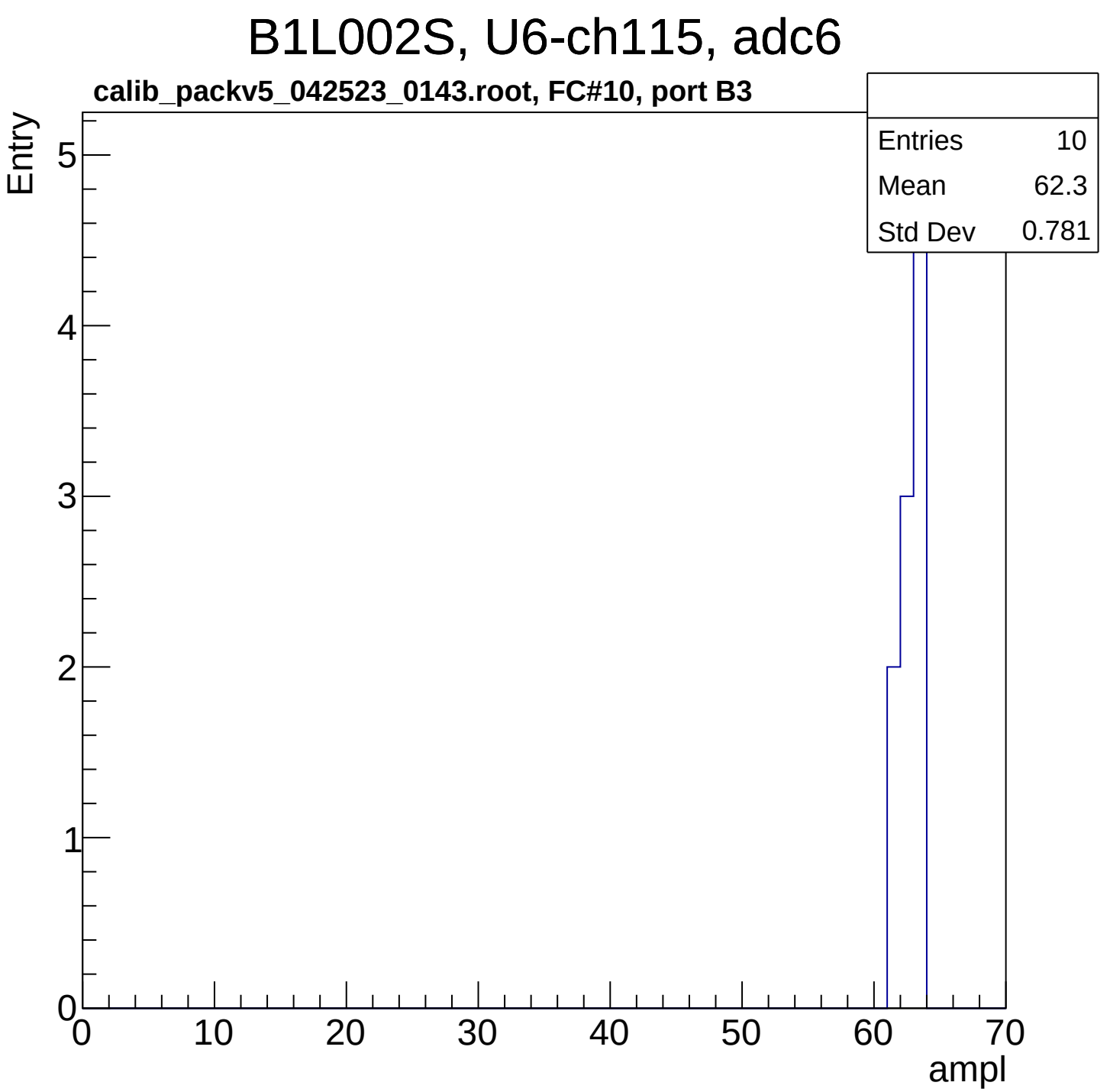

B1L002S, U6-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 36.01
Std Dev 3.876
Gaus mean : 36.3000
Gaus Width: 3.7066
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

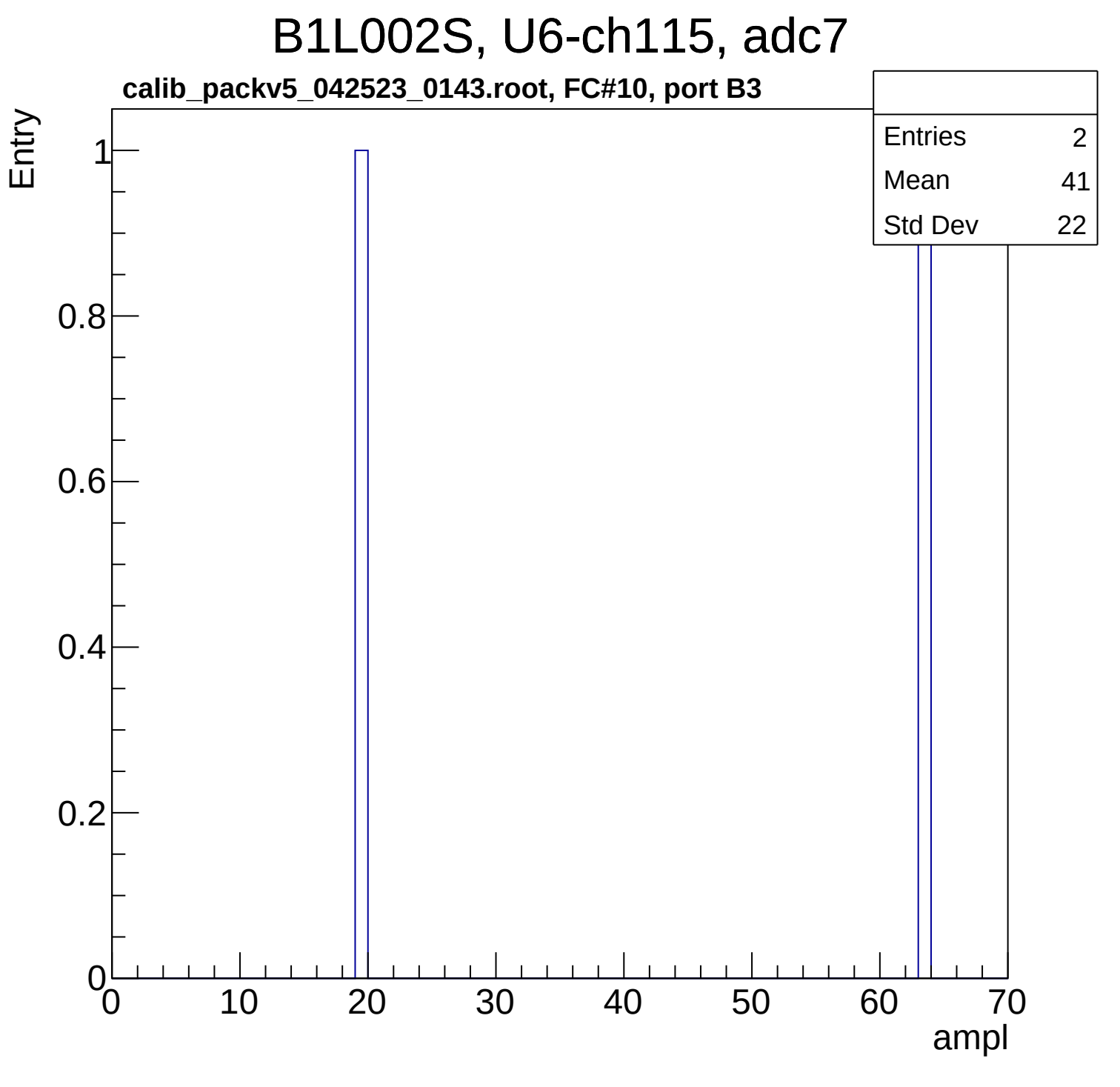

B1L002S, U6-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.09
Std Dev 3.164
Gaus mean : 42.8691
Gaus Width: 3.1098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

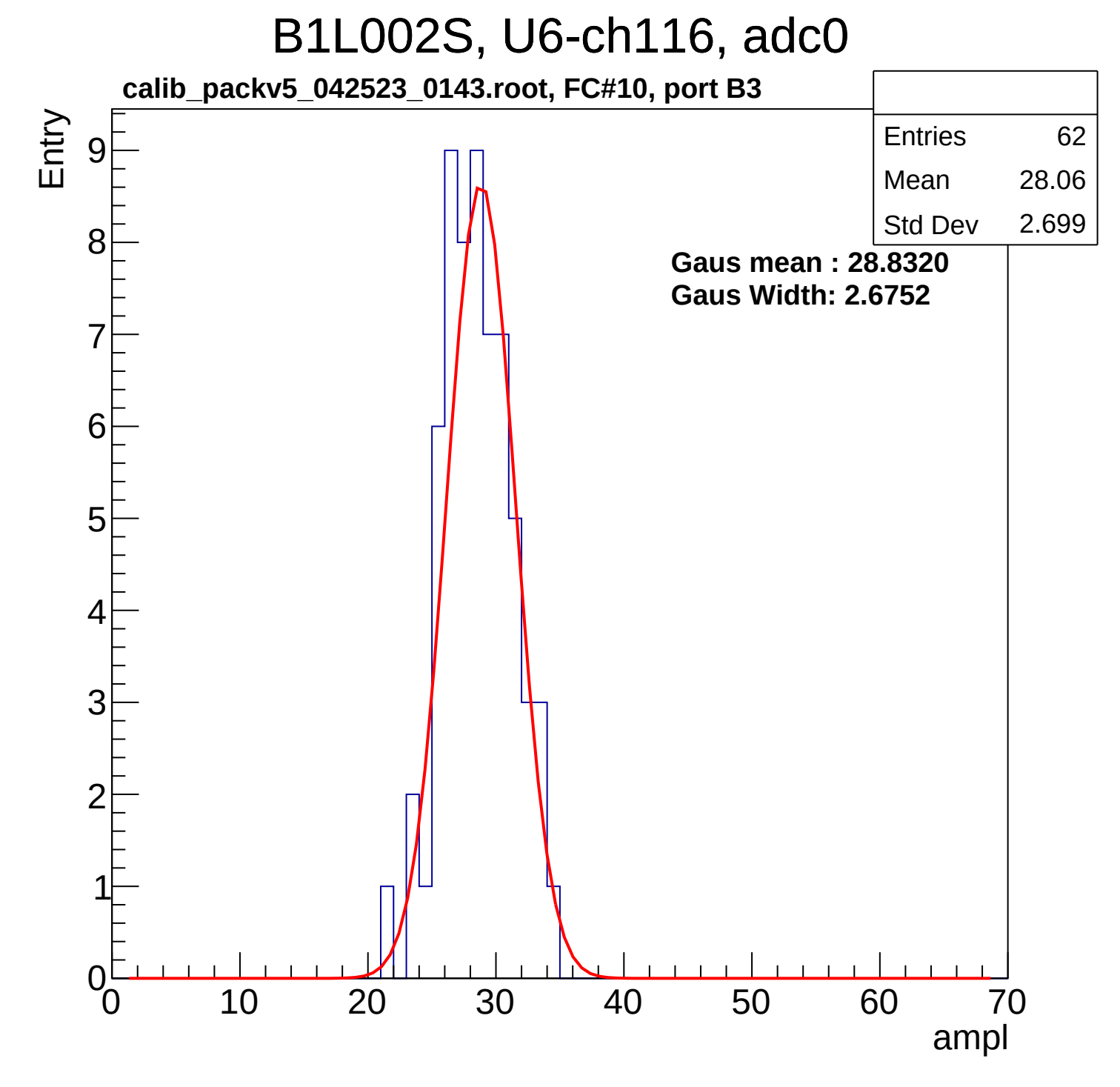

B1L002S, U6-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 49.28
Std Dev 3.307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

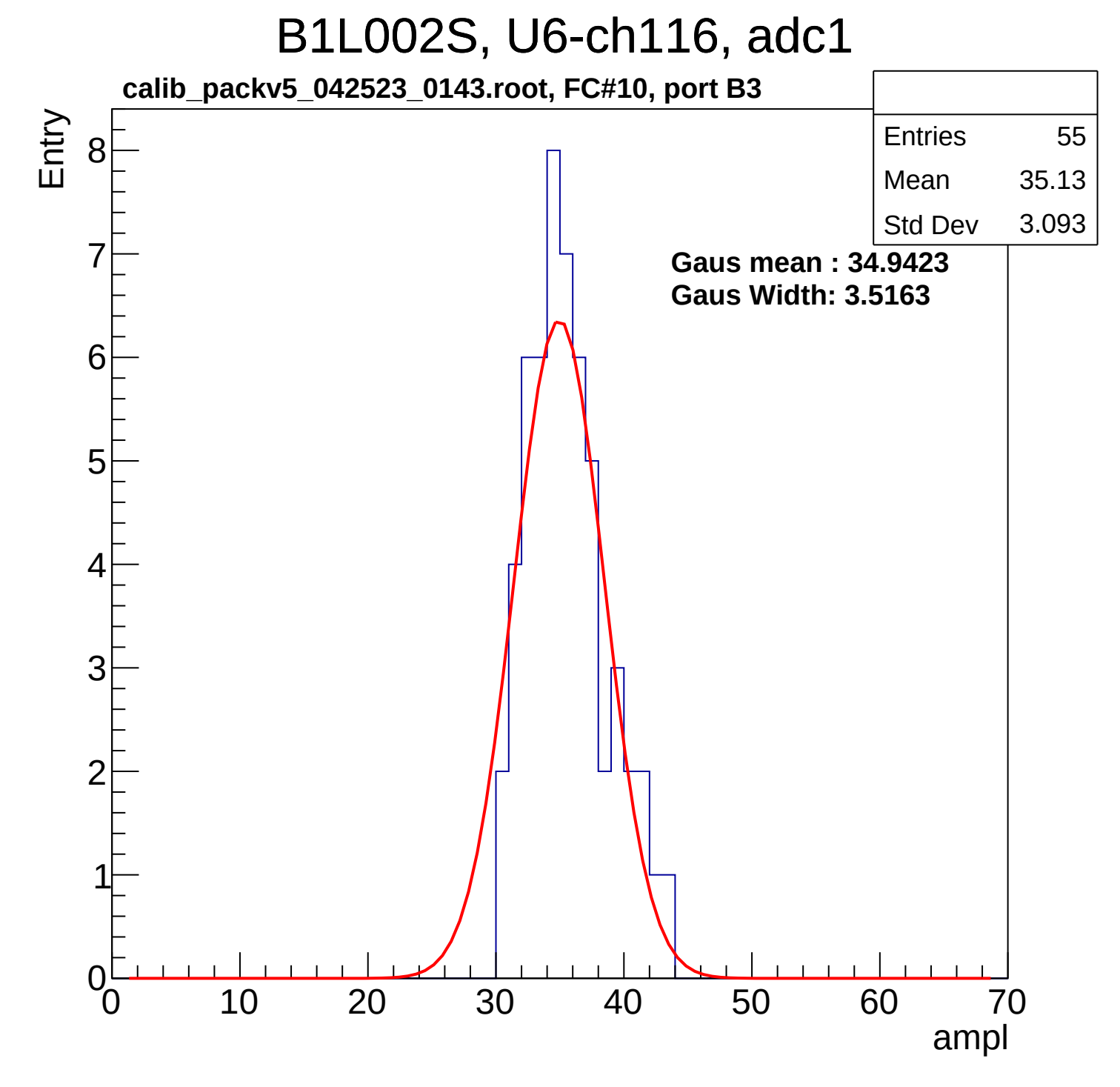

B1L002S, U6-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.49
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

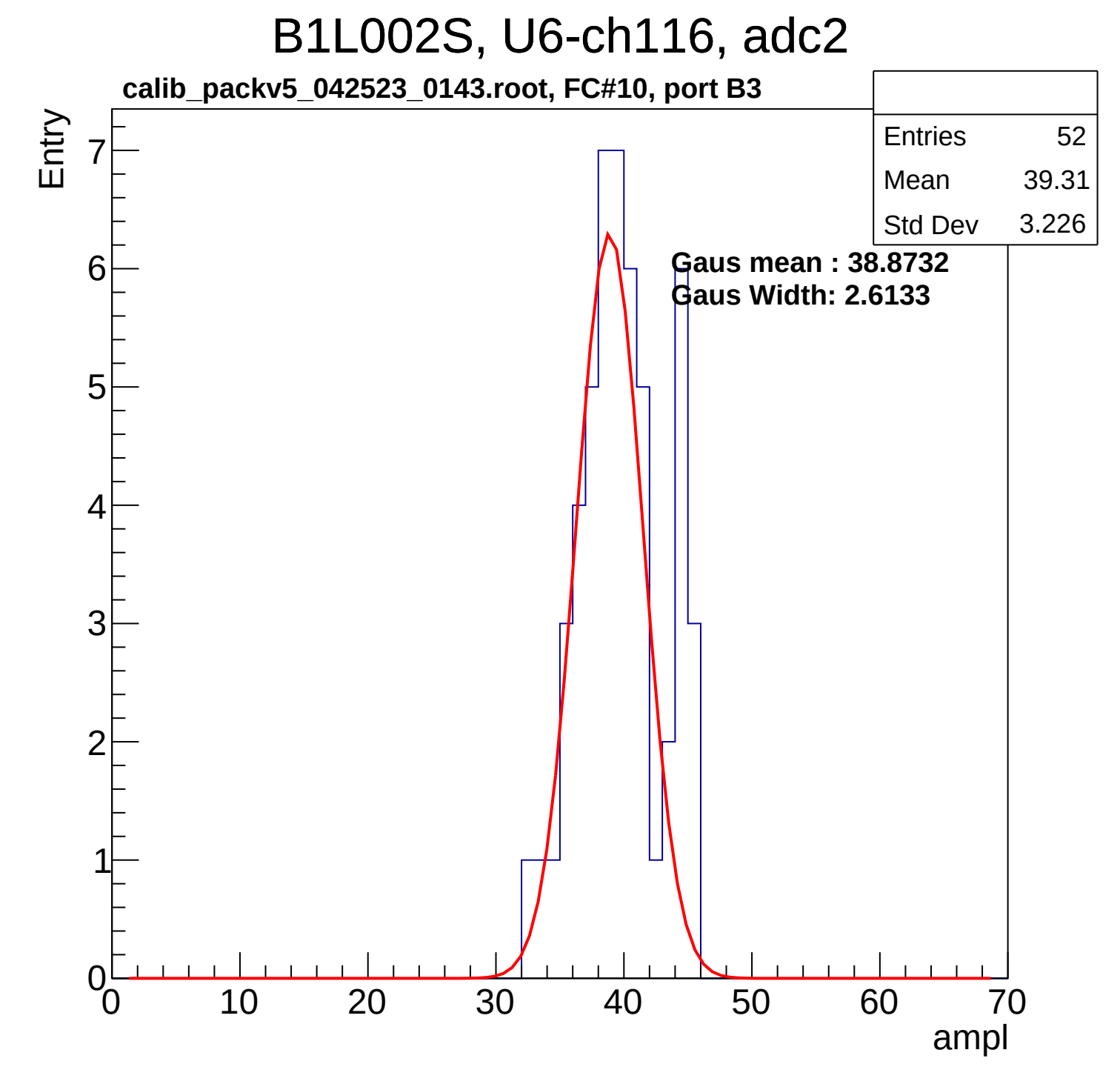

B1L002S, U6-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.45
Std Dev 8.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

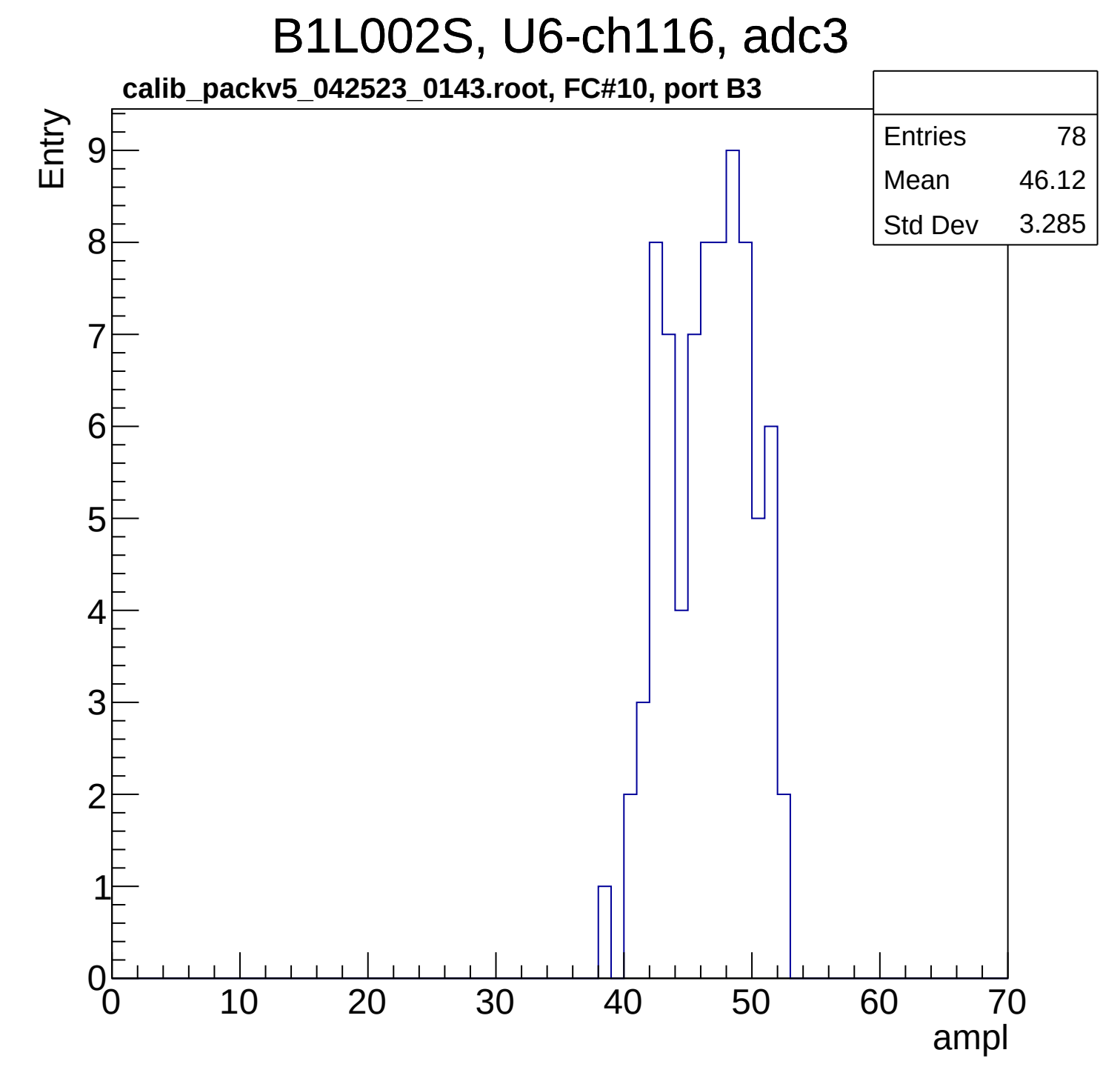

B1L002S, U6-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 62.3
Std Dev 0.781
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

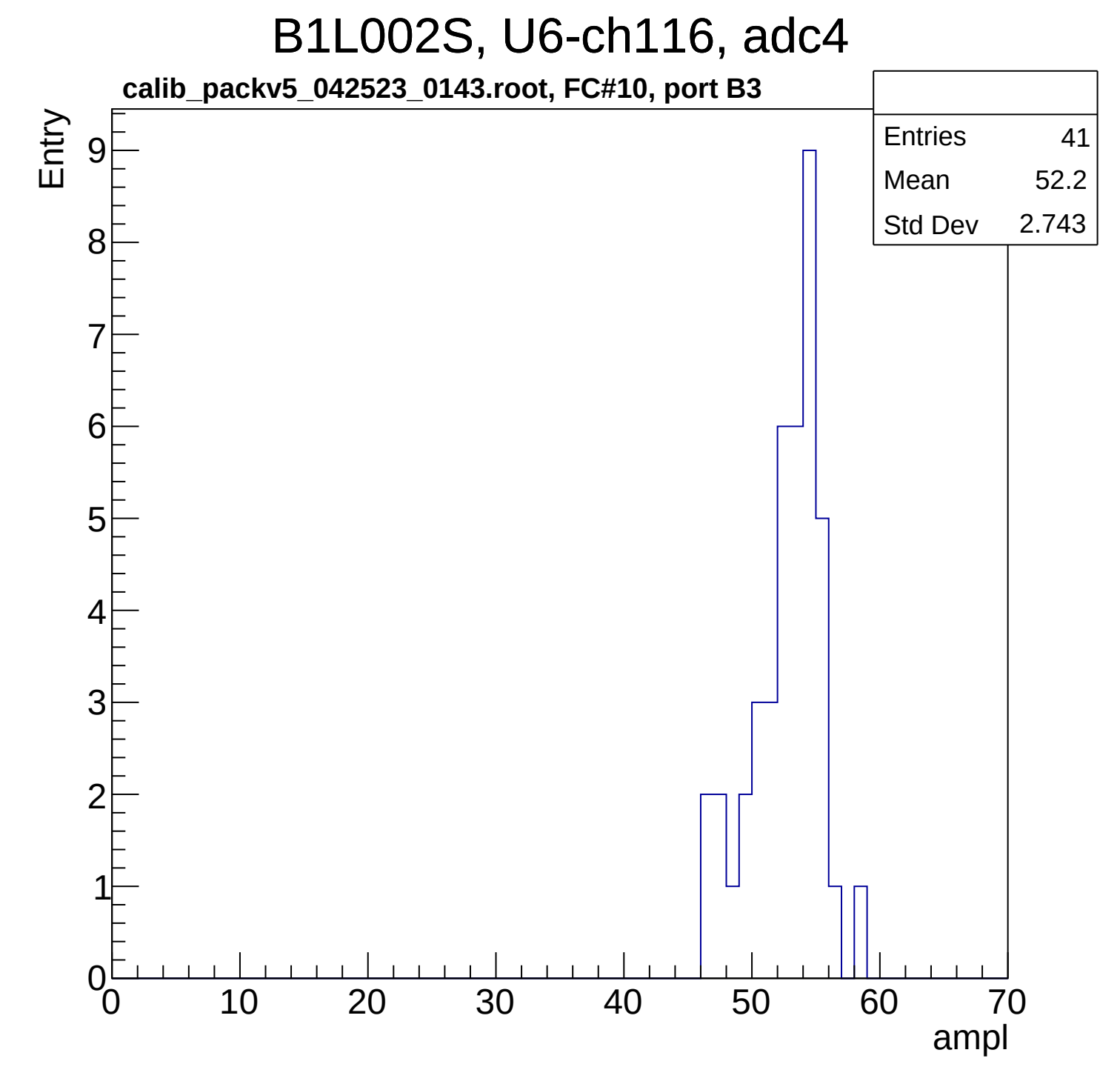

B1L002S, U6-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 22
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

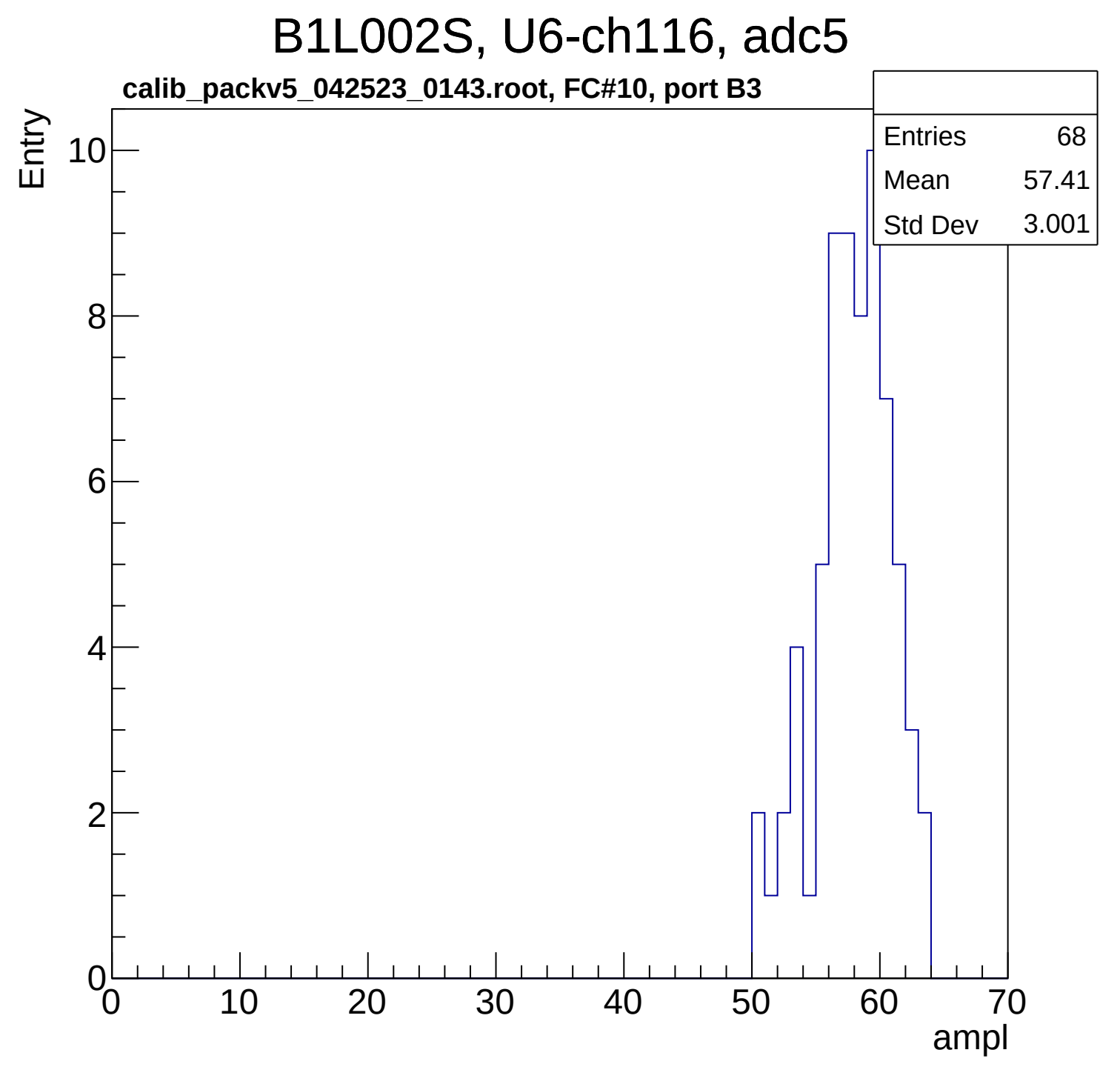

B1L002S, U6-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.06
Std Dev 2.699
Gaus mean : 28.8320
Gaus Width: 2.6752
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

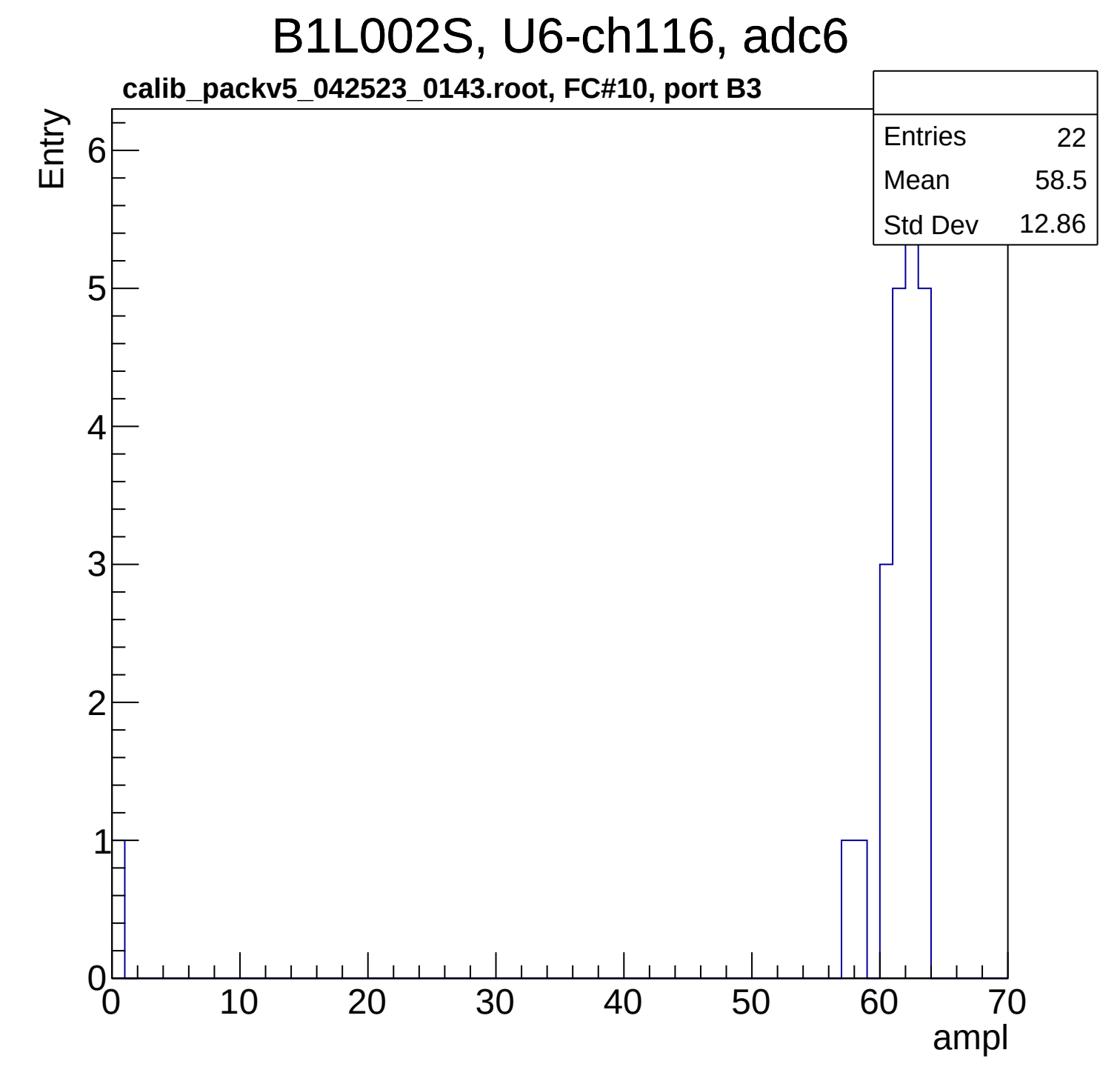

B1L002S, U6-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.13
Std Dev 3.093
Gaus mean : 34.9423
Gaus Width: 3.5163
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

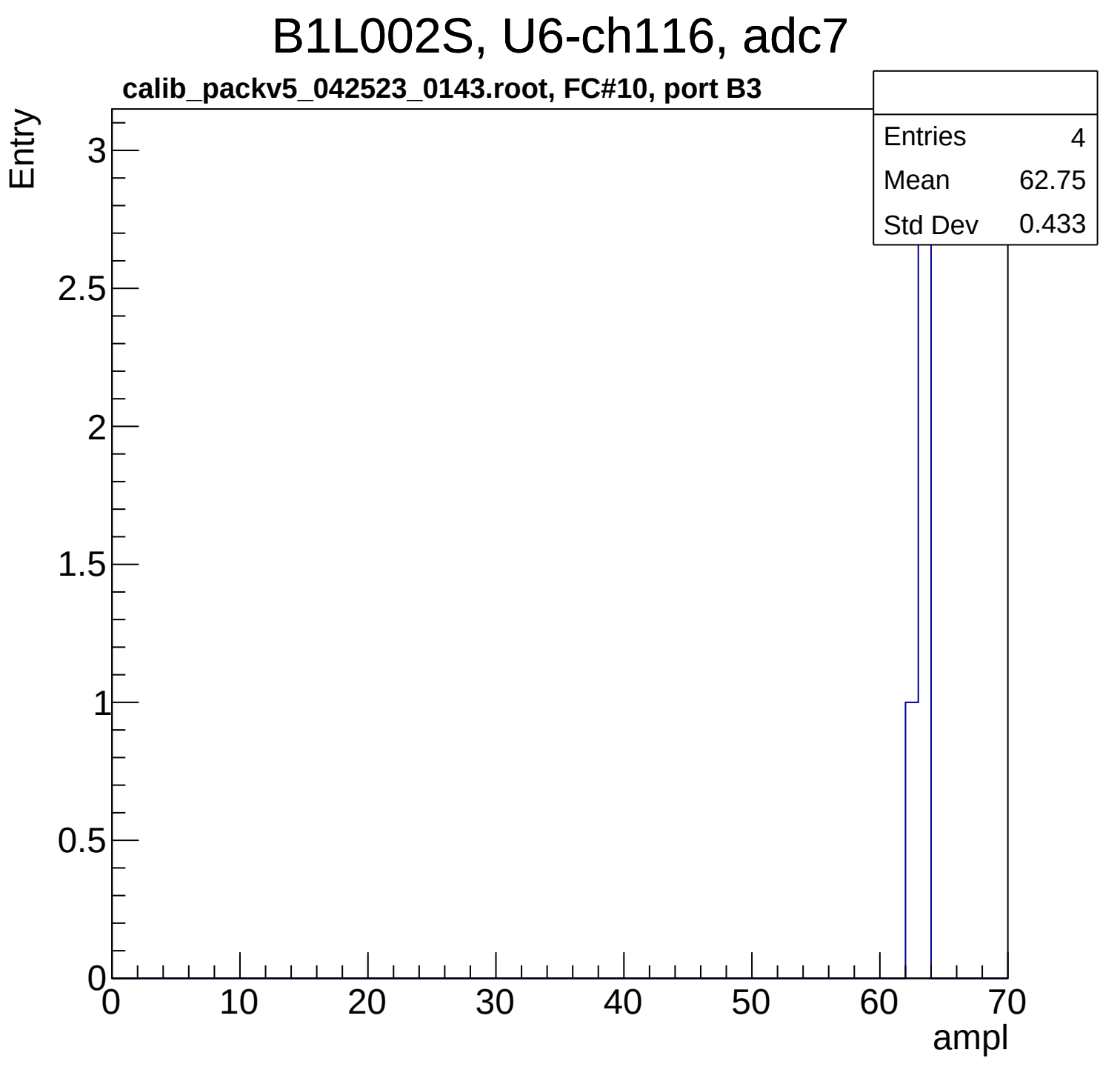

B1L002S, U6-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 39.31
Std Dev 3.226
Gaus mean : 38.8732
Gaus Width: 2.6133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

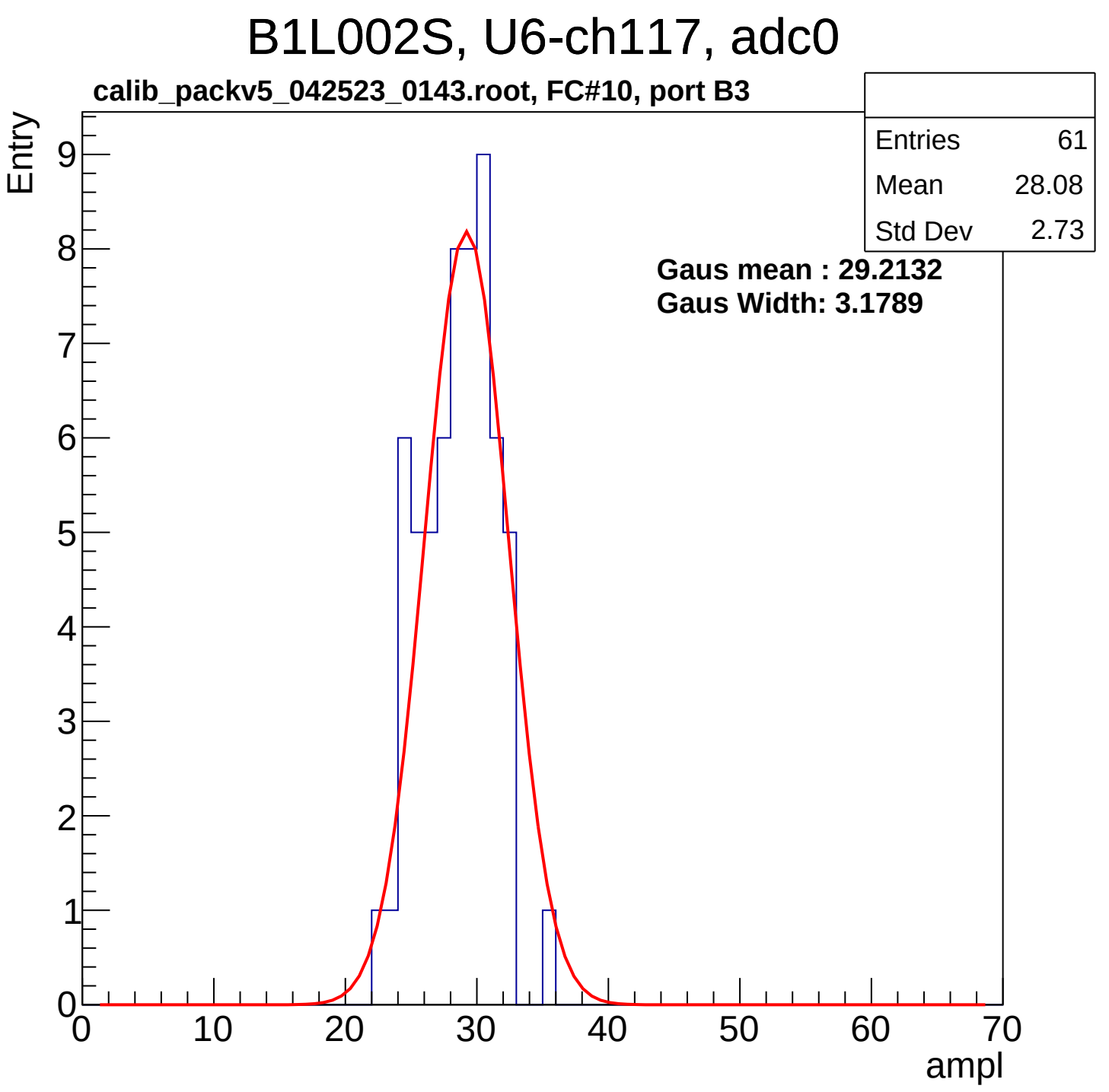

B1L002S, U6-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 46.12
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

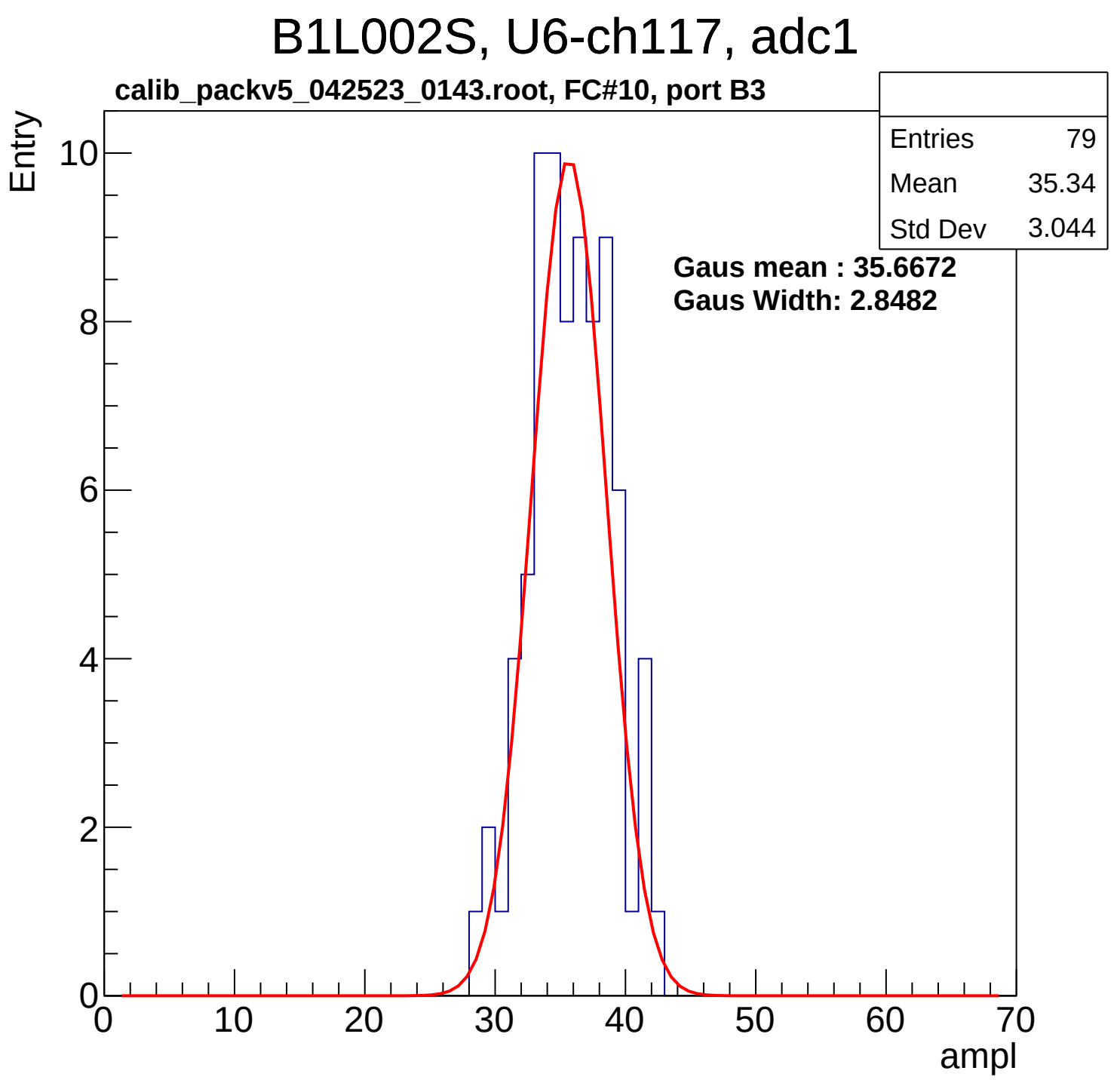

B1L002S, U6-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 52.2
Std Dev 2.743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

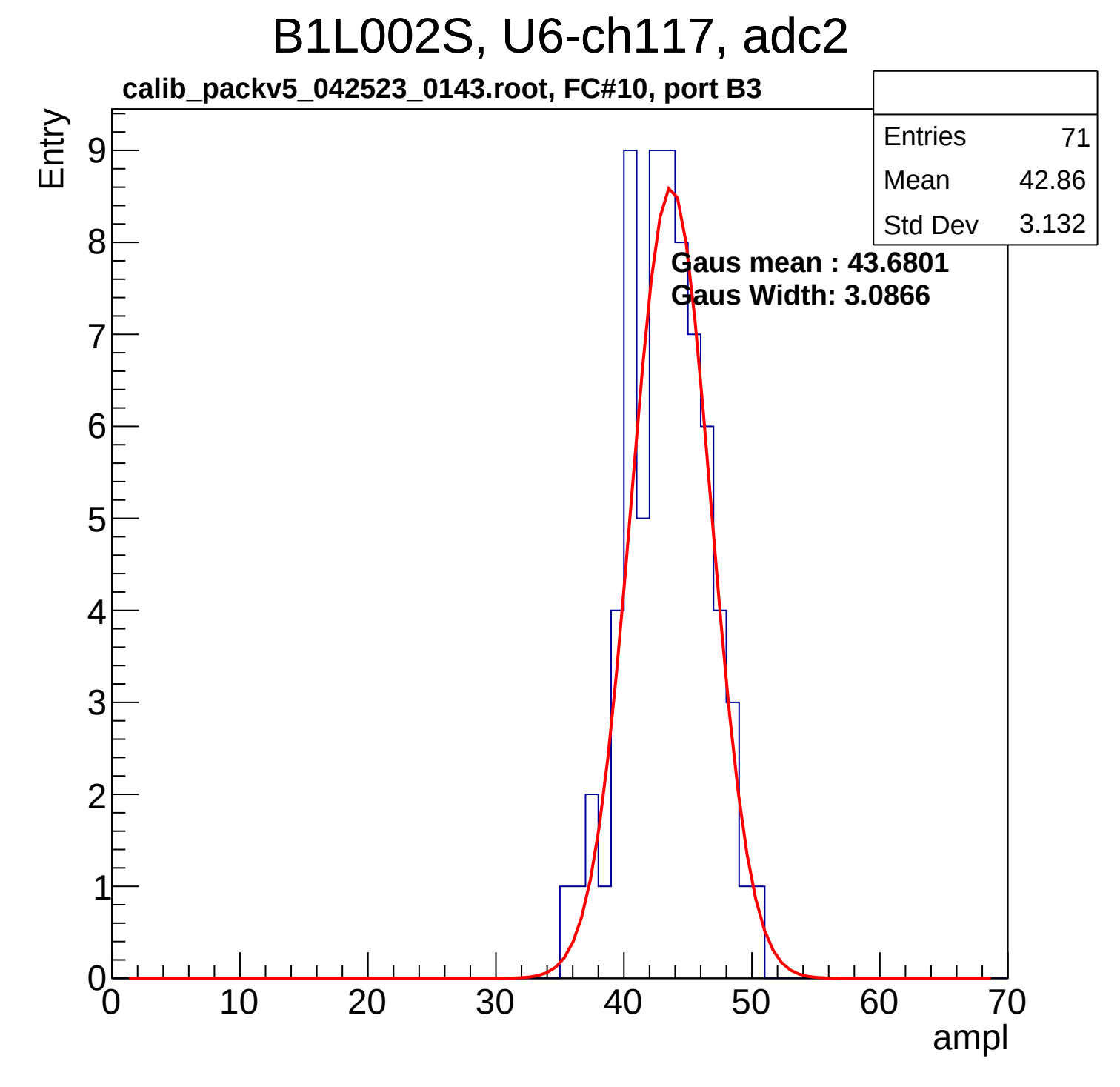

B1L002S, U6-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.41
Std Dev 3.001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

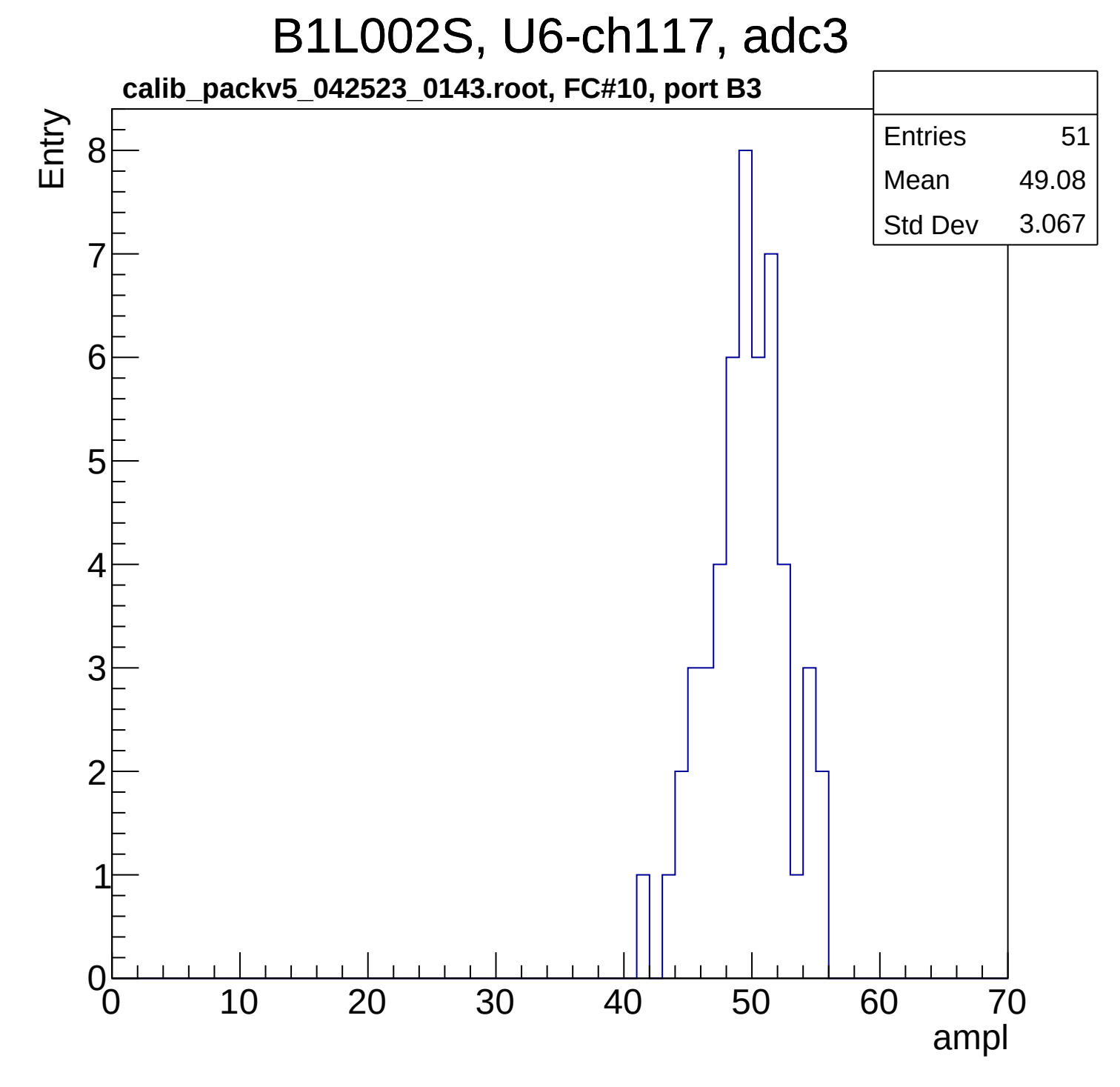

B1L002S, U6-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.5
Std Dev 12.86
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

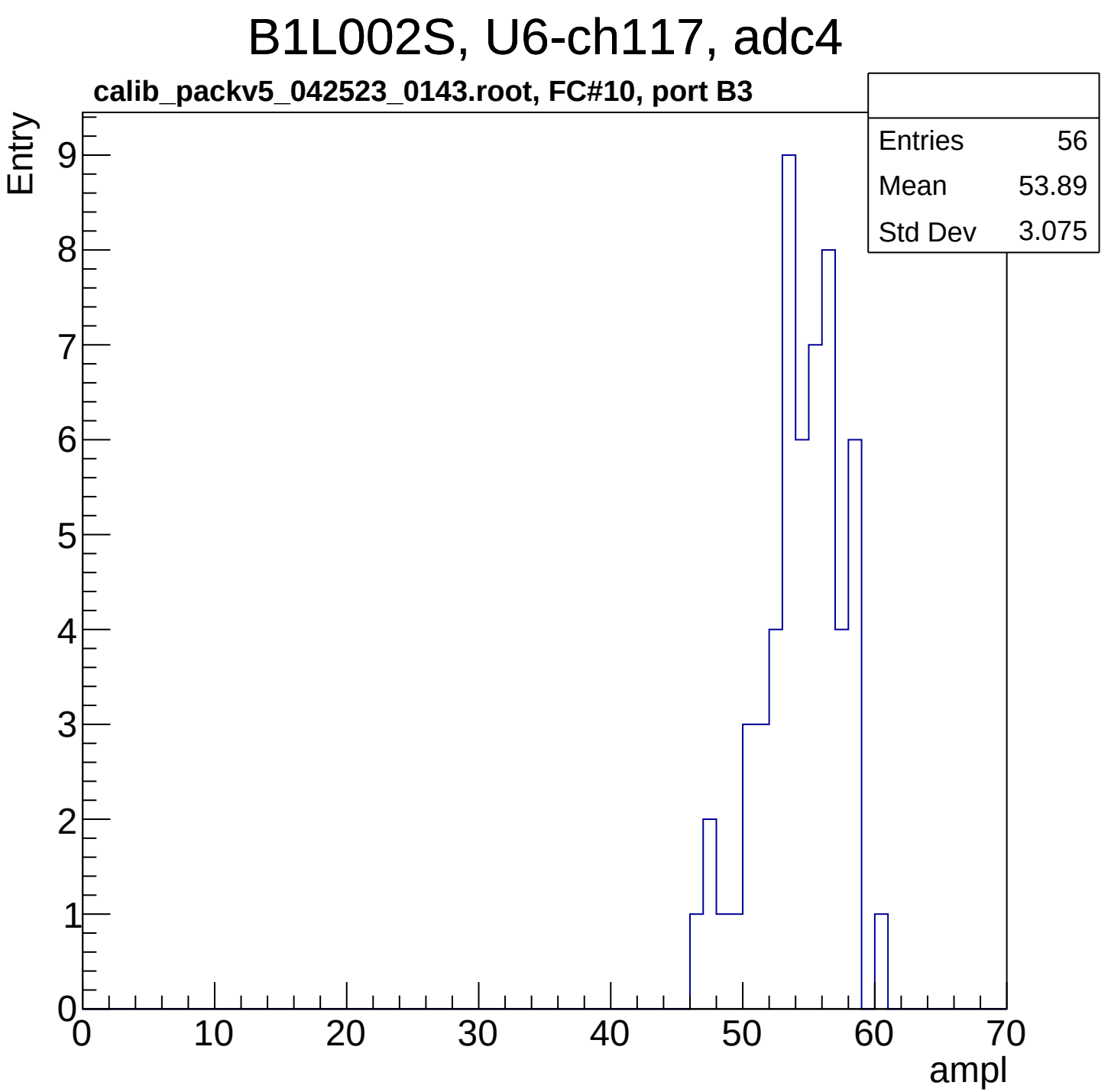

B1L002S, U6-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

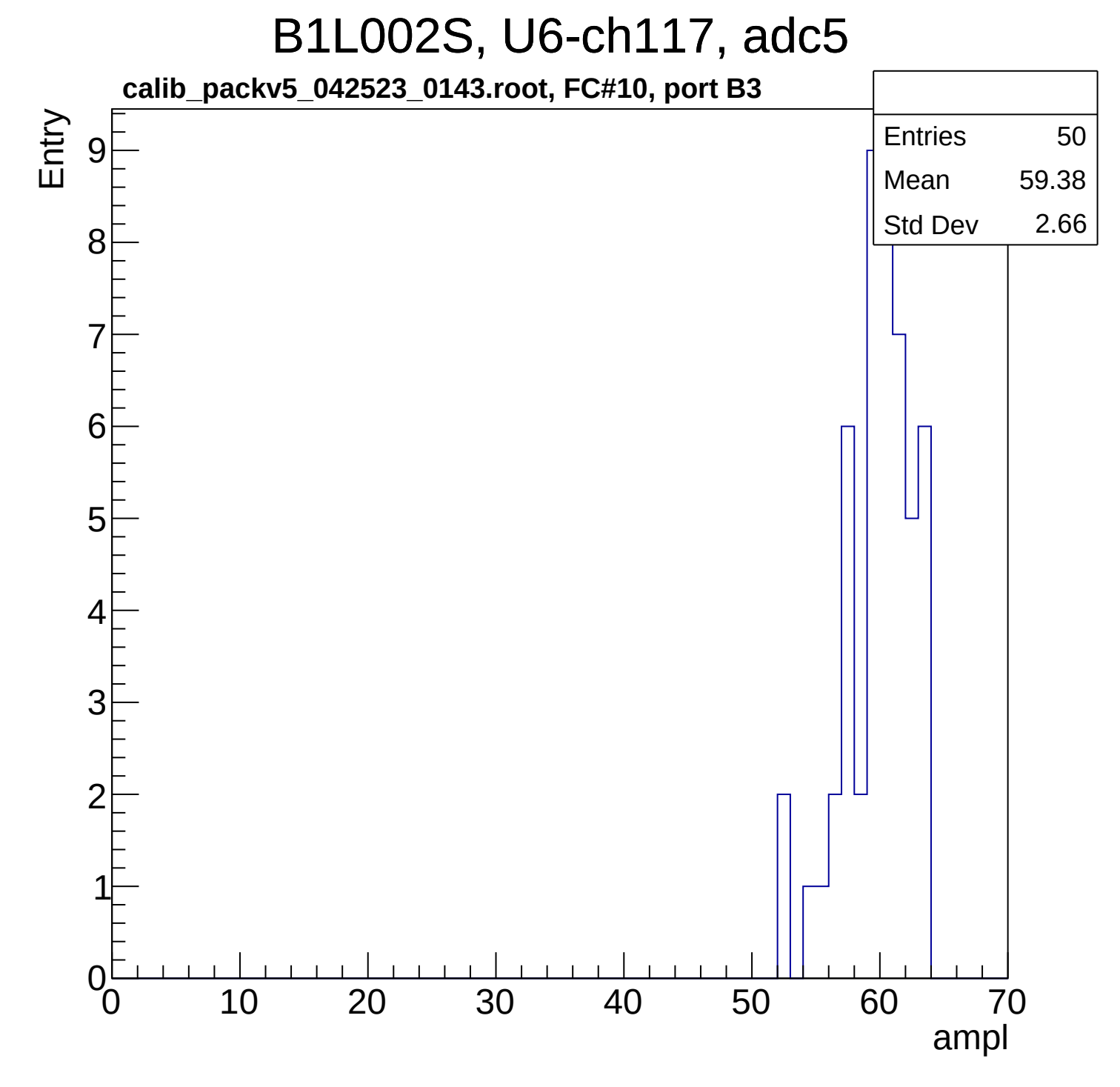

B1L002S, U6-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.08
Std Dev 2.73
Gaus mean : 29.2132
Gaus Width: 3.1789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

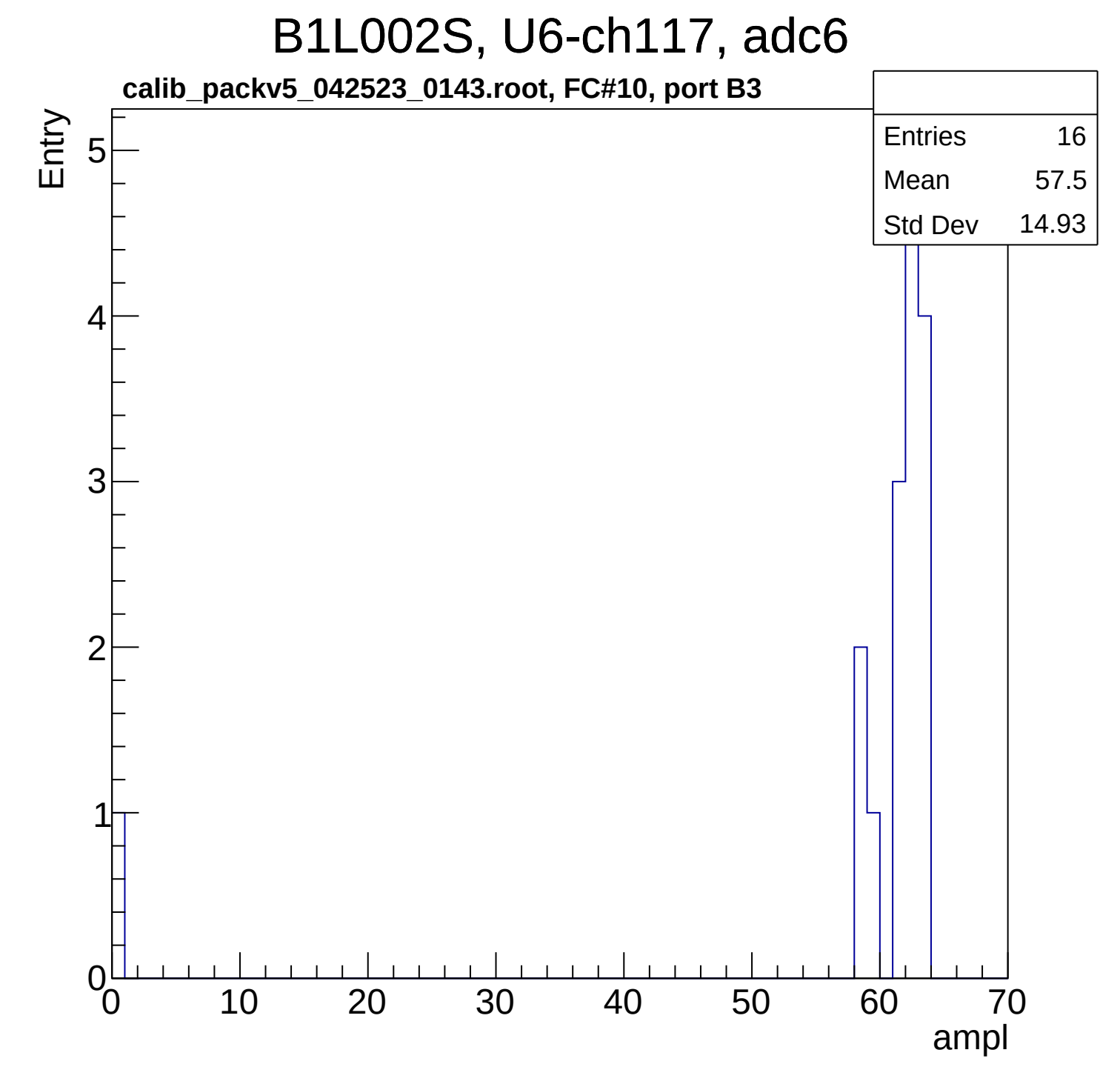

B1L002S, U6-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.34
Std Dev 3.044
Gaus mean : 35.6672
Gaus Width: 2.8482
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

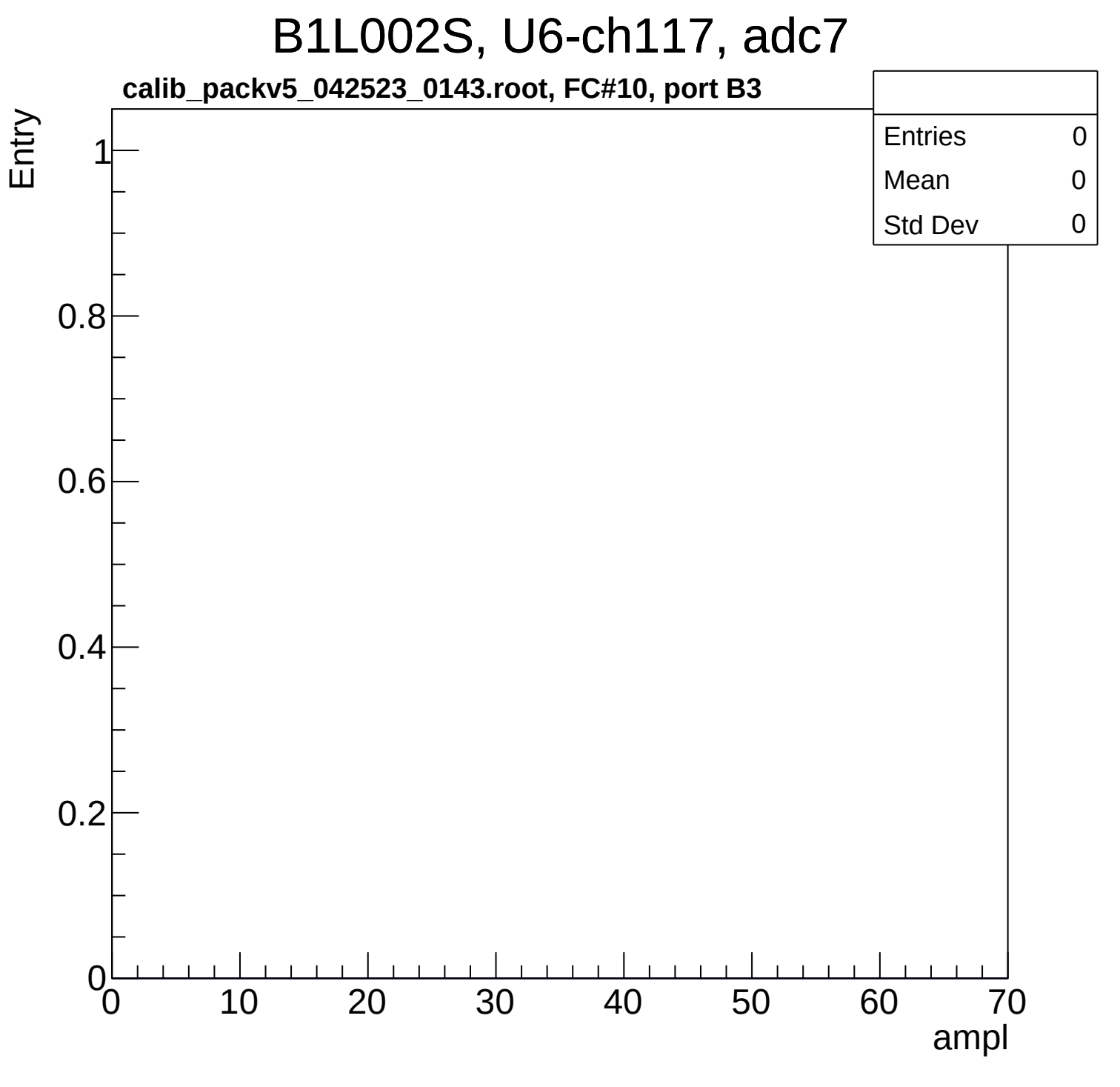

B1L002S, U6-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 42.86
Std Dev 3.132
Gaus mean : 43.6801
Gaus Width: 3.0866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

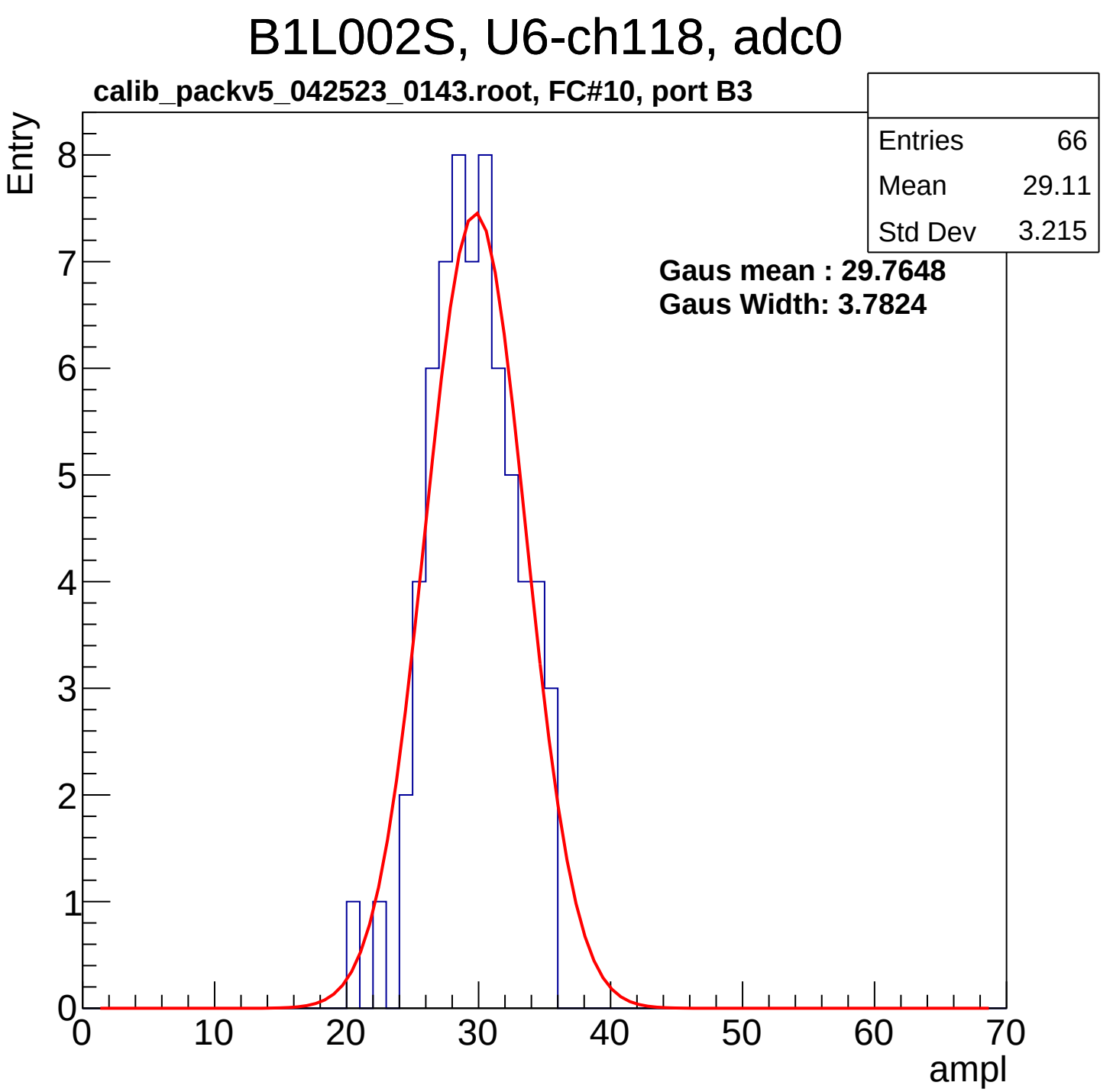

B1L002S, U6-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 49.08
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

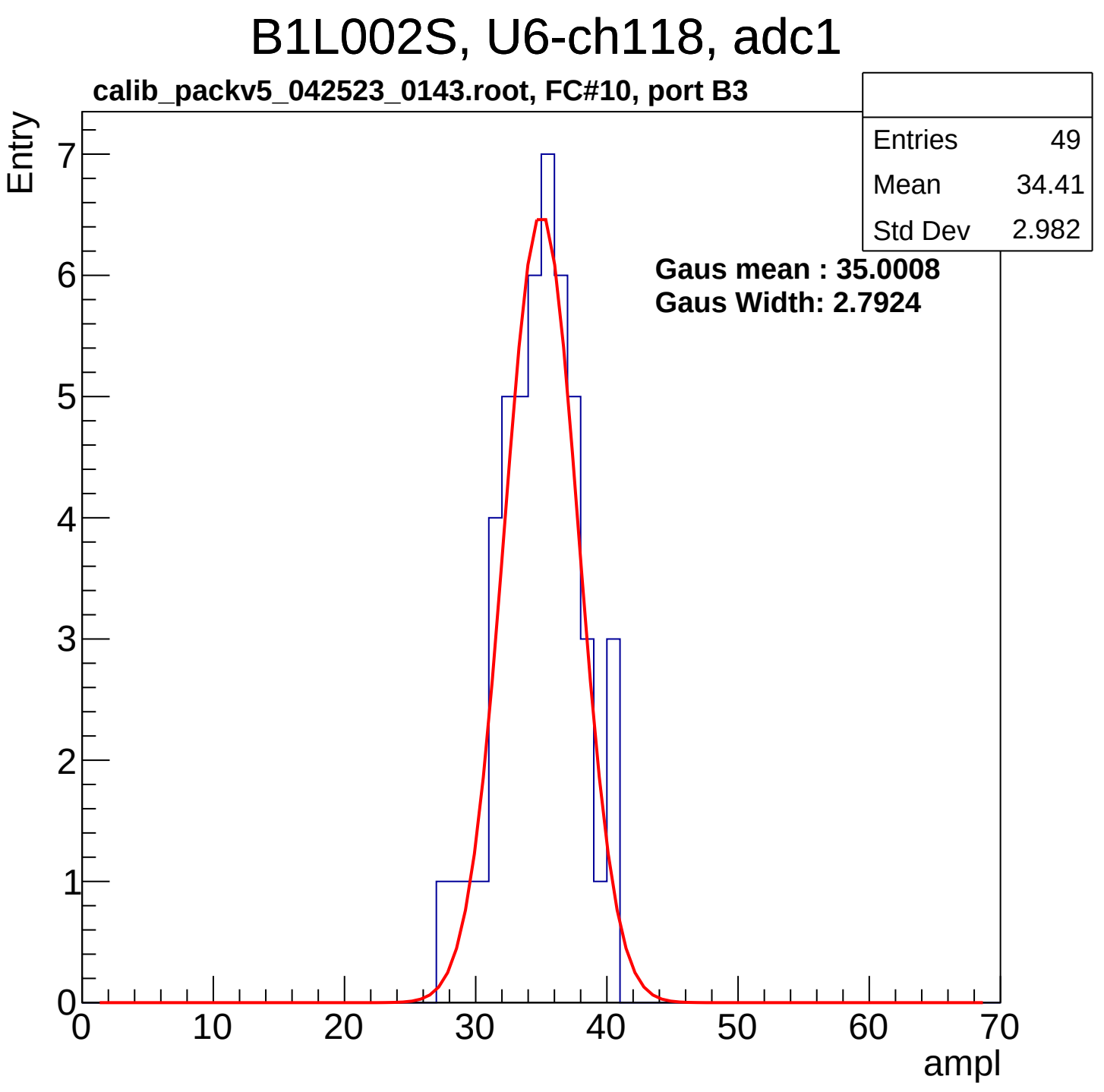

B1L002S, U6-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.89
Std Dev 3.075
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

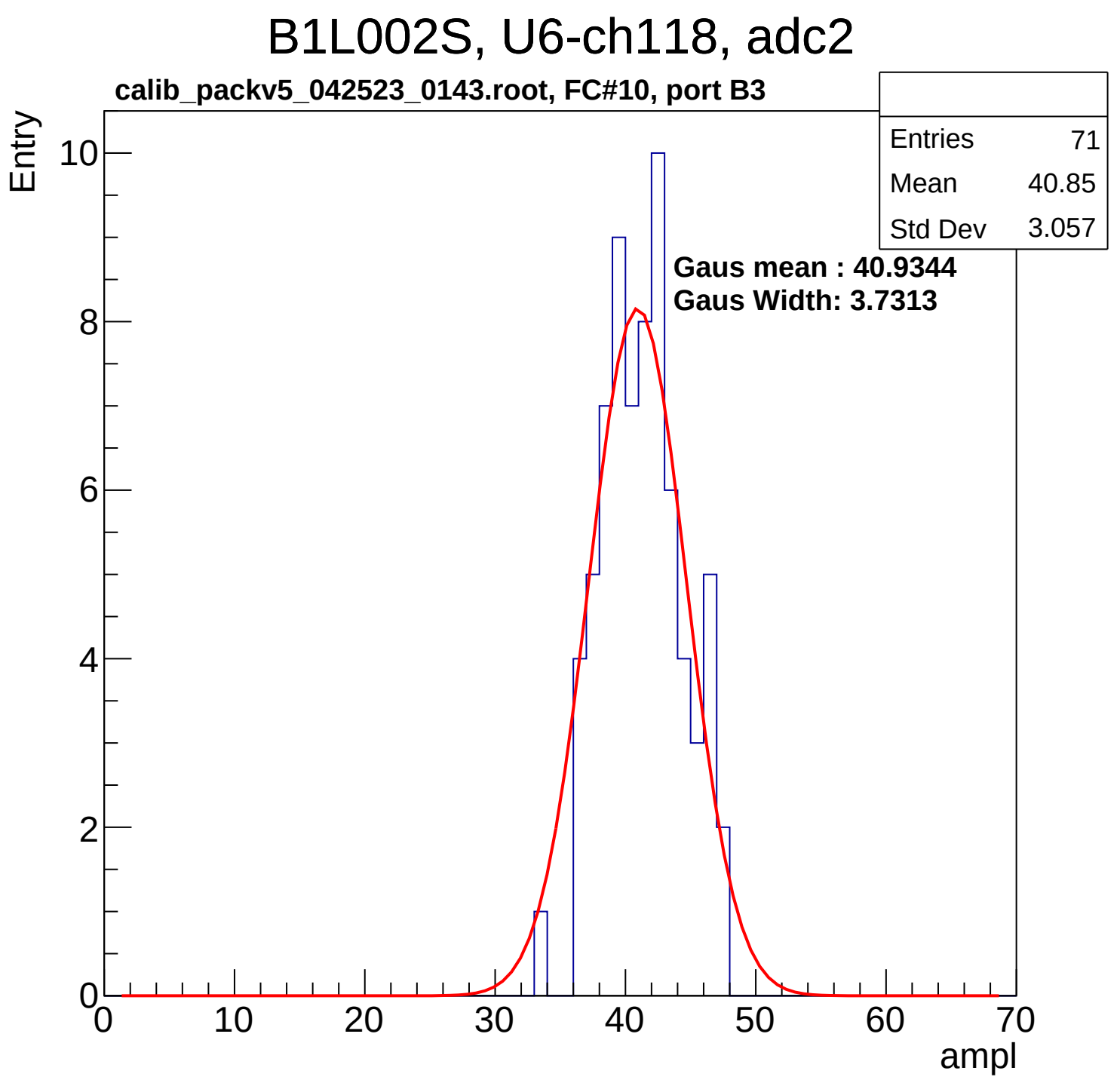

B1L002S, U6-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.38
Std Dev 2.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

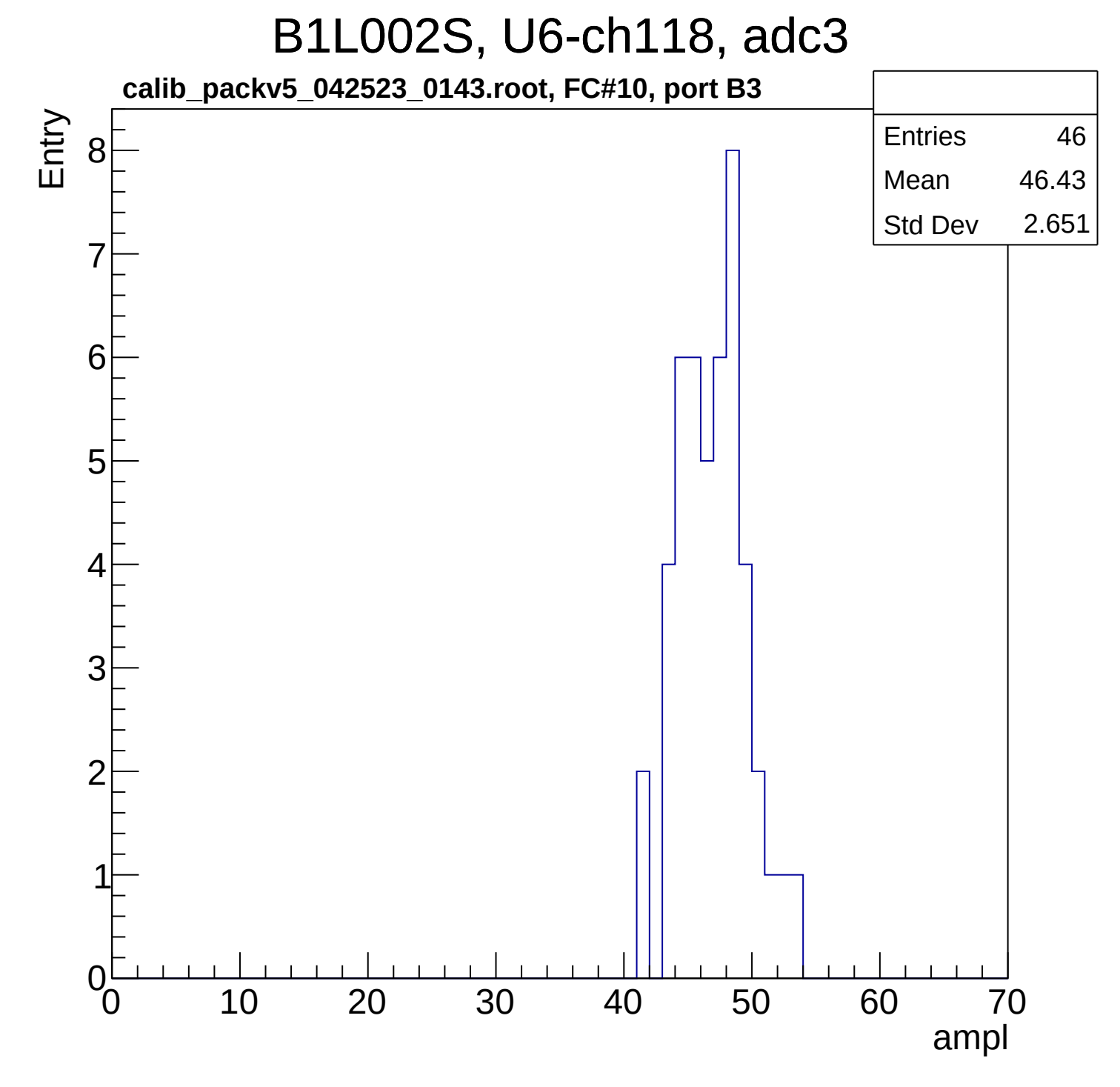

B1L002S, U6-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.5
Std Dev 14.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

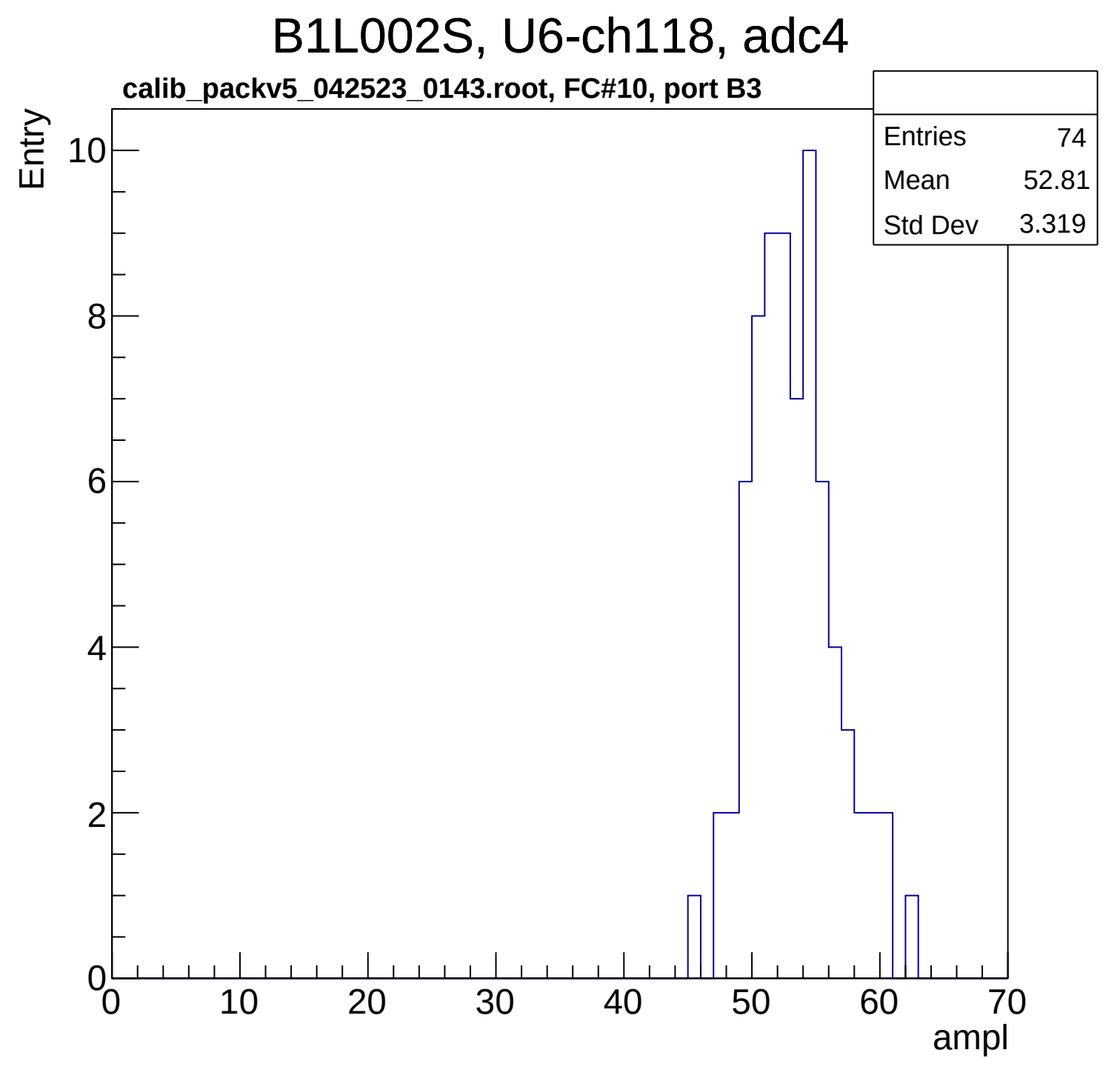

B1L002S, U6-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

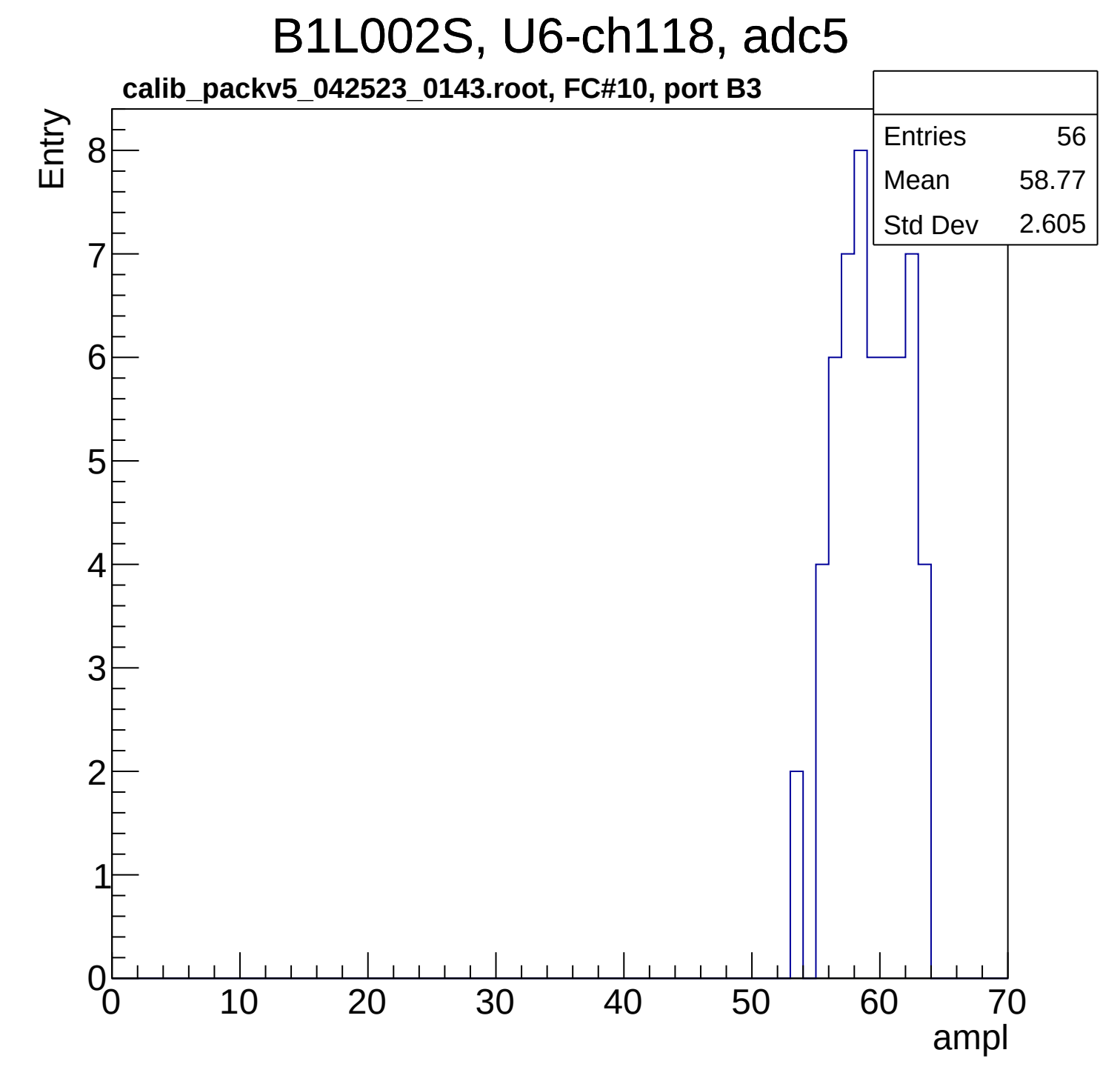

B1L002S, U6-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.11
Std Dev 3.215
Gaus mean : 29.7648
Gaus Width: 3.7824
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

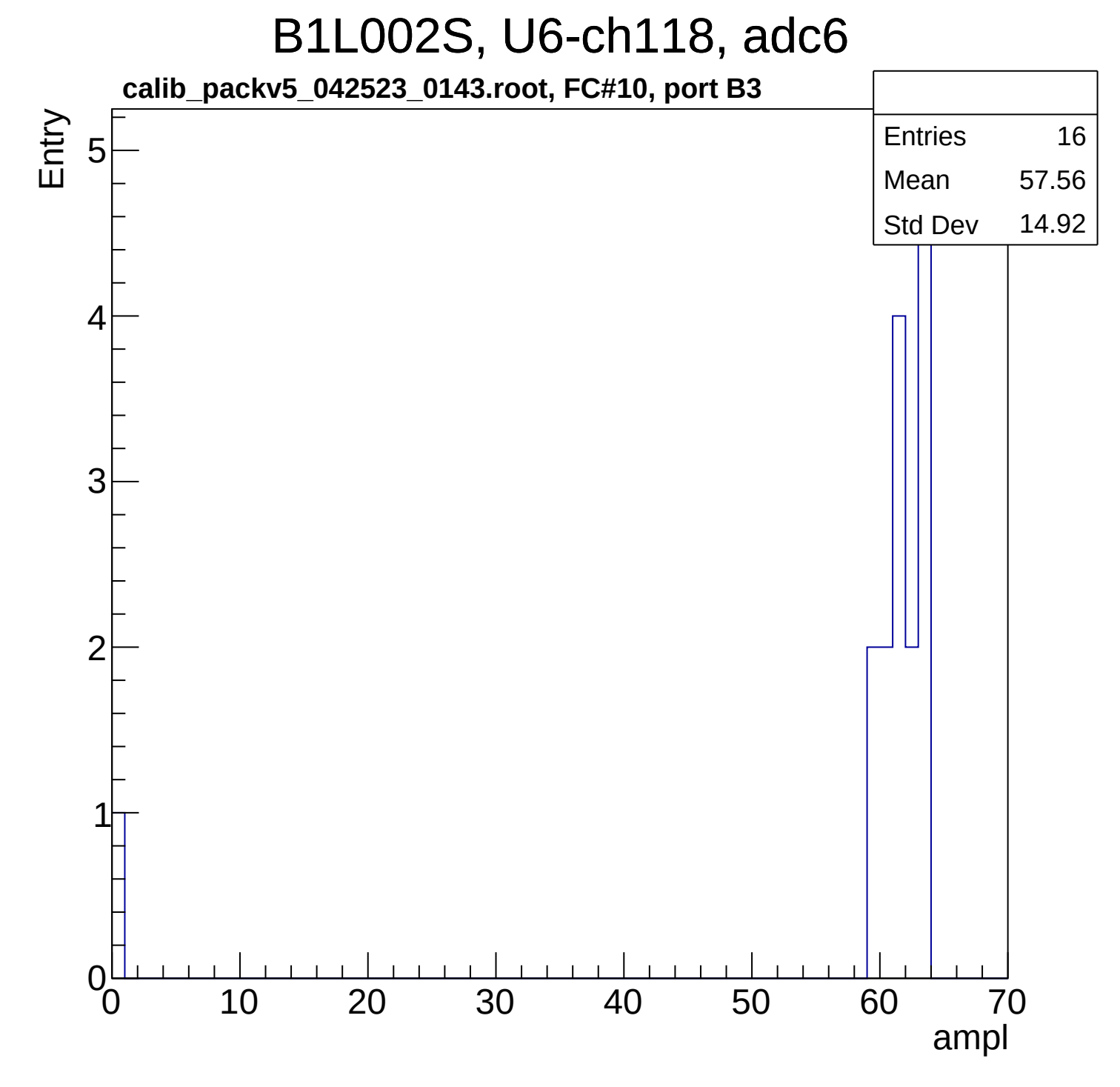

B1L002S, U6-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 34.41
Std Dev 2.982
Gaus mean : 35.0008
Gaus Width: 2.7924
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

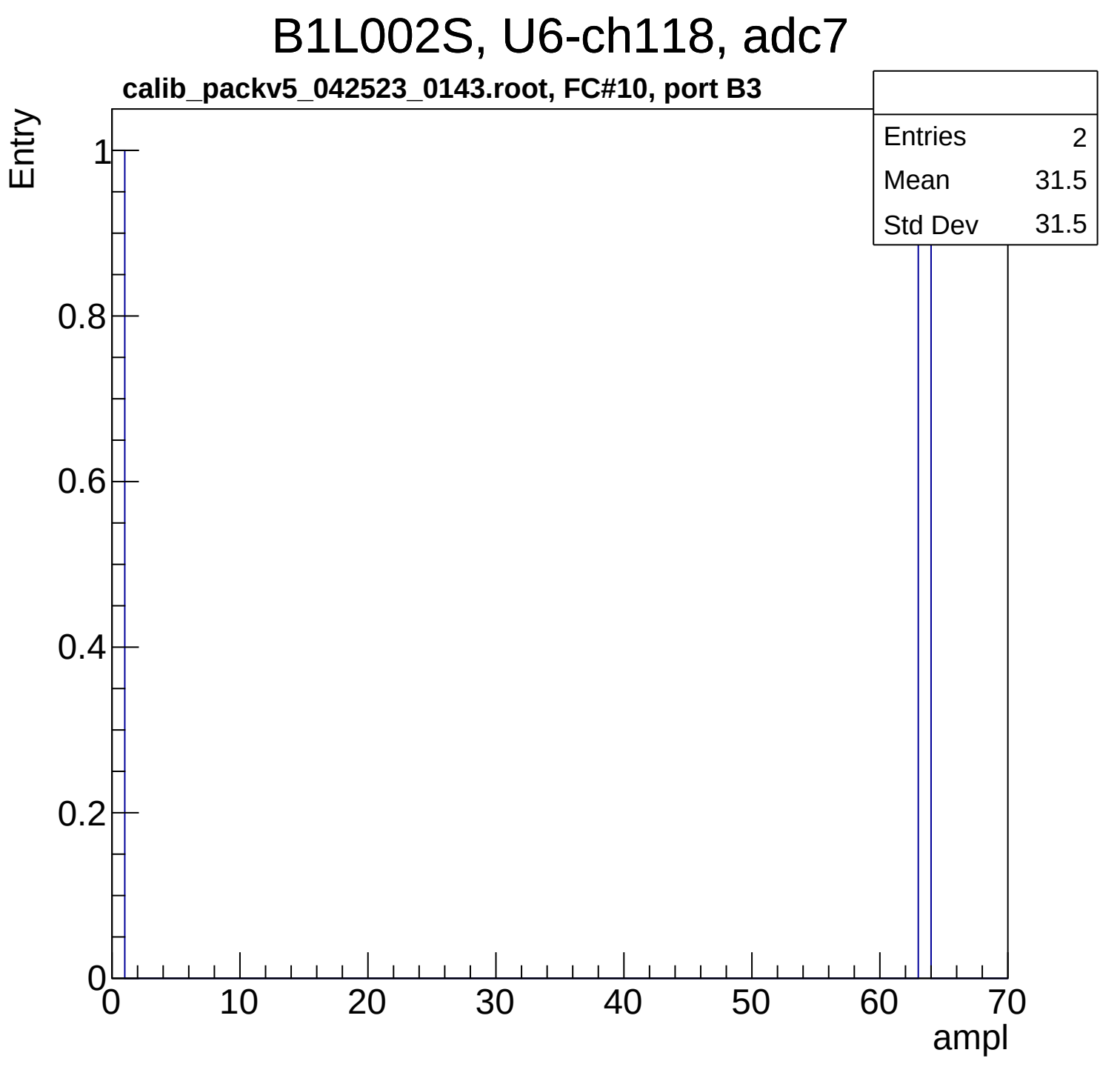

B1L002S, U6-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.85
Std Dev 3.057
Gaus mean : 40.9344
Gaus Width: 3.7313
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

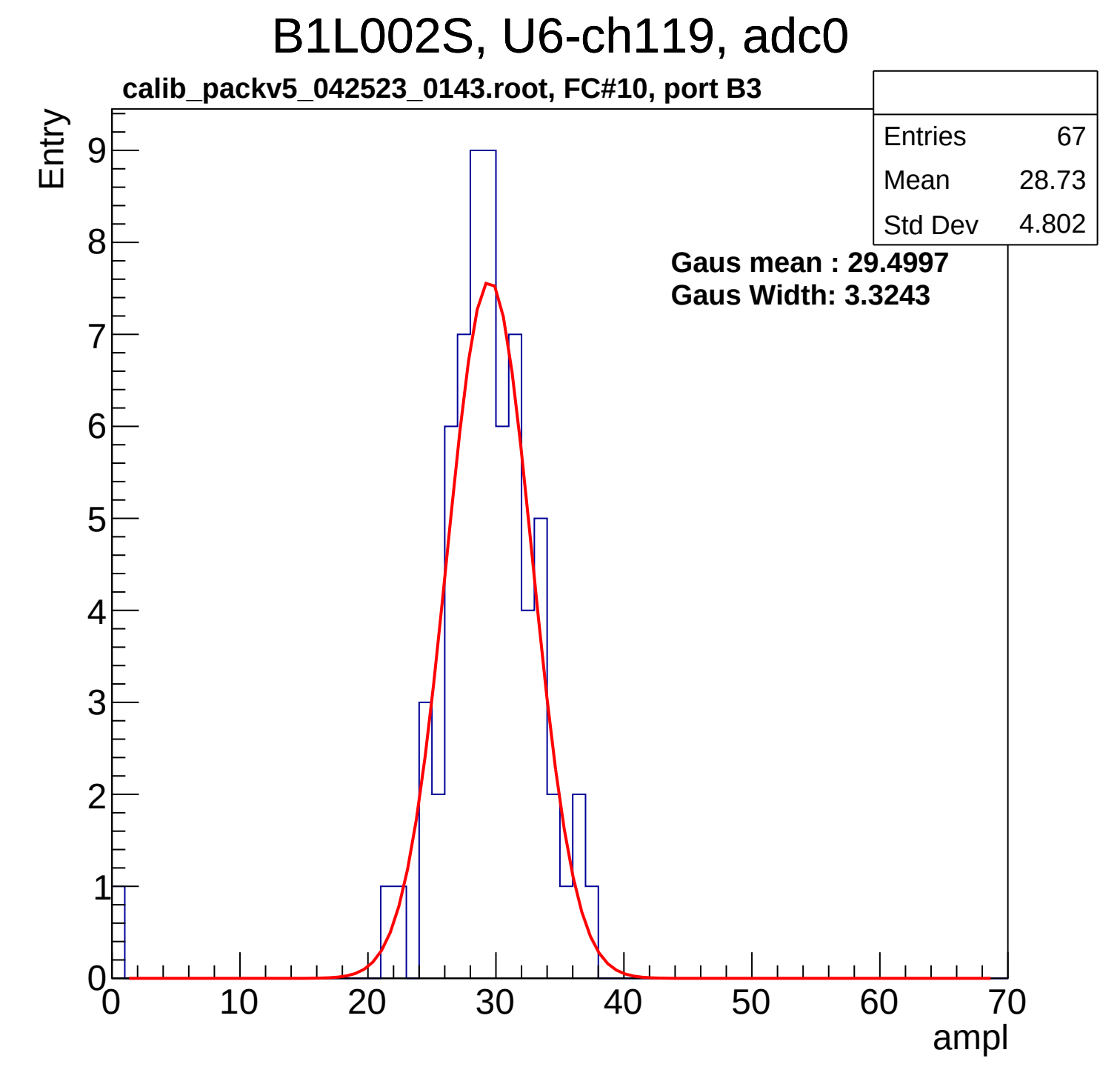

B1L002S, U6-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 46.43
Std Dev 2.651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

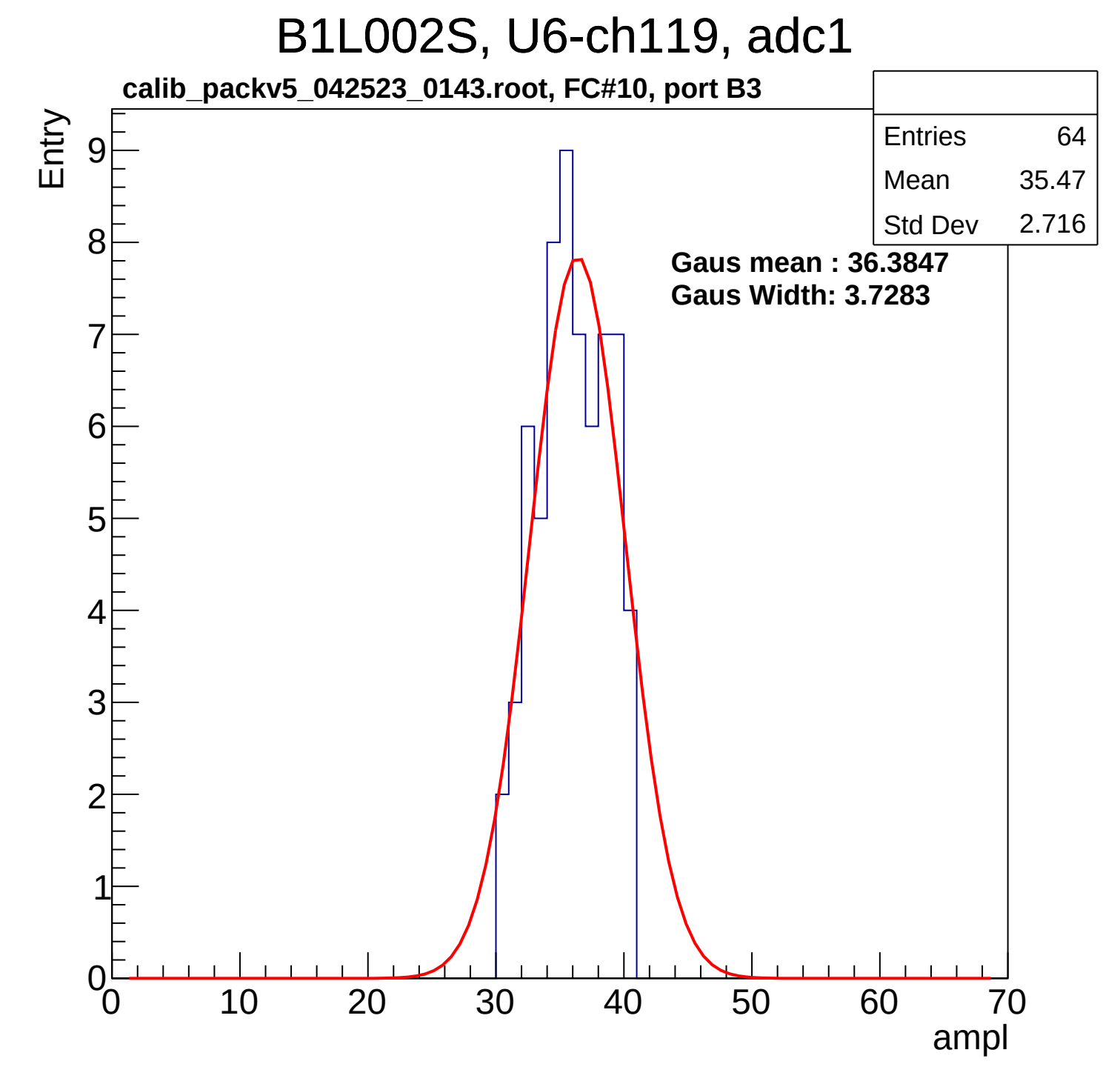

B1L002S, U6-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 52.81
Std Dev 3.319
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

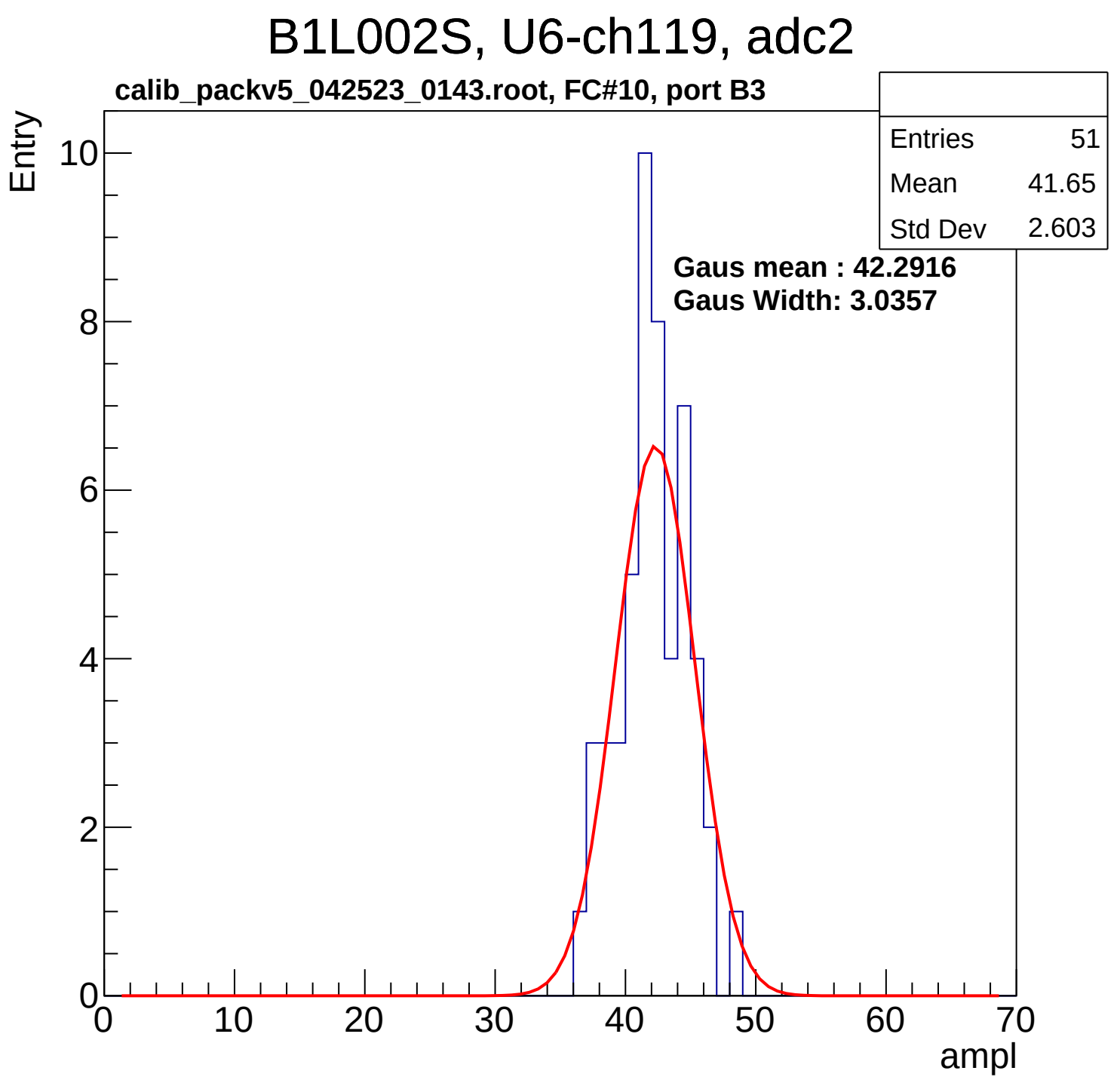

B1L002S, U6-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.77
Std Dev 2.605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

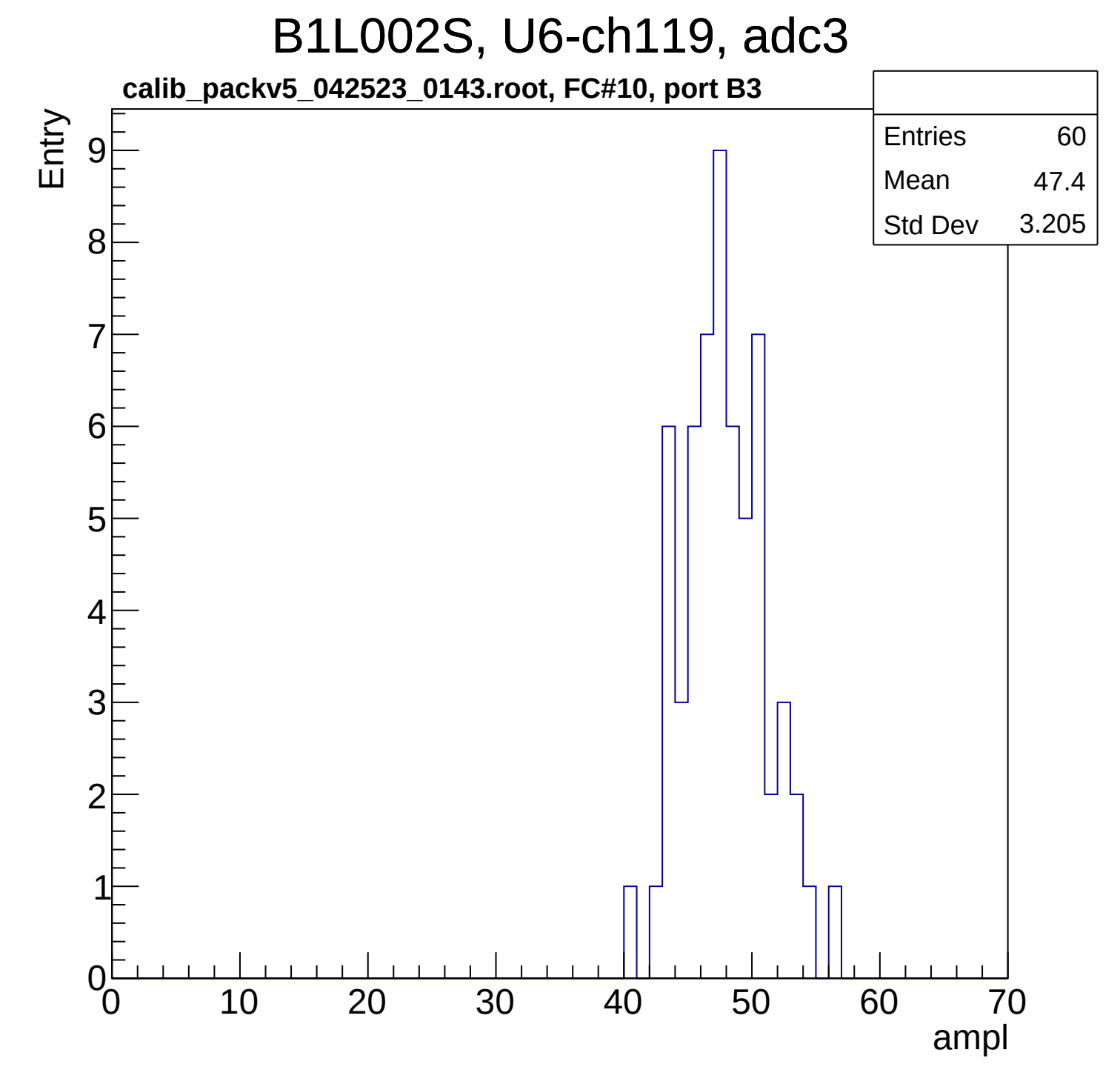

B1L002S, U6-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.56
Std Dev 14.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

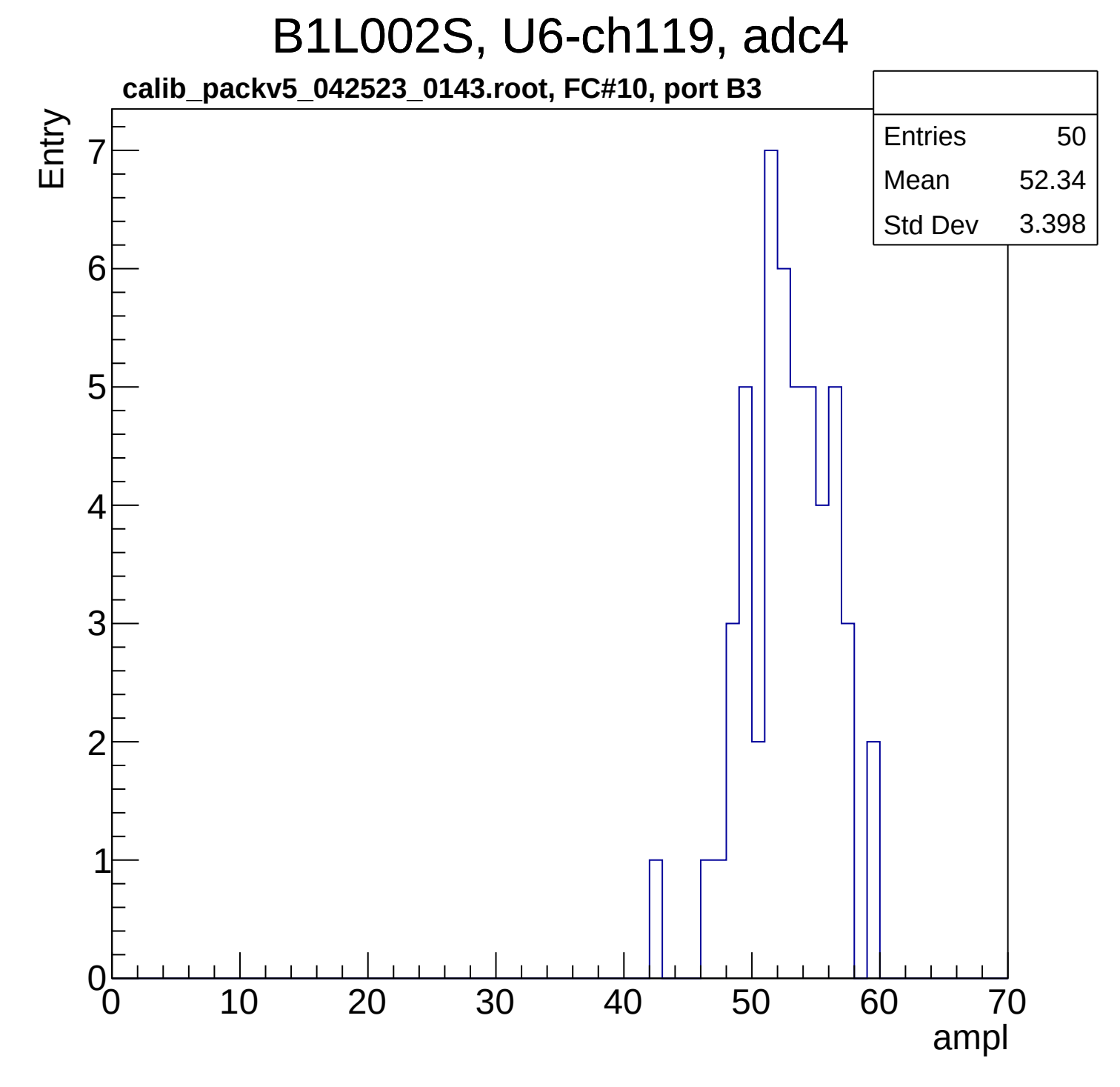

B1L002S, U6-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

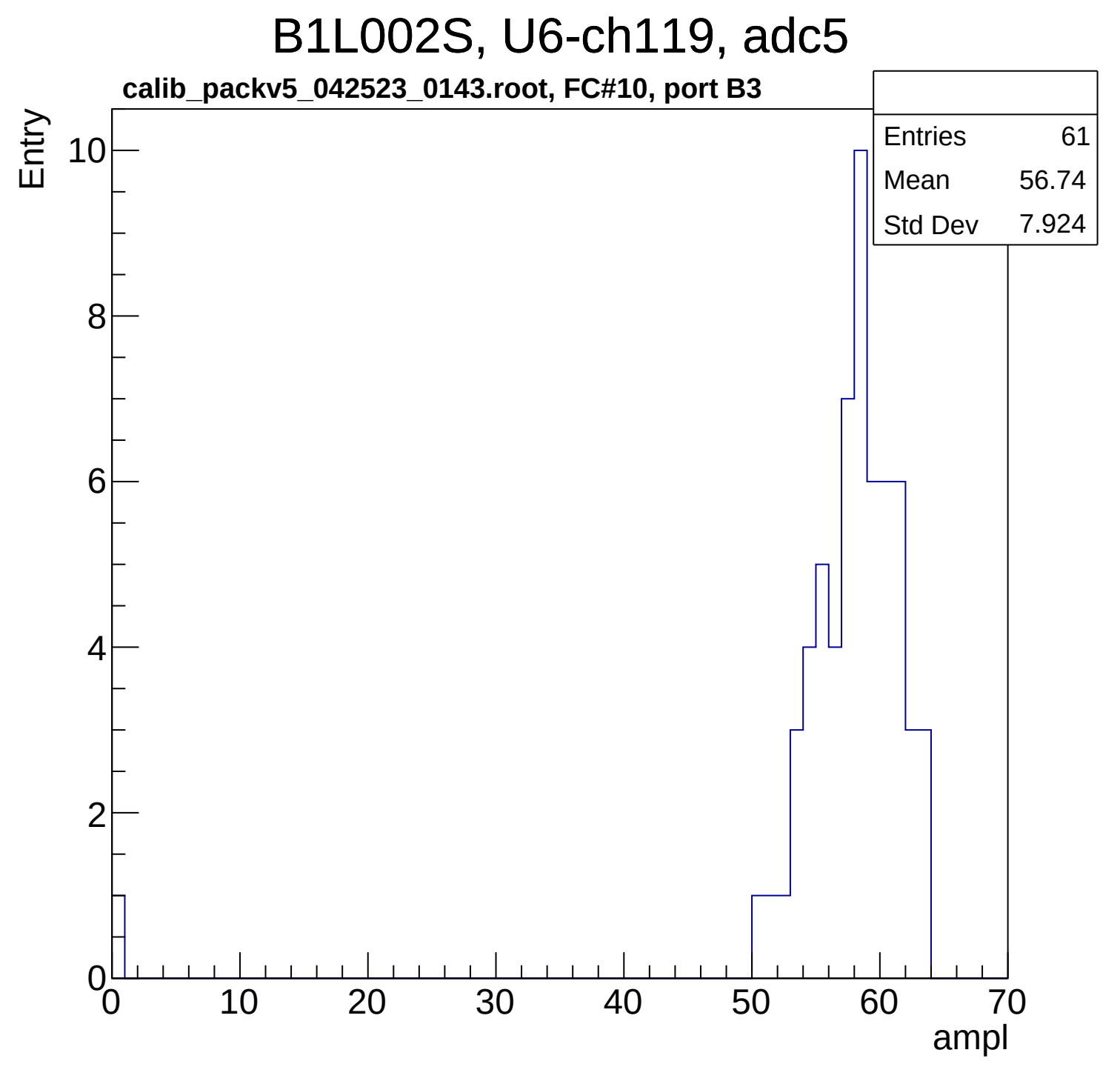

B1L002S, U6-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.73
Std Dev 4.802
Gaus mean : 29.4997
Gaus Width: 3.3243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

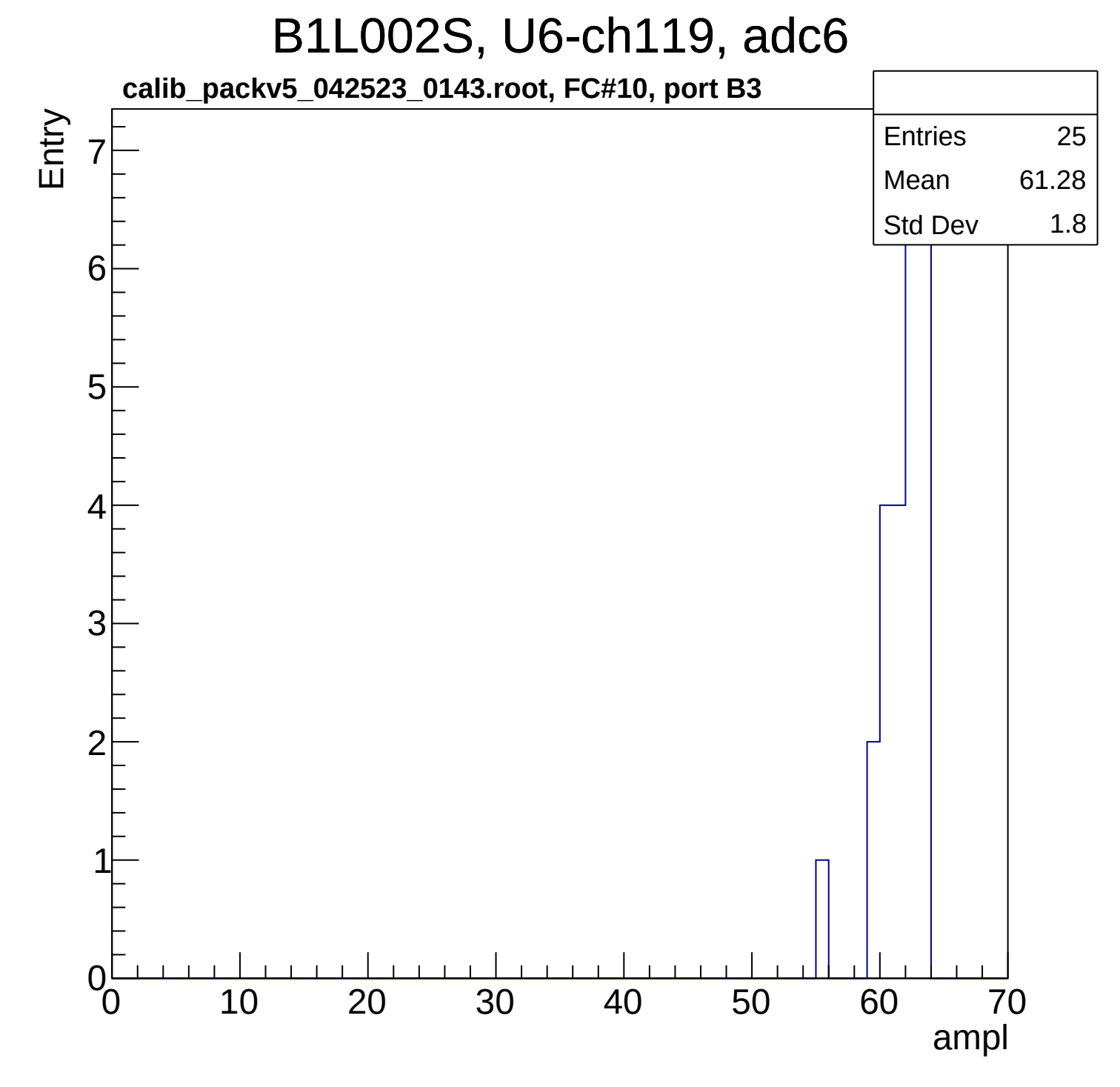

B1L002S, U6-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.47
Std Dev 2.716
Gaus mean : 36.3847
Gaus Width: 3.7283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

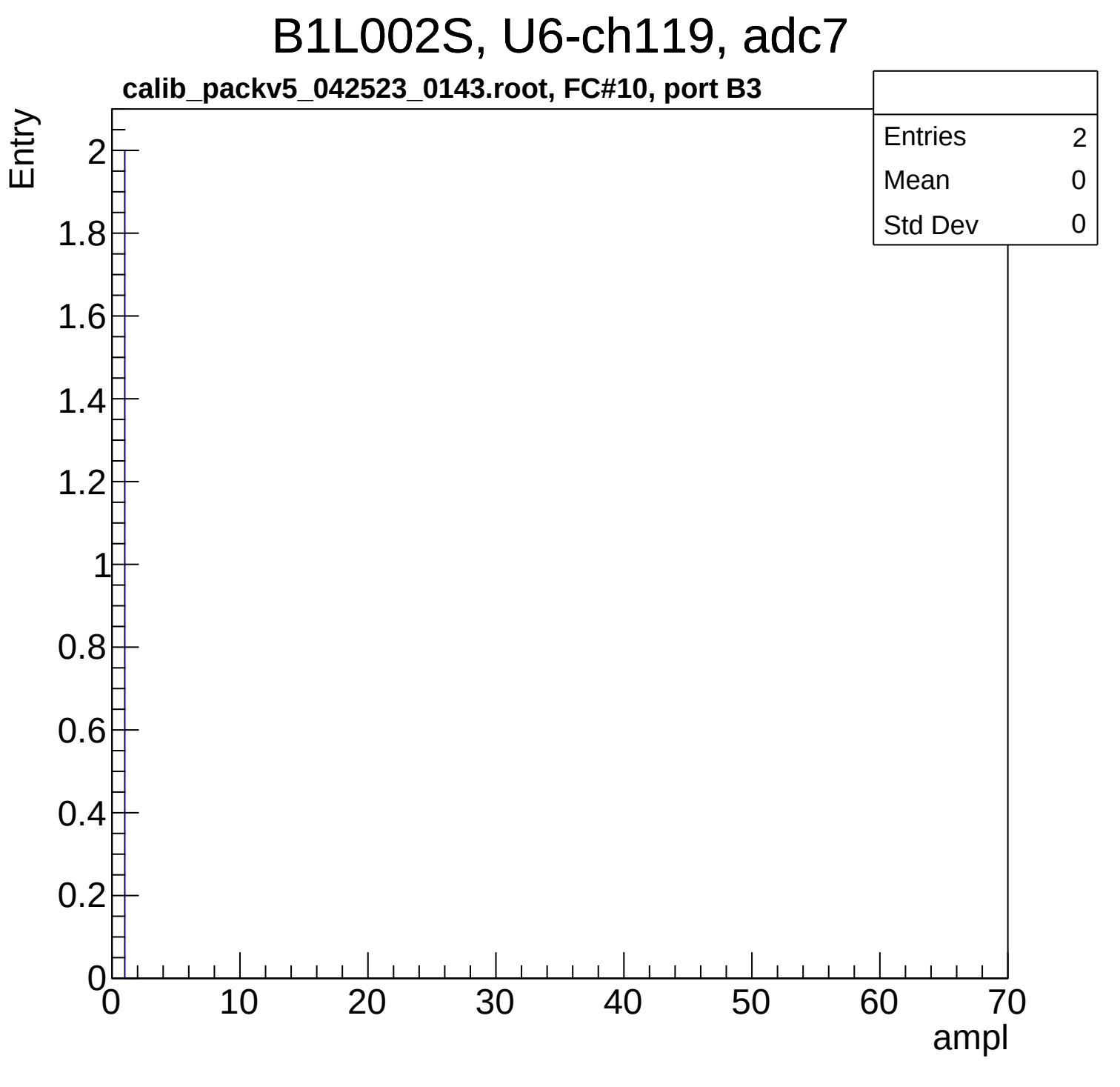

B1L002S, U6-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 41.65
Std Dev 2.603
Gaus mean : 42.2916
Gaus Width: 3.0357
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

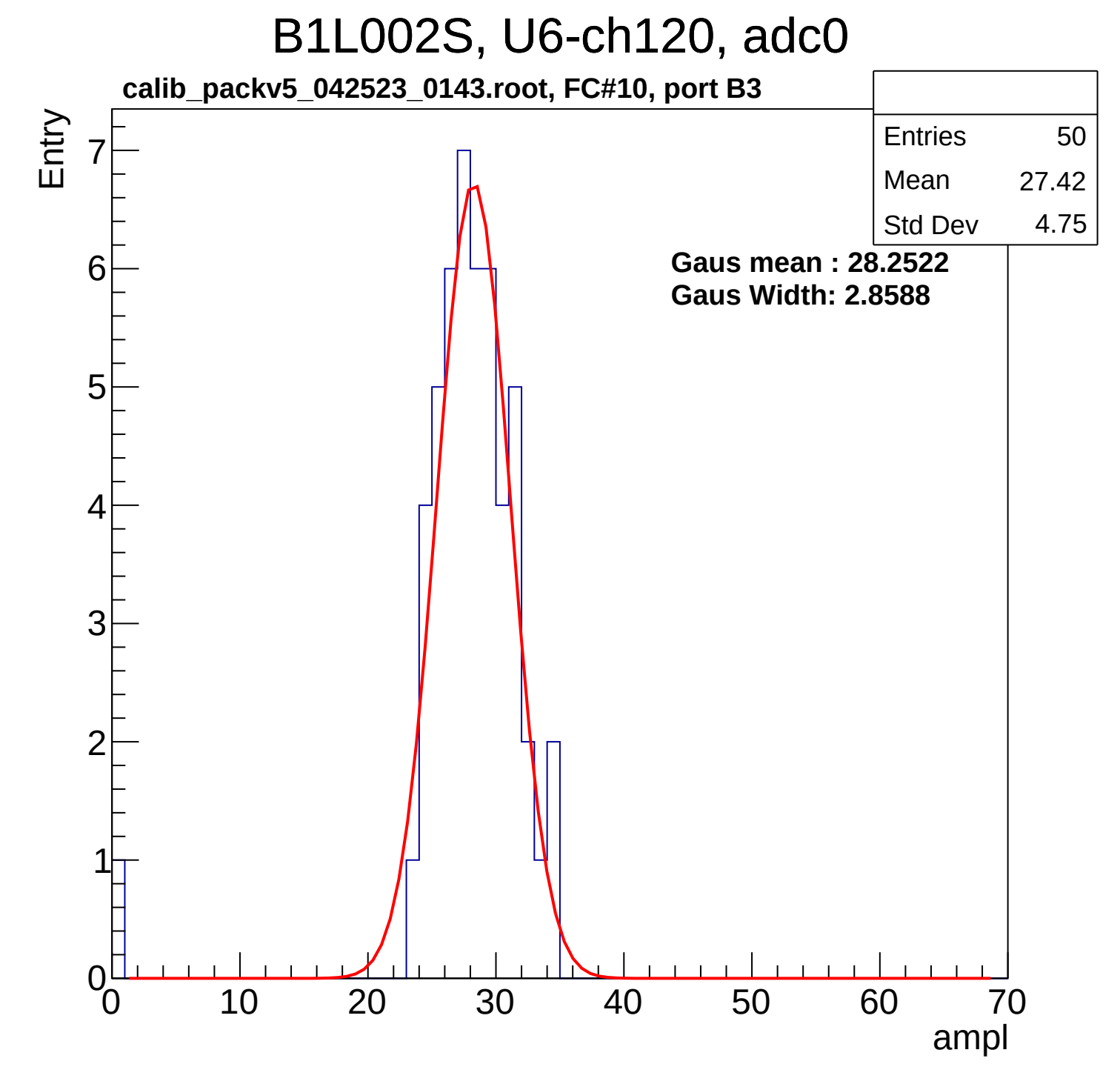

B1L002S, U6-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.4
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

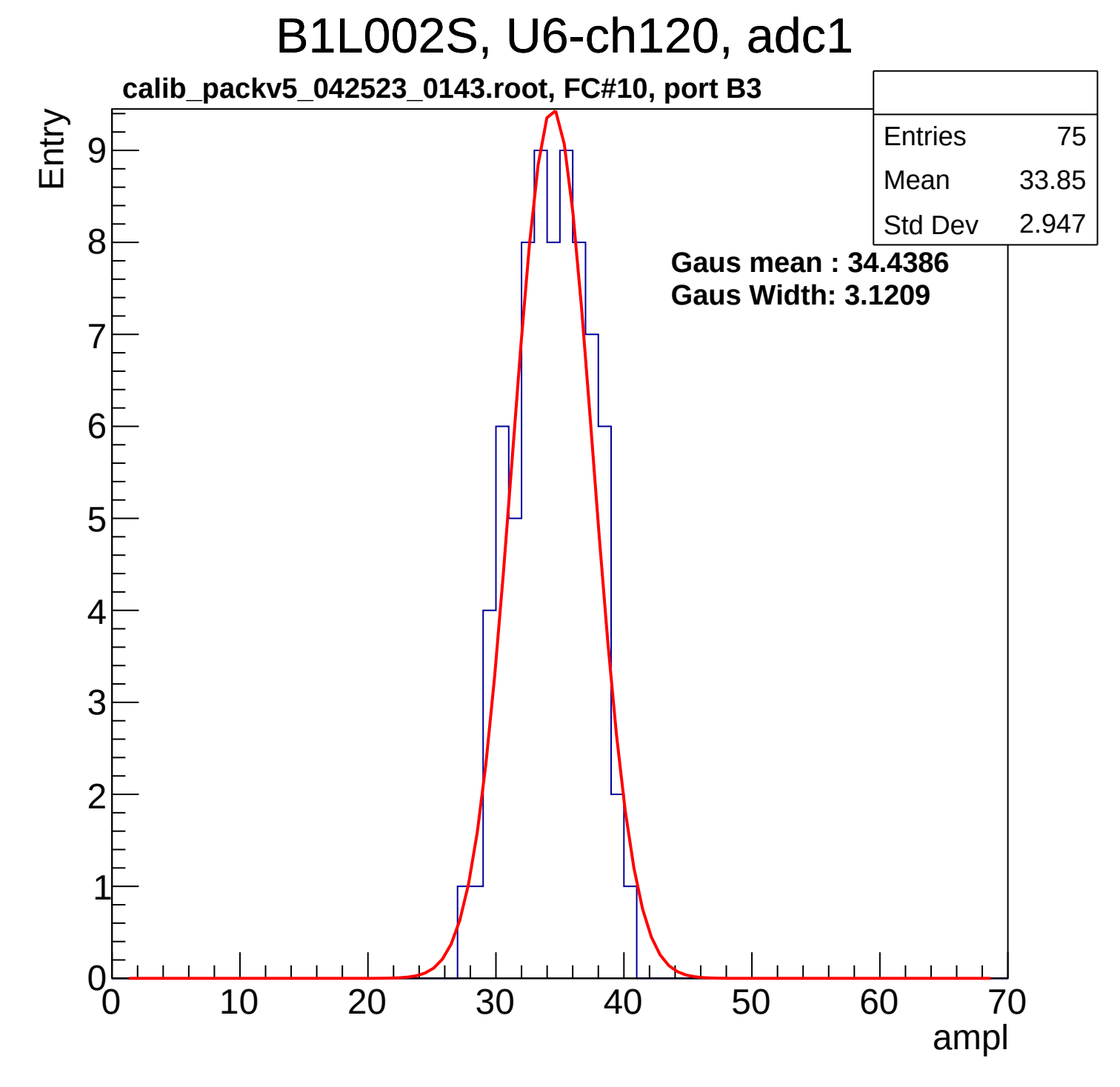

B1L002S, U6-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.34
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

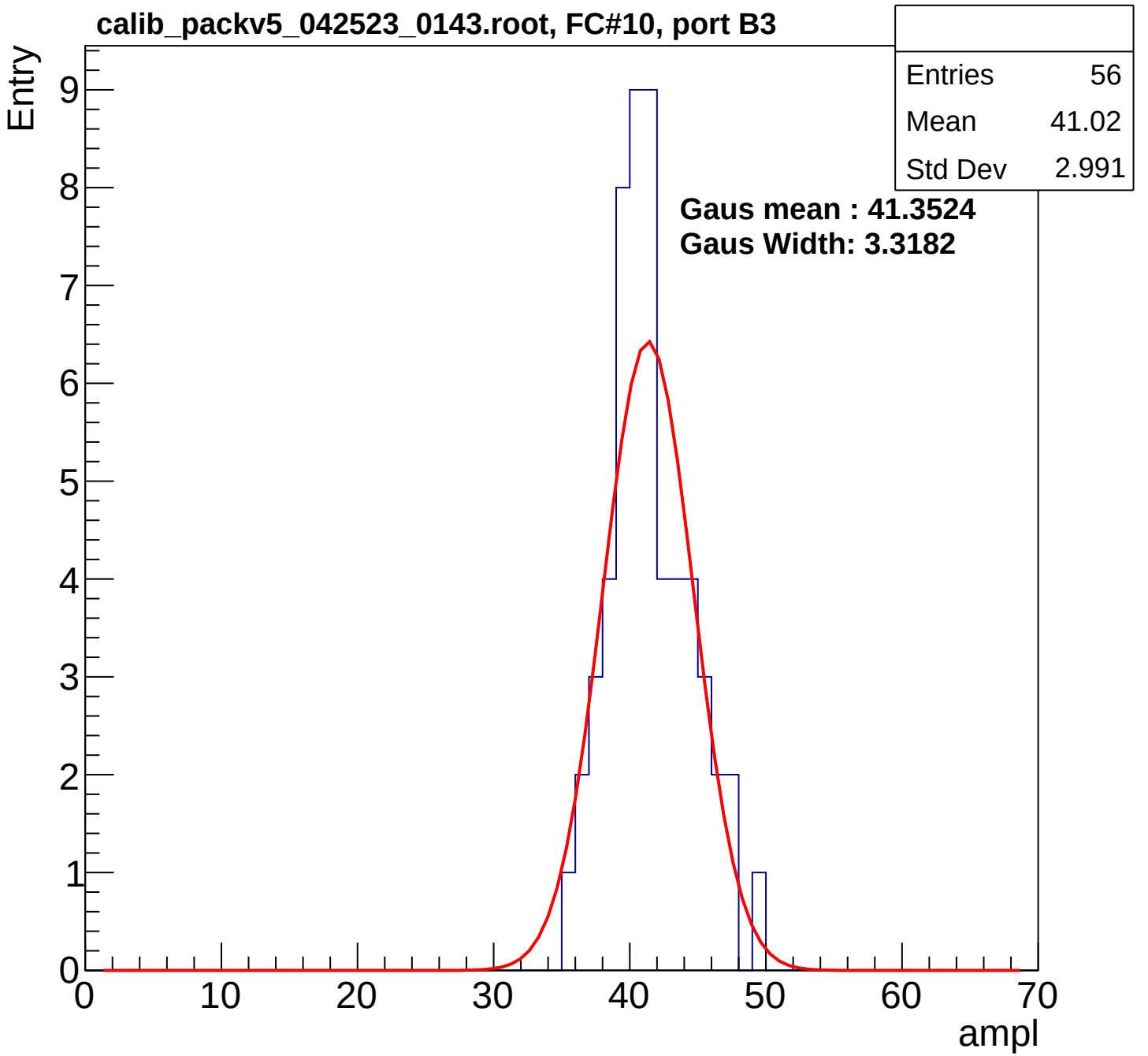

B1L002S, U6-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.74
Std Dev 7.924
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

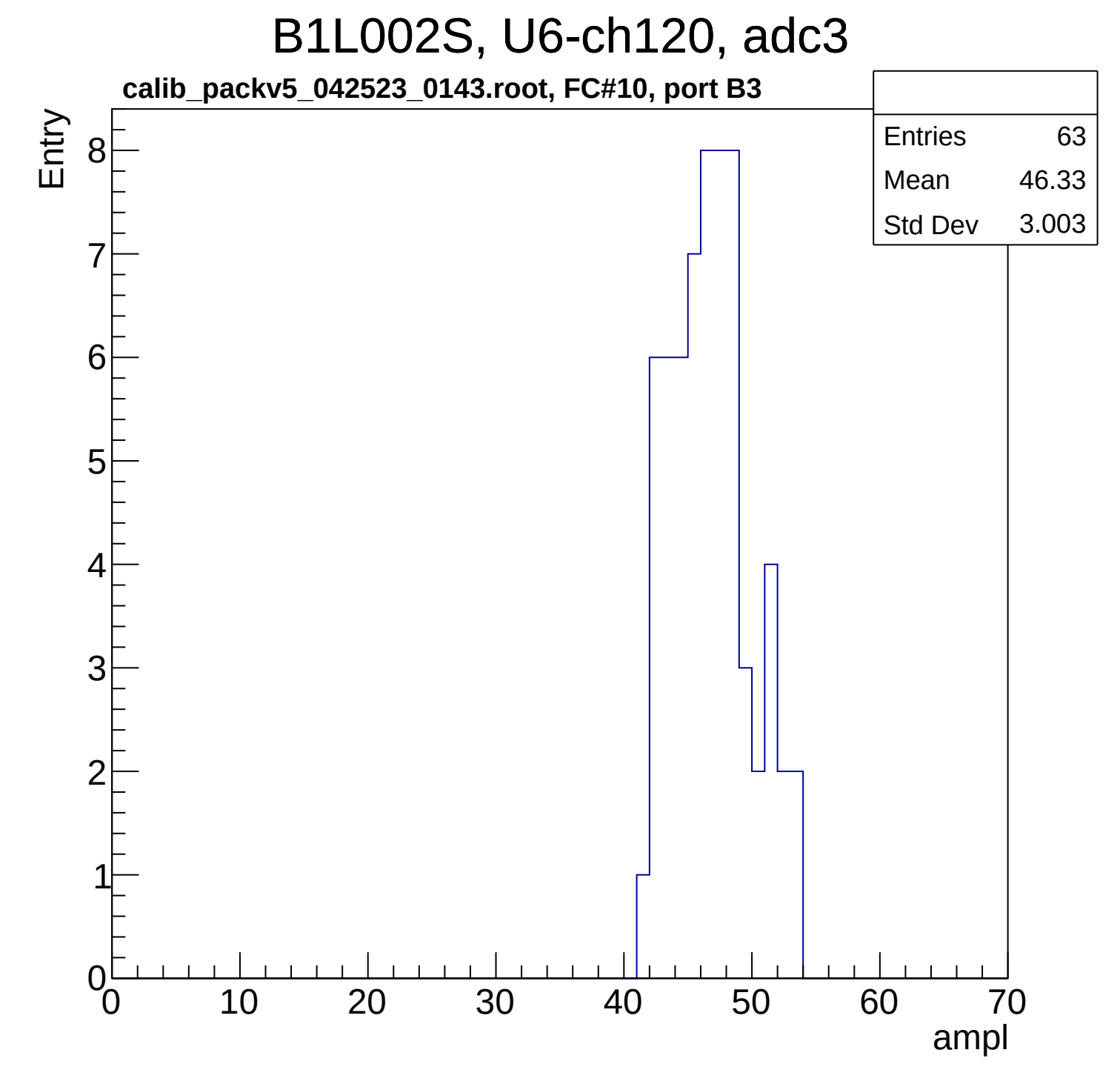

B1L002S, U6-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.28
Std Dev 1.8
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

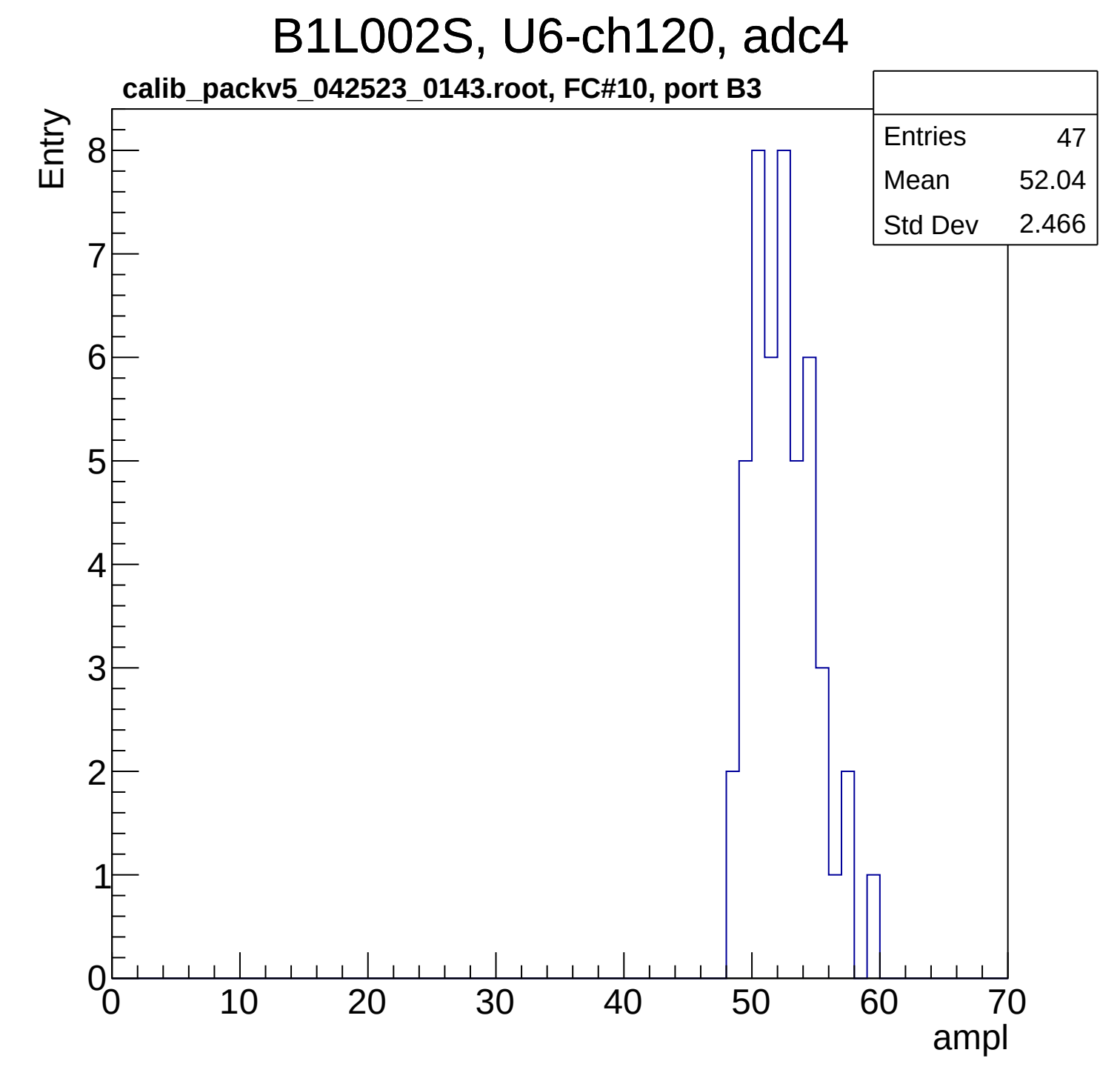

B1L002S, U6-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

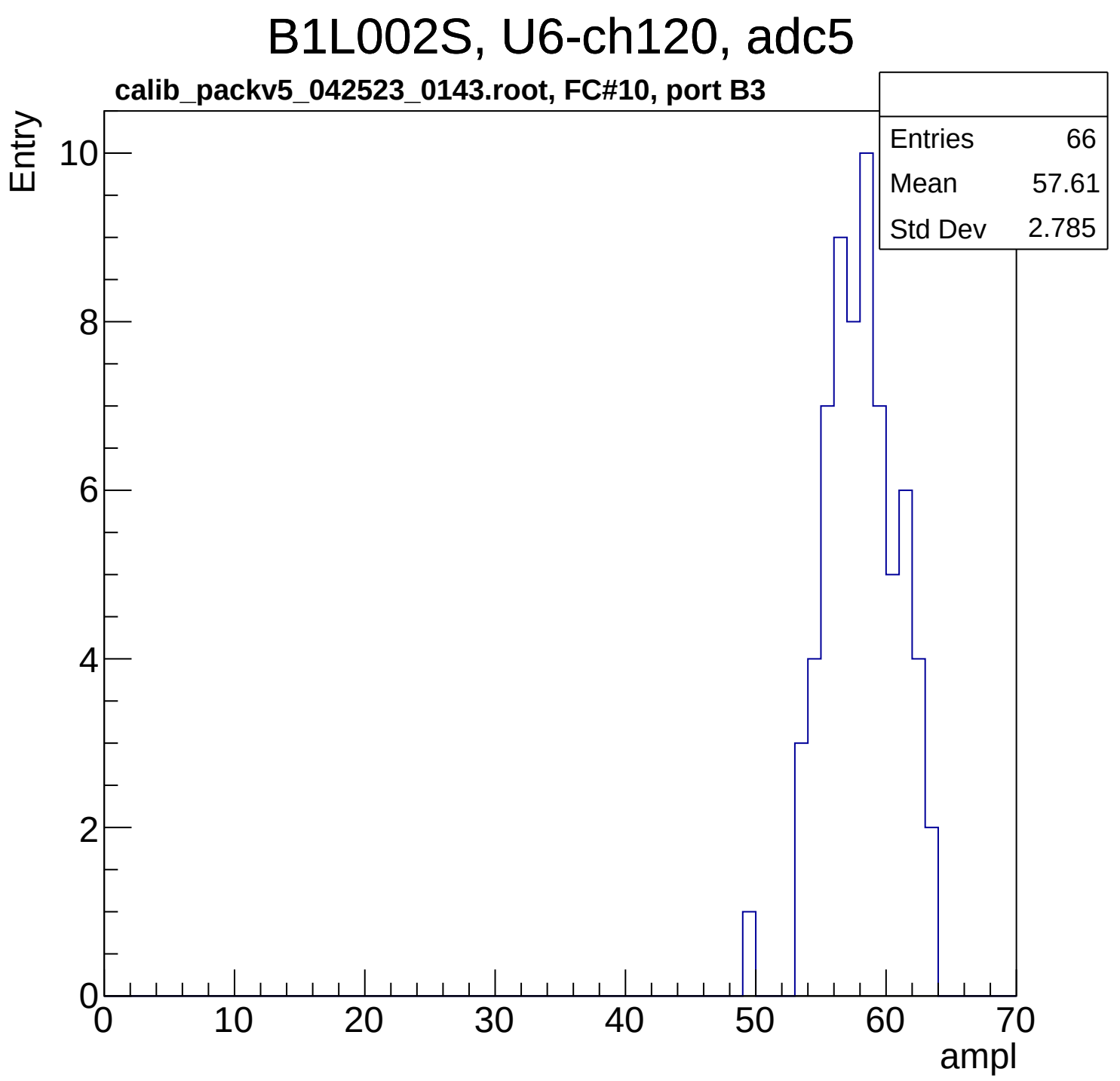

B1L002S, U6-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 27.42
Std Dev 4.75
Gaus mean : 28.2522
Gaus Width: 2.8588
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

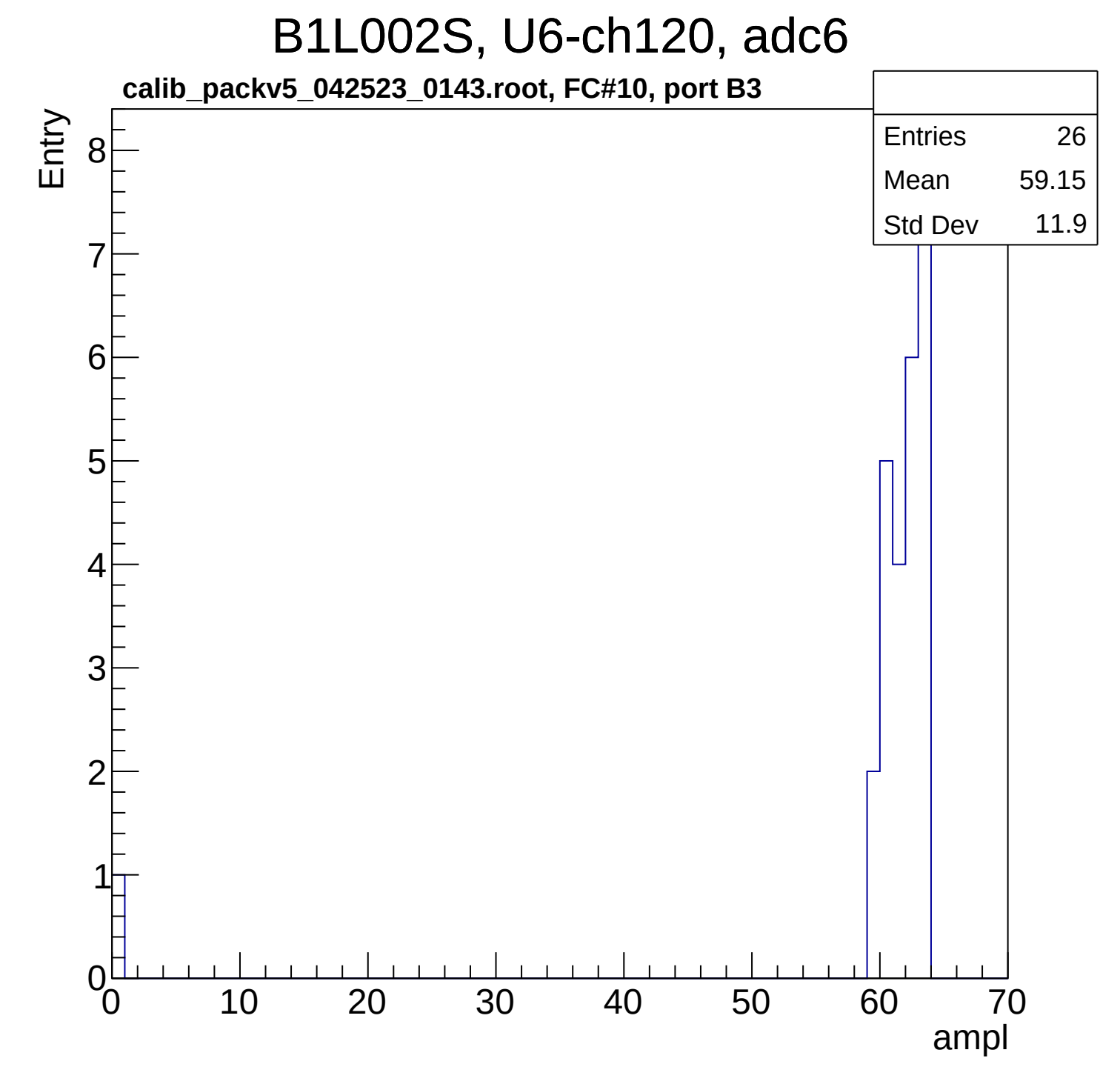

B1L002S, U6-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.85
Std Dev 2.947
Gaus mean : 34.4386
Gaus Width: 3.1209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

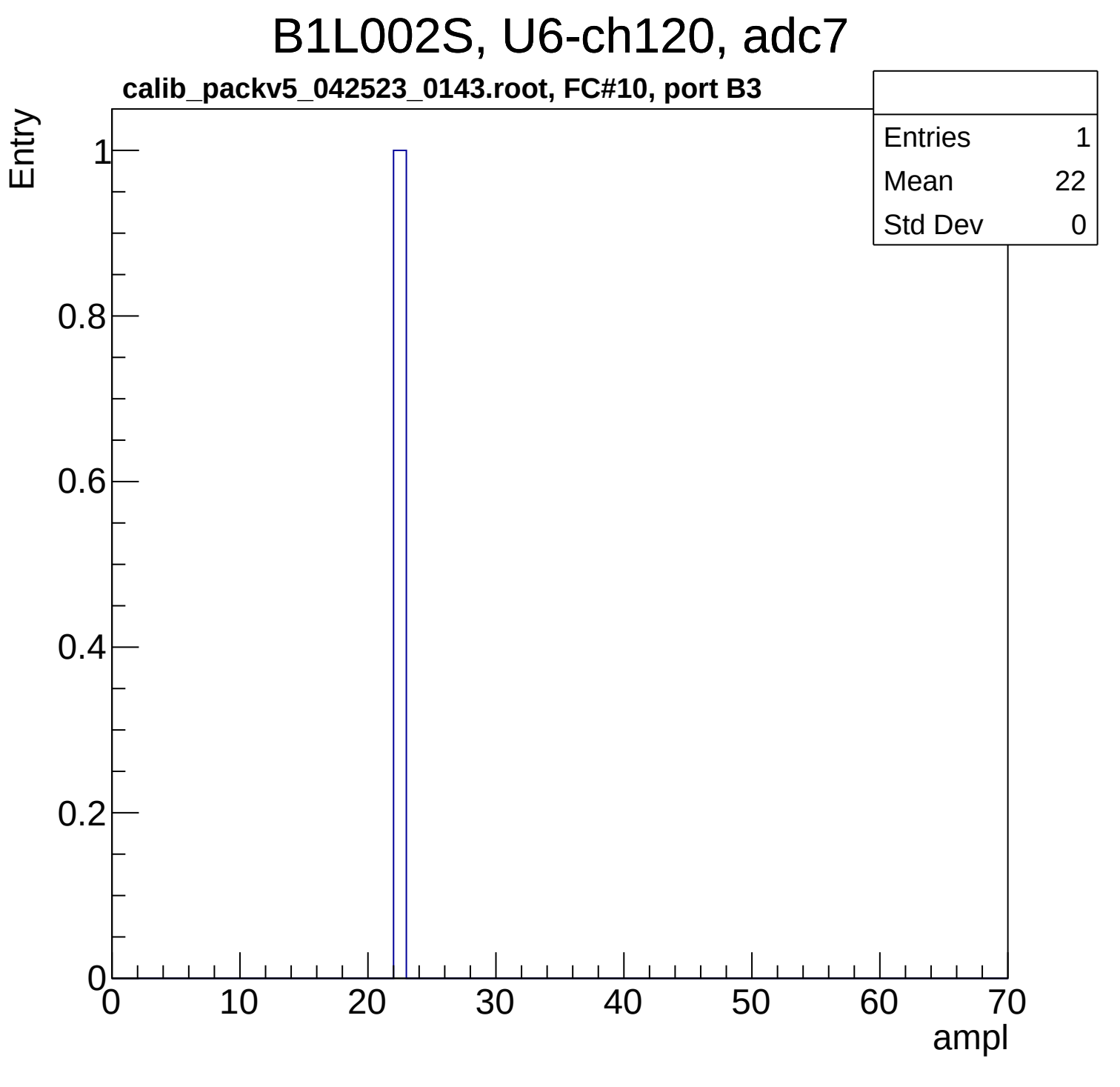

B1L002S, U6-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.02
Std Dev 2.991
Gaus mean : 41.3524
Gaus Width: 3.3182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

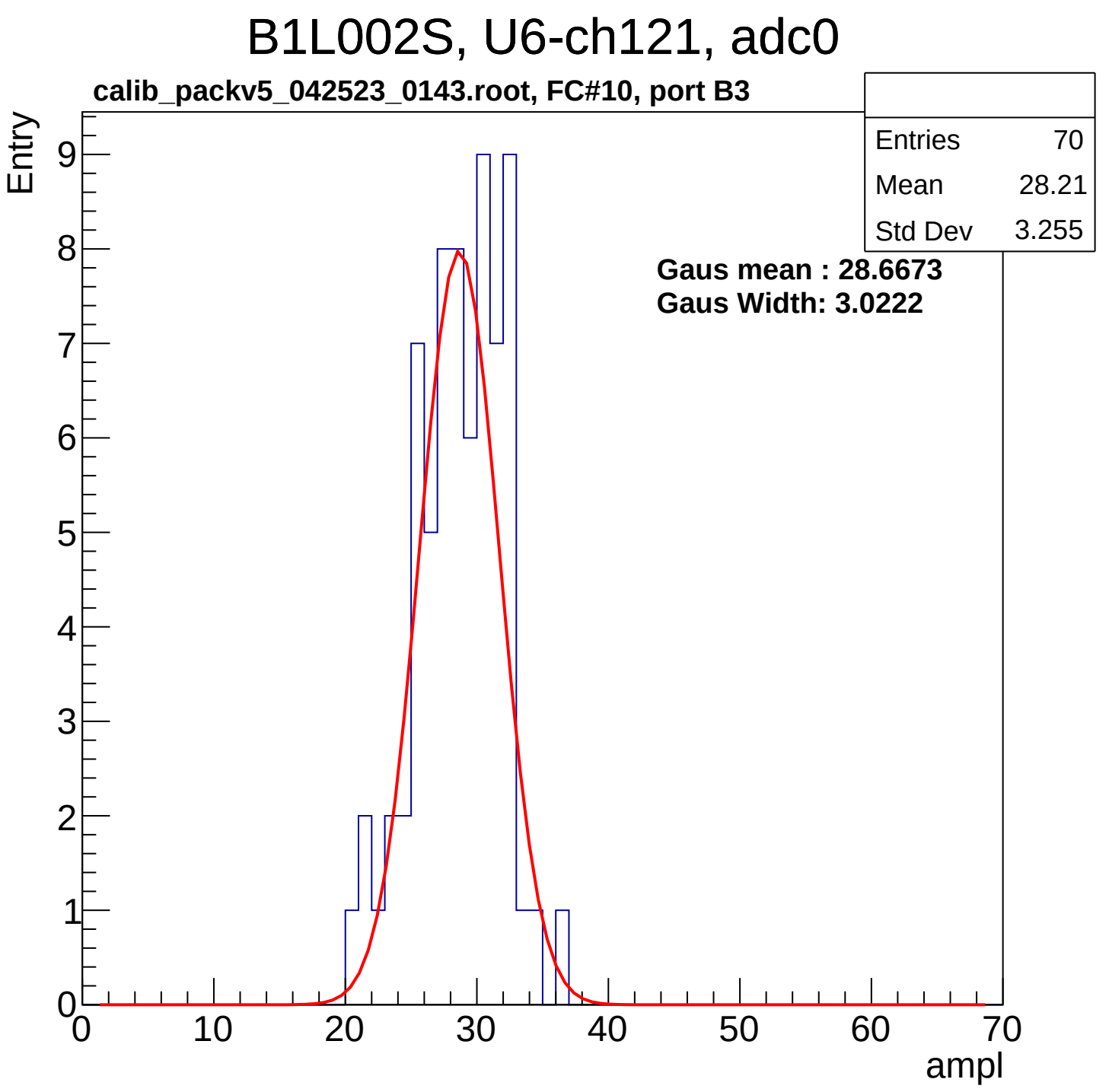

B1L002S, U6-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.33
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

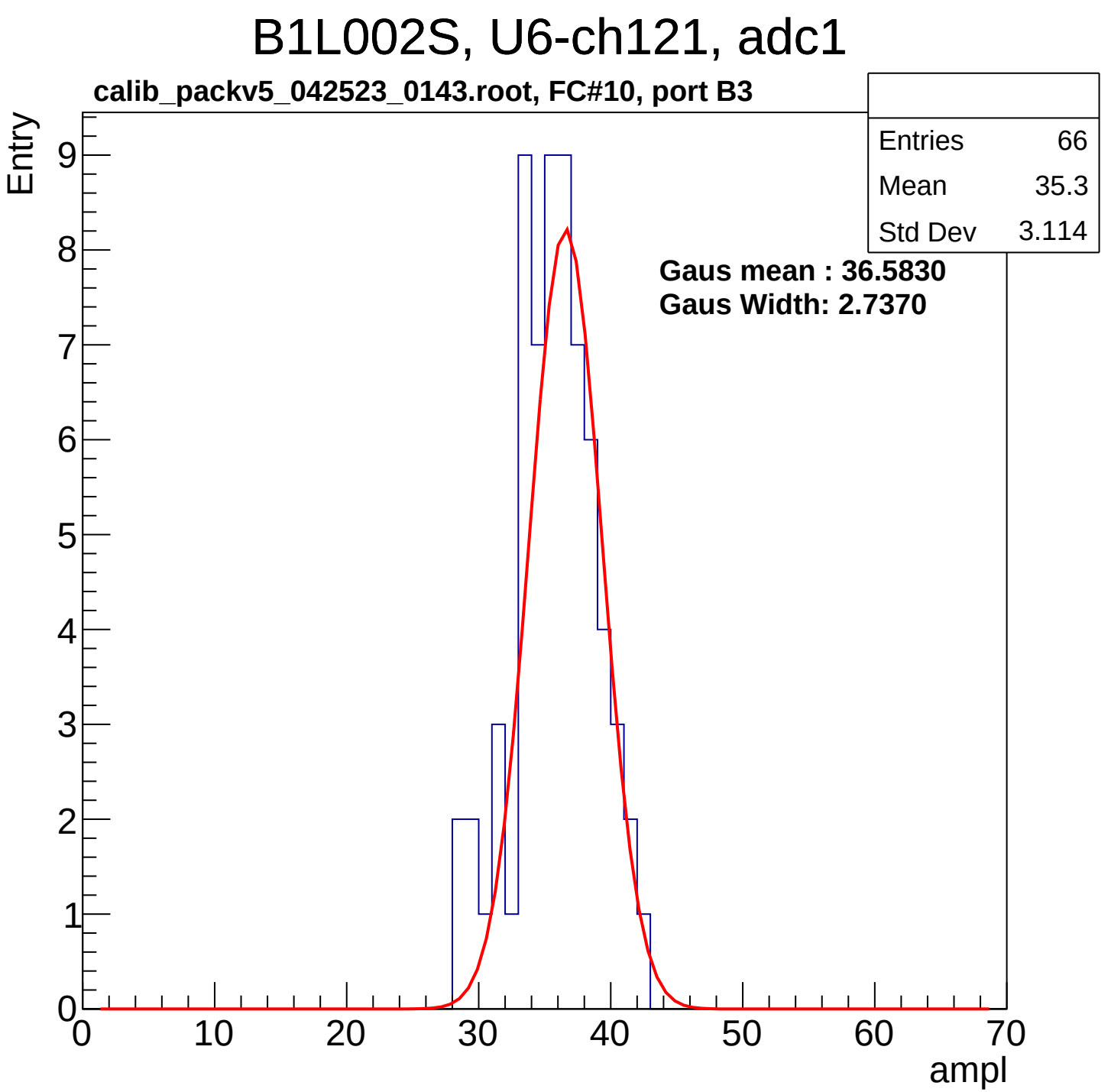

B1L002S, U6-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 52.04
Std Dev 2.466
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

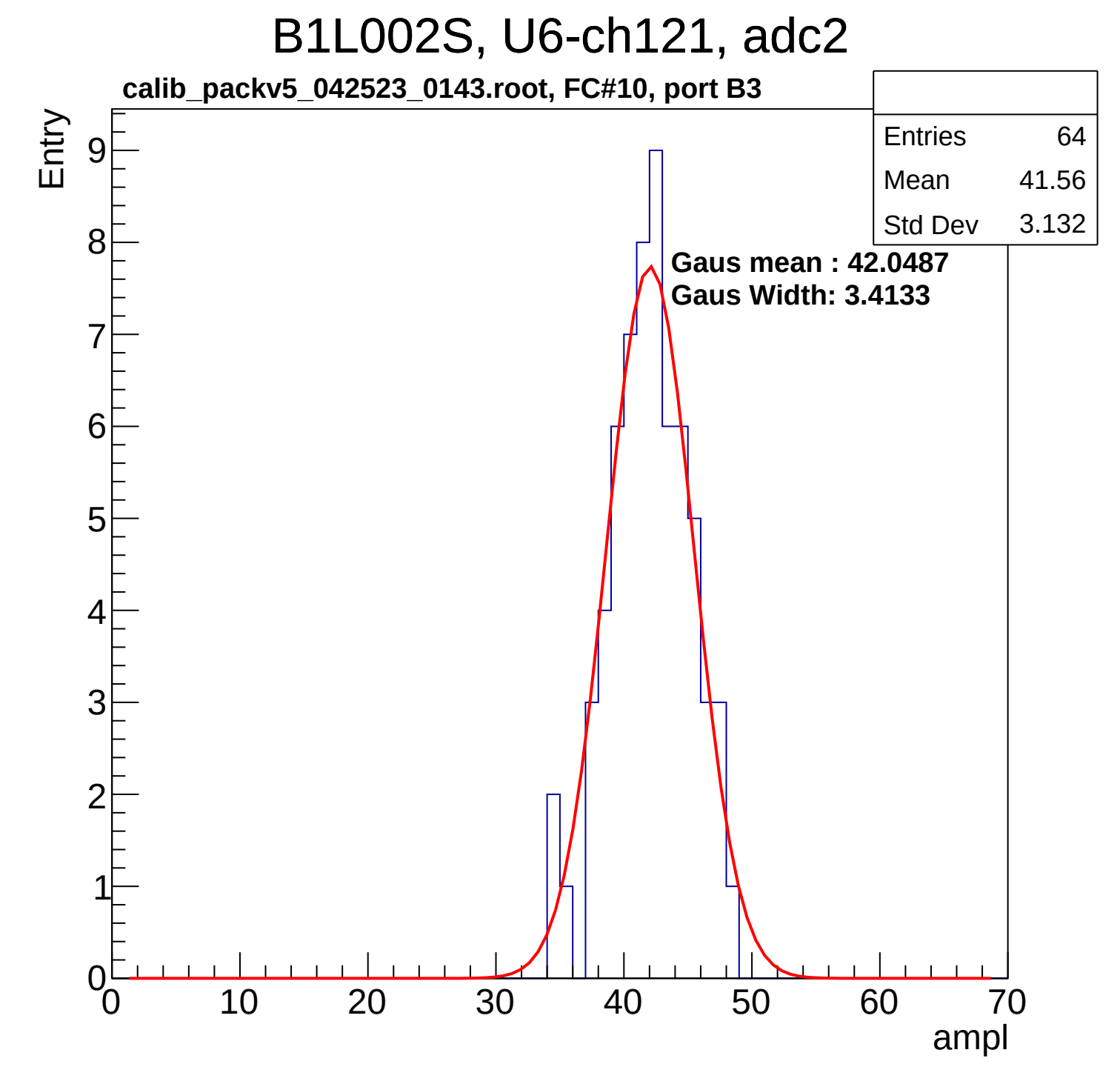

B1L002S, U6-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.61
Std Dev 2.785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

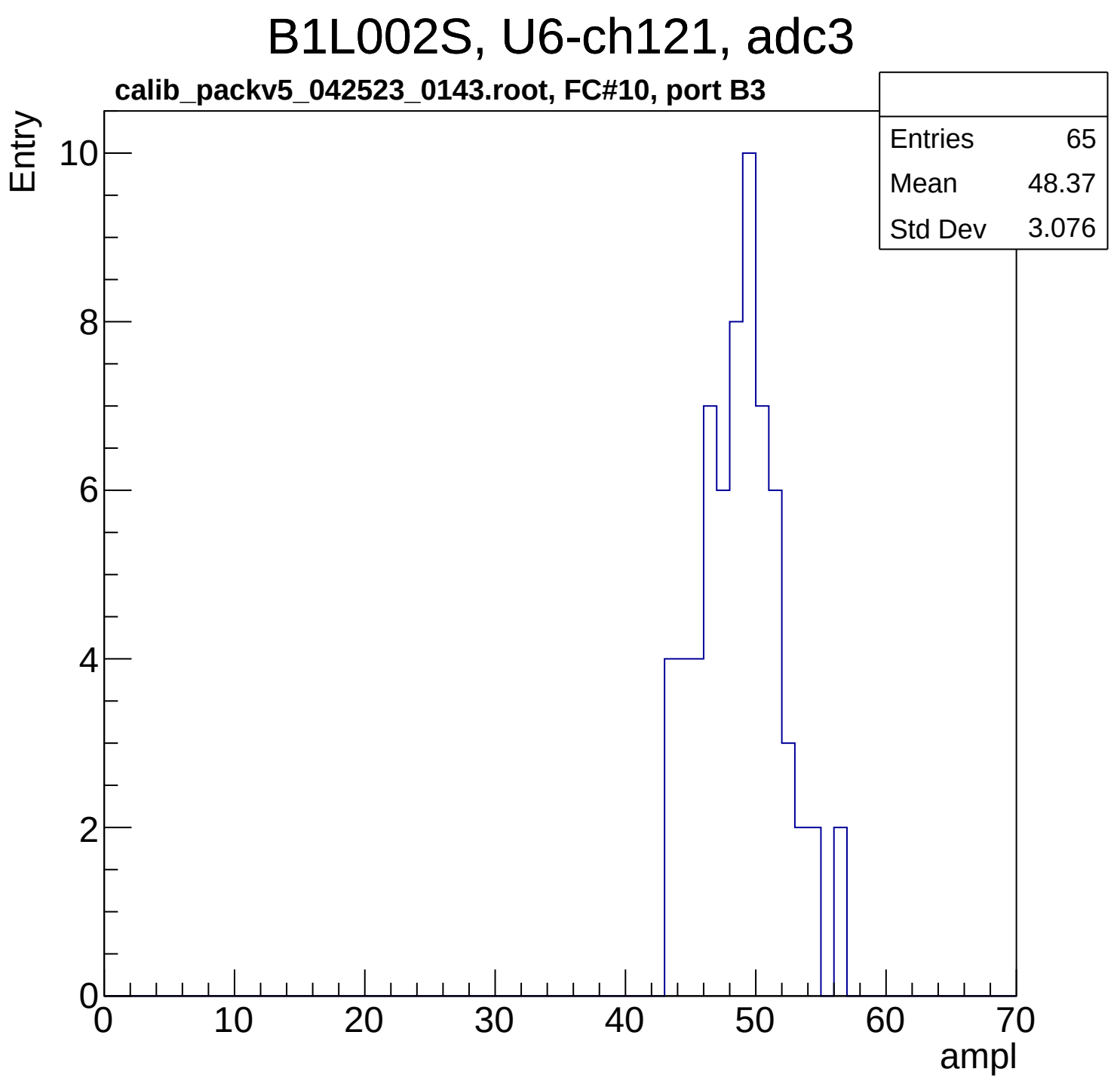

B1L002S, U6-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.15
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

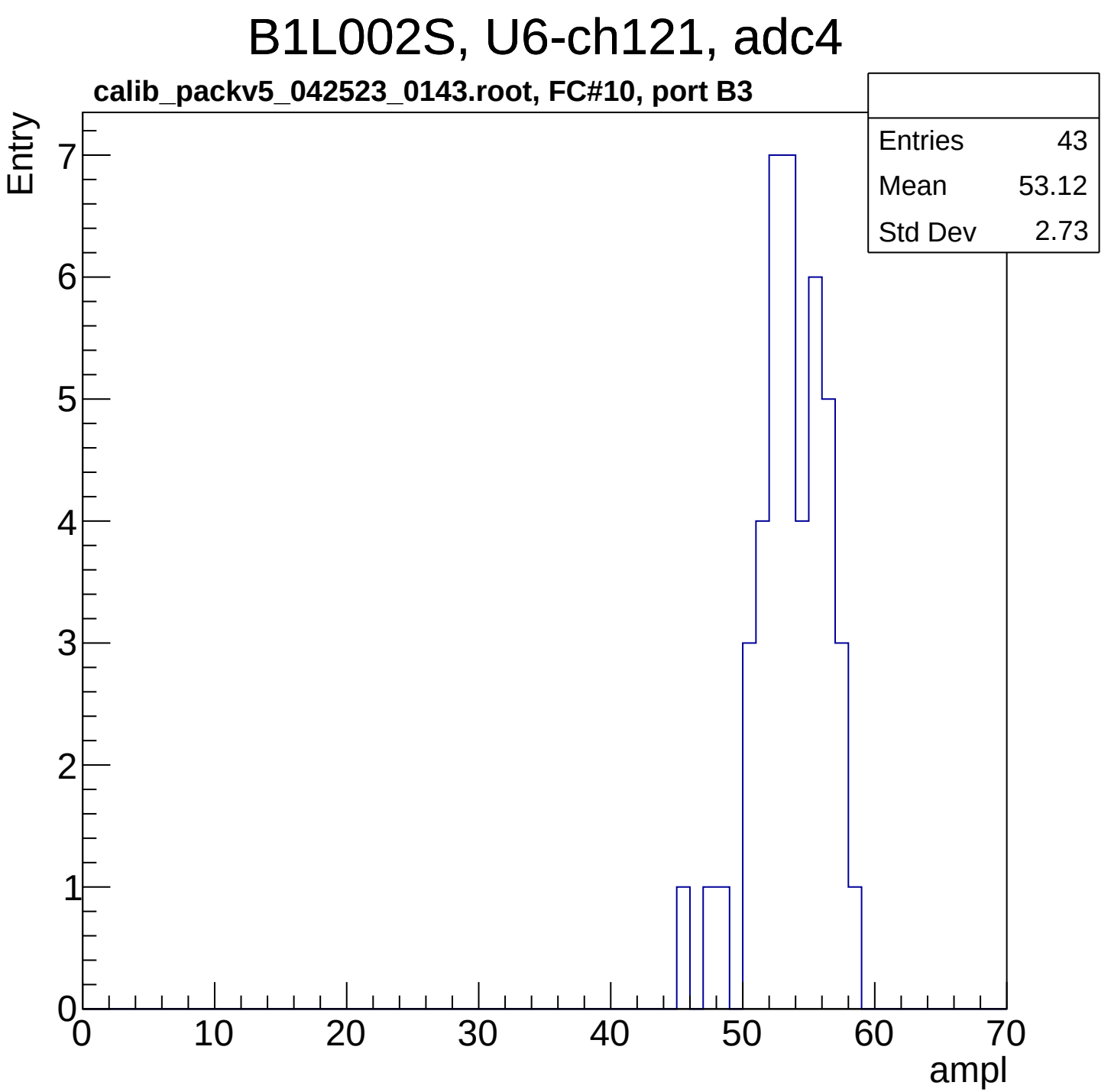

B1L002S, U6-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

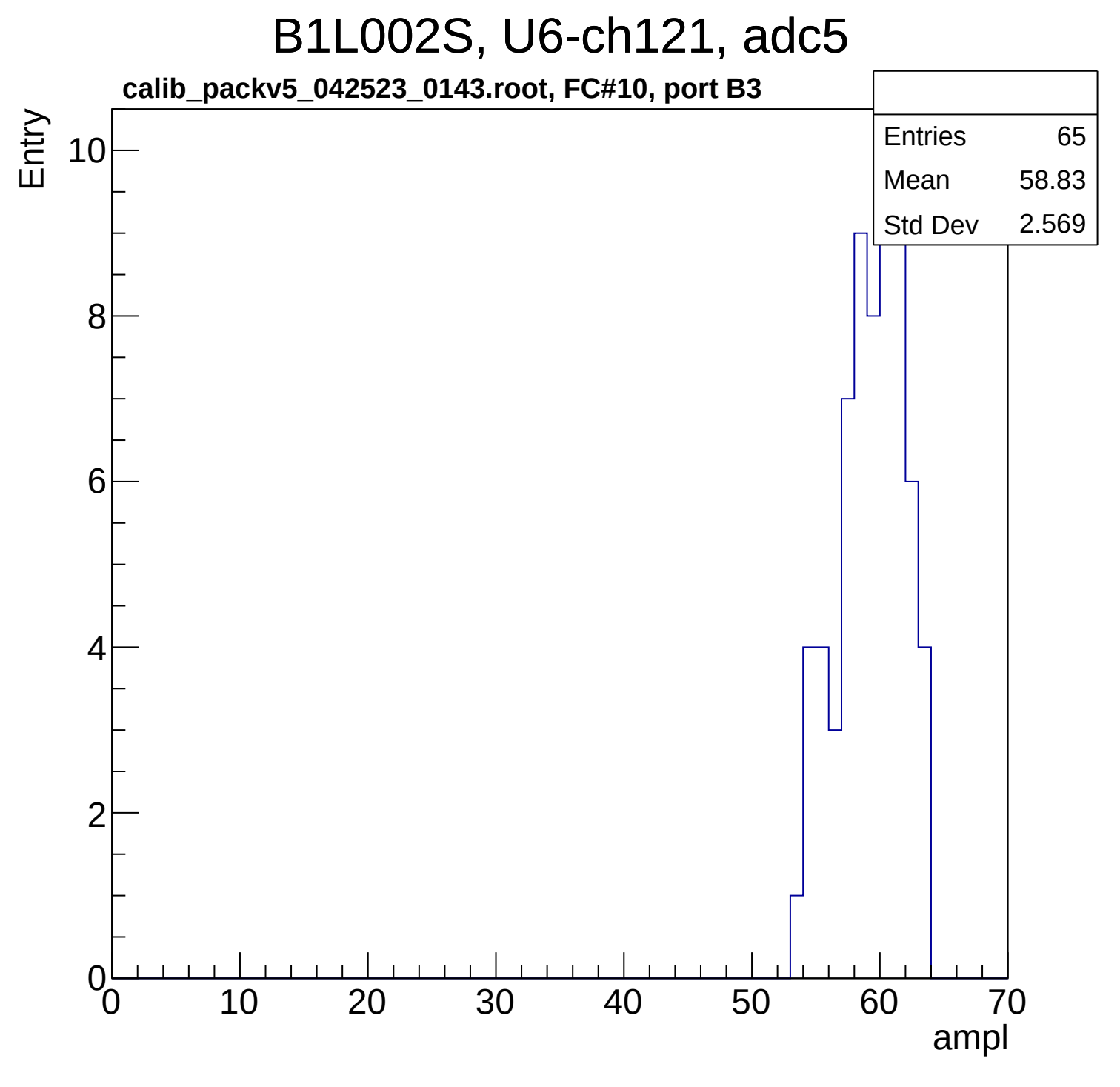

B1L002S, U6-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.21
Std Dev 3.255
Gaus mean : 28.6673
Gaus Width: 3.0222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

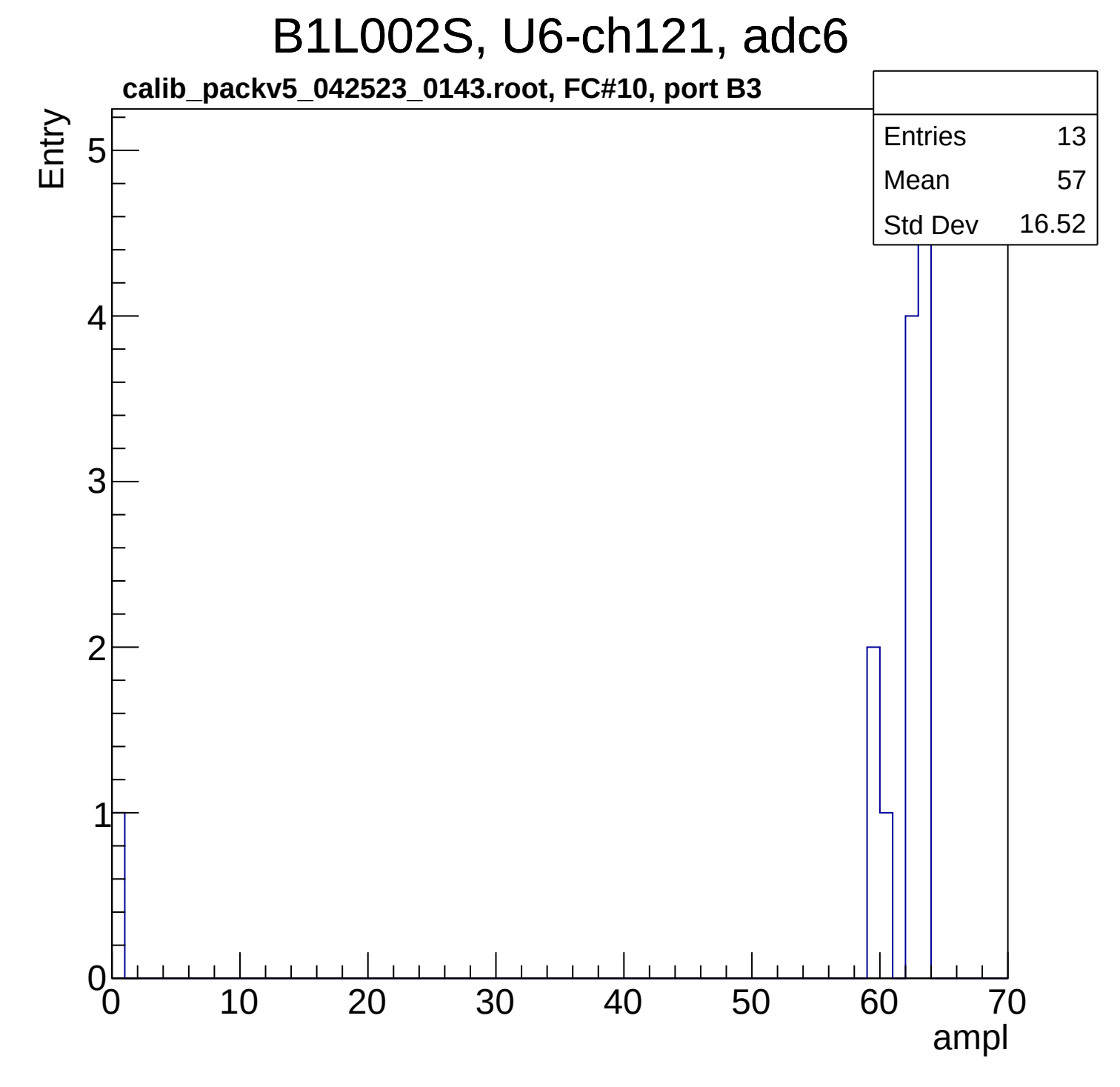

B1L002S, U6-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.3
Std Dev 3.114
Gaus mean : 36.5830
Gaus Width: 2.7370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

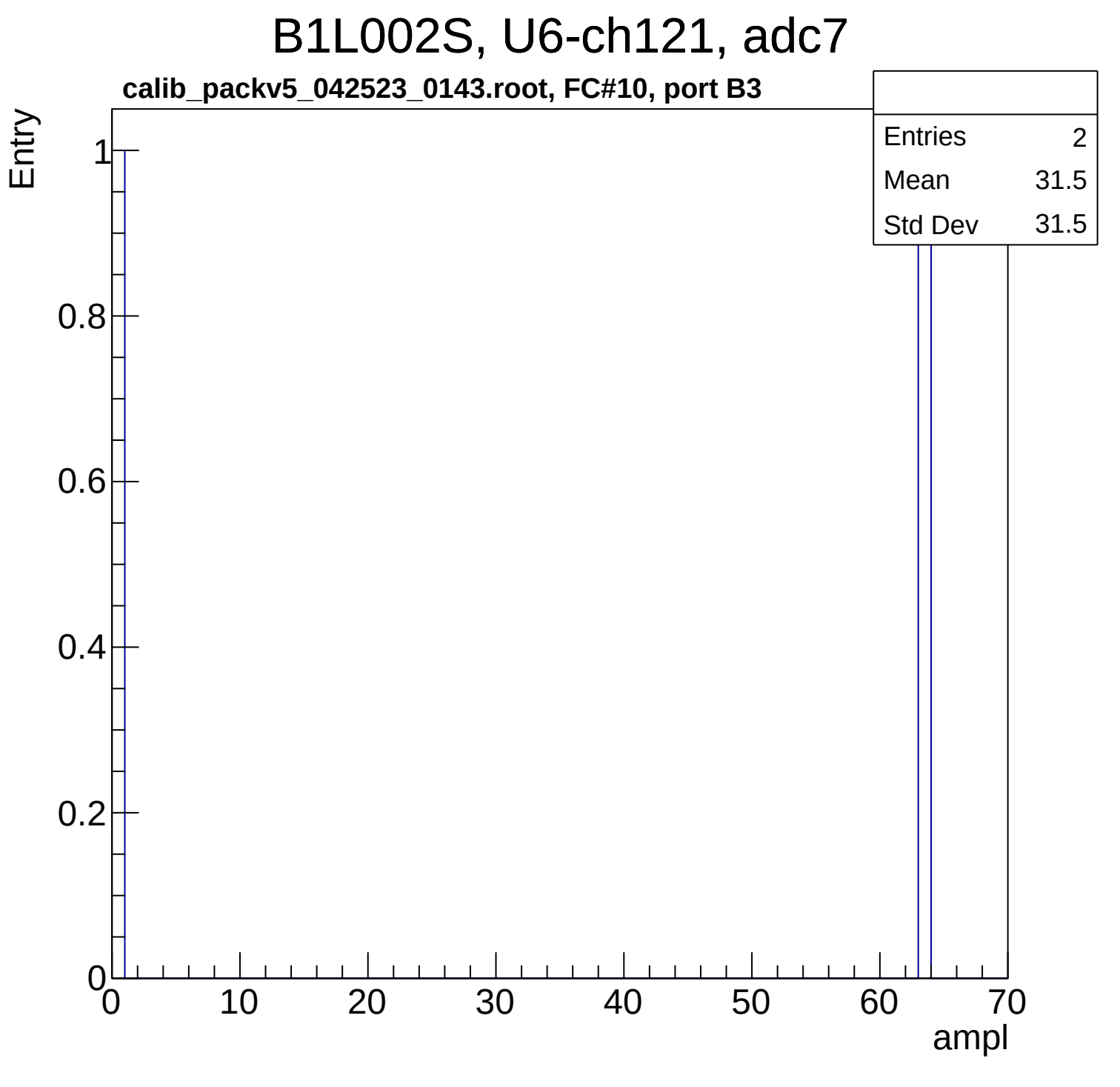

B1L002S, U6-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.56
Std Dev 3.132
Gaus mean : 42.0487
Gaus Width: 3.4133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

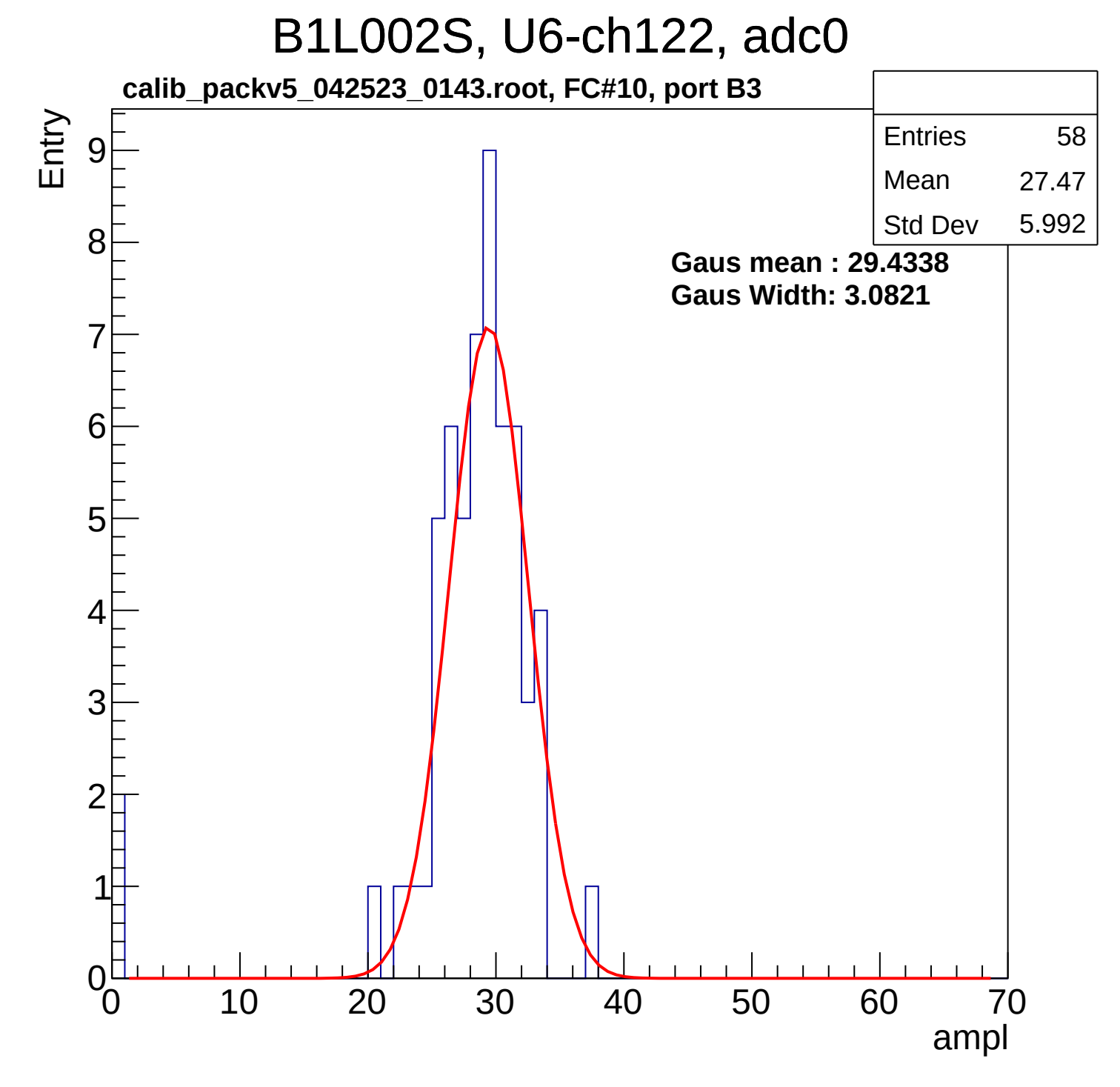

B1L002S, U6-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.37
Std Dev 3.076
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

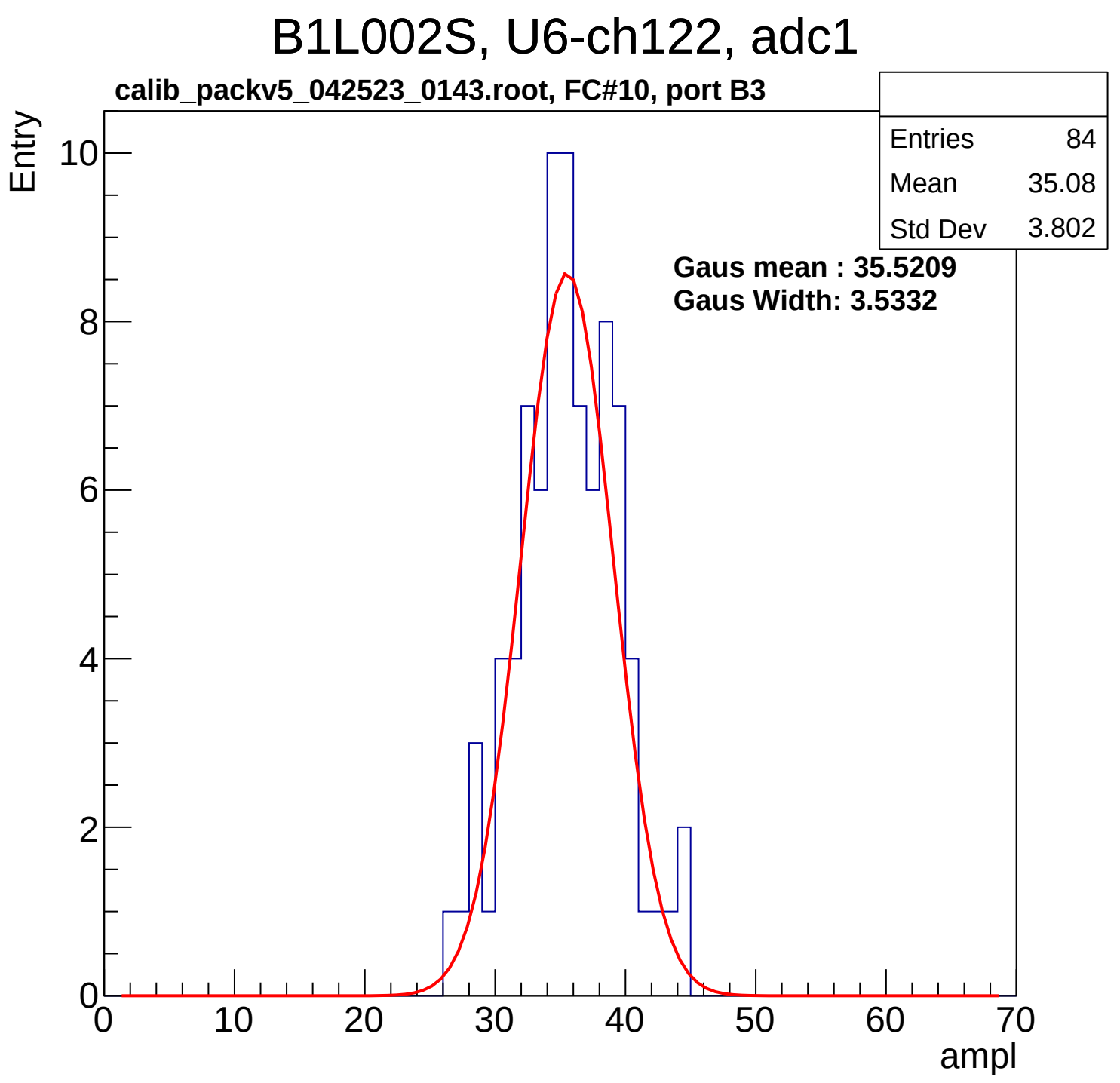

B1L002S, U6-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.12
Std Dev 2.73
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

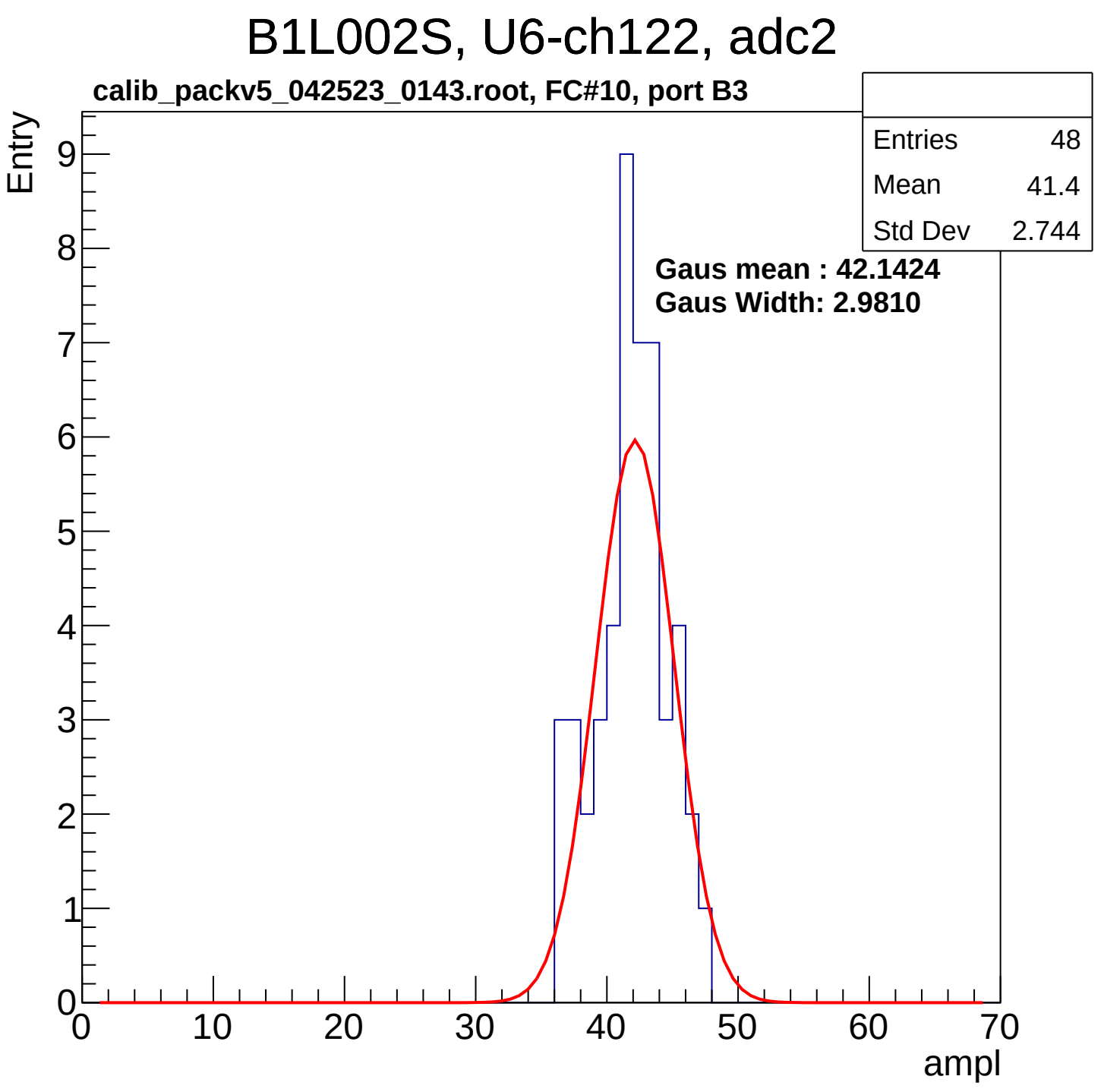

B1L002S, U6-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.83
Std Dev 2.569
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

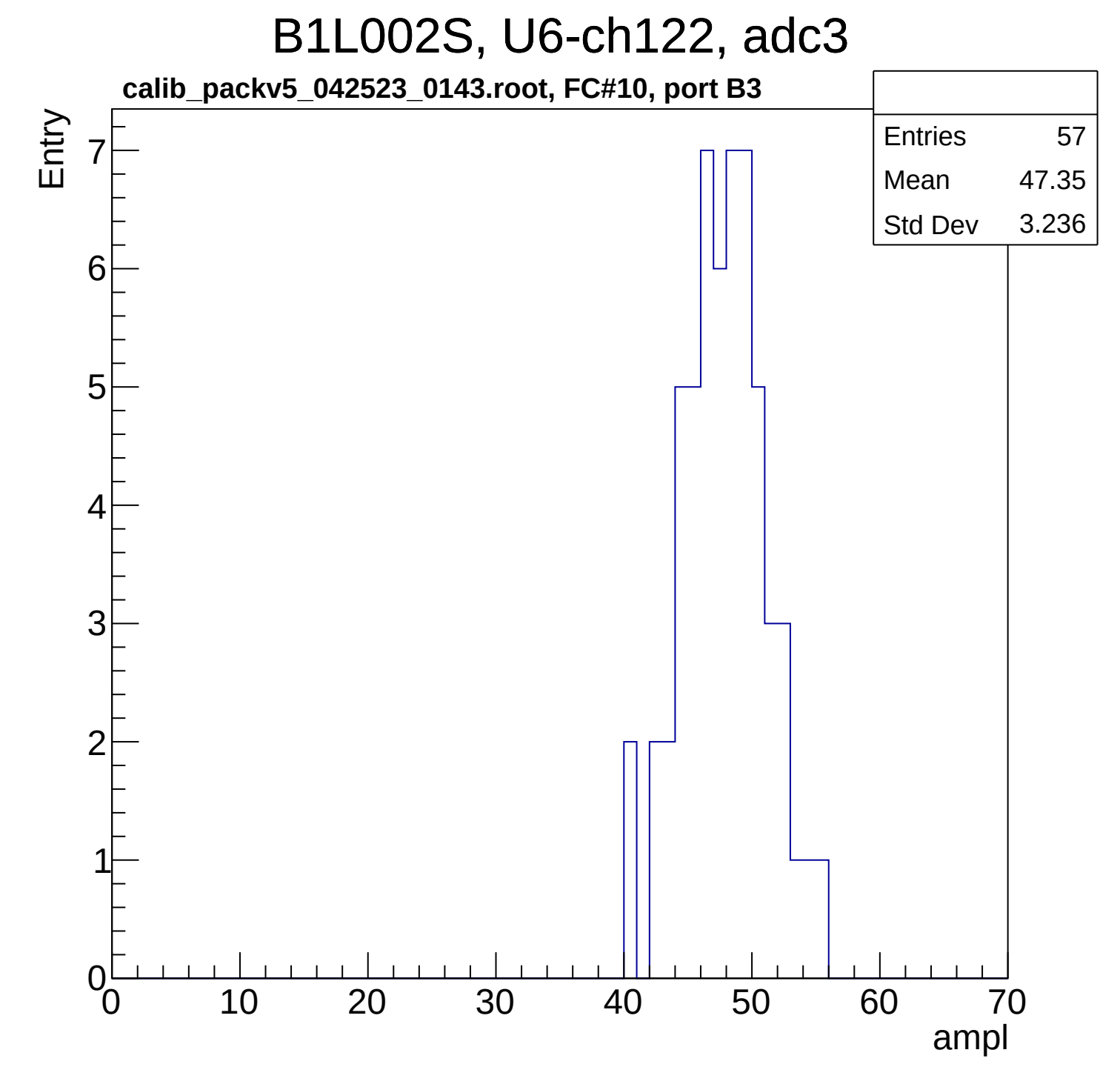

B1L002S, U6-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57
Std Dev 16.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

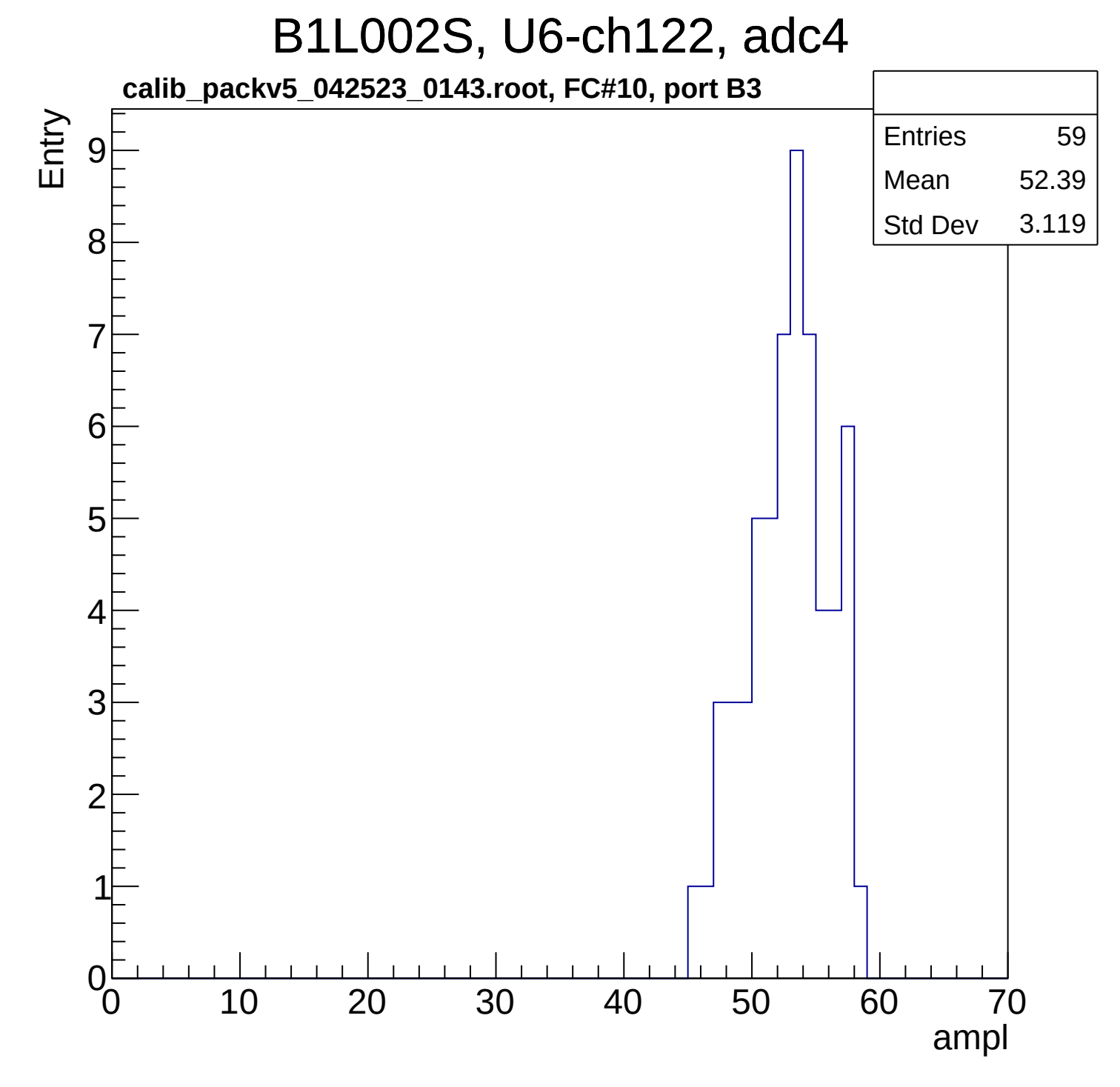

B1L002S, U6-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

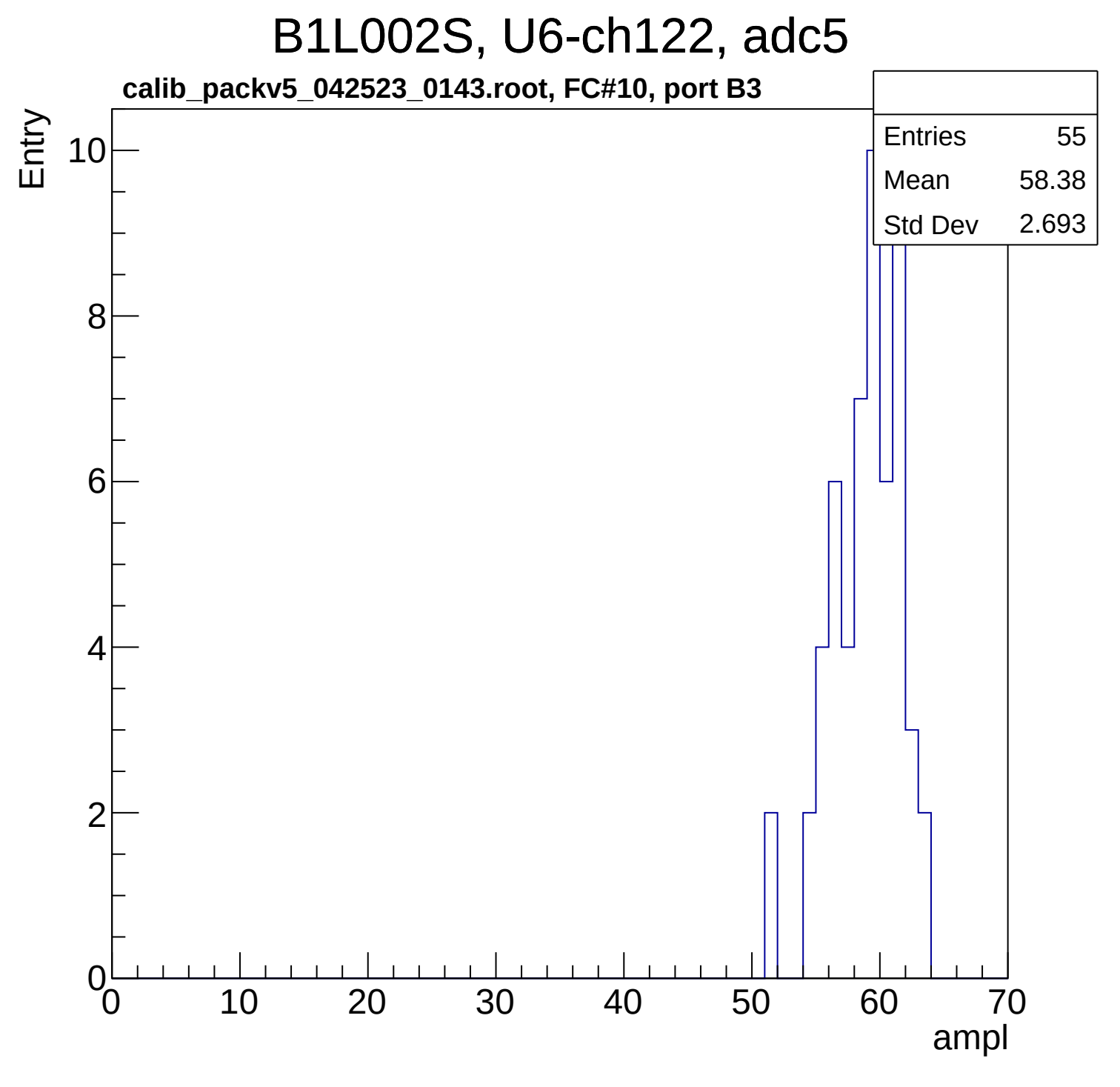

B1L002S, U6-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.47
Std Dev 5.992
Gaus mean : 29.4338
Gaus Width: 3.0821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

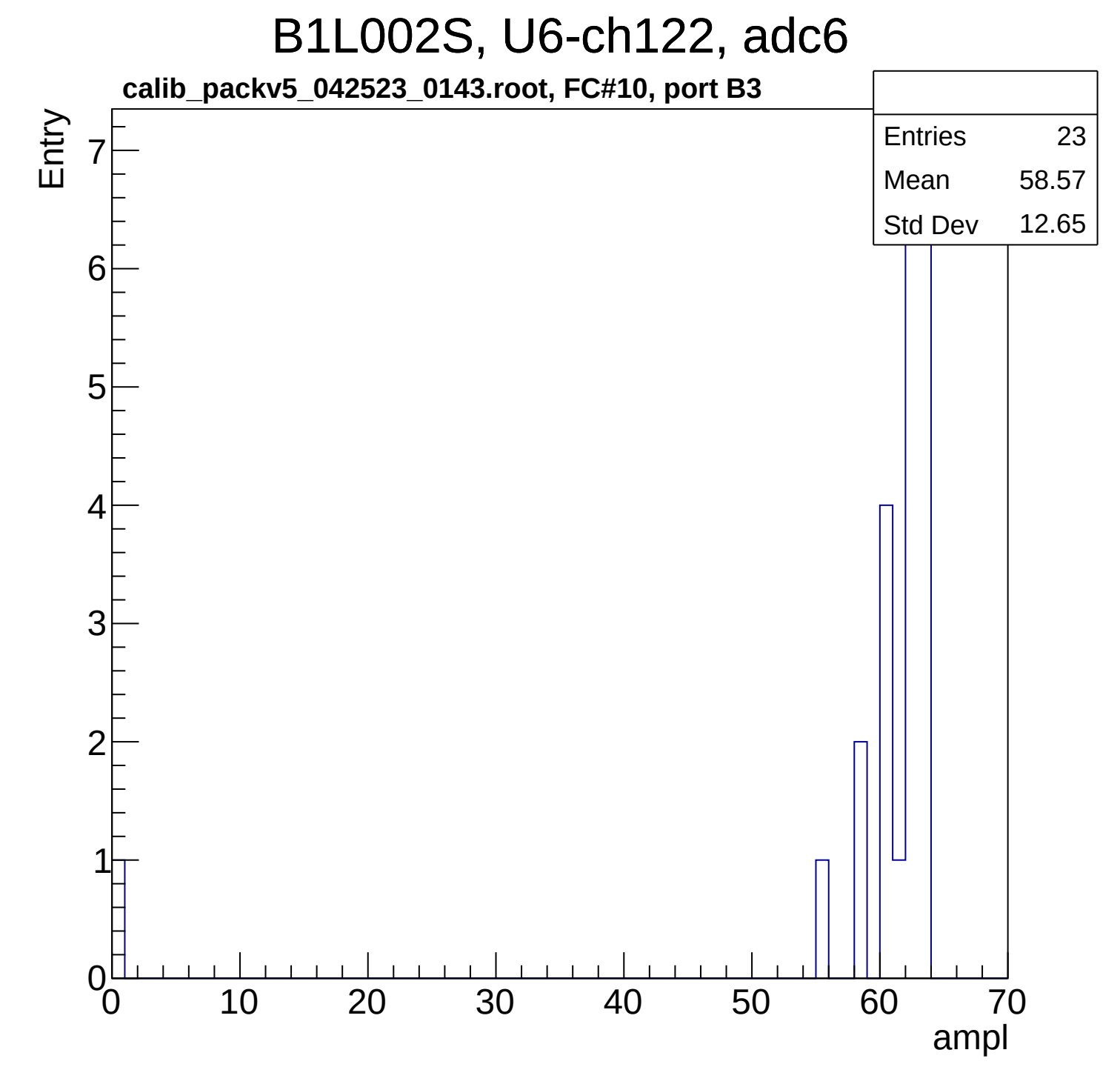

B1L002S, U6-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 35.08
Std Dev 3.802
Gaus mean : 35.5209
Gaus Width: 3.5332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

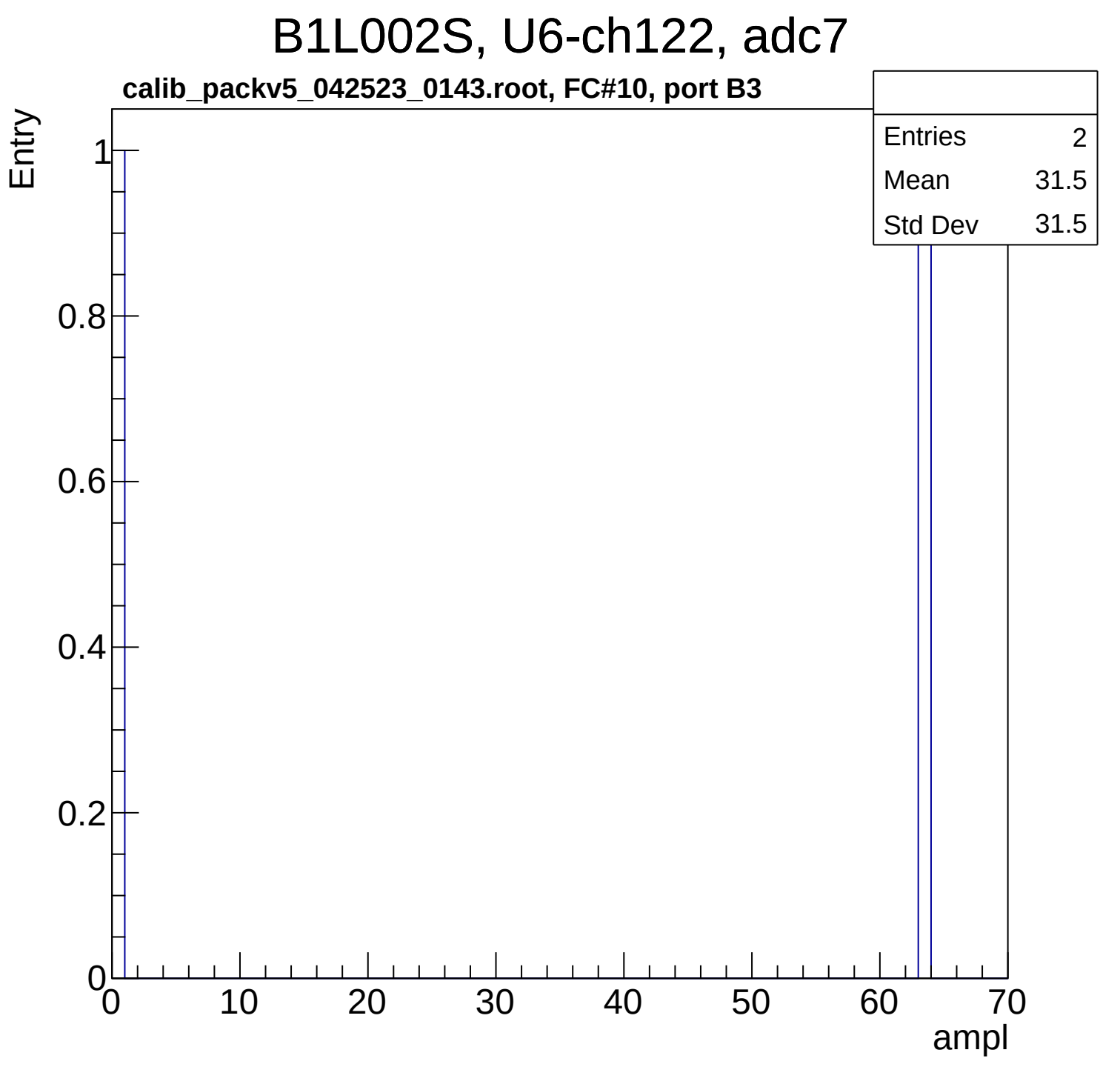

B1L002S, U6-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.4
Std Dev 2.744
Gaus mean : 42.1424
Gaus Width: 2.9810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

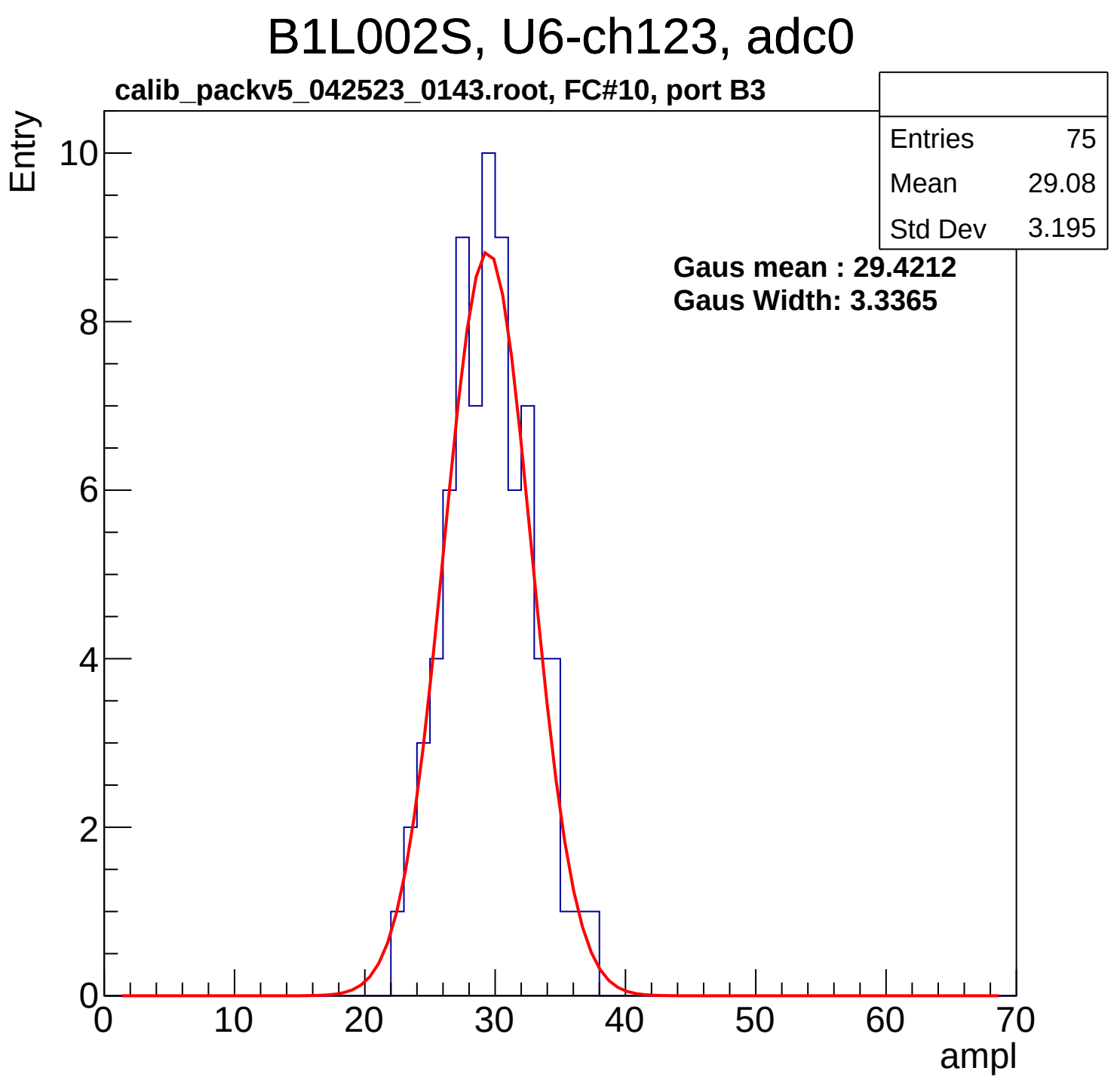

B1L002S, U6-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.35
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

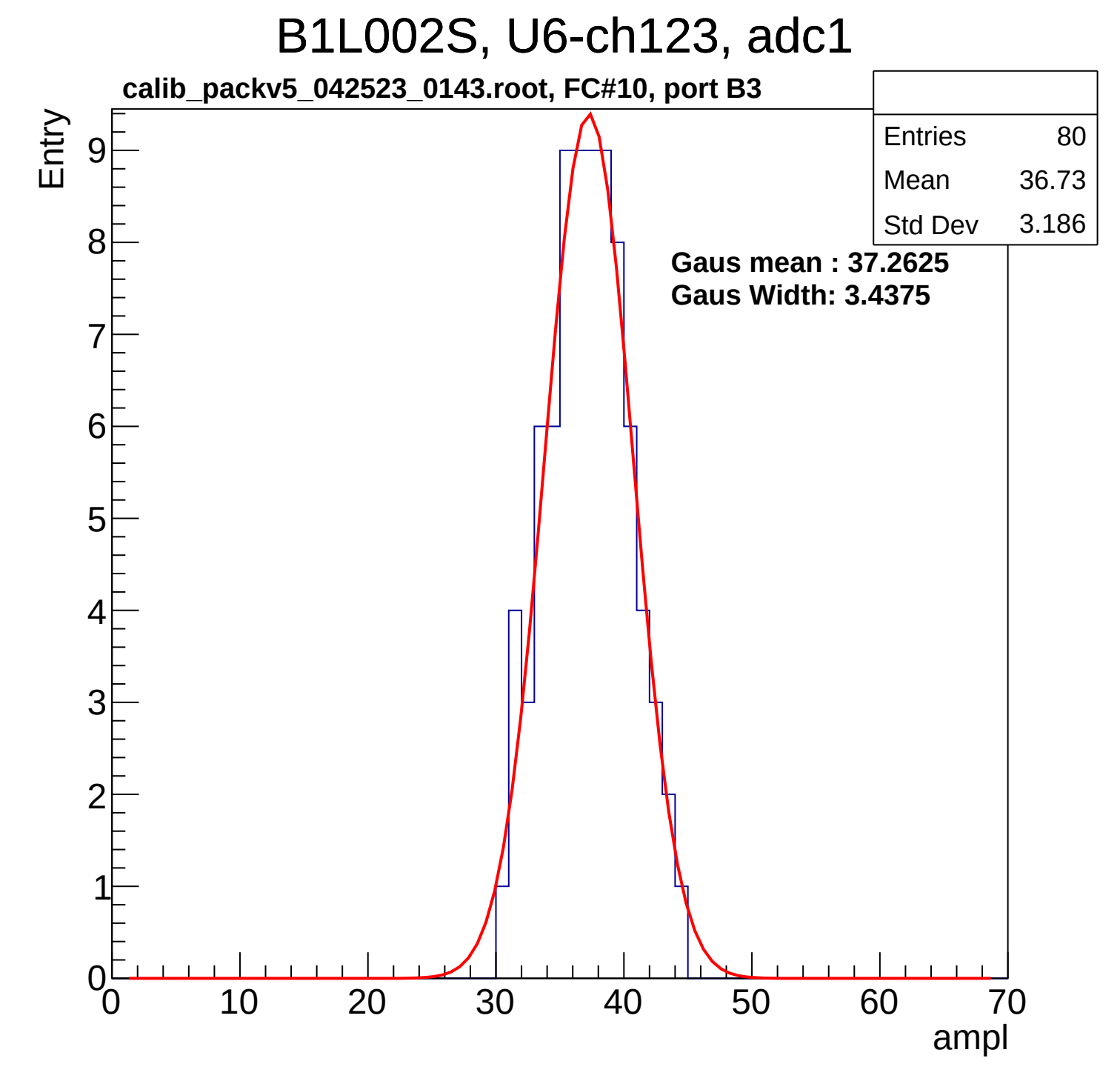

B1L002S, U6-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.39
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

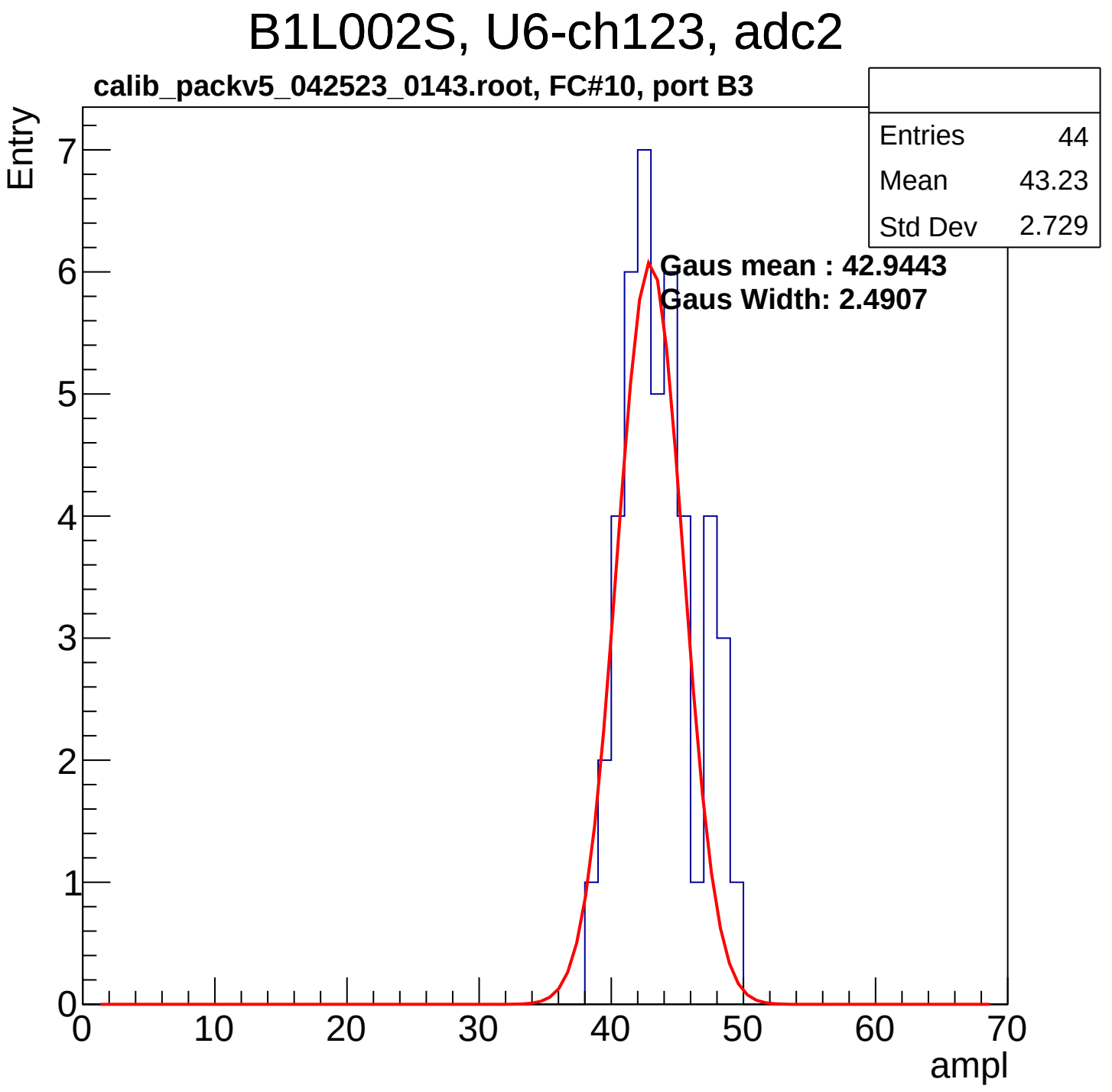

B1L002S, U6-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.38
Std Dev 2.693
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

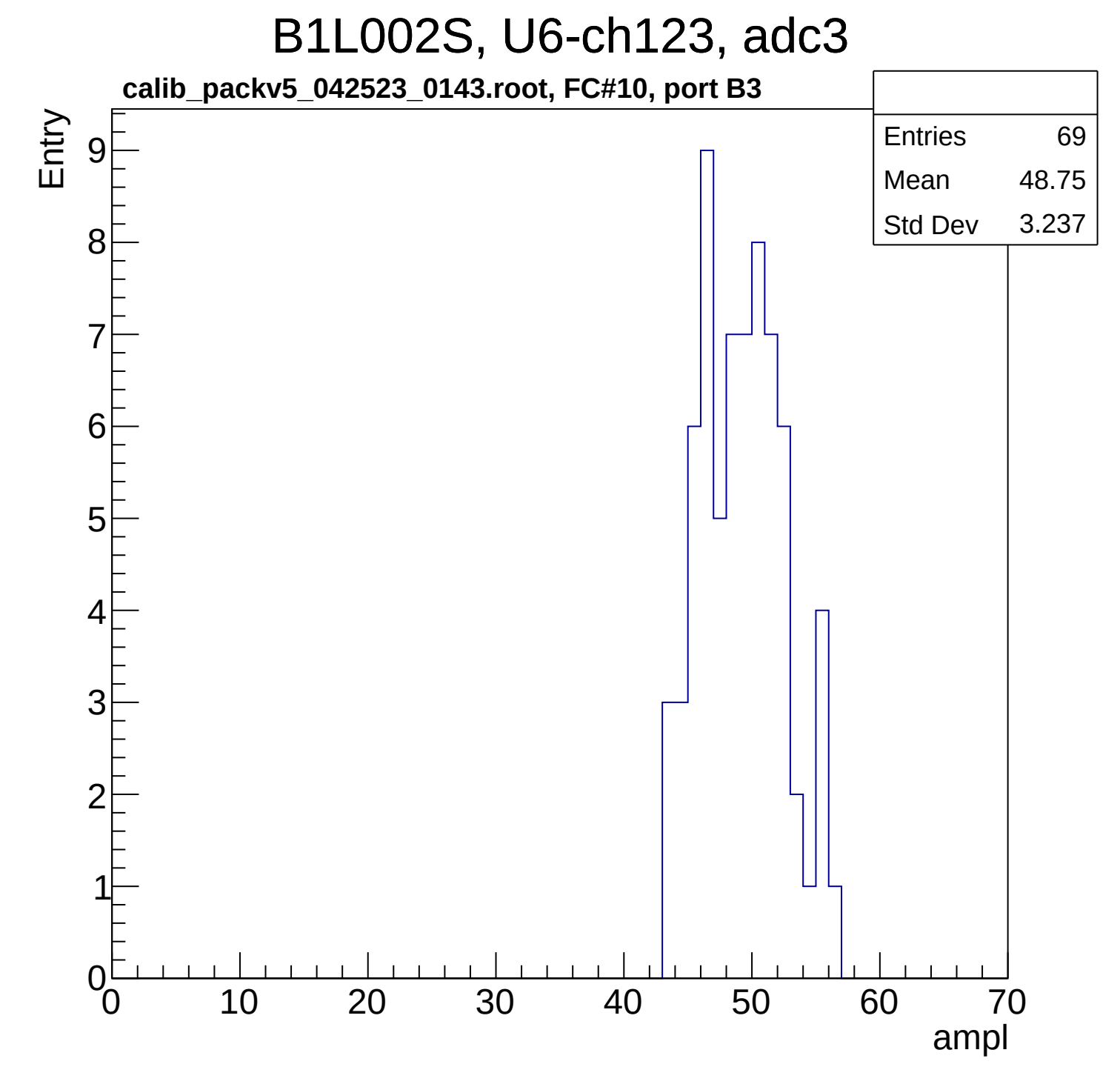

B1L002S, U6-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.57
Std Dev 12.65
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

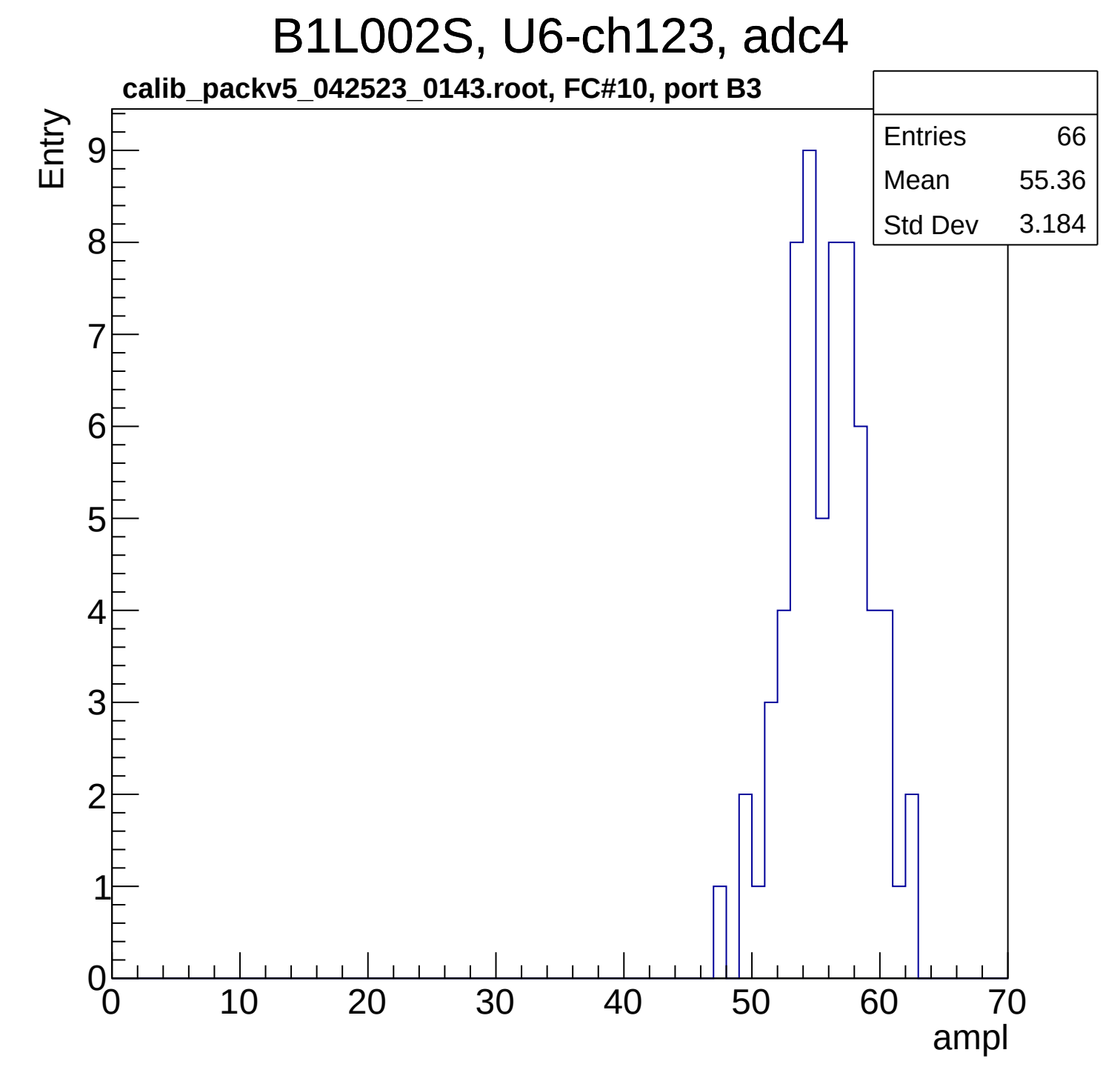

B1L002S, U6-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

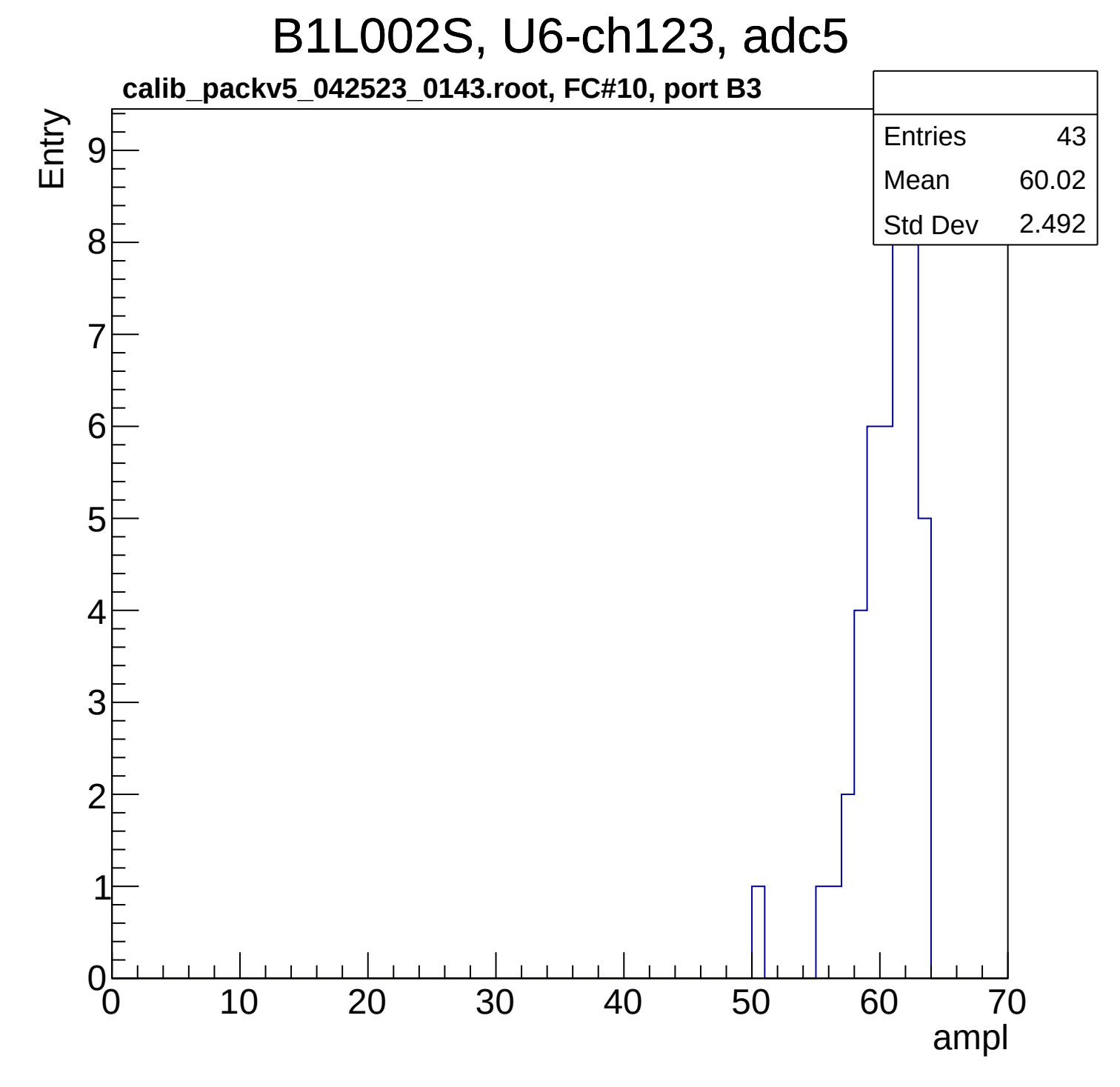

B1L002S, U6-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 29.08
Std Dev 3.195
Gaus mean : 29.4212
Gaus Width: 3.3365
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

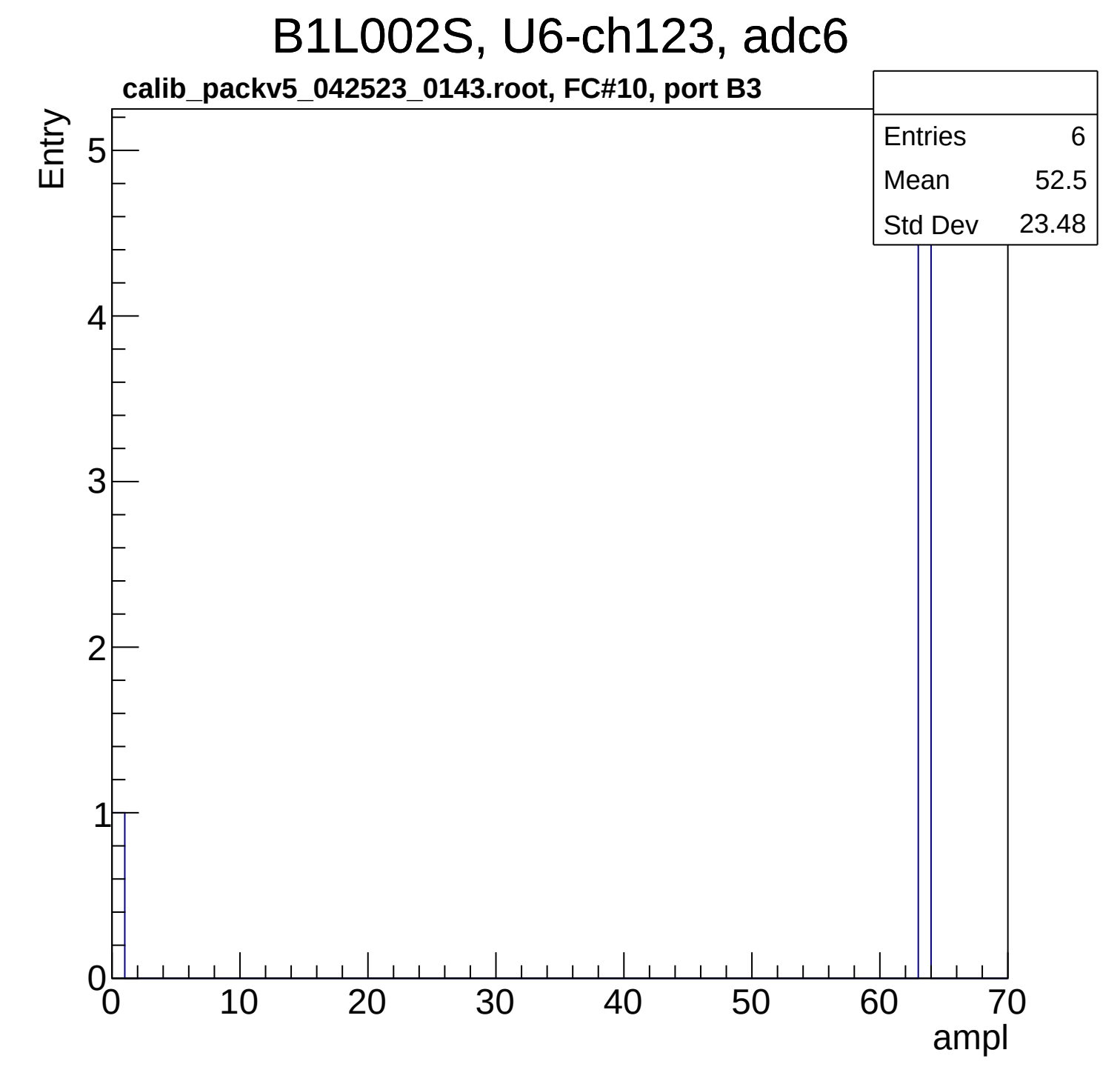

B1L002S, U6-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 36.73
Std Dev 3.186
Gaus mean : 37.2625
Gaus Width: 3.4375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

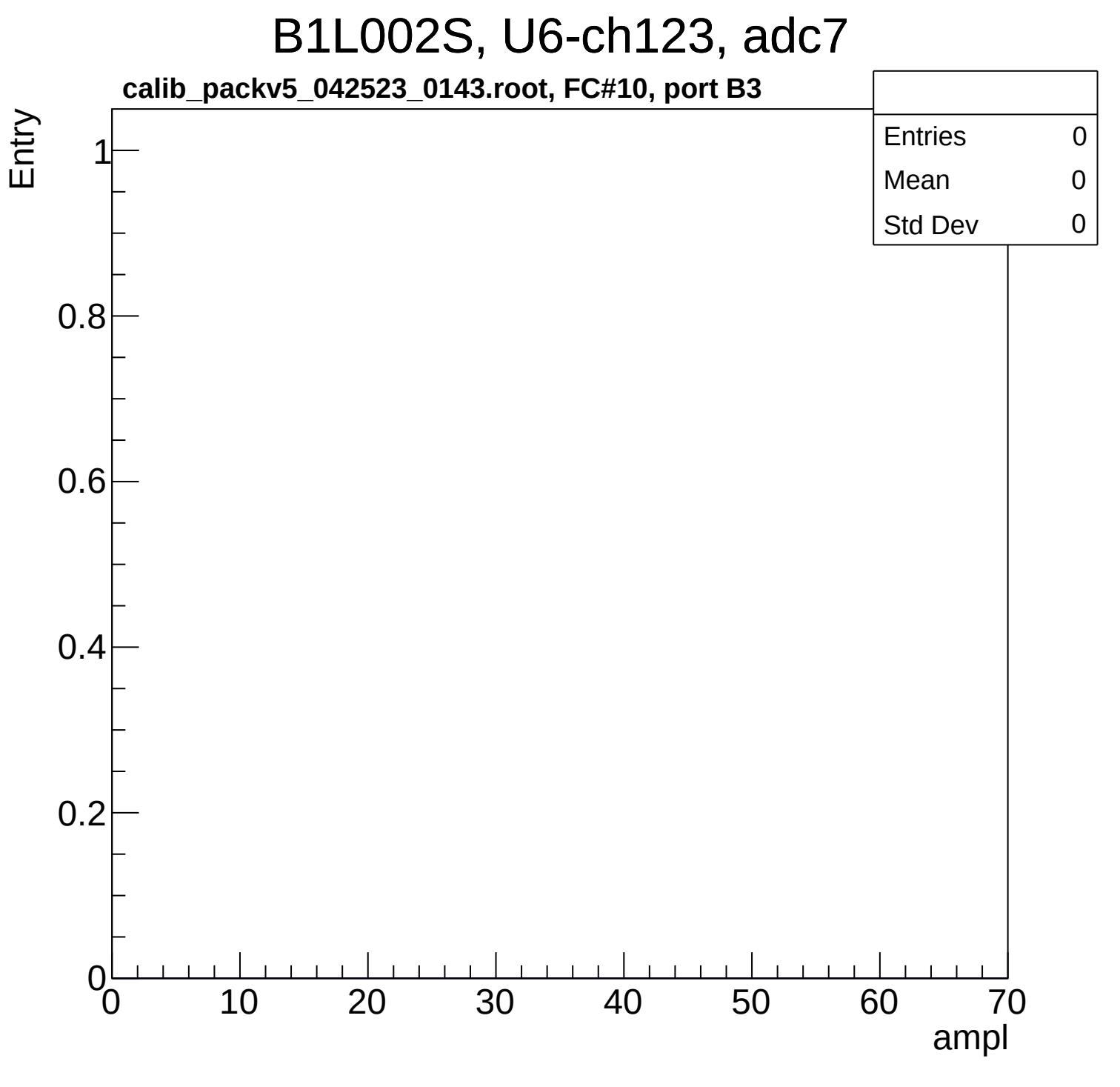

B1L002S, U6-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 43.23
Std Dev 2.729
Gaus mean : 42.9443
Gaus Width: 2.4907
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

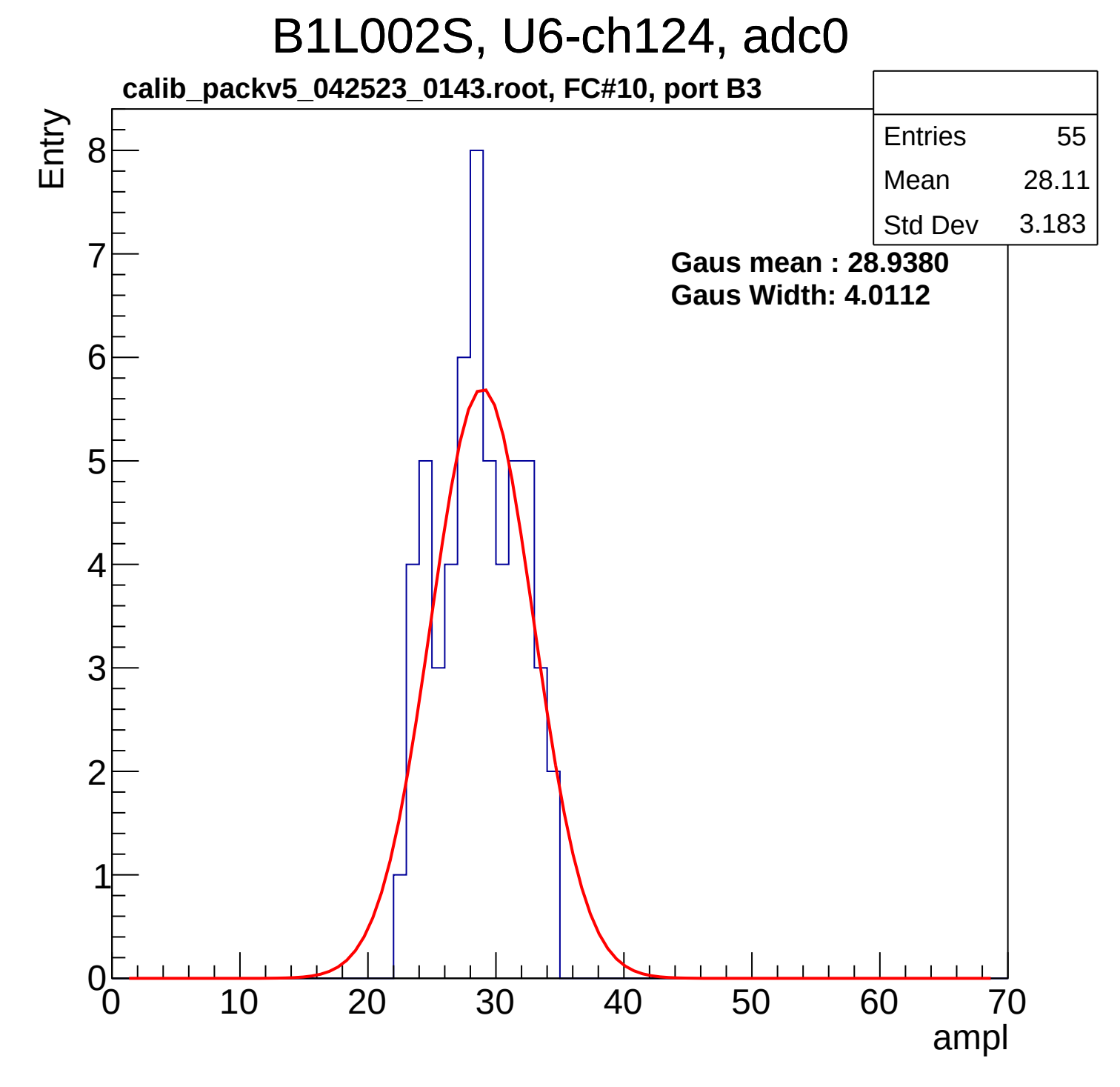

B1L002S, U6-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.75
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

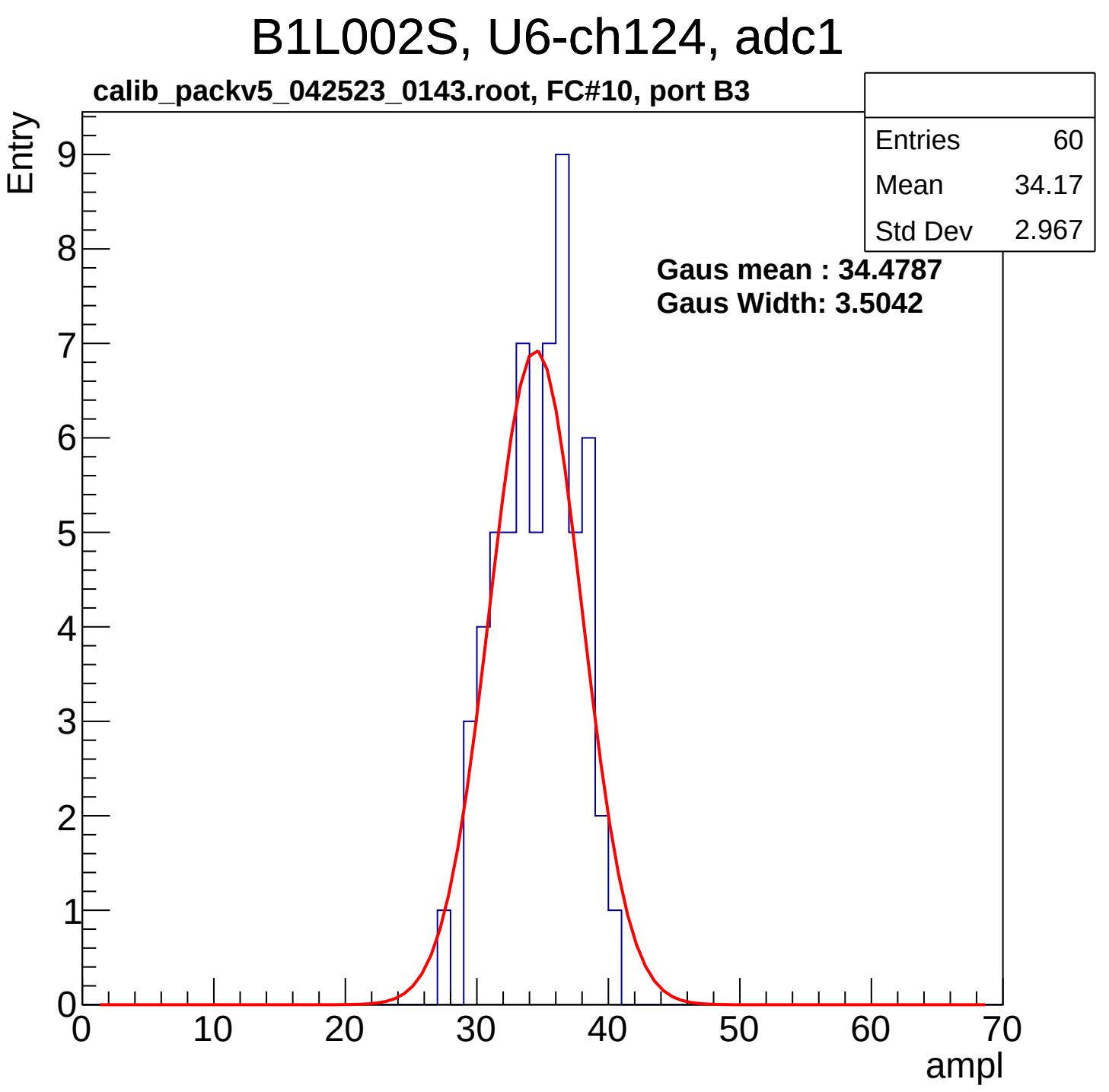

B1L002S, U6-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 55.36
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

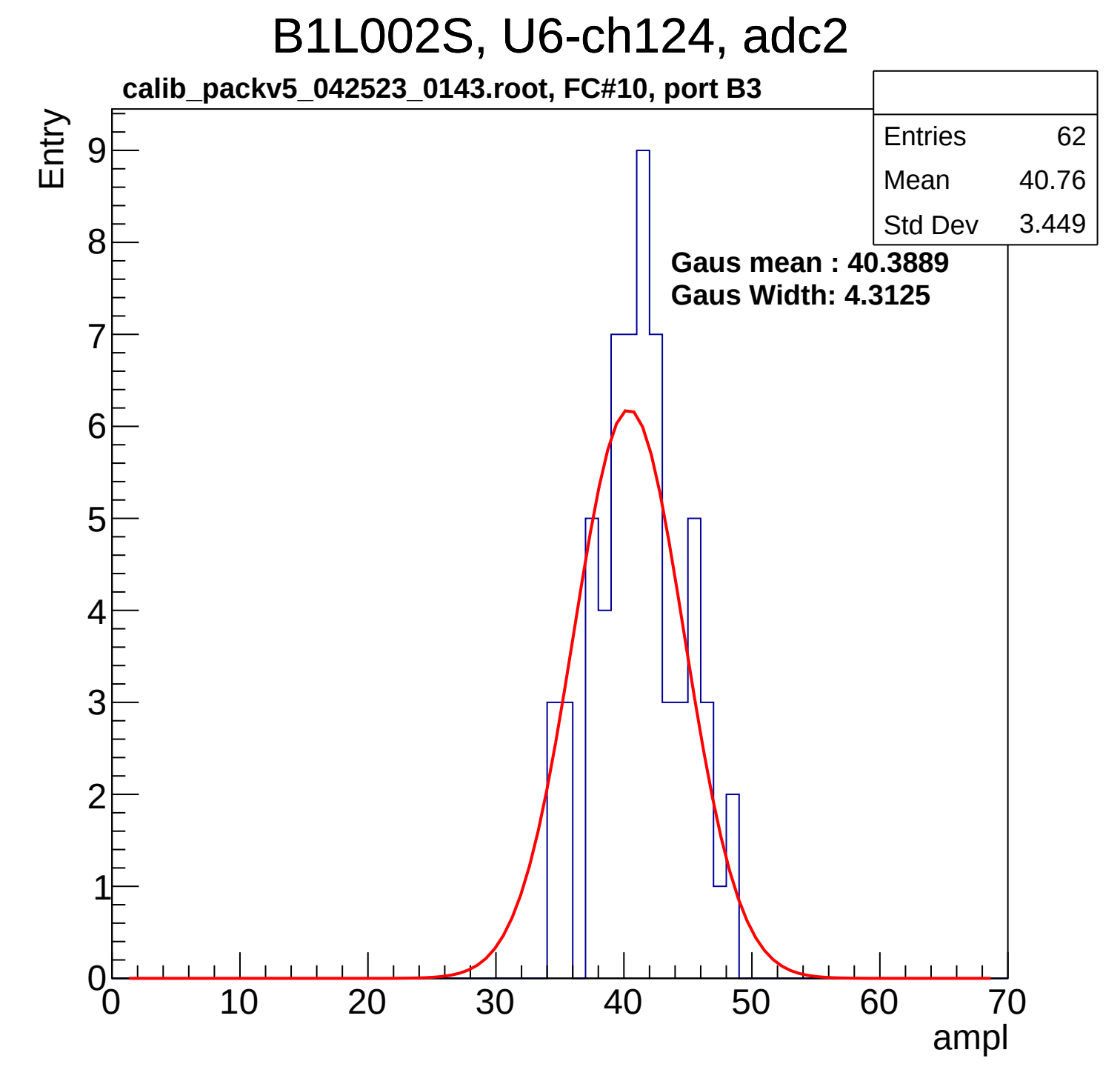

B1L002S, U6-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 60.02
Std Dev 2.492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

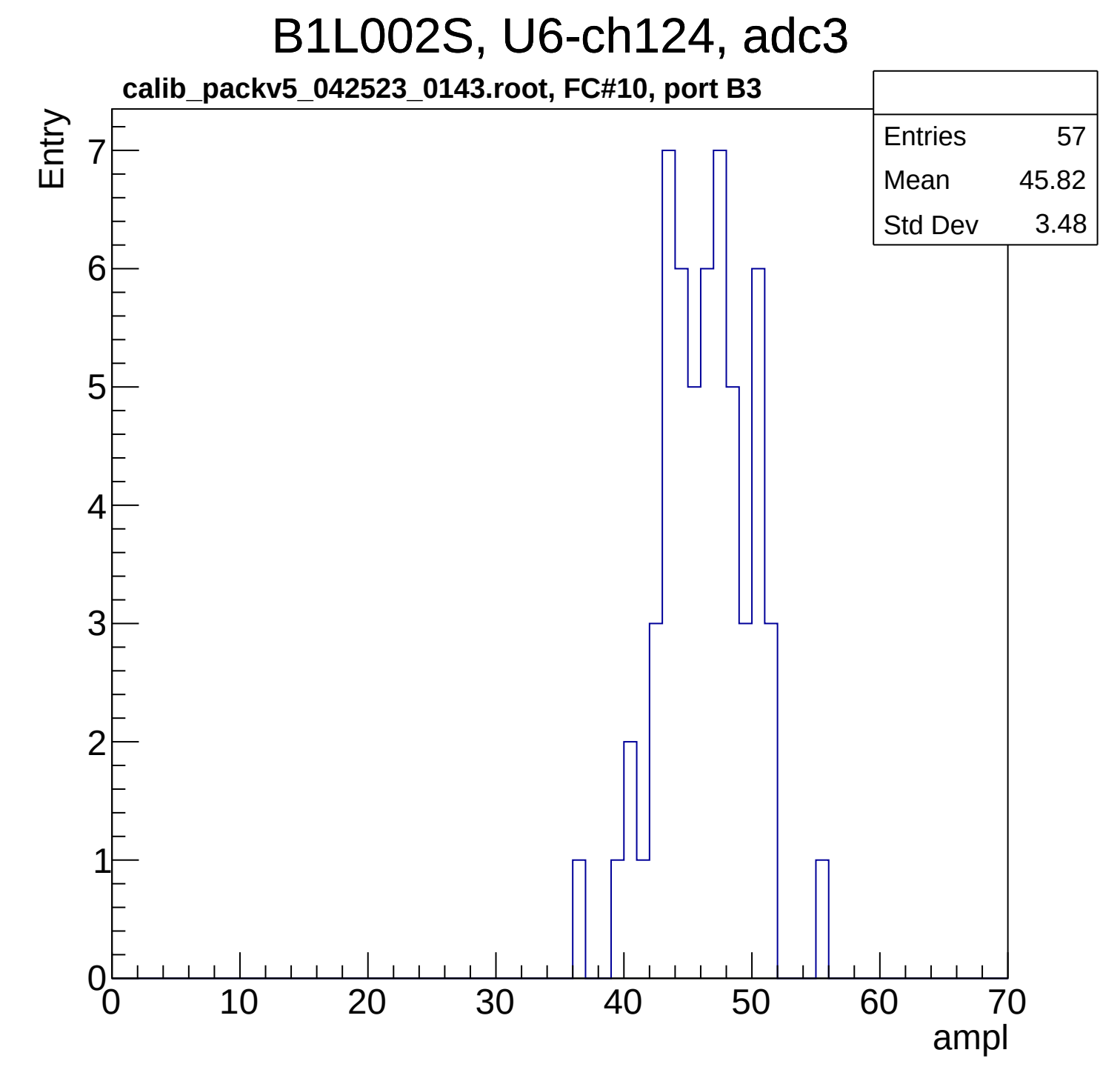

B1L002S, U6-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52.5
Std Dev 23.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

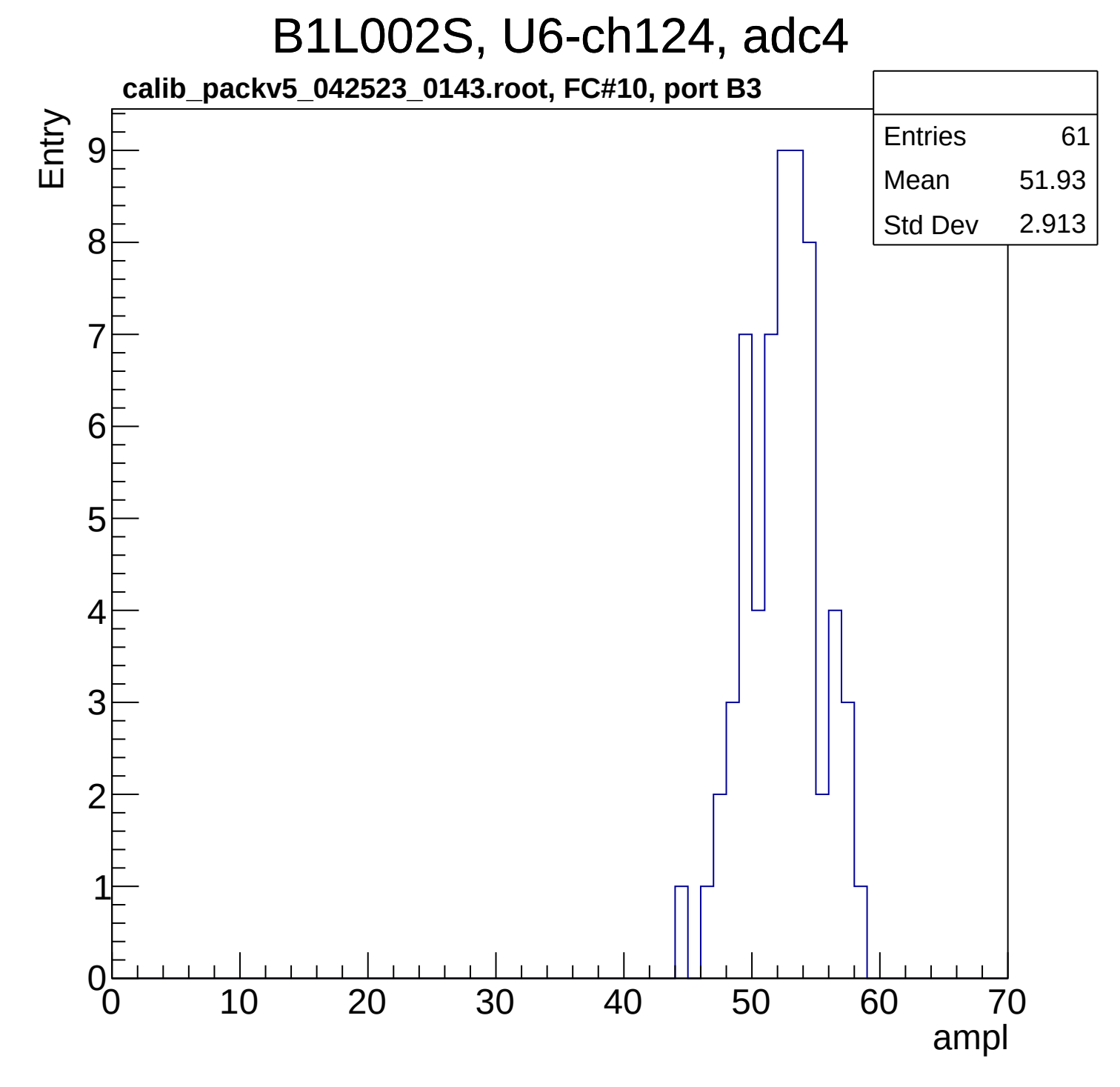

B1L002S, U6-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

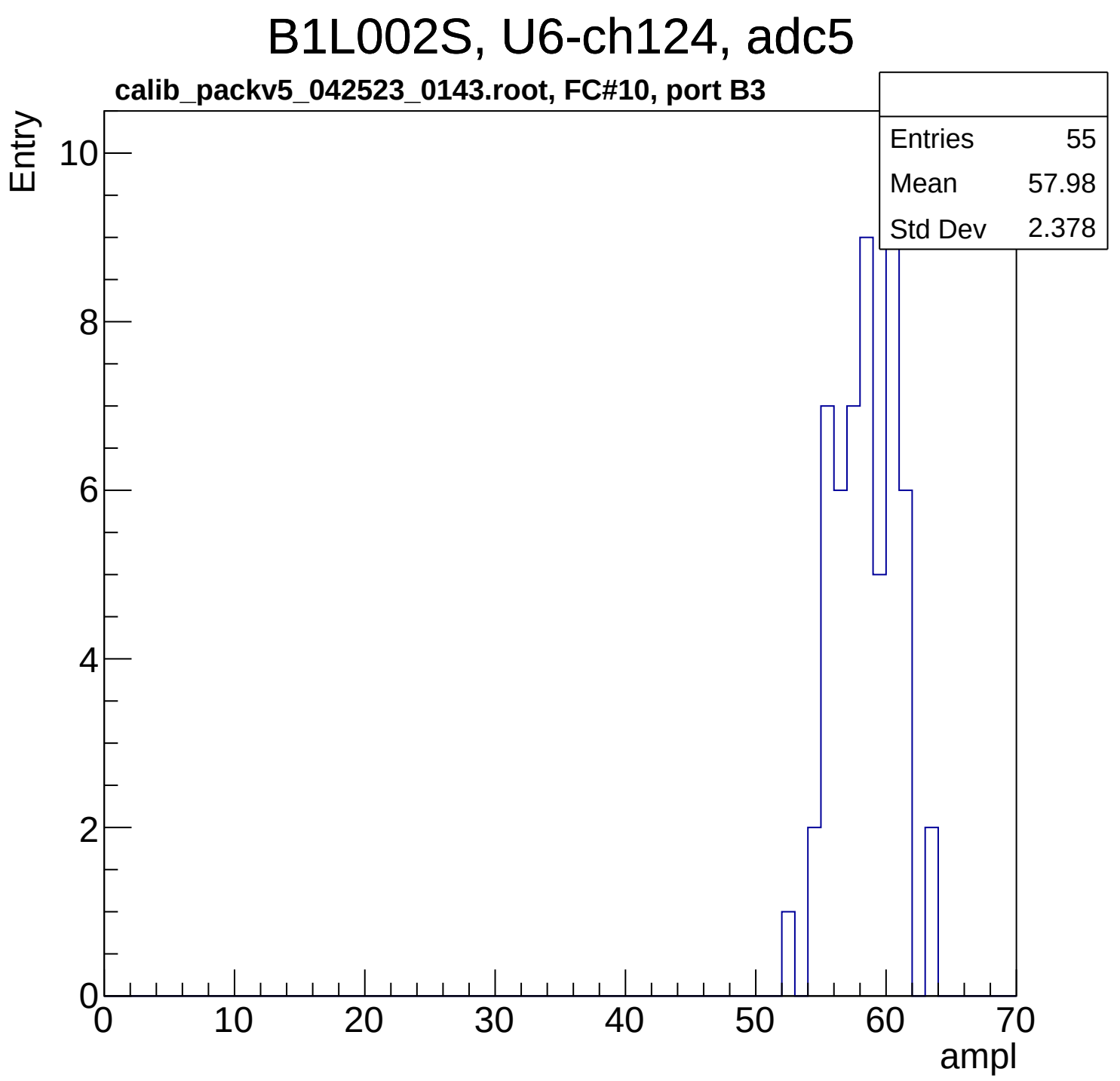

B1L002S, U6-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.11
Std Dev 3.183
Gaus mean : 28.9380
Gaus Width: 4.0112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

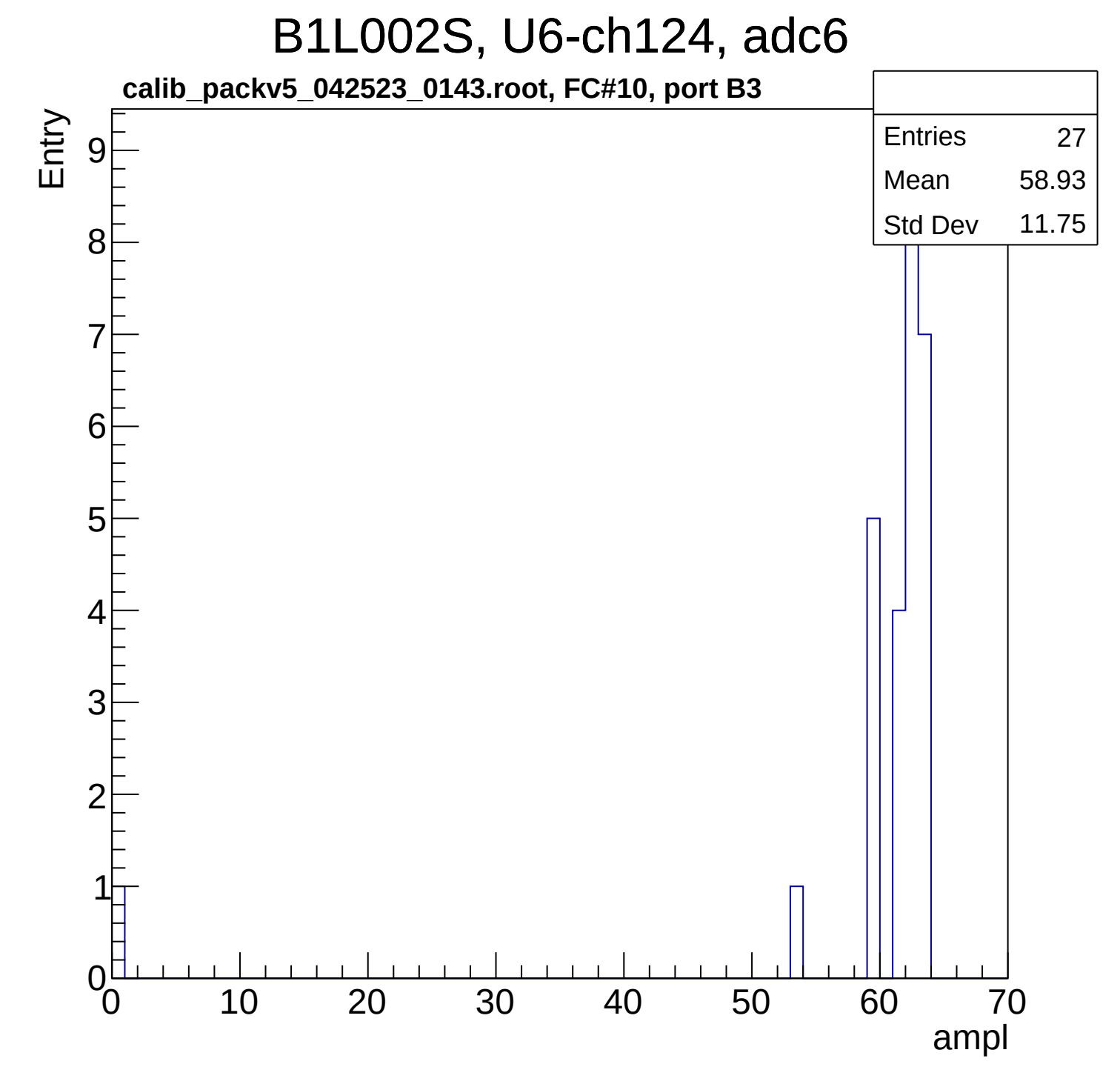

B1L002S, U6-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.17
Std Dev 2.967
Gaus mean : 34.4787
Gaus Width: 3.5042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

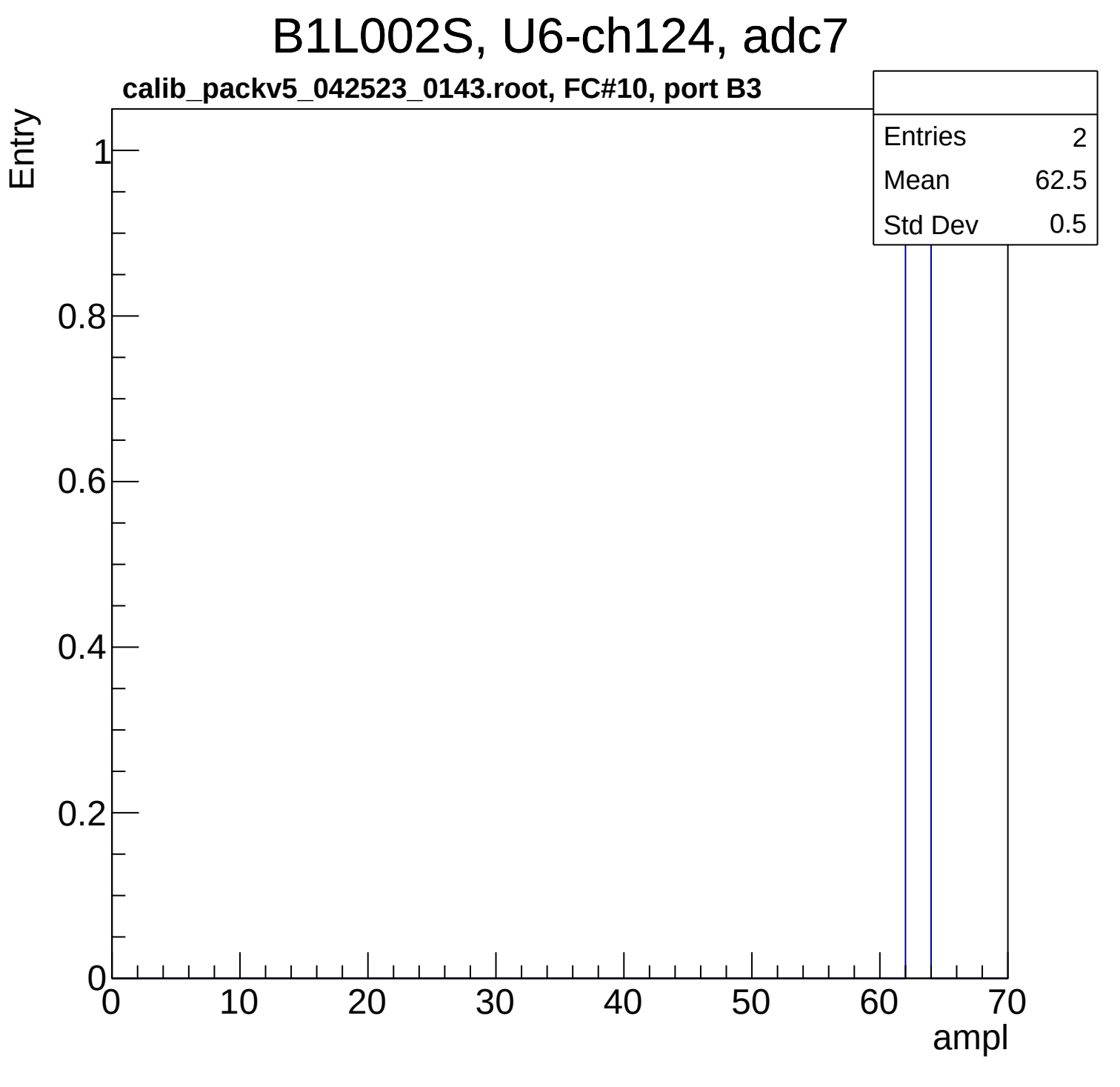

B1L002S, U6-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.76
Std Dev 3.449
Gaus mean : 40.3889
Gaus Width: 4.3125
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

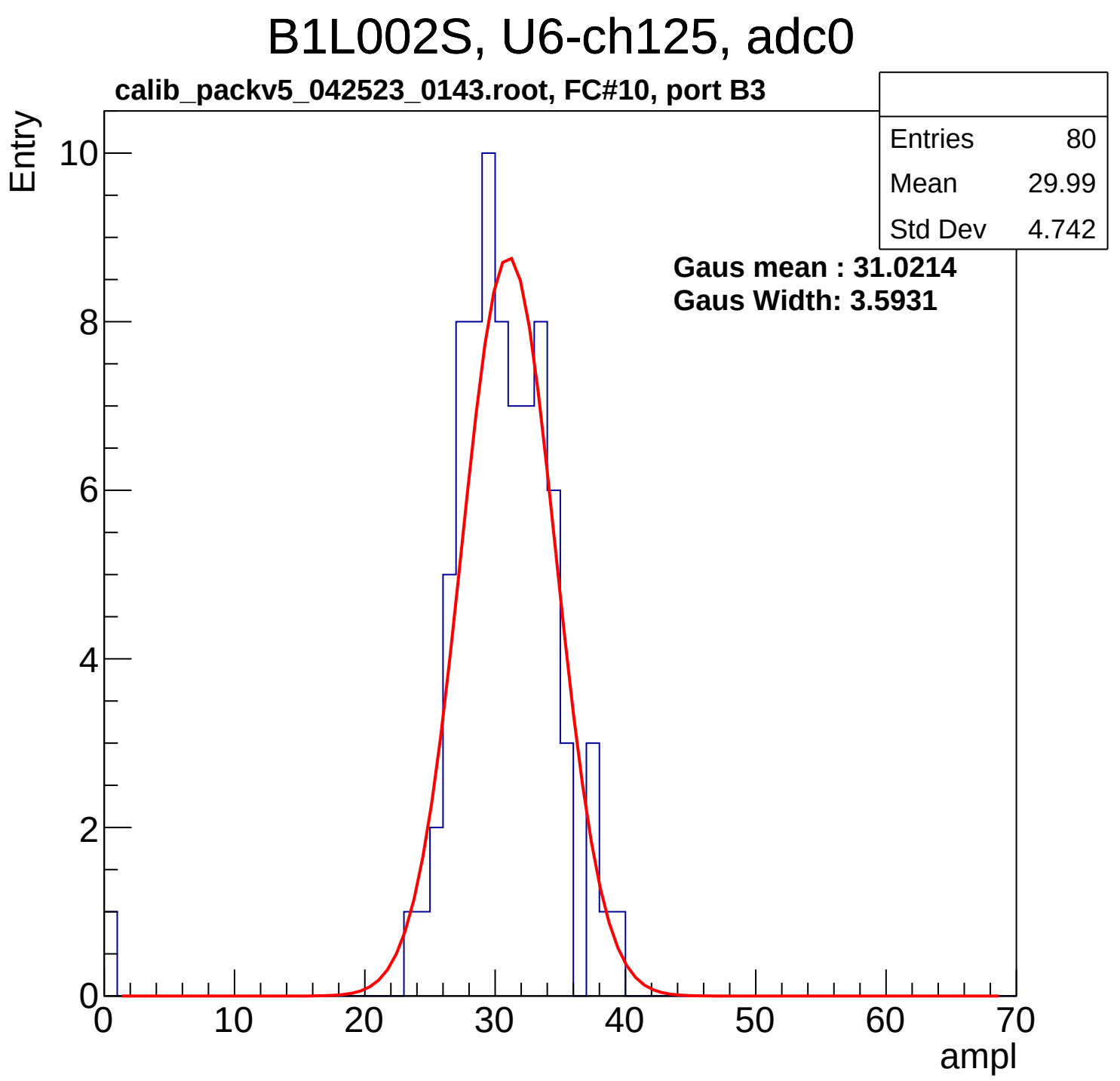

B1L002S, U6-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 45.82
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

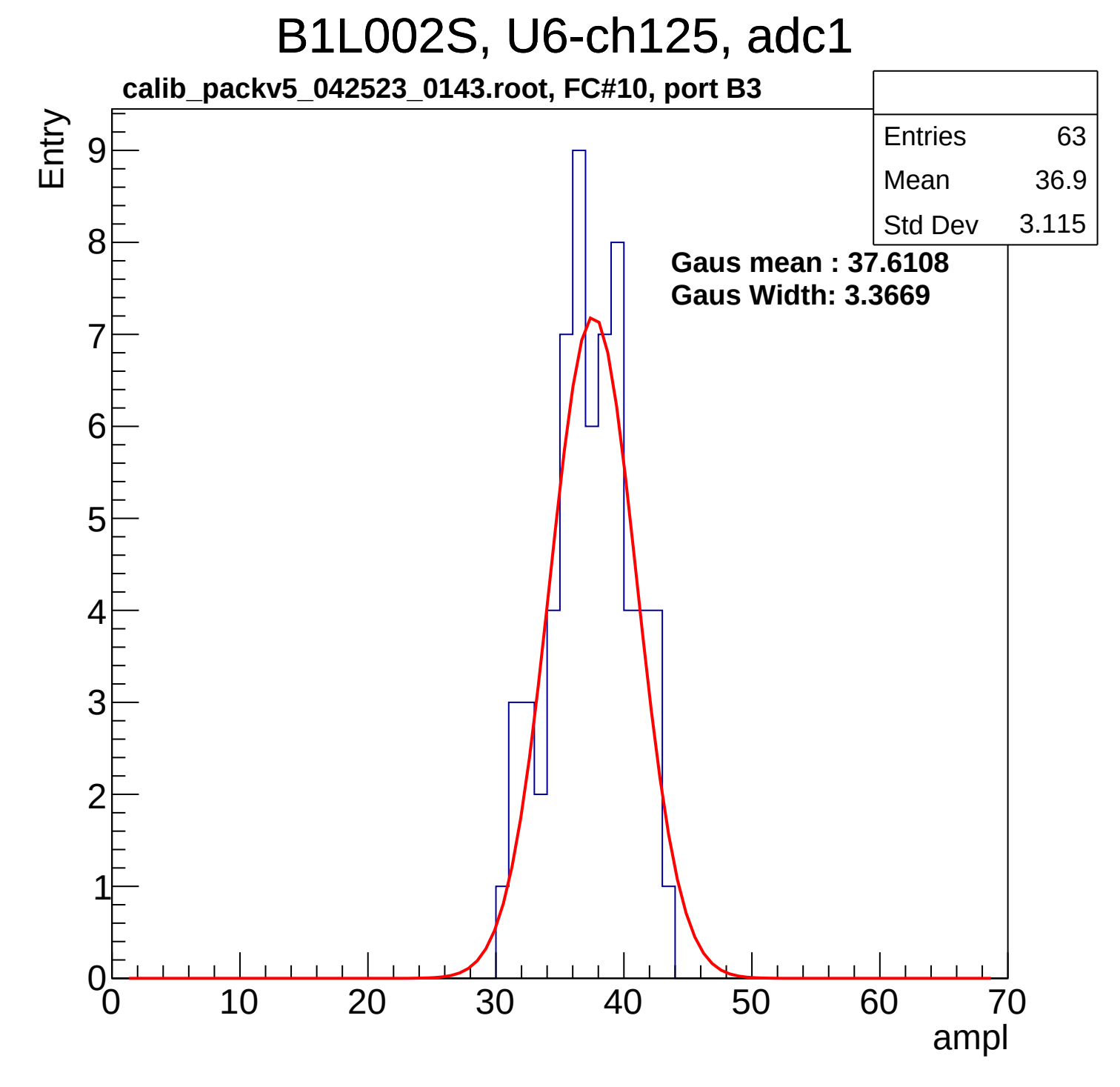

B1L002S, U6-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 51.93
Std Dev 2.913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

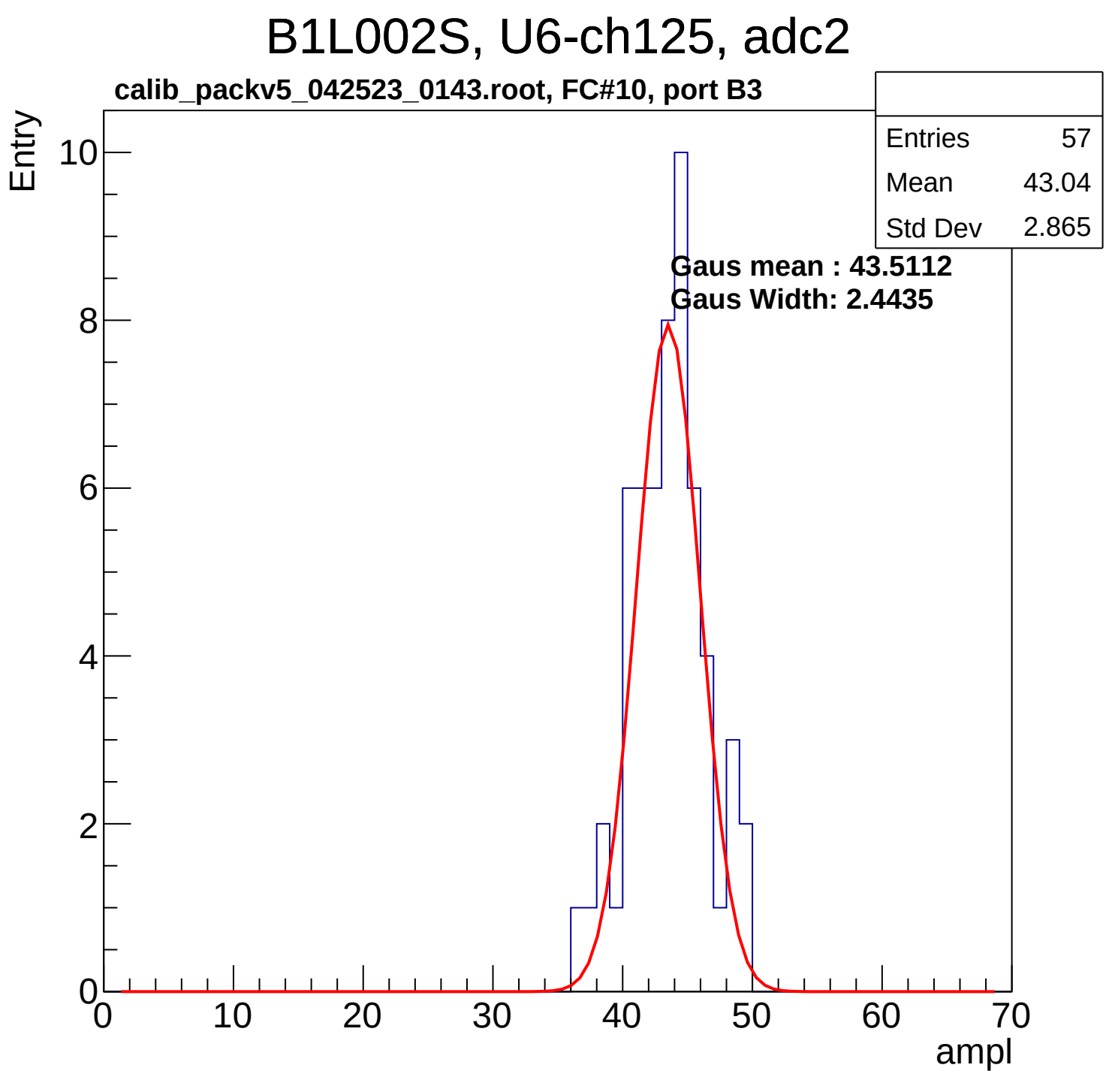

B1L002S, U6-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.98
Std Dev 2.378
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

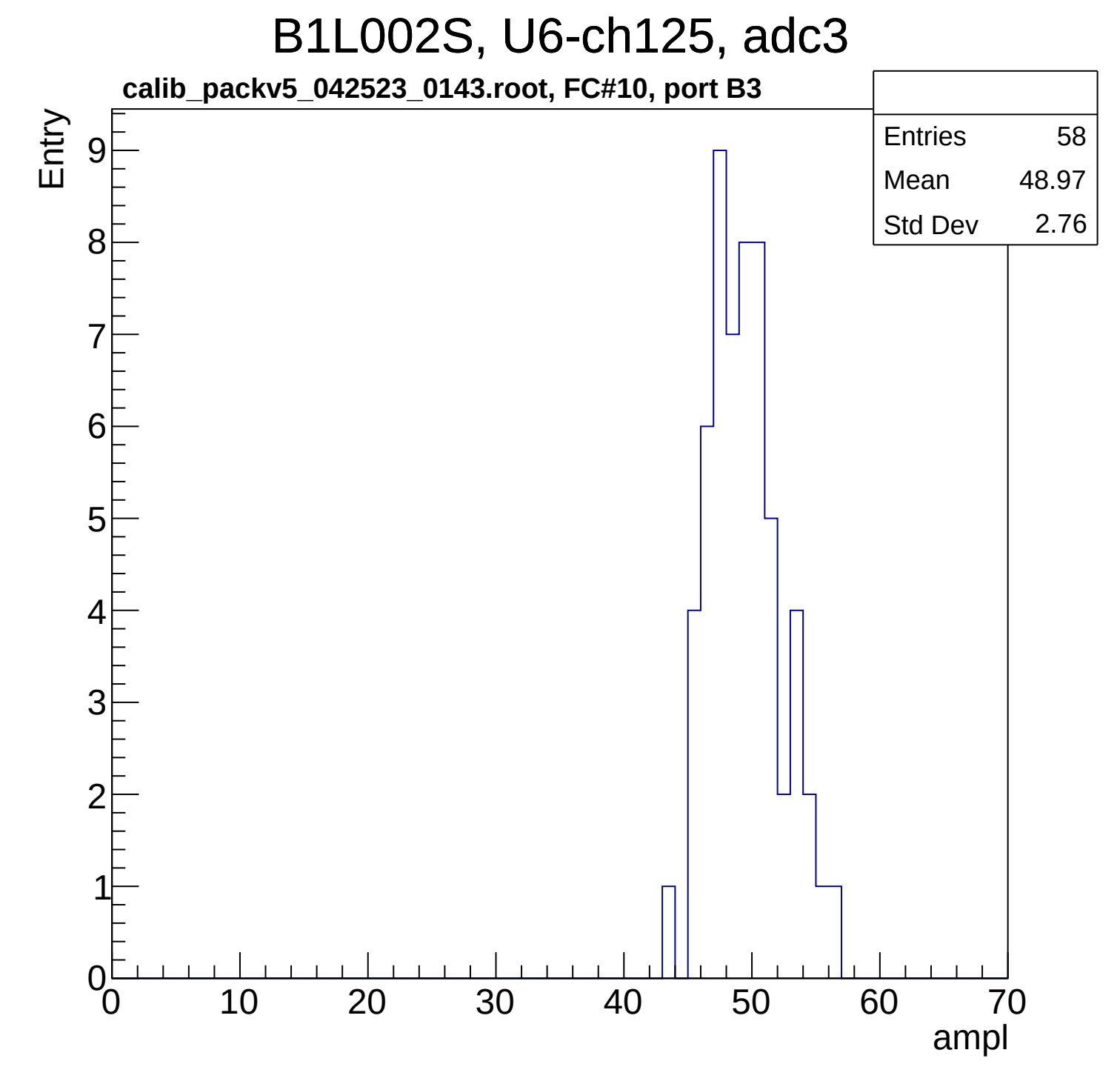

B1L002S, U6-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.93
Std Dev 11.75
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

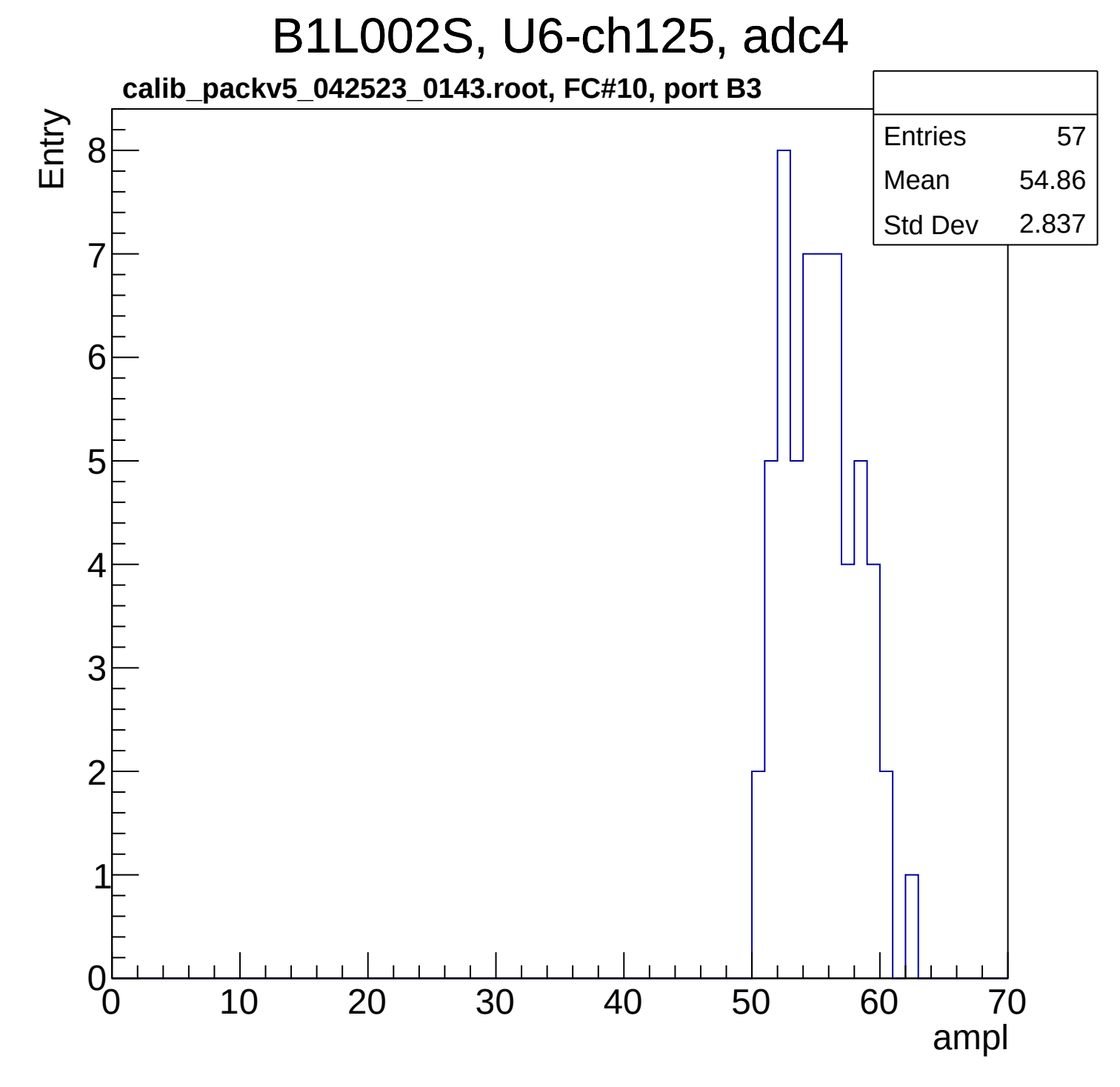

B1L002S, U6-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

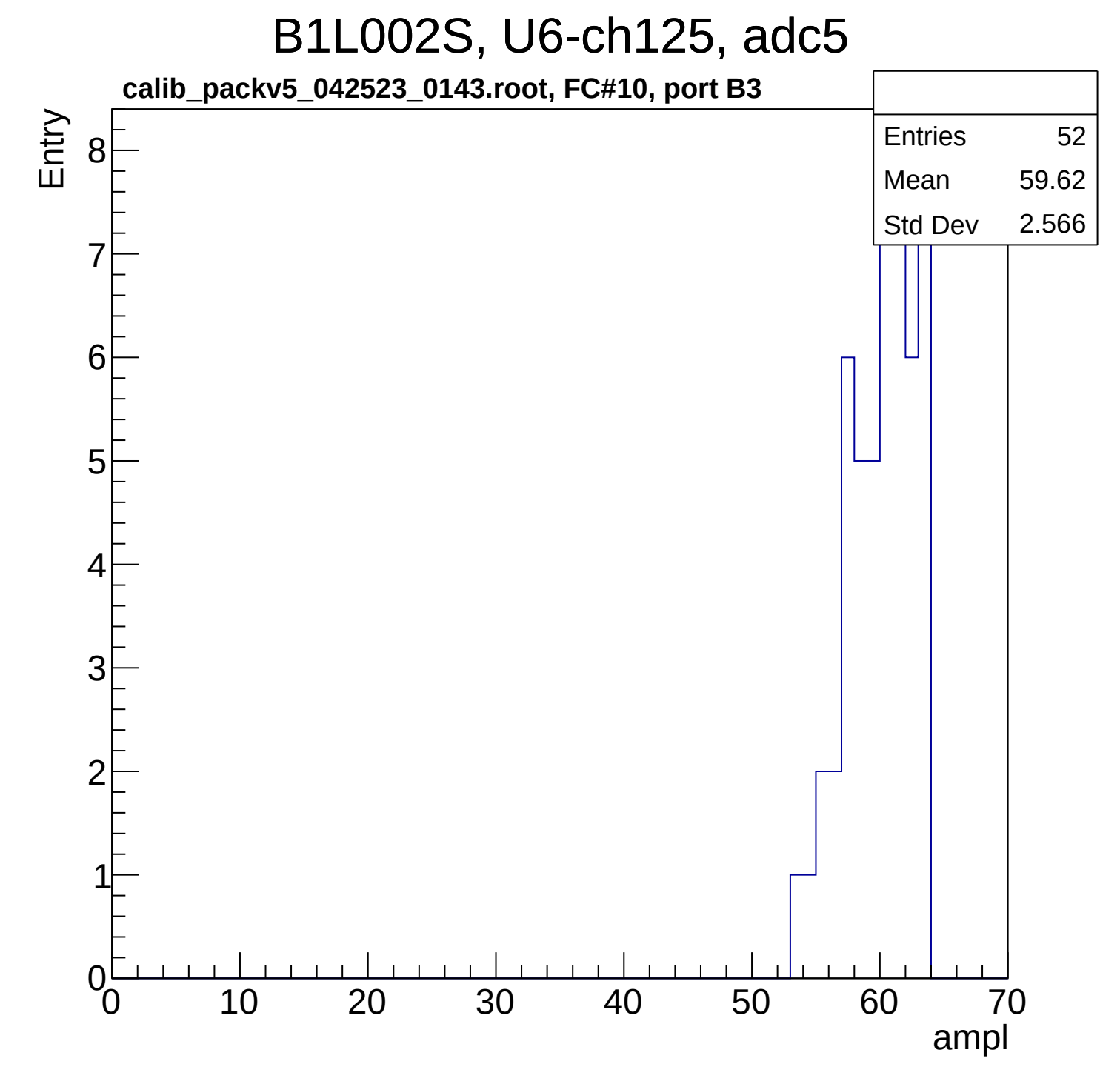

B1L002S, U6-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 29.99
Std Dev 4.742
Gaus mean : 31.0214
Gaus Width: 3.5931
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

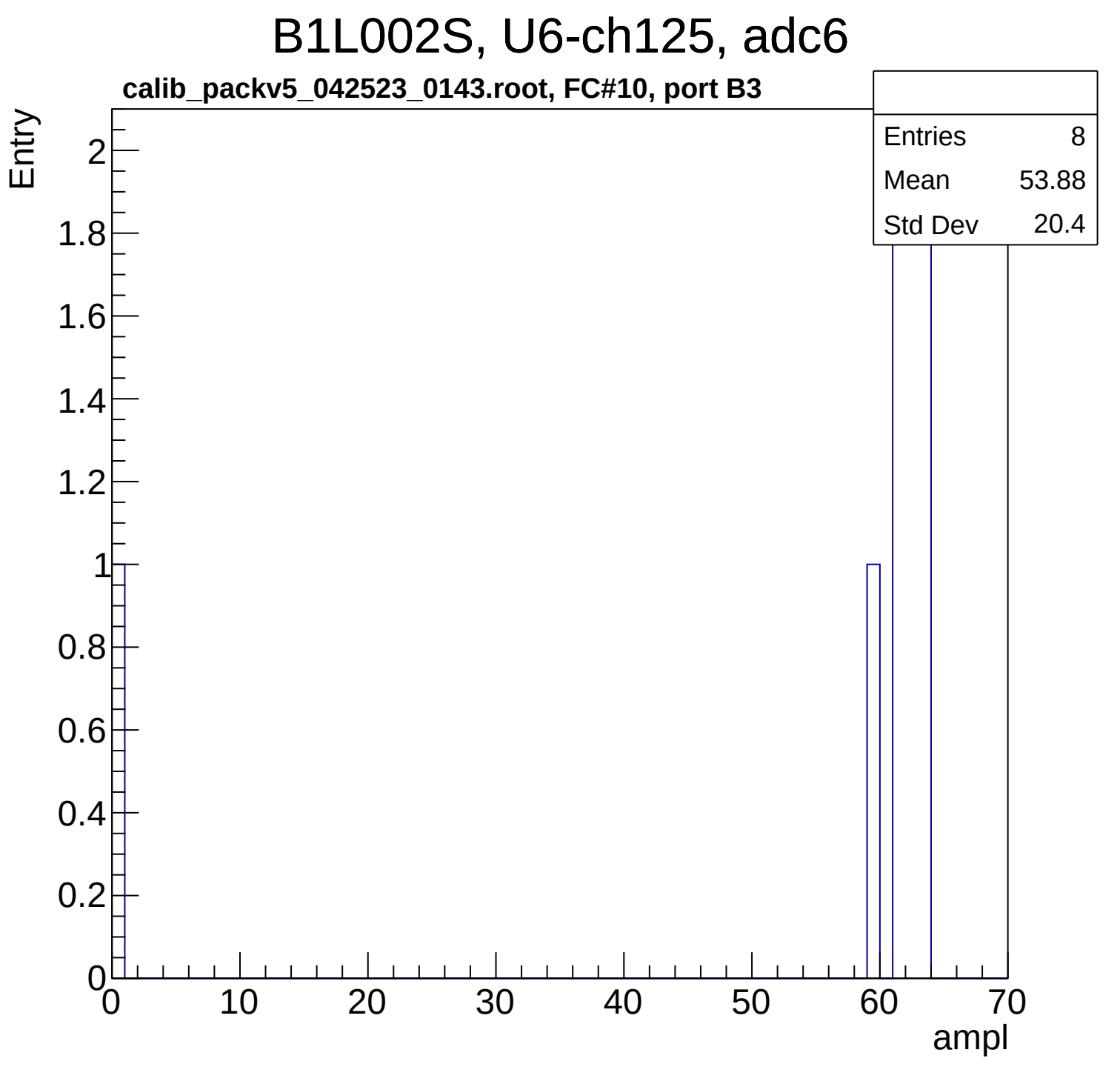

B1L002S, U6-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.9
Std Dev 3.115
Gaus mean : 37.6108
Gaus Width: 3.3669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

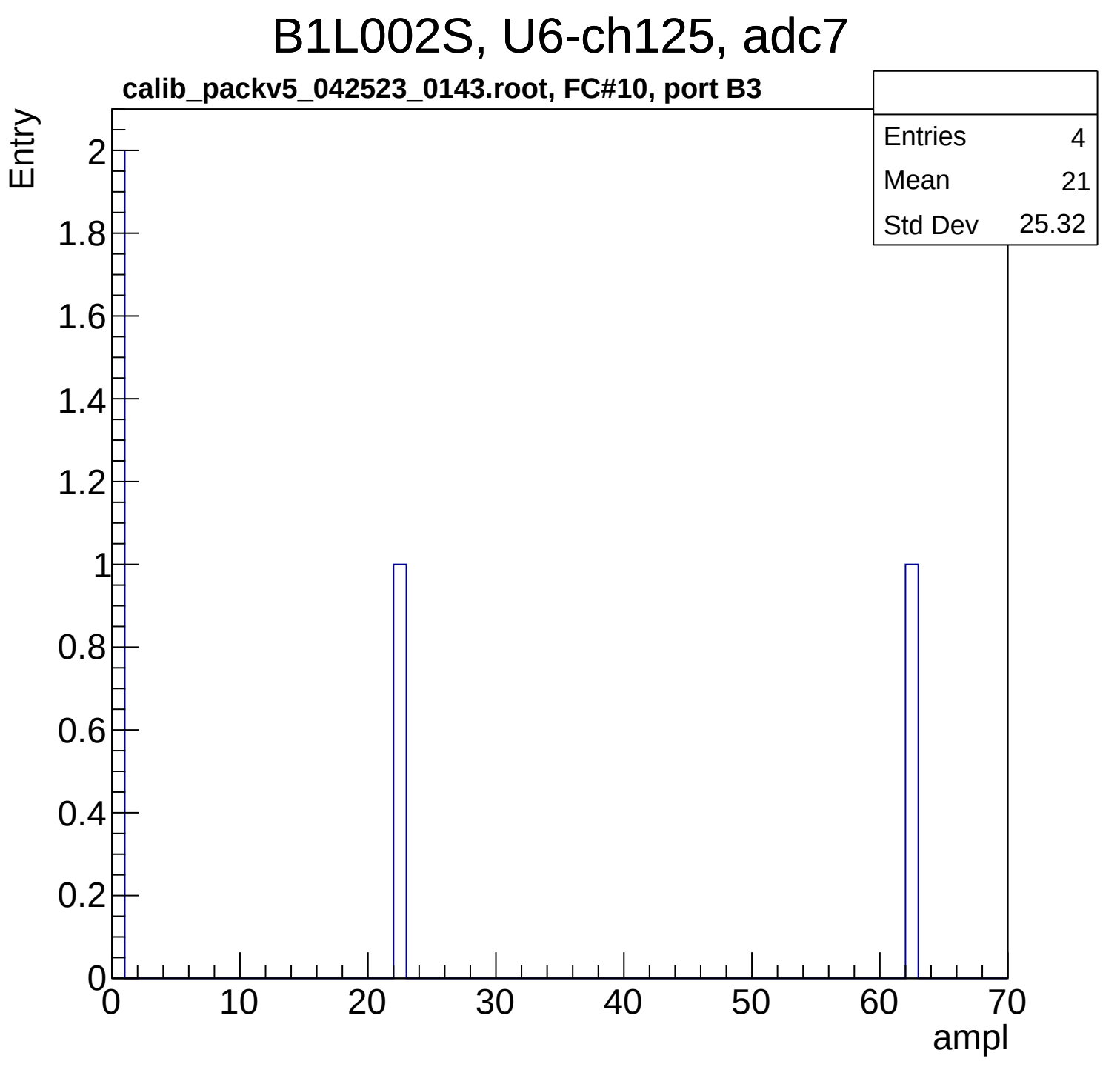

B1L002S, U6-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 43.04
Std Dev 2.865
Gaus mean : 43.5112
Gaus Width: 2.4435
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

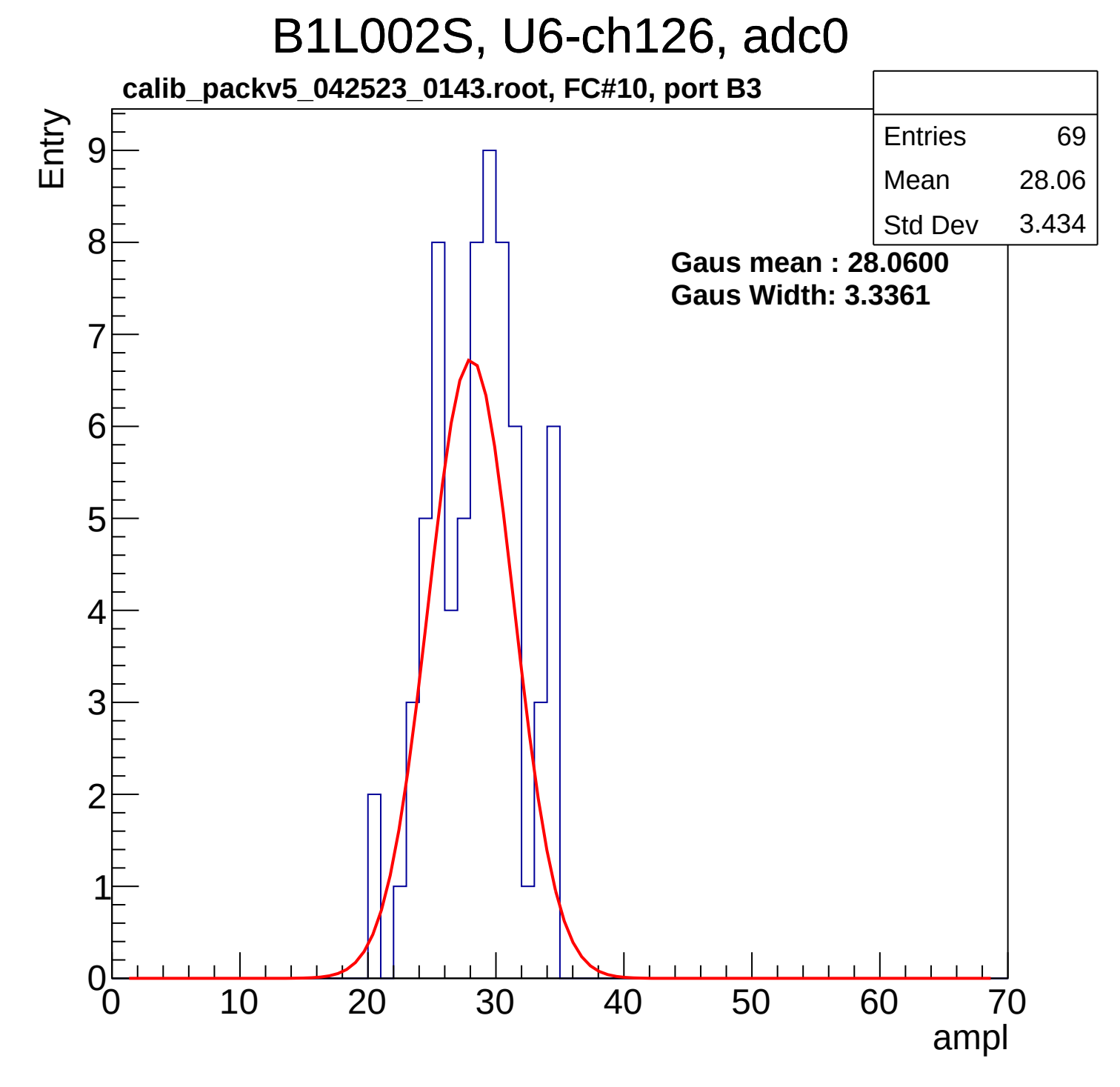

B1L002S, U6-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 48.97
Std Dev 2.76
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

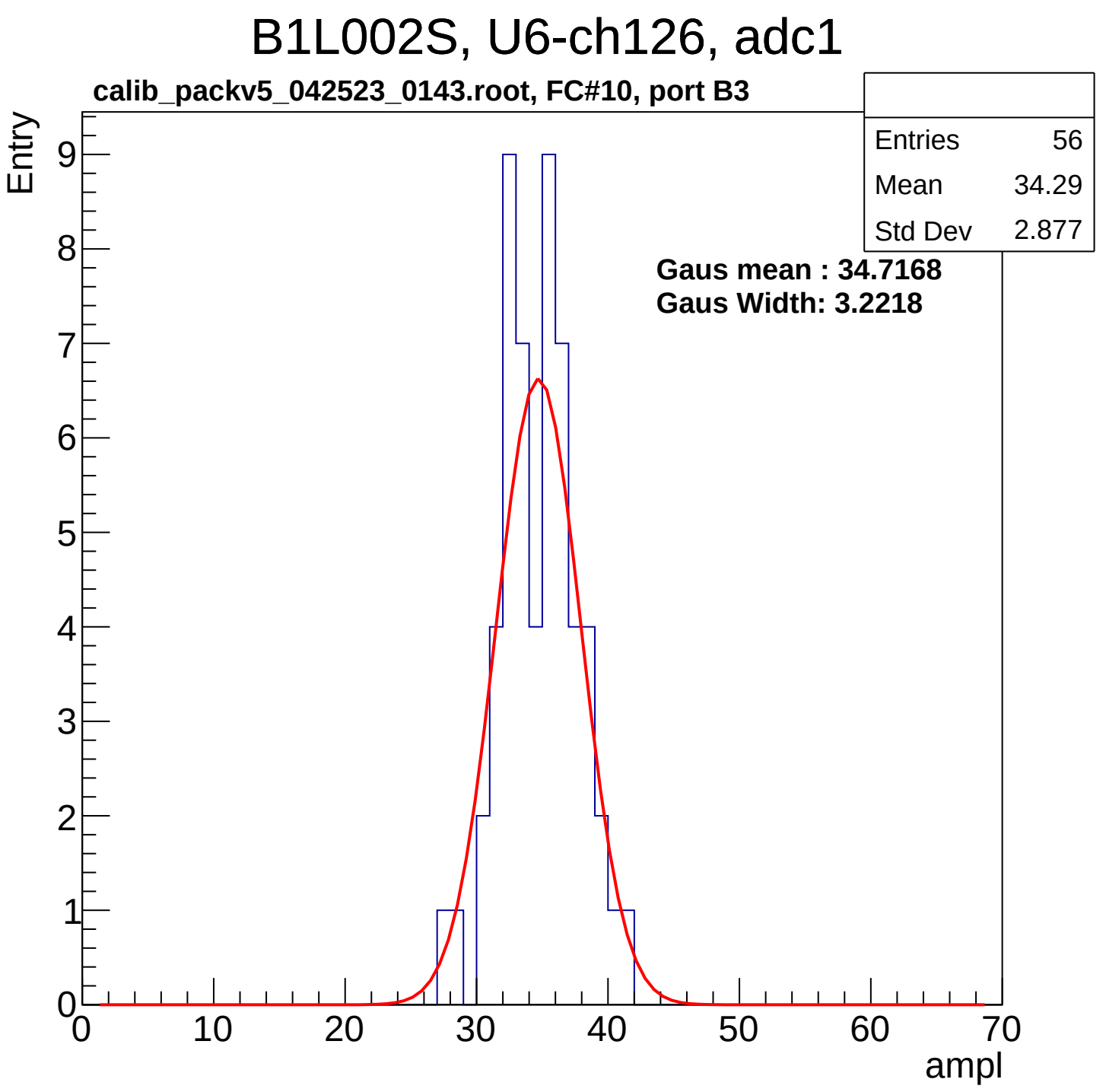

B1L002S, U6-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.86
Std Dev 2.837
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

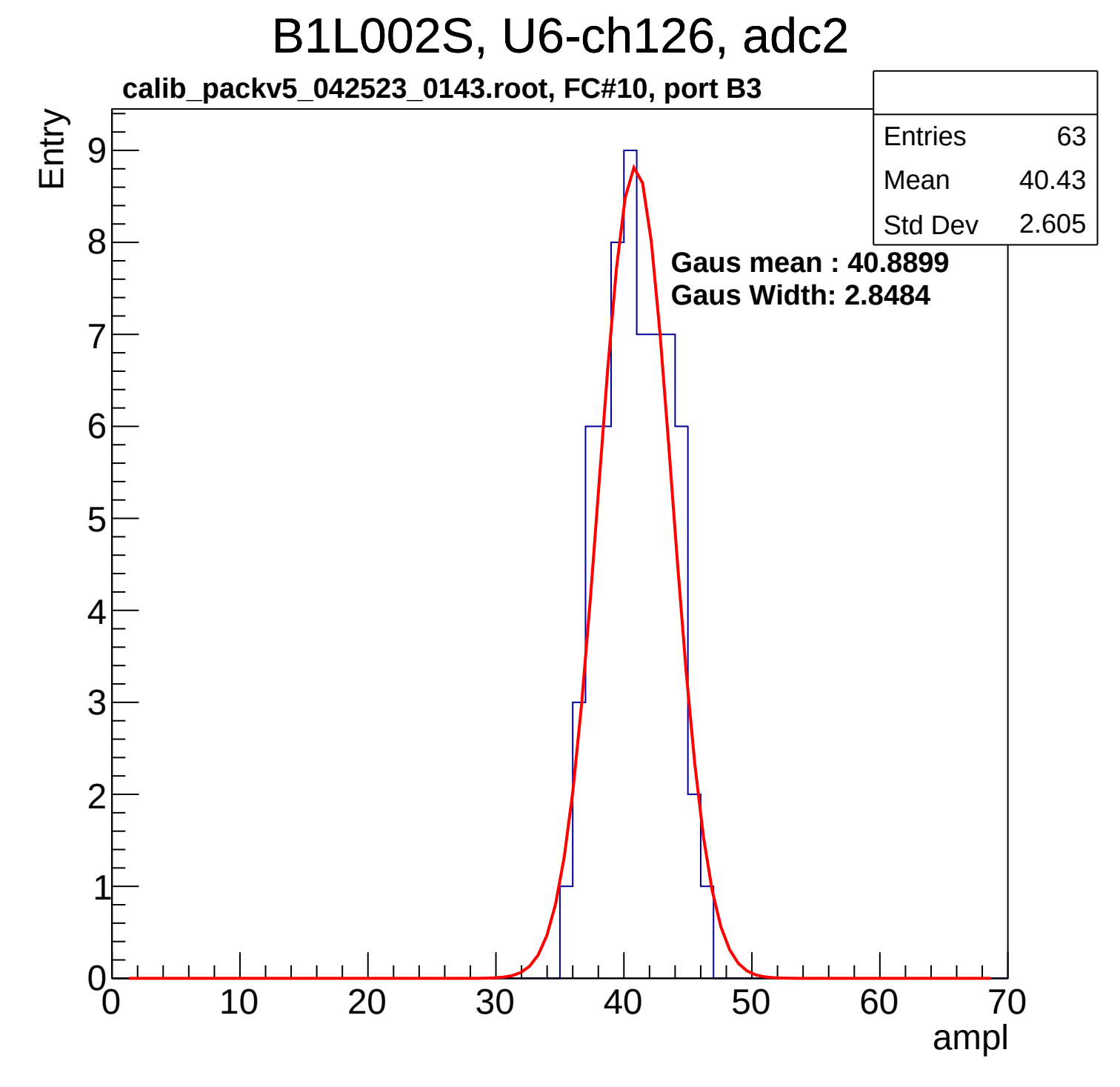

B1L002S, U6-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 59.62
Std Dev 2.566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

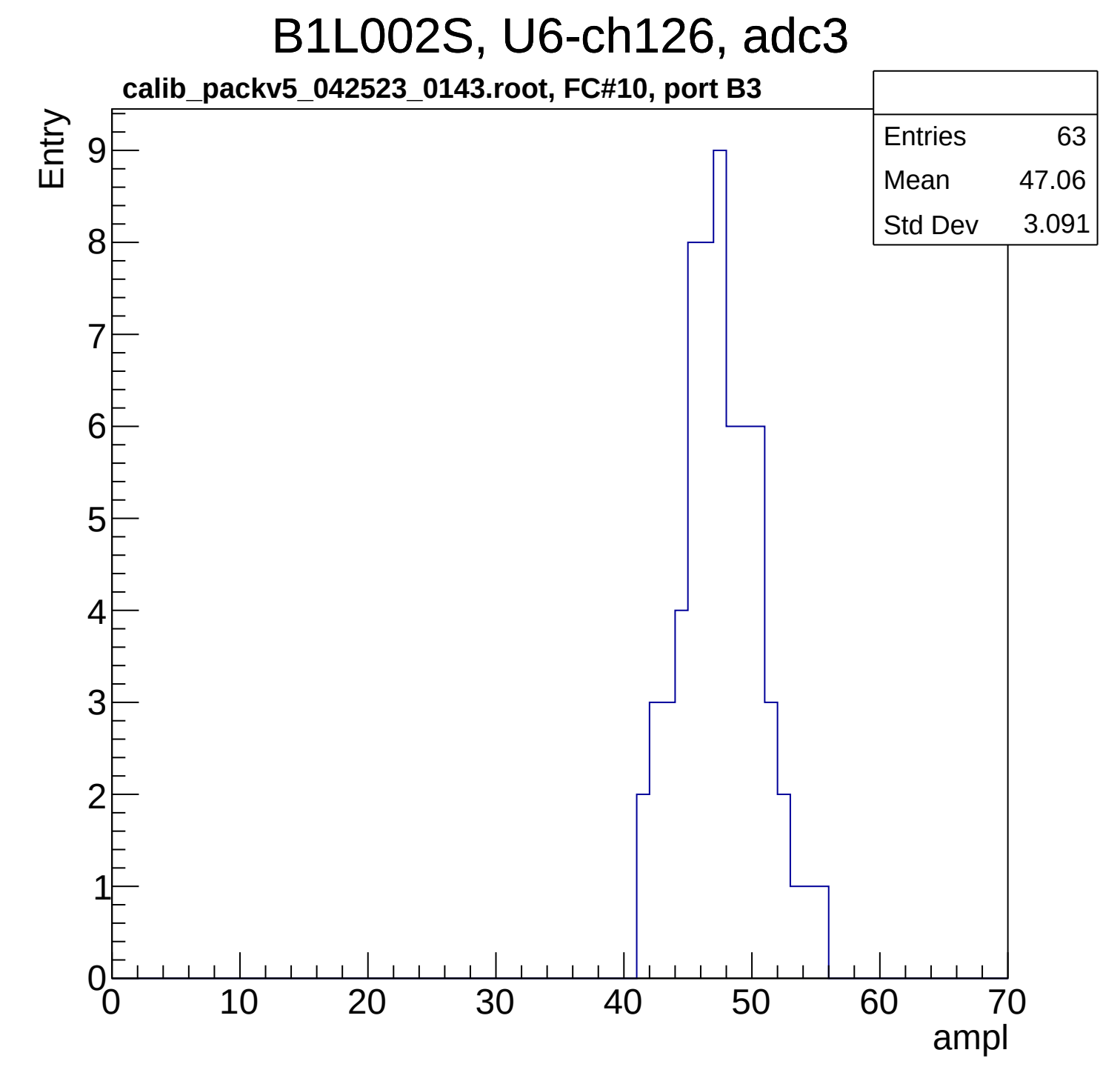

B1L002S, U6-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 53.88
Std Dev 20.4
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

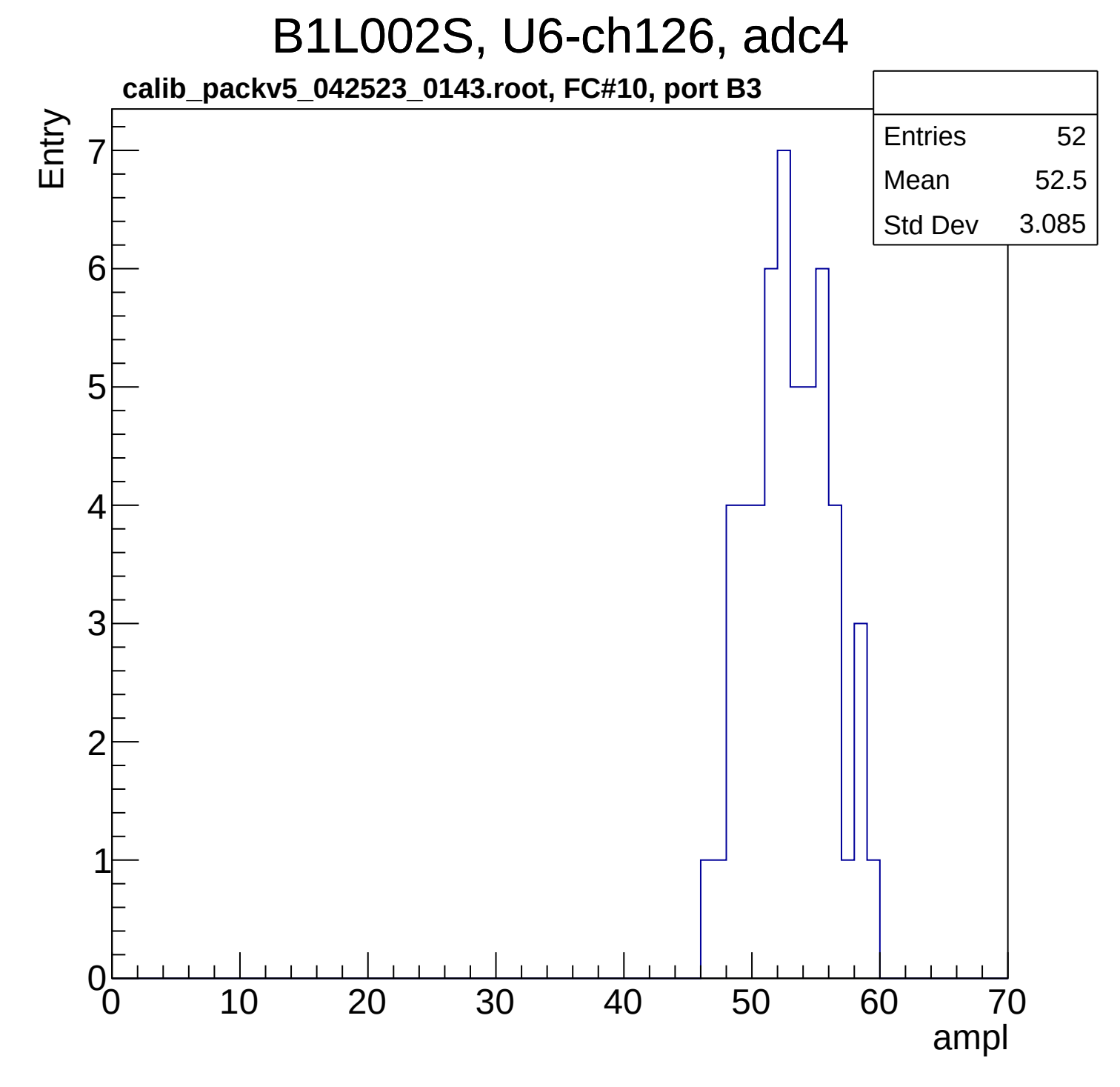

B1L002S, U6-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21
Std Dev 25.32
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

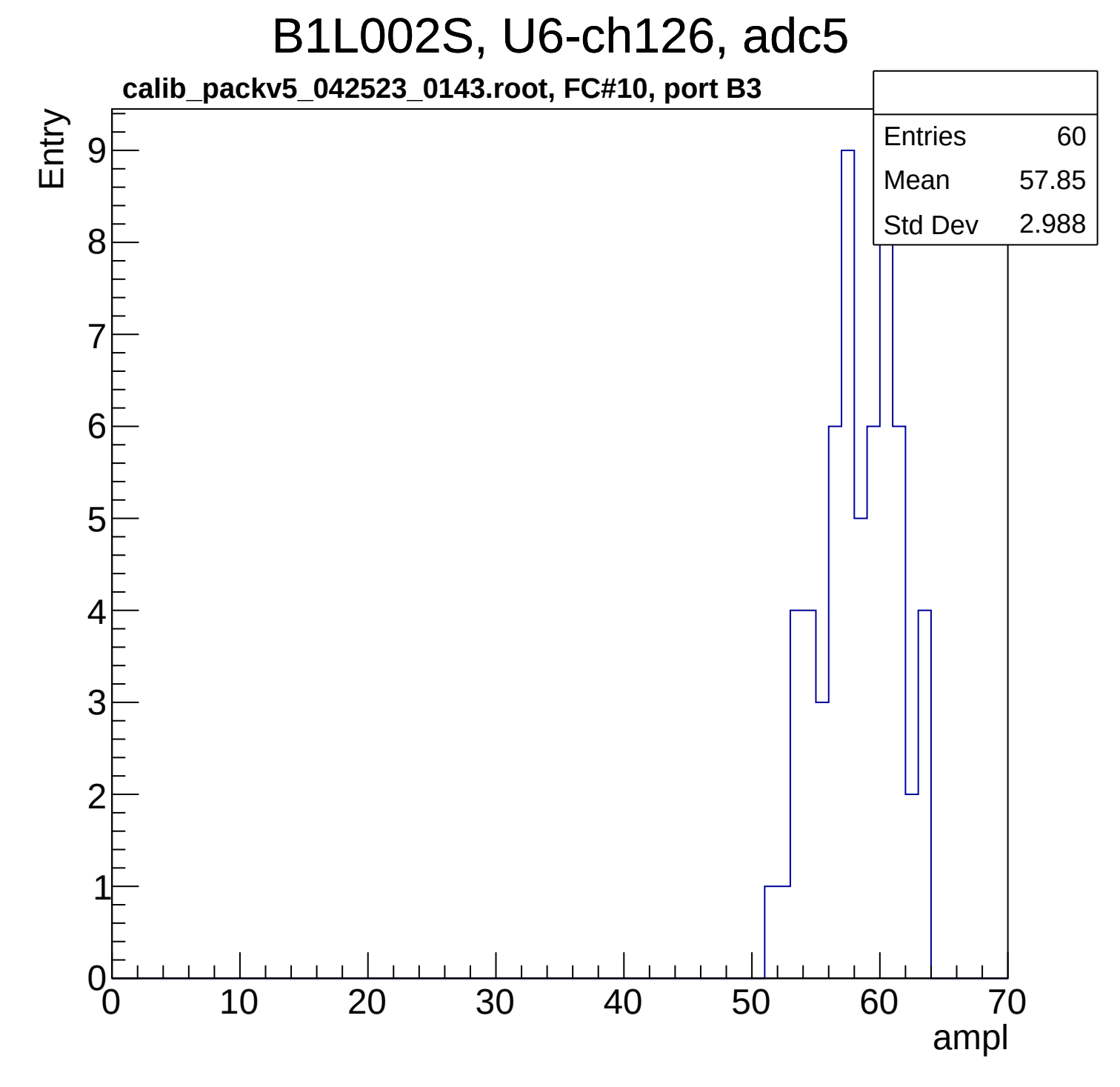

B1L002S, U6-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.06
Std Dev 3.434
Gaus mean : 28.0600
Gaus Width: 3.3361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

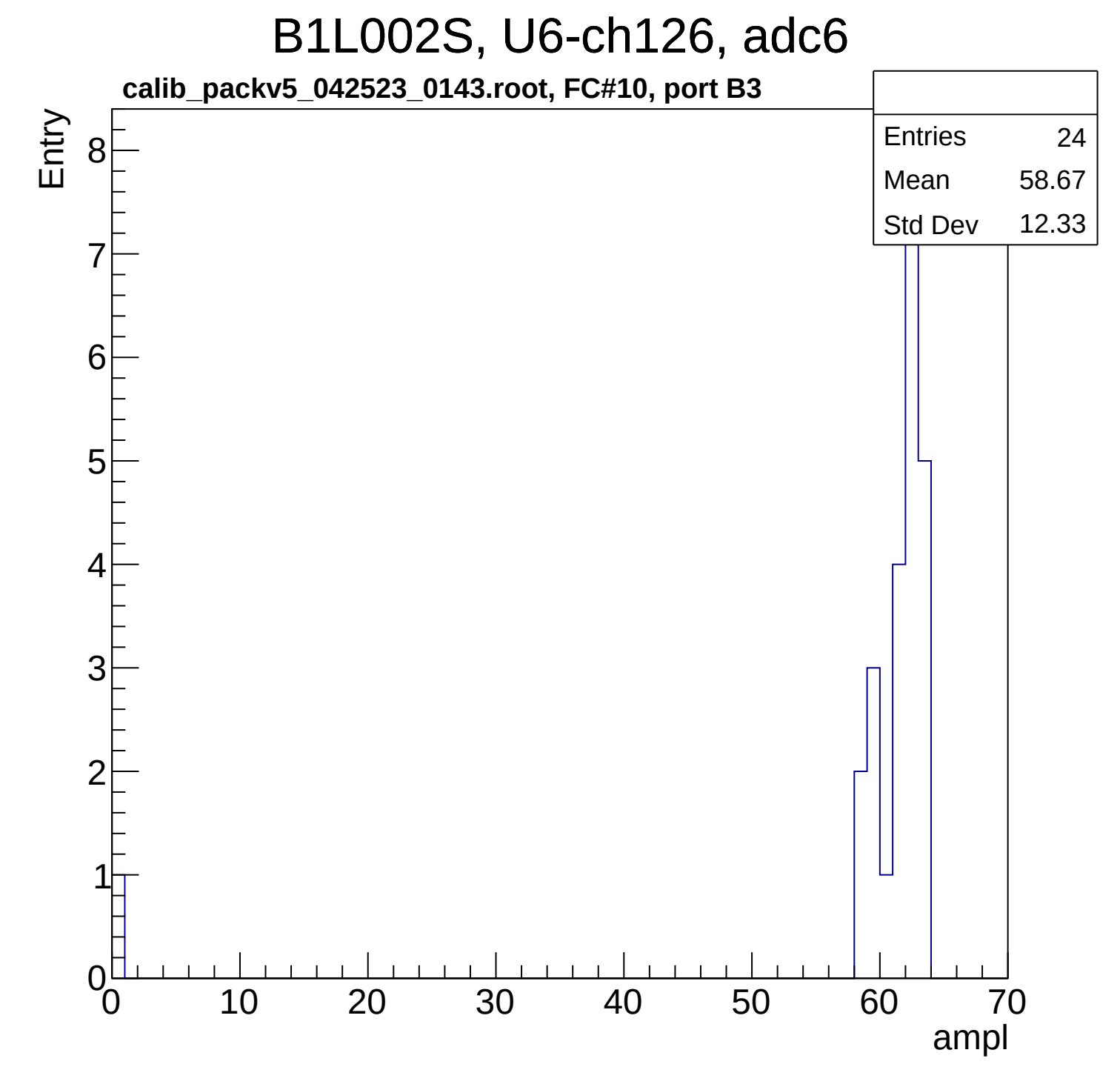

B1L002S, U6-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.29
Std Dev 2.877
Gaus mean : 34.7168
Gaus Width: 3.2218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

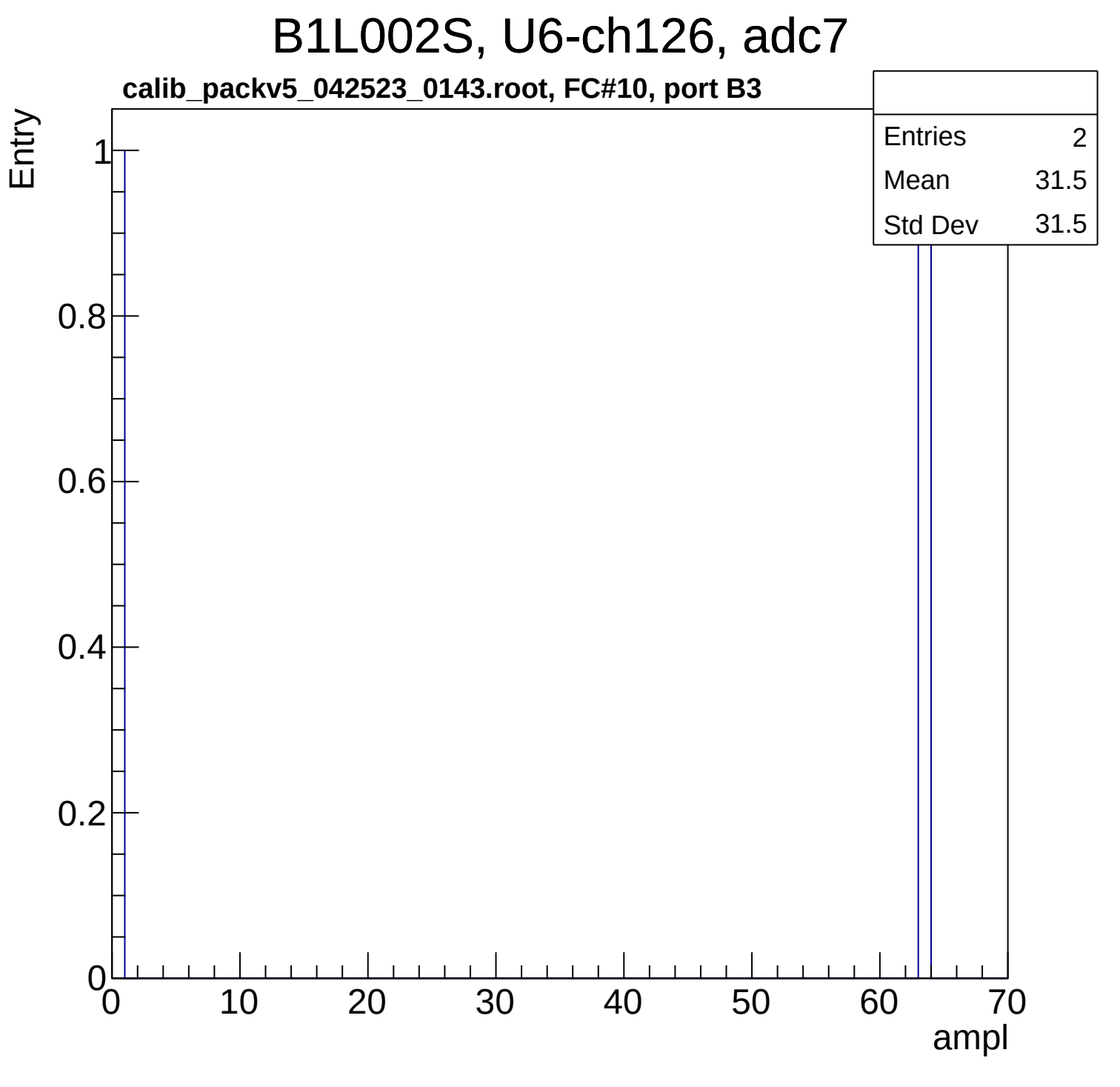

B1L002S, U6-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.43
Std Dev 2.605
Gaus mean : 40.8899
Gaus Width: 2.8484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

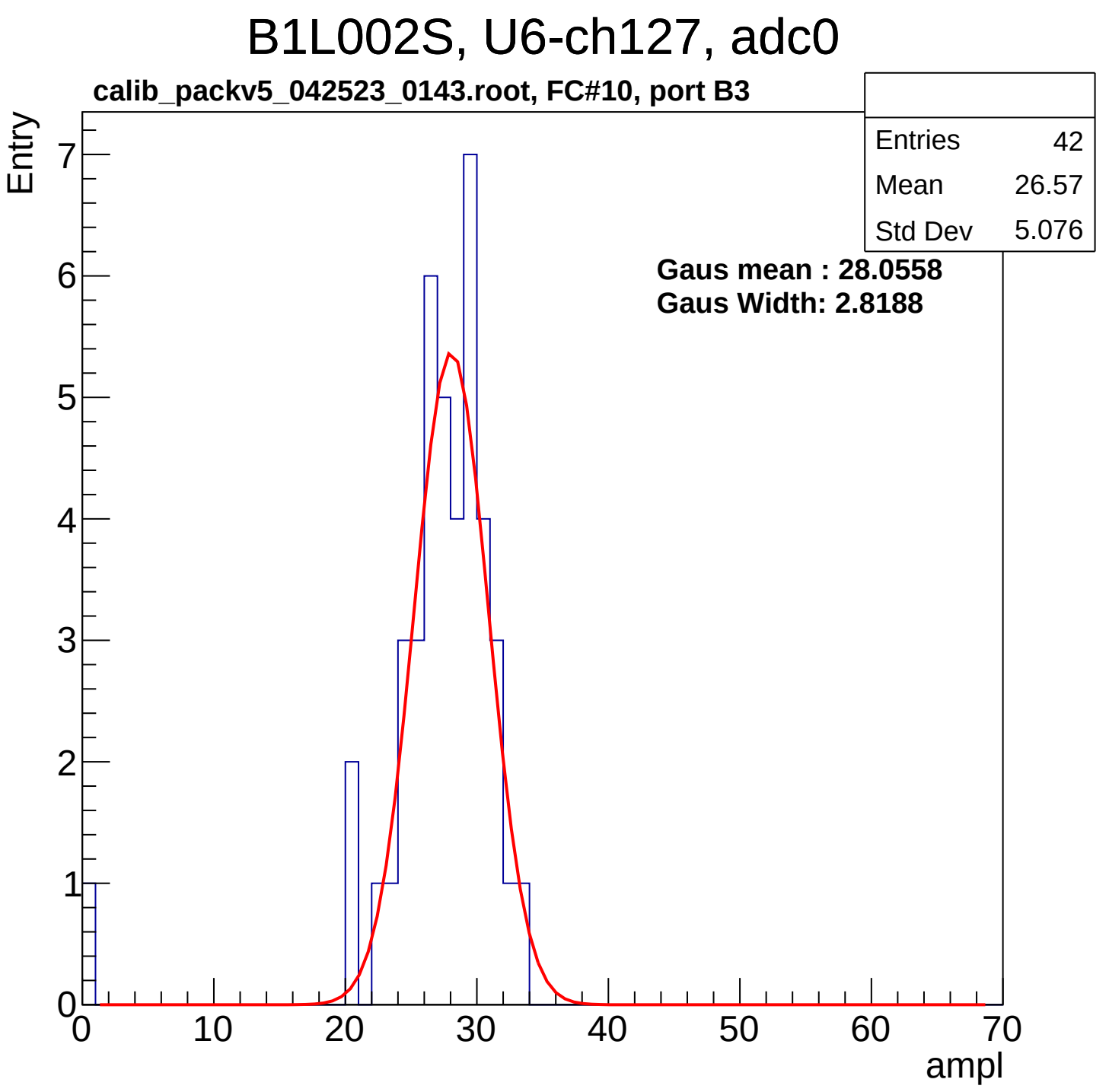

B1L002S, U6-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.06
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

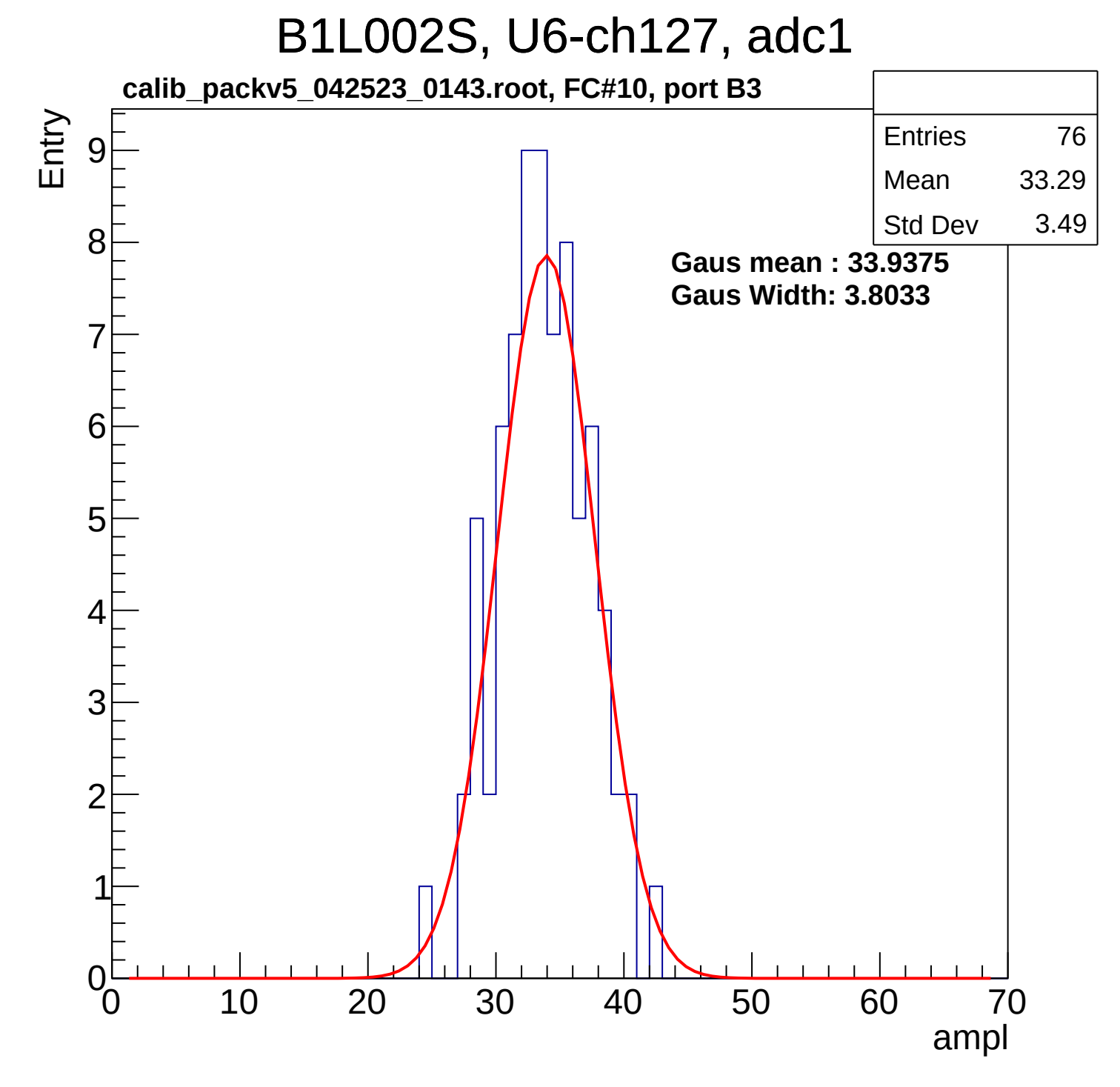

B1L002S, U6-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.5
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

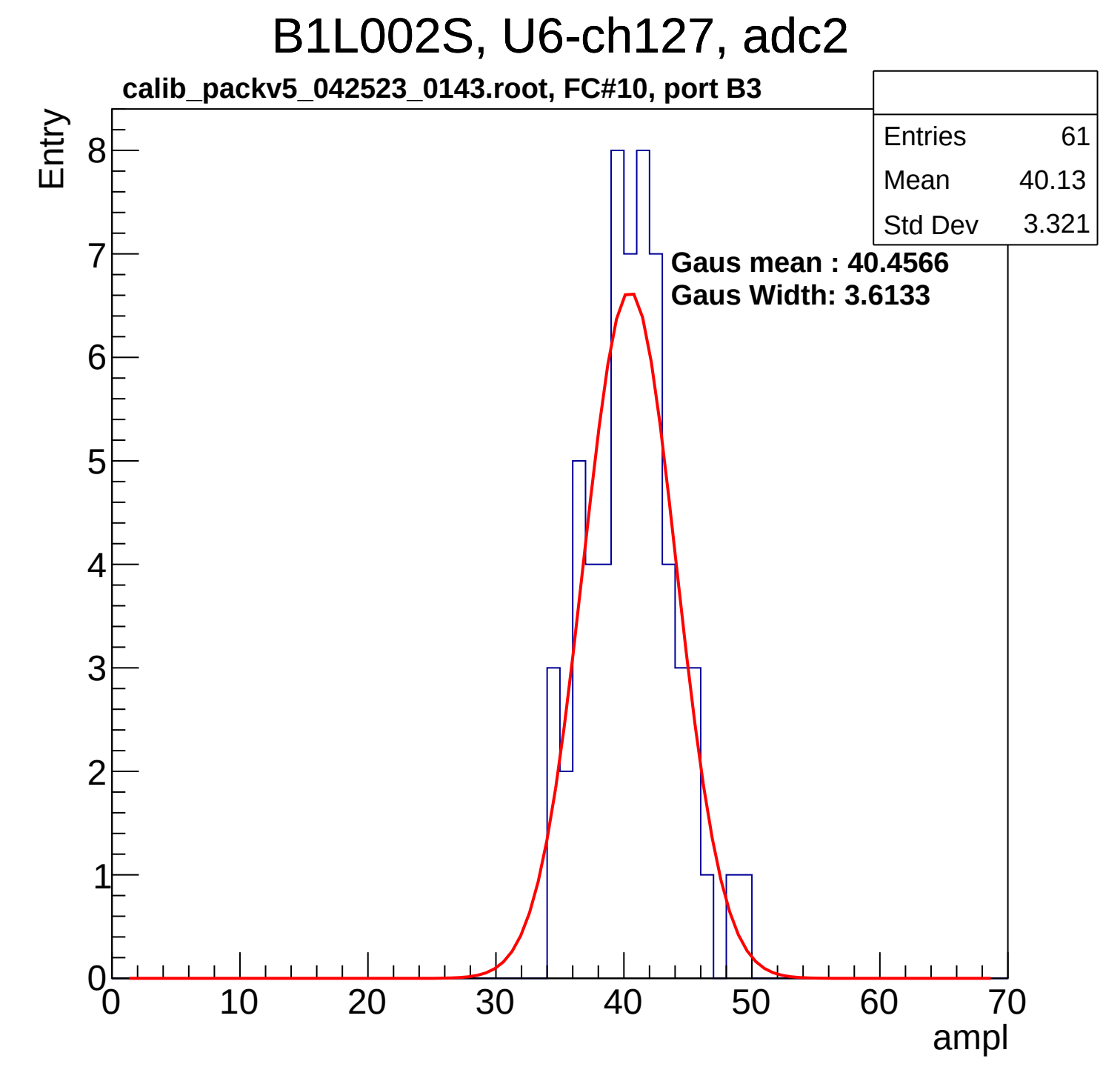

B1L002S, U6-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.85
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

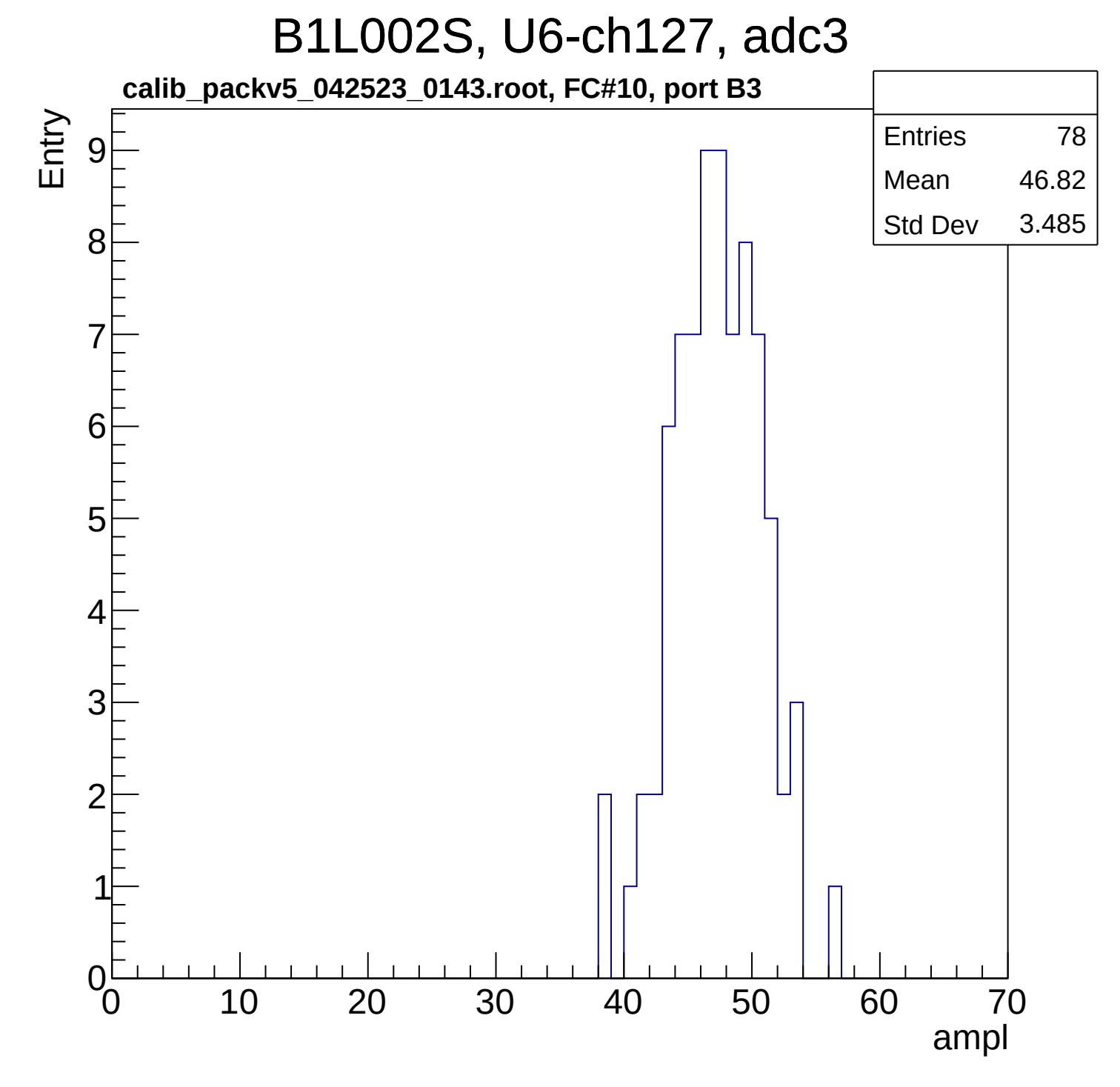

B1L002S, U6-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.67
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

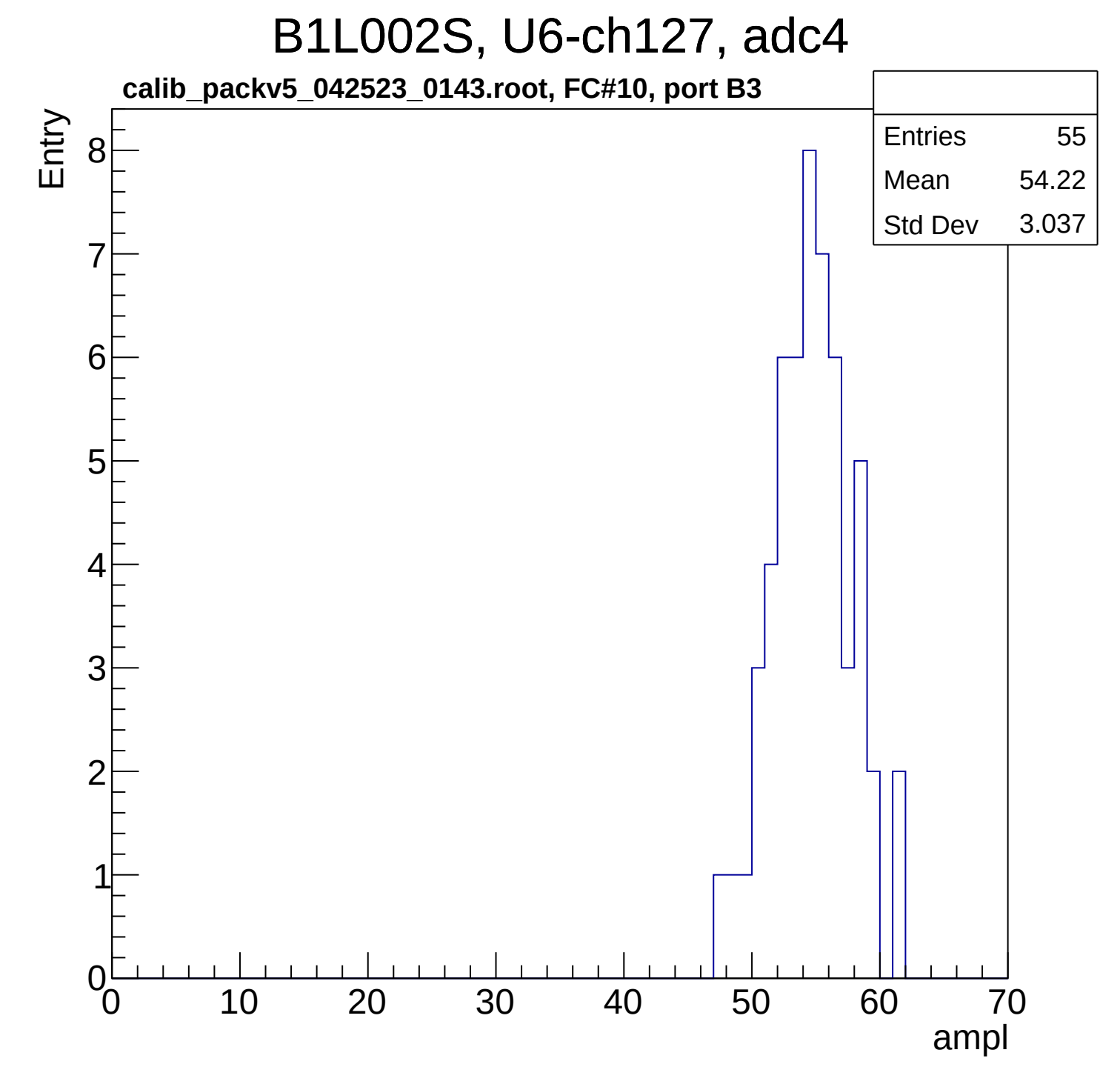

B1L002S, U6-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

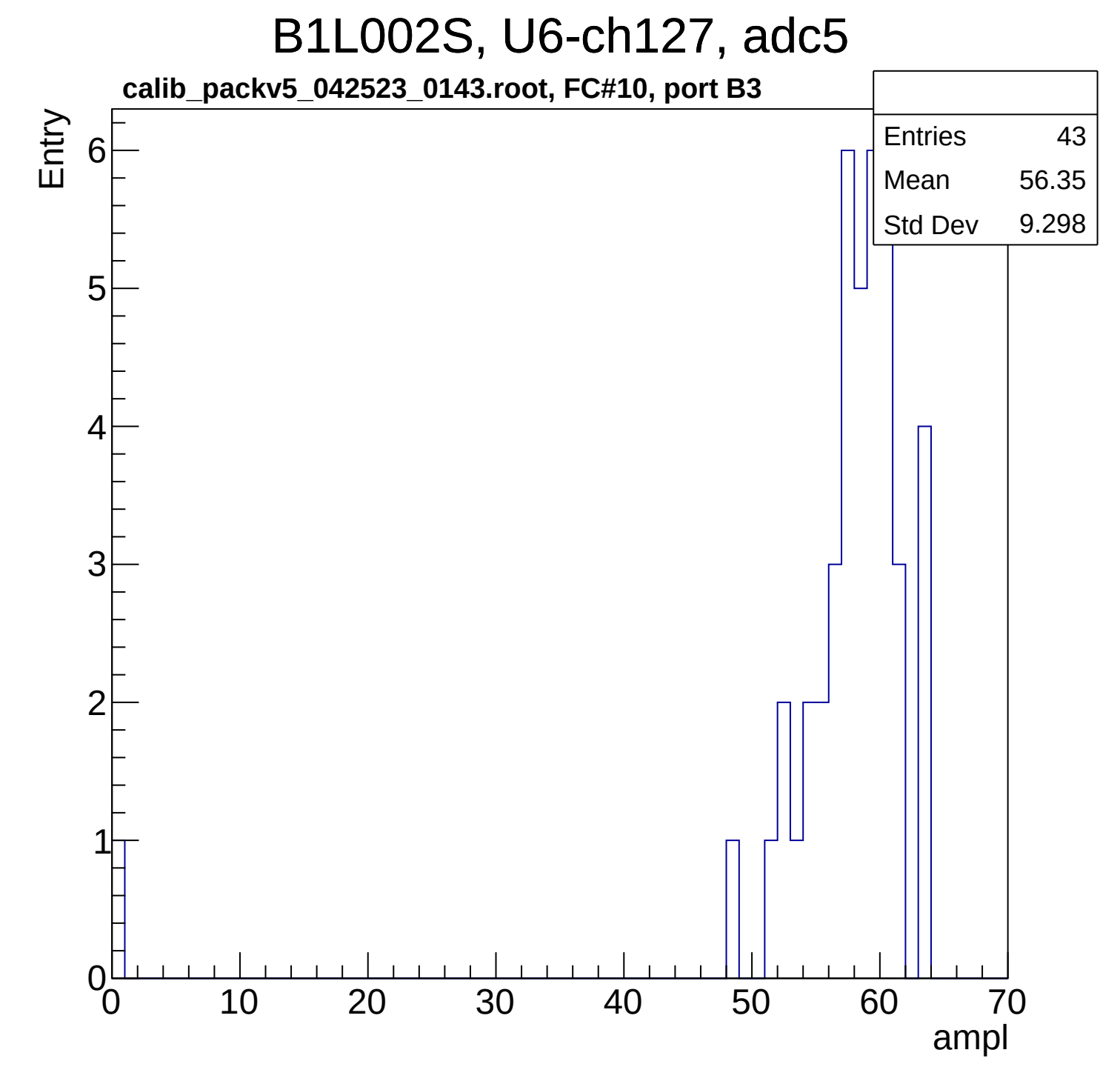

B1L002S, U6-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 26.57
Std Dev 5.076
Gaus mean : 28.0558
Gaus Width: 2.8188
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

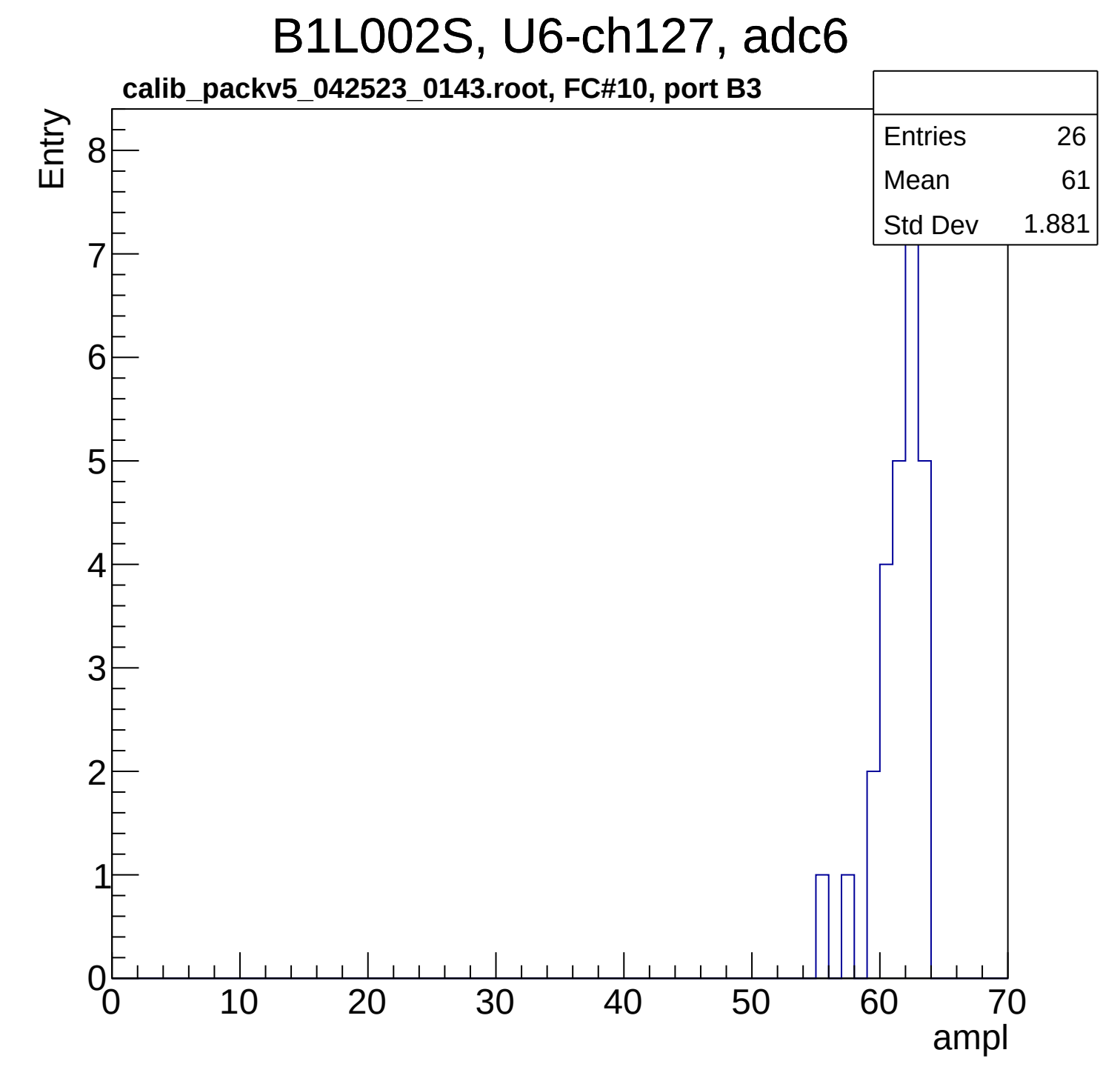

B1L002S, U6-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.29
Std Dev 3.49
Gaus mean : 33.9375
Gaus Width: 3.8033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

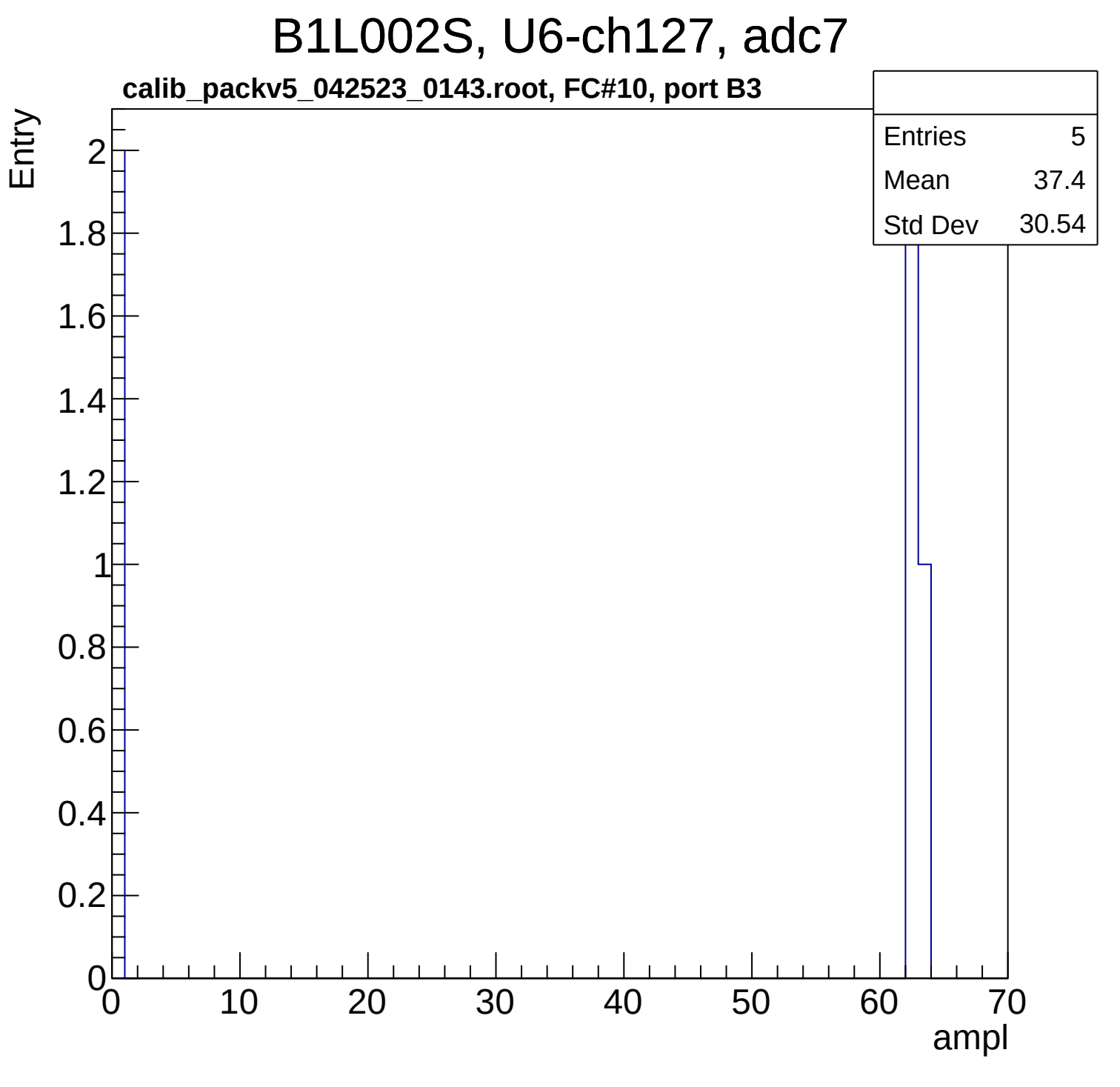

B1L002S, U6-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.13
Std Dev 3.321
Gaus mean : 40.4566
Gaus Width: 3.6133
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

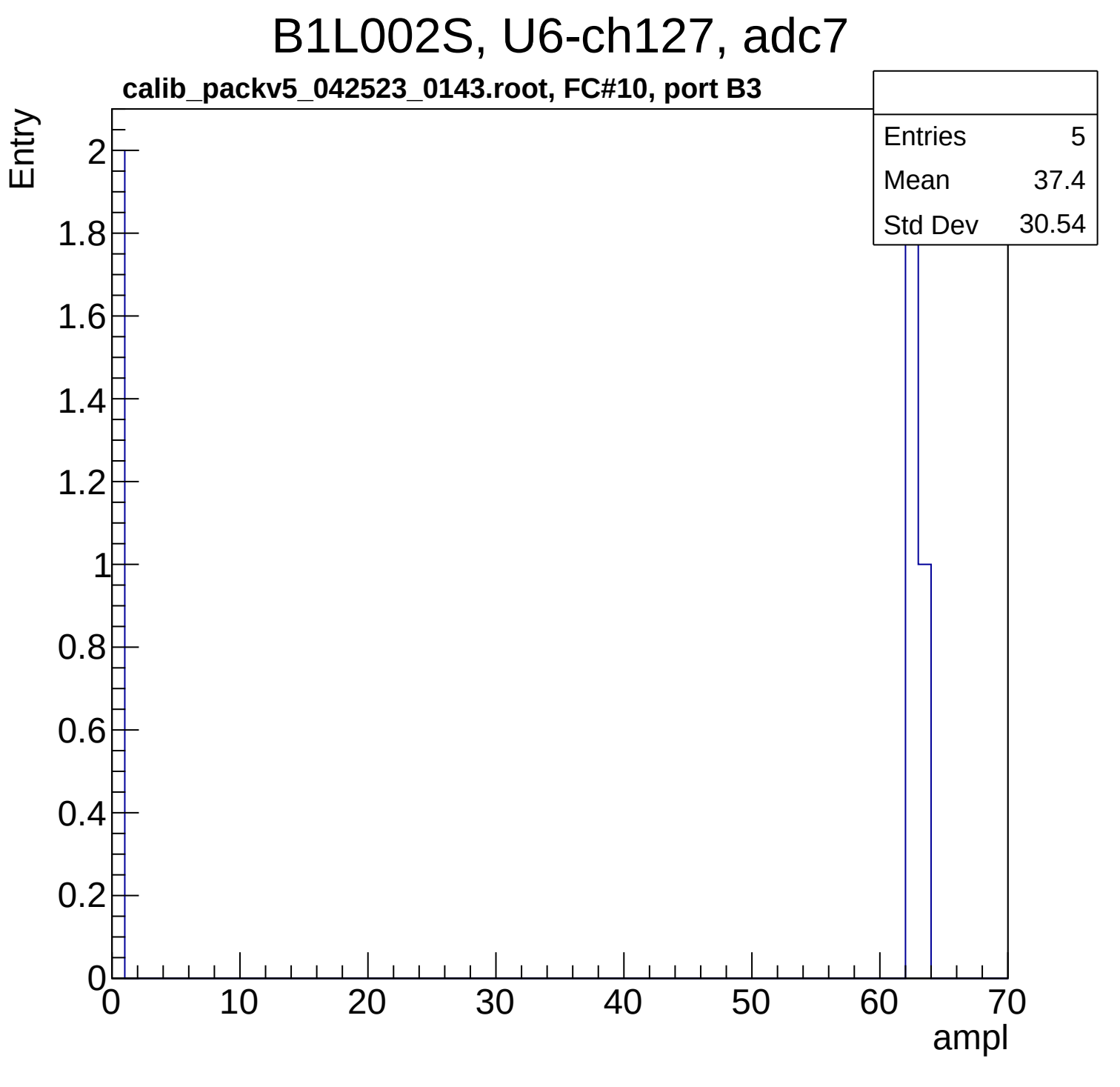

B1L002S, U6-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 46.82
Std Dev 3.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U6-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.22
Std Dev 3.037
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U6-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 56.35
Std Dev 9.298
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U6-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61
Std Dev 1.881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70