

B0L000S, U15-ch0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.1
Std Dev	11.69

Turn on : 25.9799

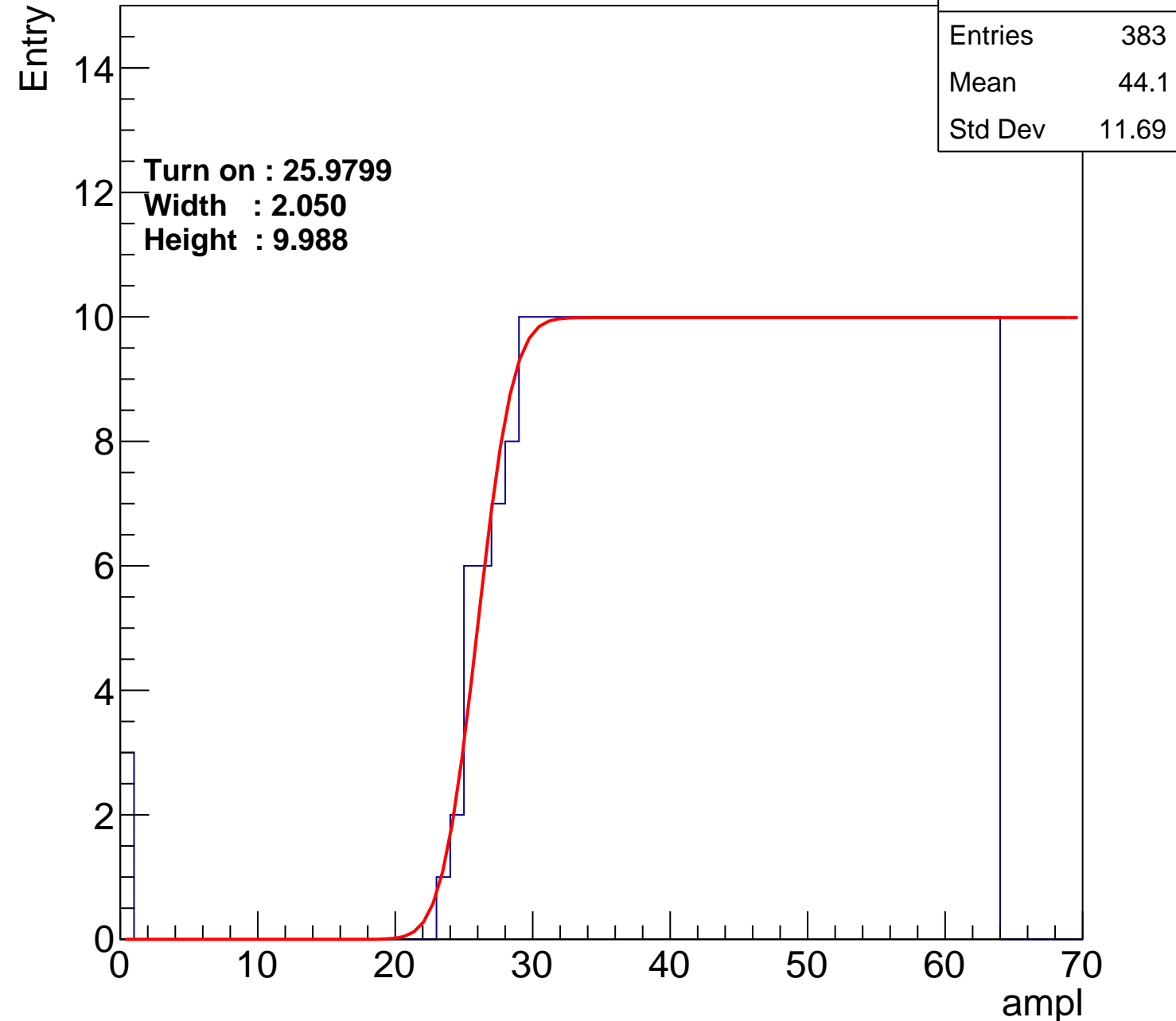
Width : 2.050

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.75
Std Dev	11.6

Turn on : 27.8948

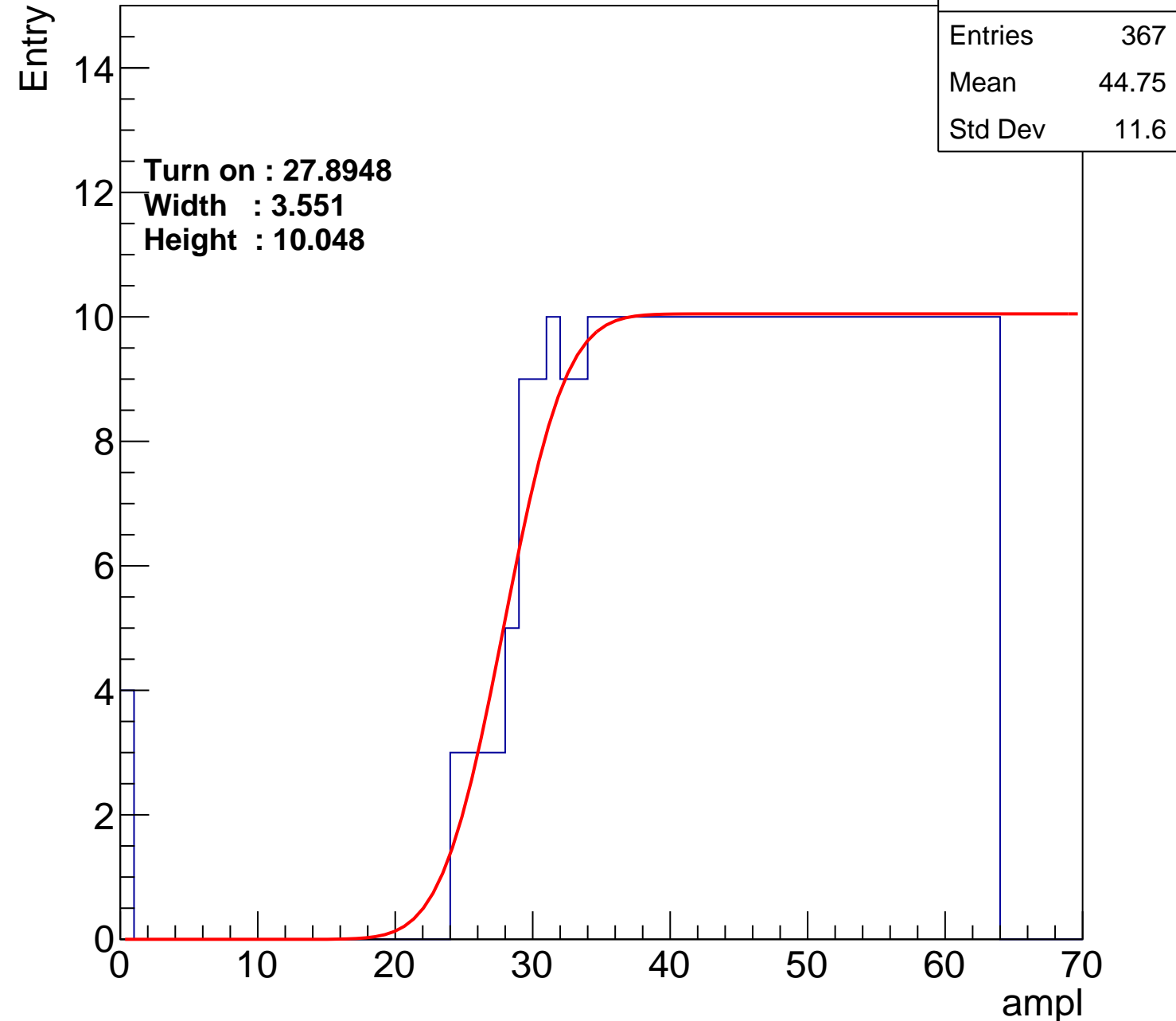
Width : 3.551

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.64
Std Dev	11.25

Turn on : 26.8606

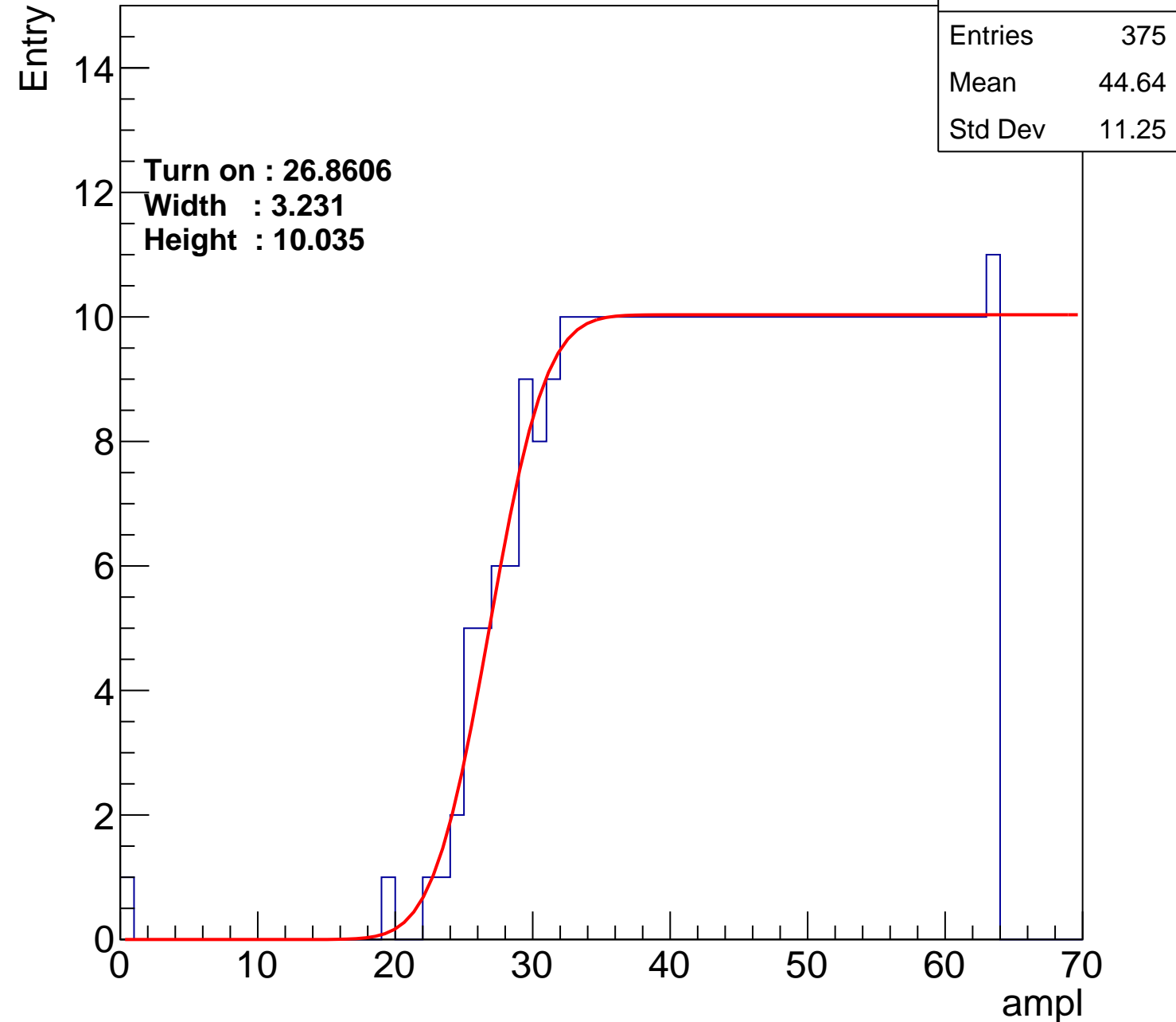
Width : 3.231

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	401
Mean	43.33
Std Dev	11.9

Turn on : 24.5015

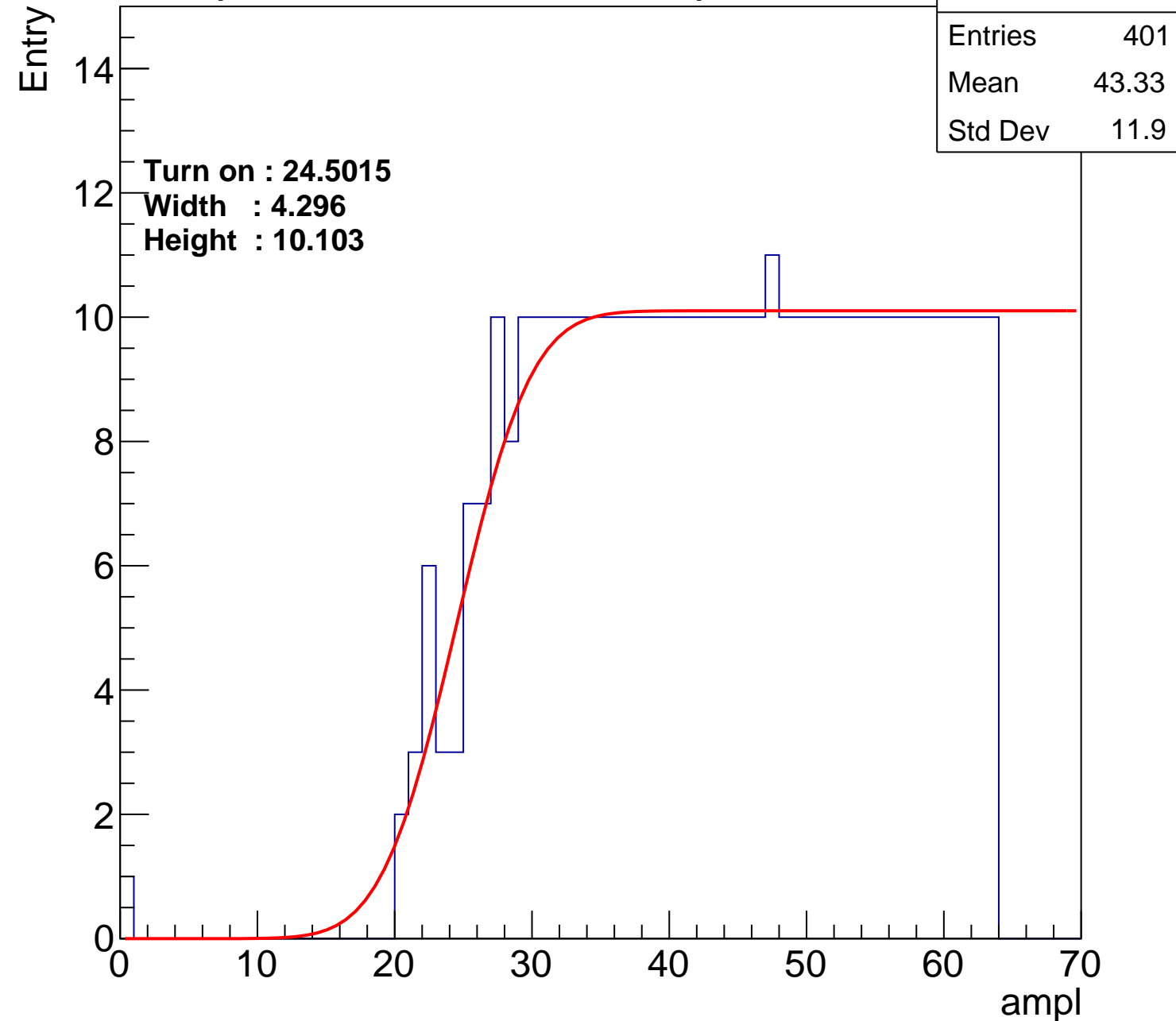
Width : 4.296

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.29
Std Dev	11.19

Turn on : 29.1933

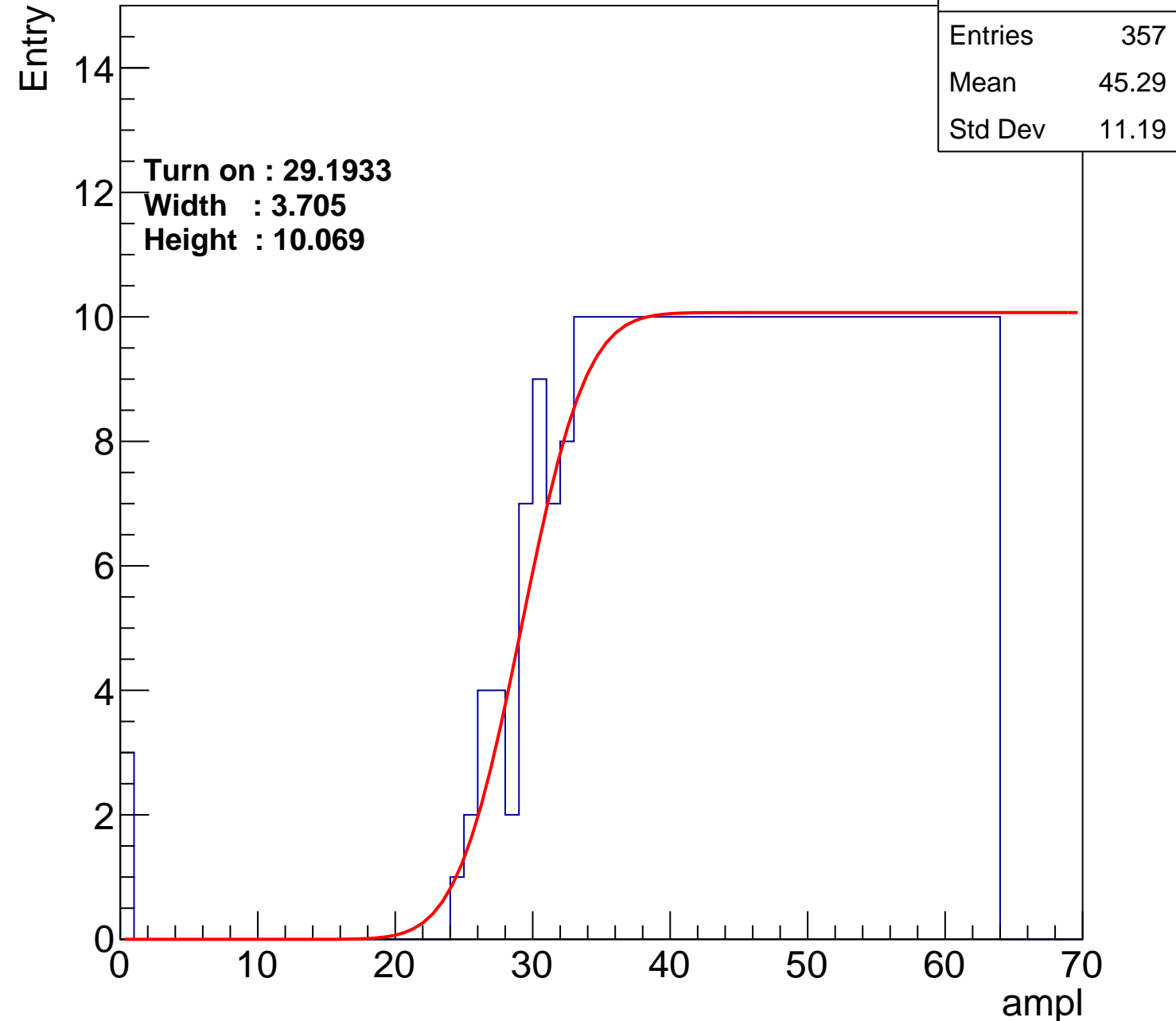
Width : 3.705

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.87
Std Dev	11.78

Turn on : 26.3940

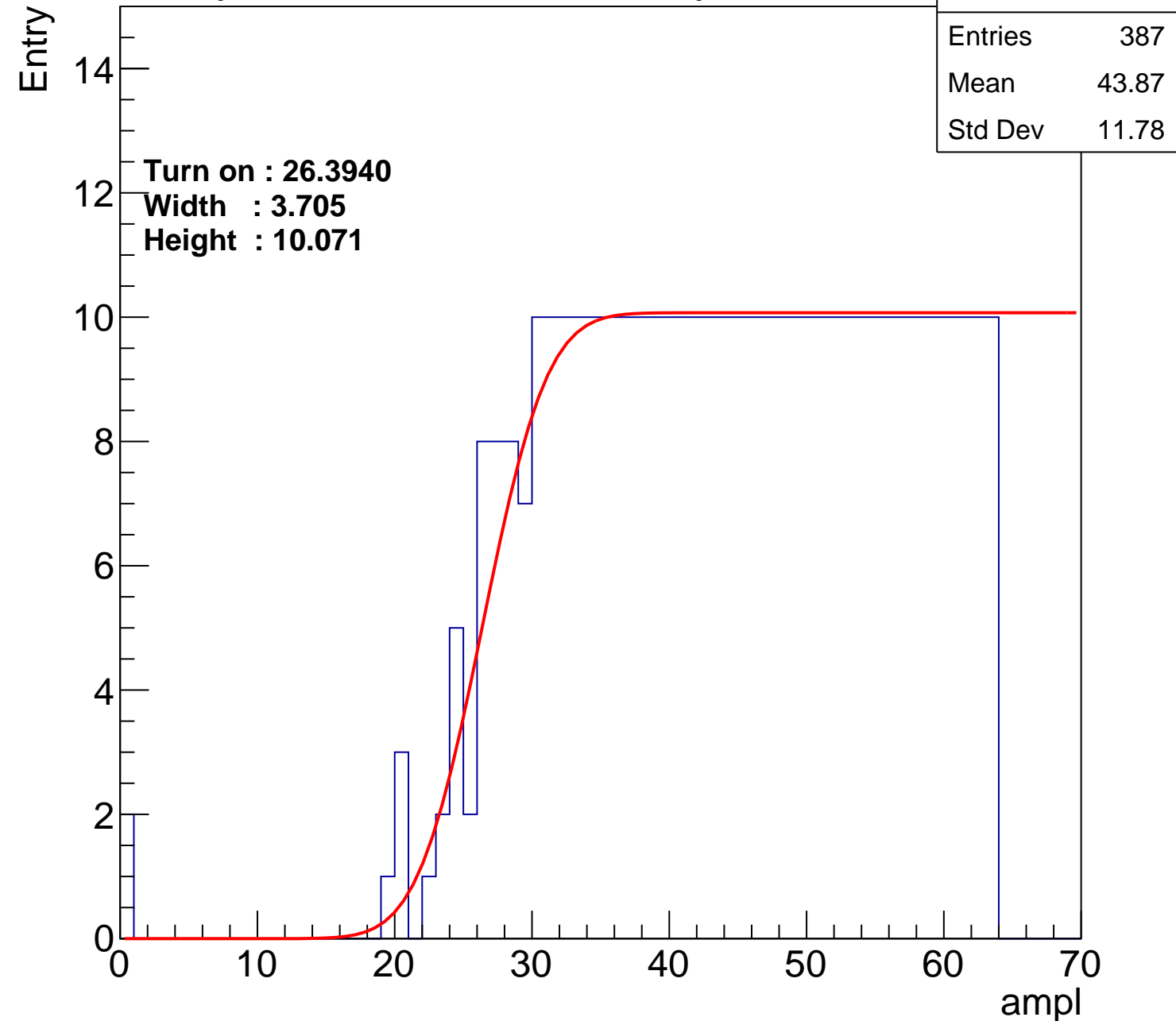
Width : 3.705

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
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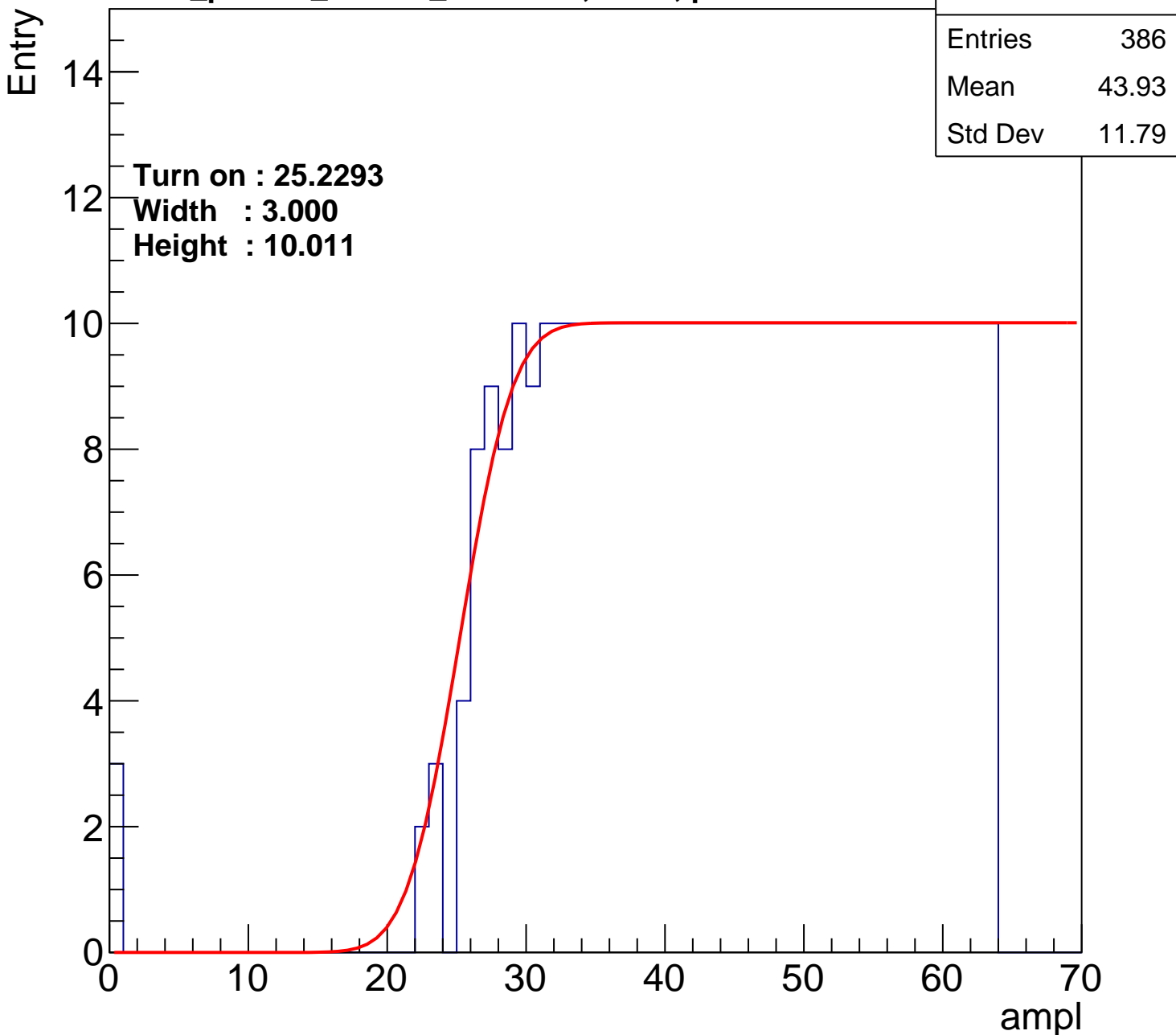
Mean	43.93
------	-------

Std Dev	11.79
---------	-------

Turn on : 25.2293

Width : 3.000

Height : 10.011



B0L000S, U15-ch7

calib_packv5_042523_0143.root, FC#5, port B1

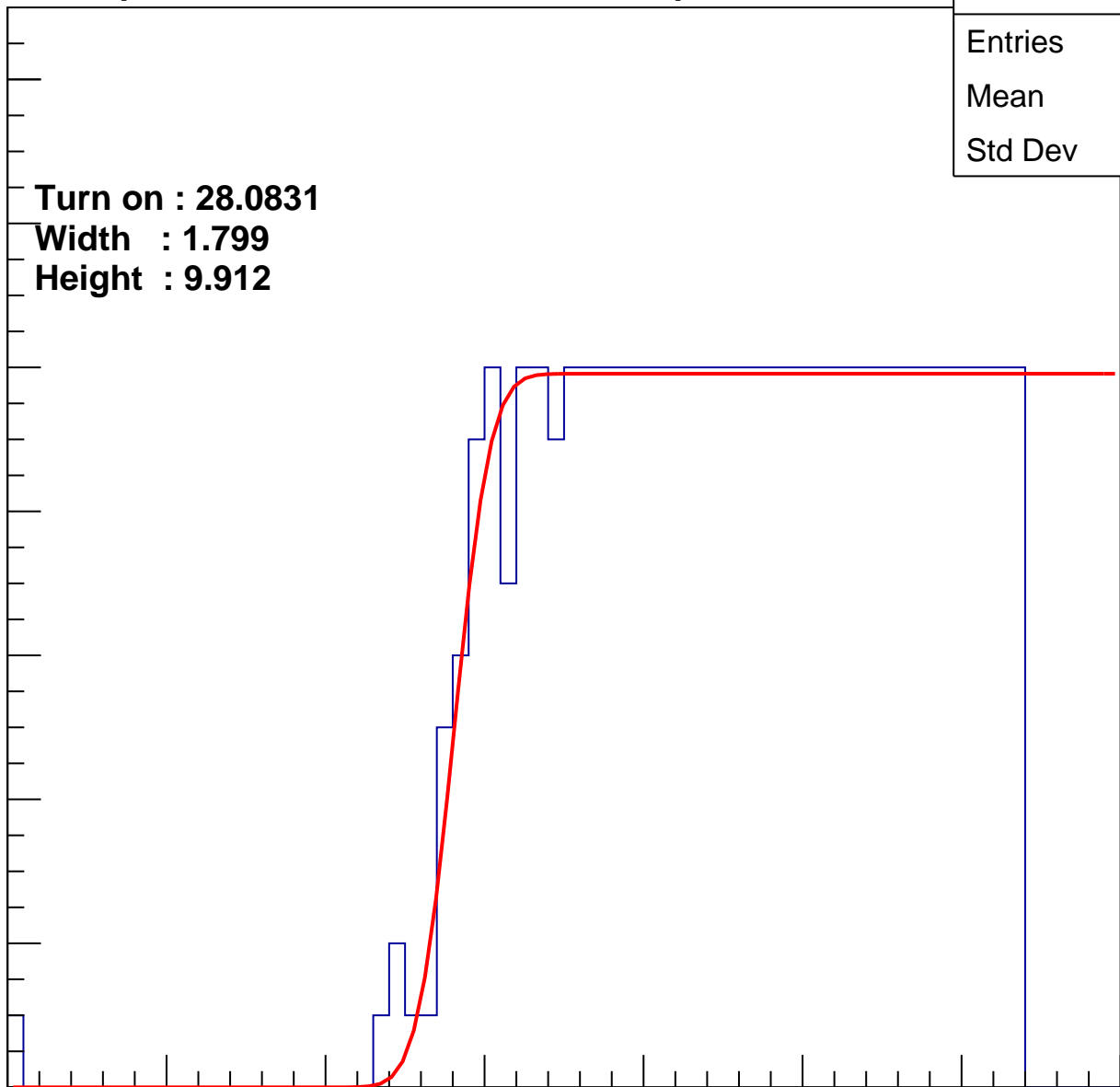
Entry

14
12
10
8
6
4
2
0

Turn on : 28.0831
Width : 1.799
Height : 9.912

Entries	362
Mean	45.22
Std Dev	10.84

ampl



B0L000S, U15-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.52
Std Dev	11.23

Turn on : 26.8371

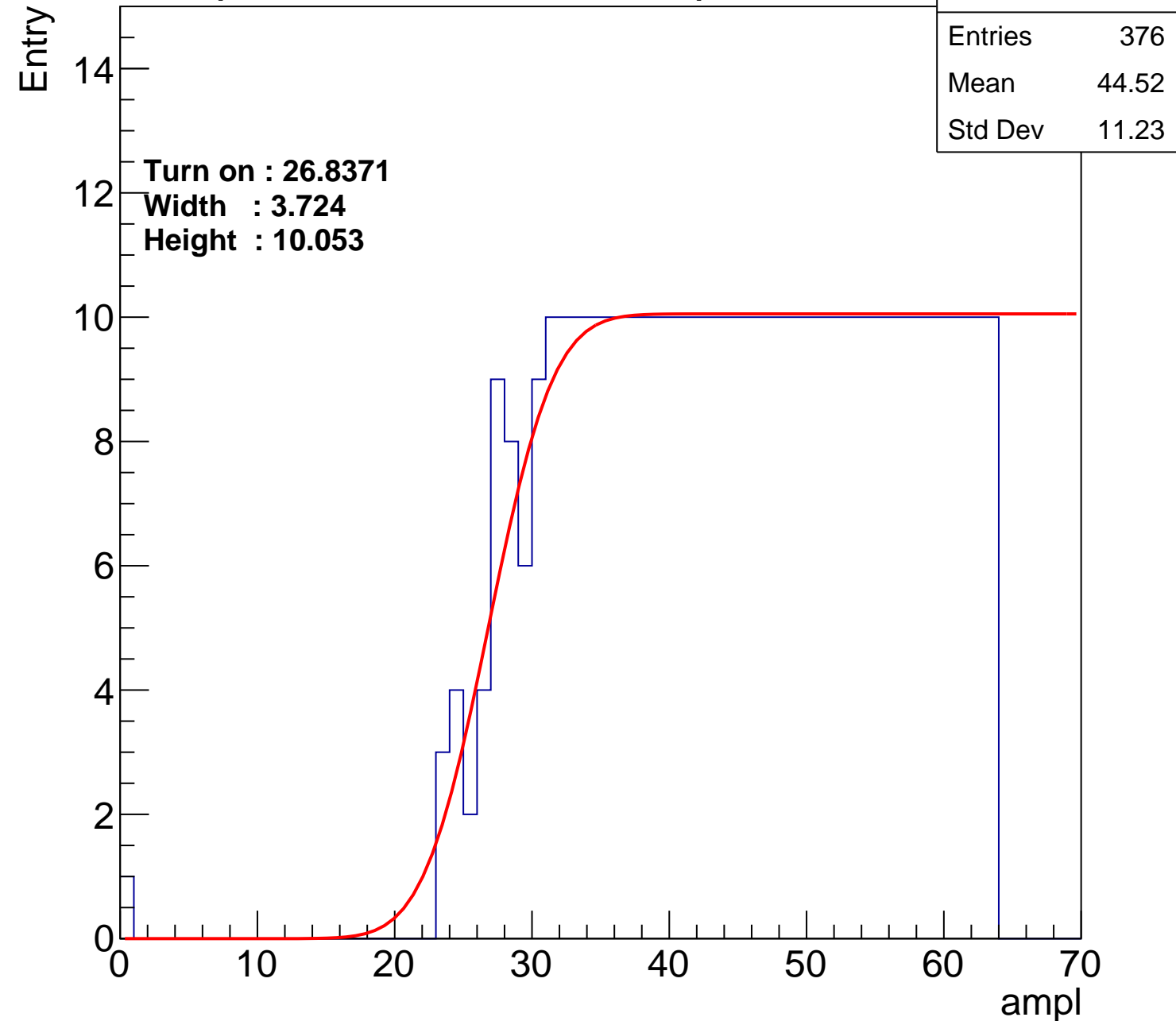
Width : 3.724

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.21
Std Dev	11.85

Turn on : 27.2585

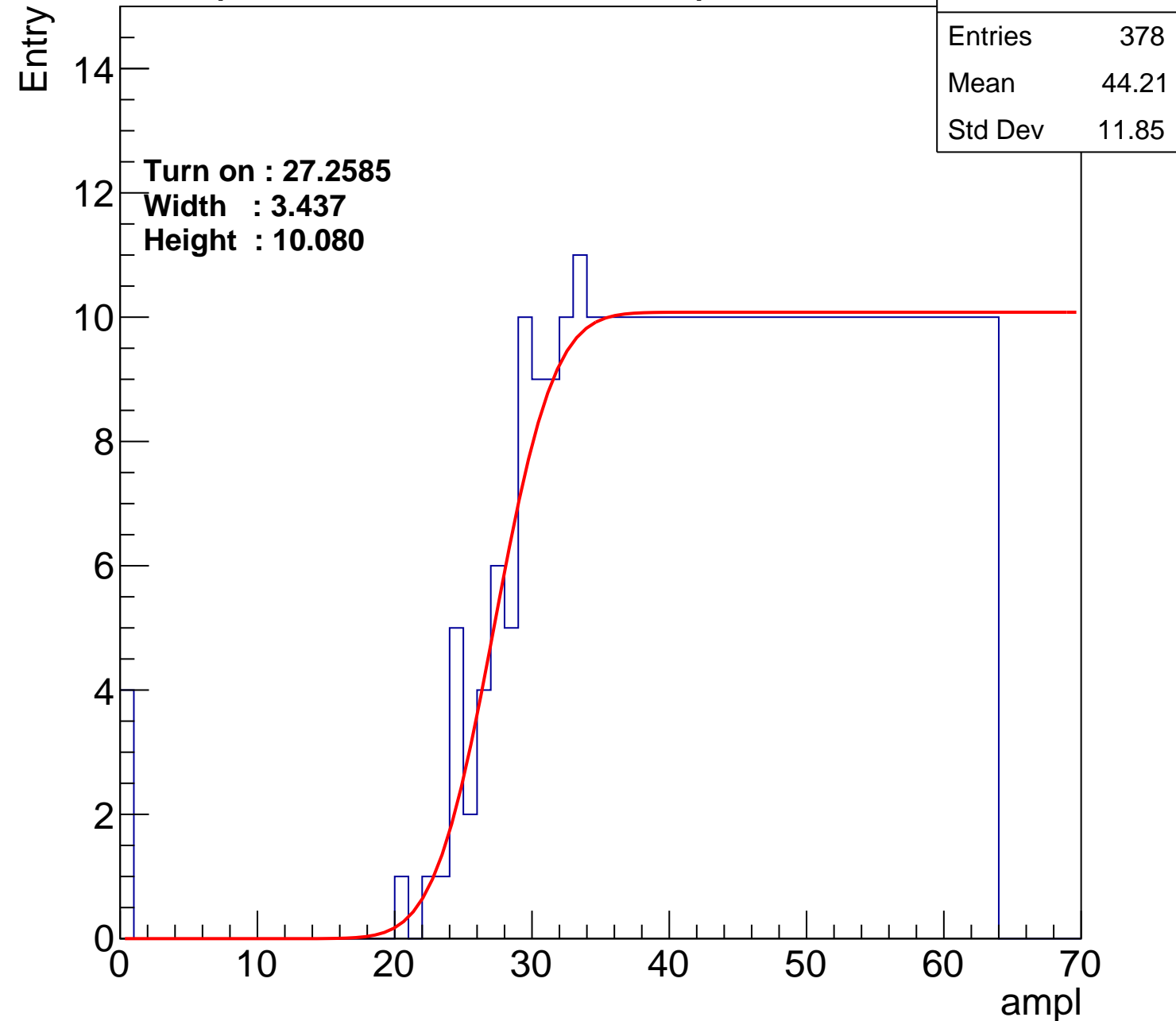
Width : 3.437

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch10

calib_packv5_042523_0143.root, FC#5, port B1

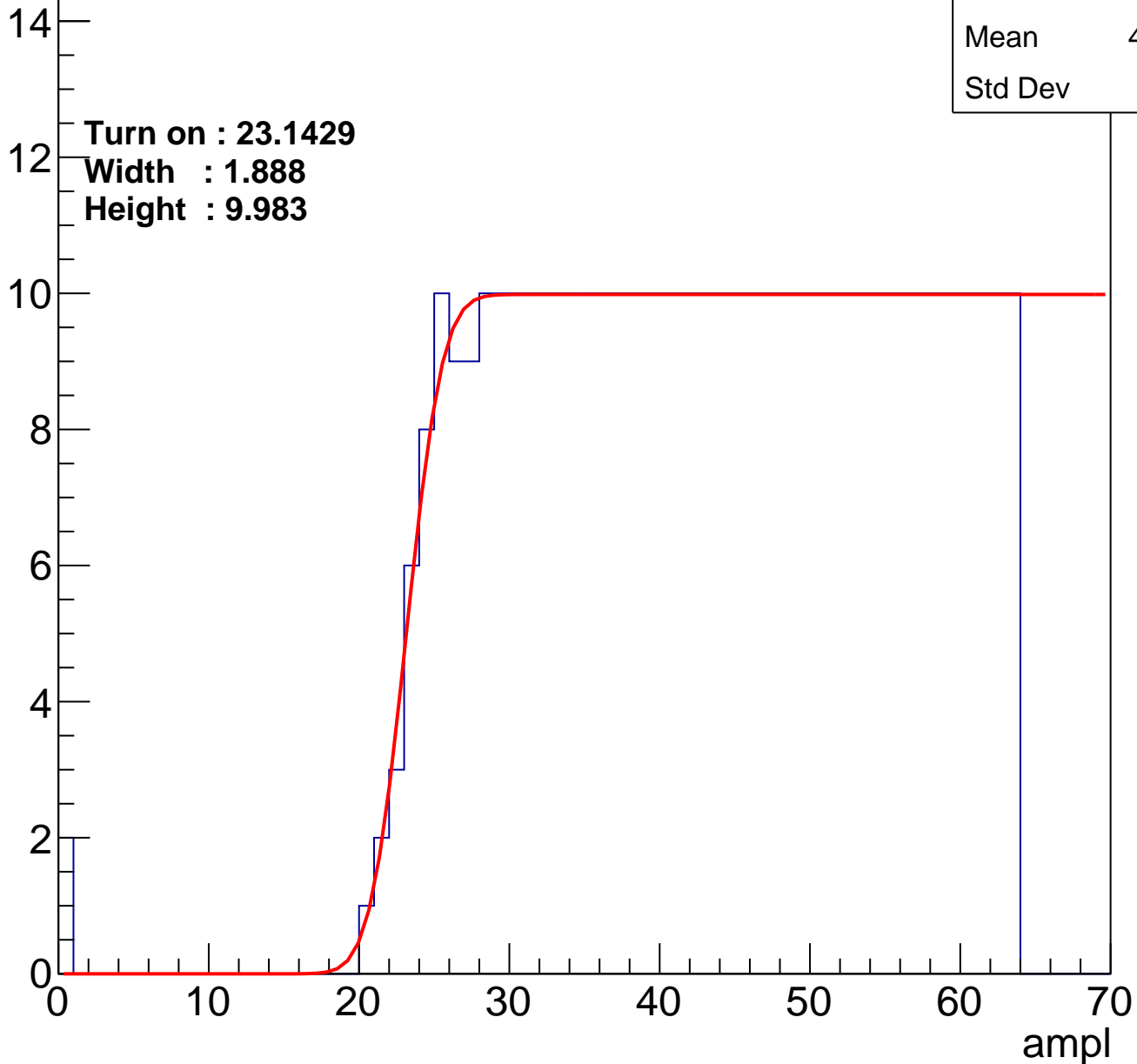
Entries	410
Mean	42.84
Std Dev	12.2

Turn on : 23.1429

Width : 1.888

Height : 9.983

Entry



B0L000S, U15-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.89
Std Dev	12.13

Turn on : 27.8747

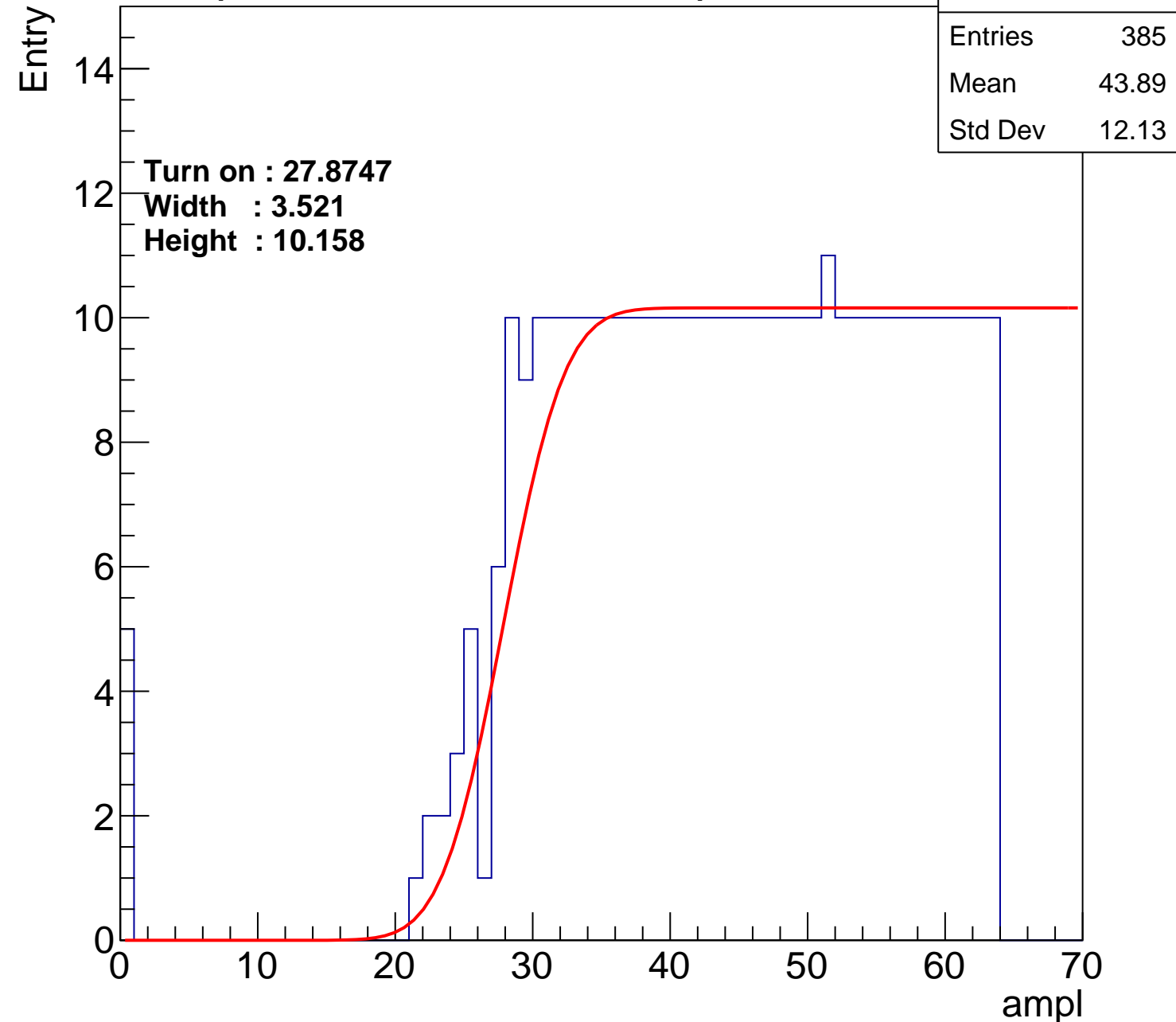
Width : 3.521

Height : 10.158

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.71
Std Dev	11.81

Turn on : 25.2145

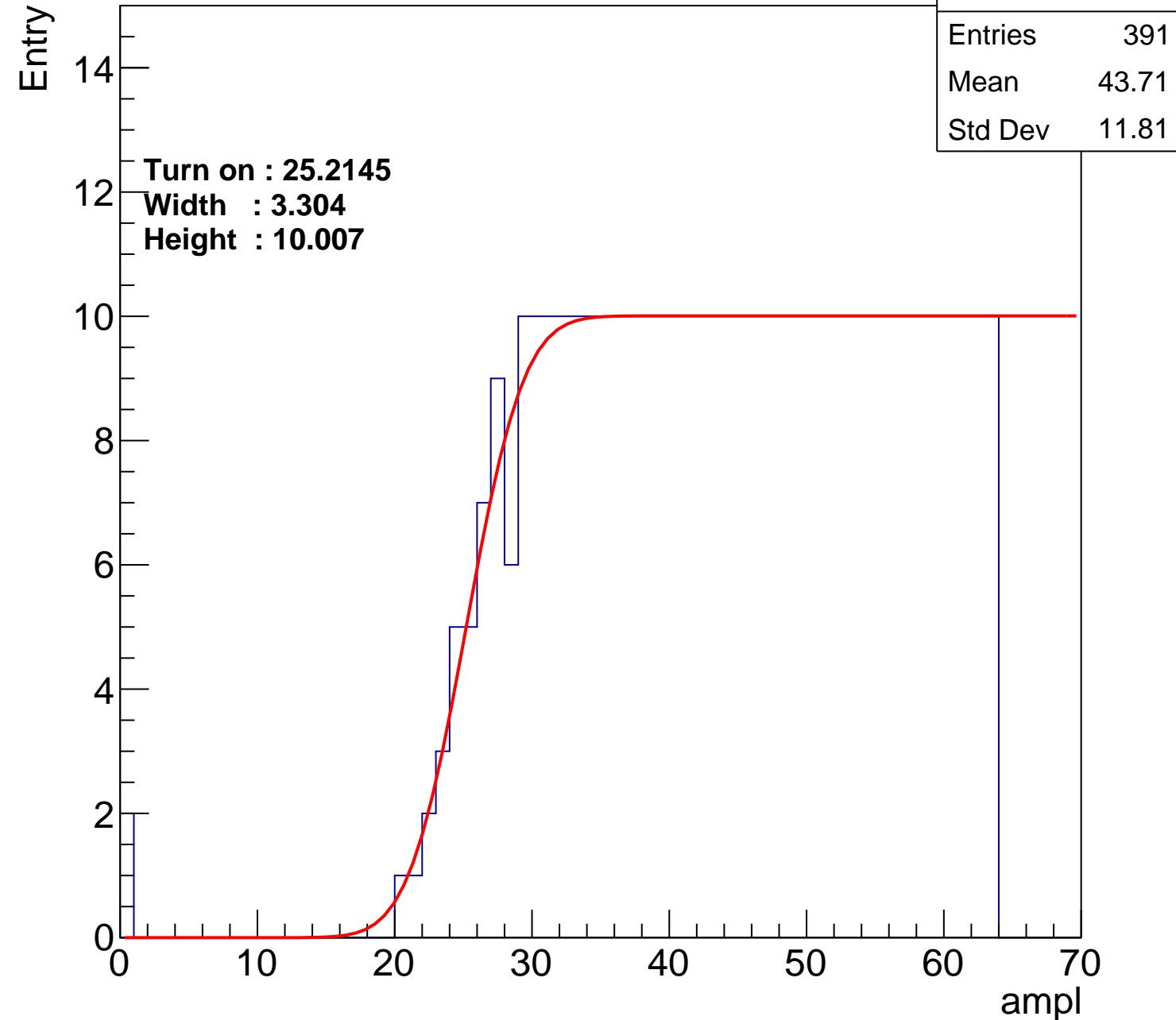
Width : 3.304

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.3736

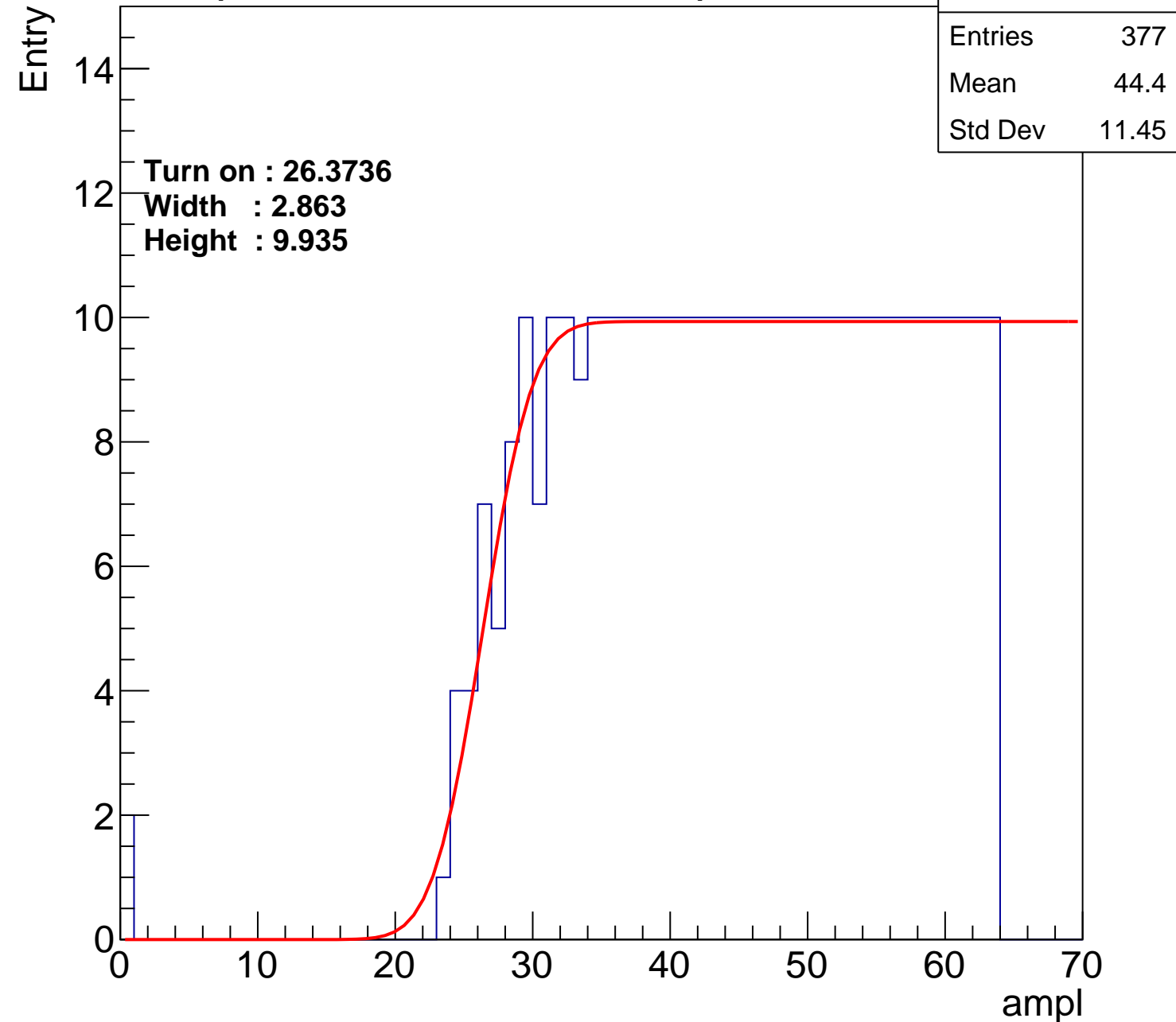
Width : 2.863

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch14

calib_packv5_042523_0143.root, FC#5, port B1

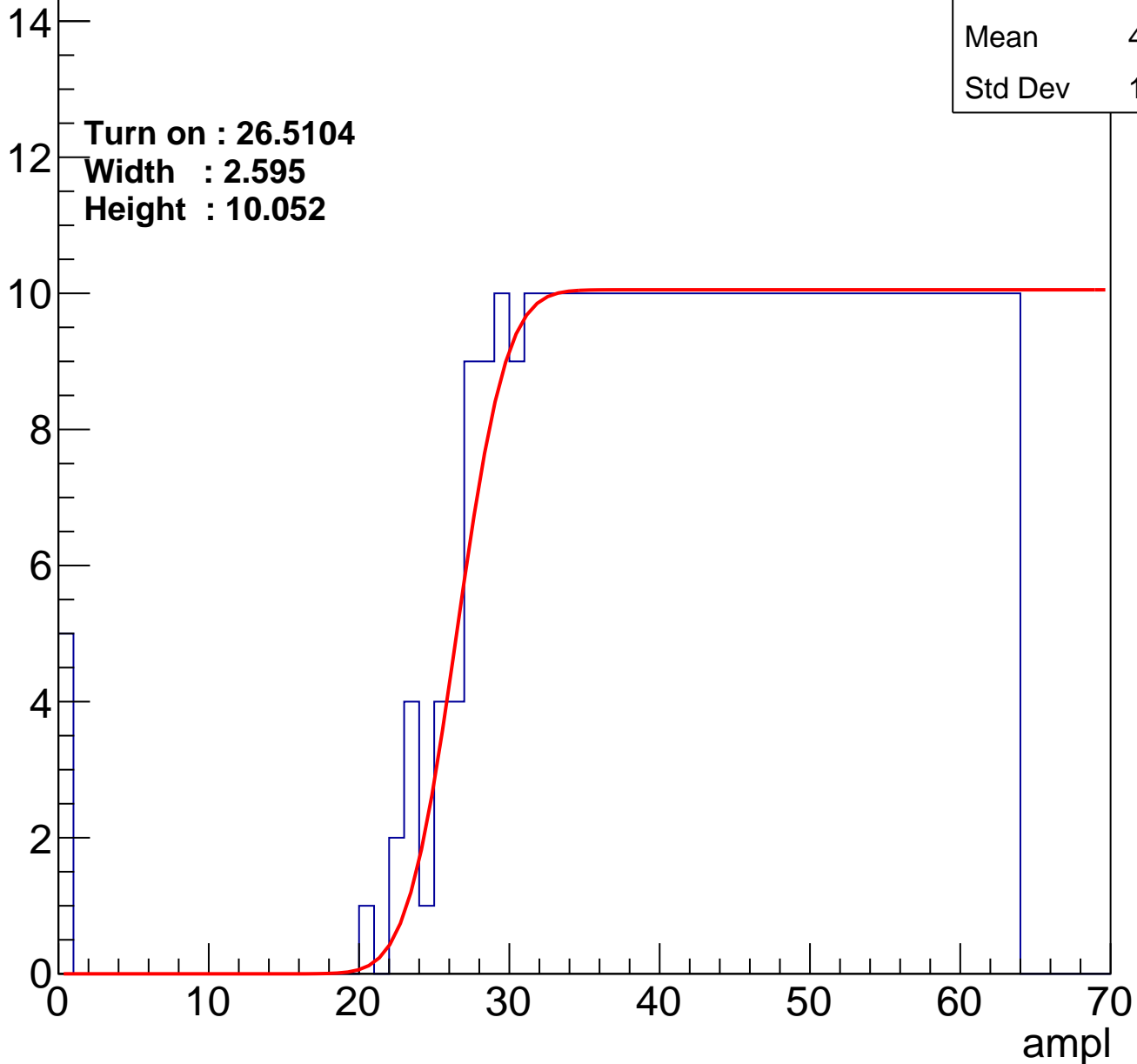
Entries	388
Mean	43.68
Std Dev	12.22

Turn on : 26.5104

Width : 2.595

Height : 10.052

Entry



B0L000S, U15-ch15

calib_packv5_042523_0143.root, FC#5, port B1

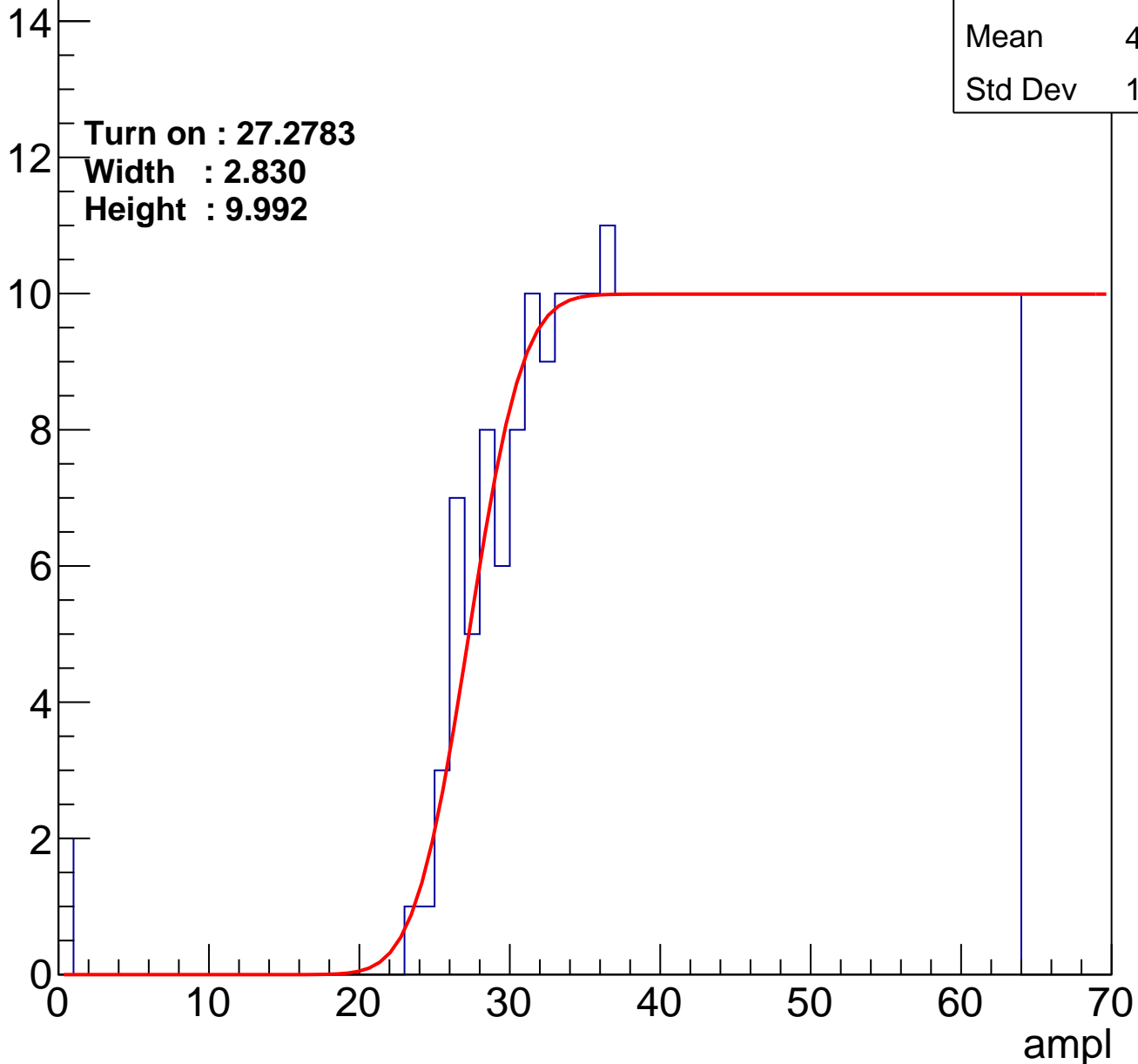
Entries	371
Mean	44.72
Std Dev	11.27

Turn on : 27.2783

Width : 2.830

Height : 9.992

Entry



B0L000S, U15-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.05
Std Dev	12.06

Turn on : 26.2765

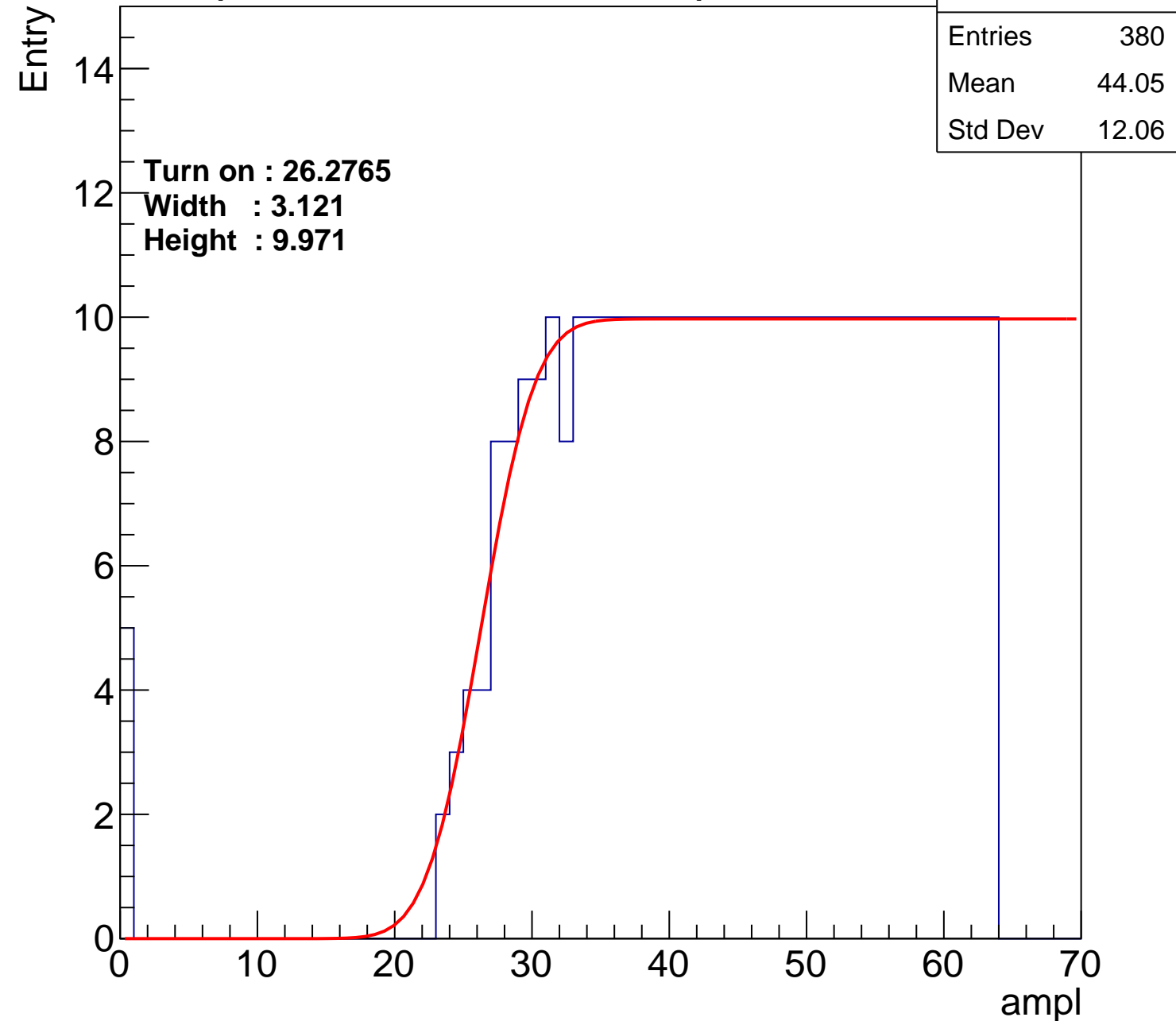
Width : 3.121

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.63
Std Dev	11.29

Turn on : 26.7439

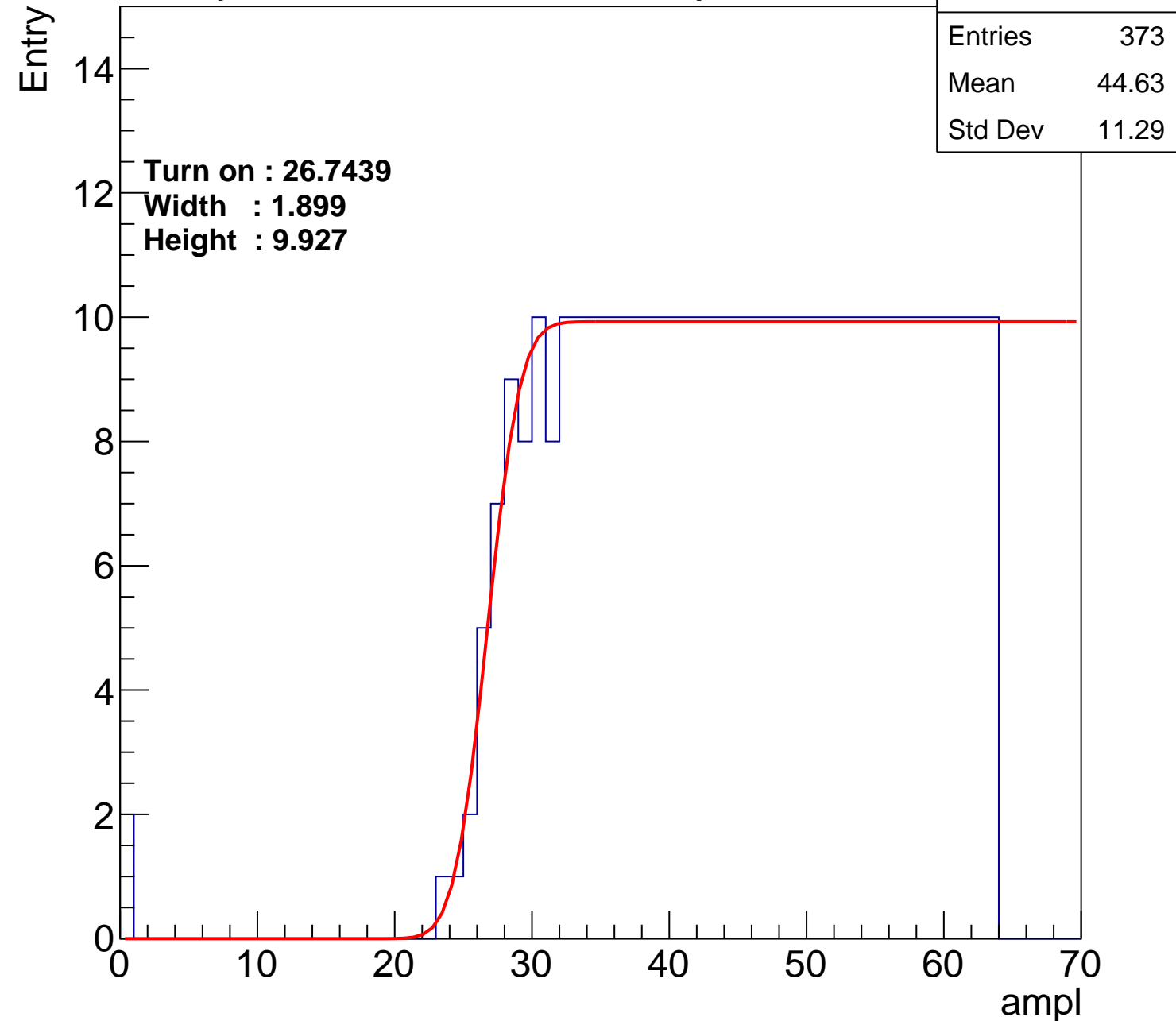
Width : 1.899

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch18

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.3081

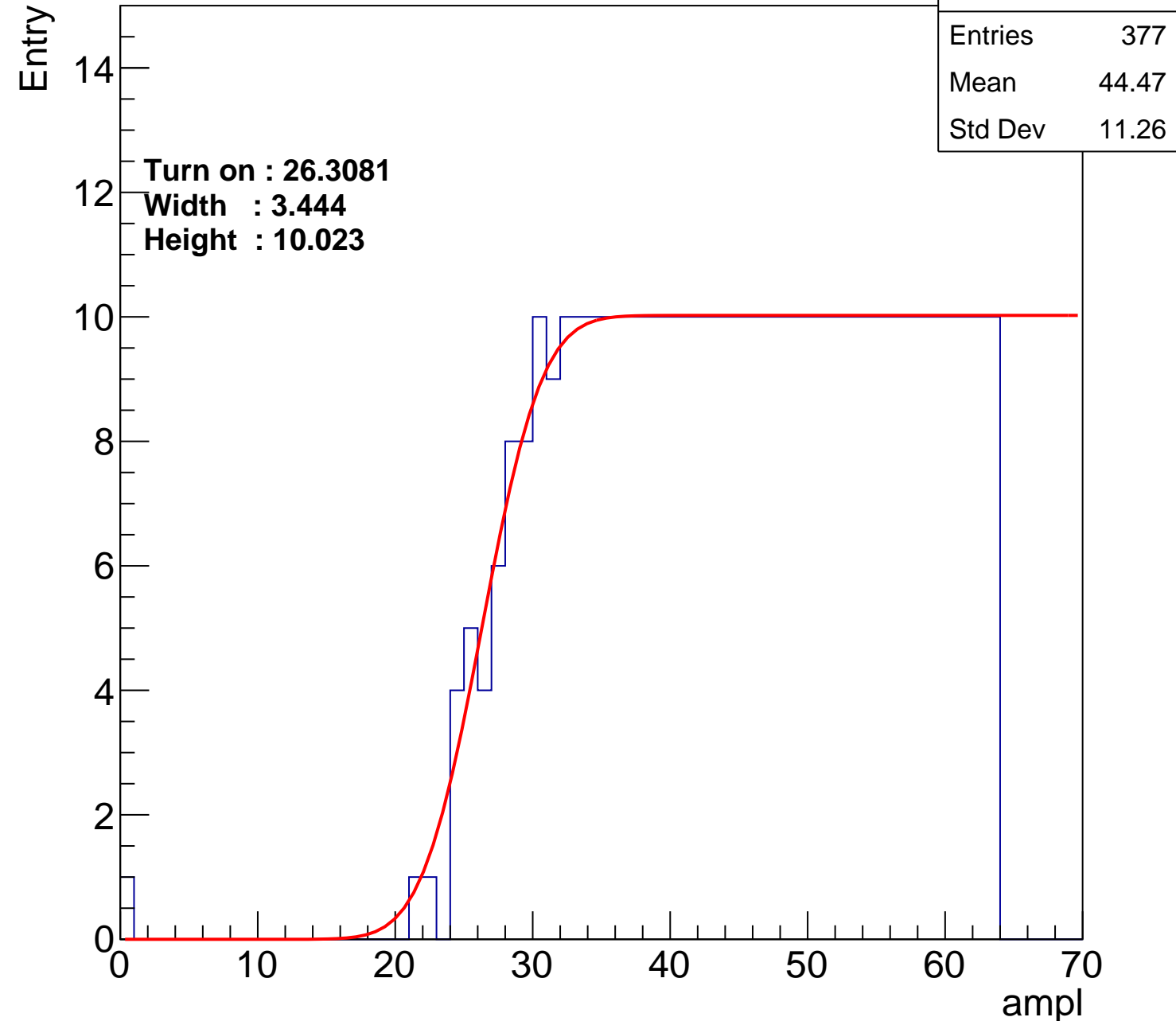
Width : 3.444

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch19

calib_packv5_042523_0143.root, FC#5, port B1

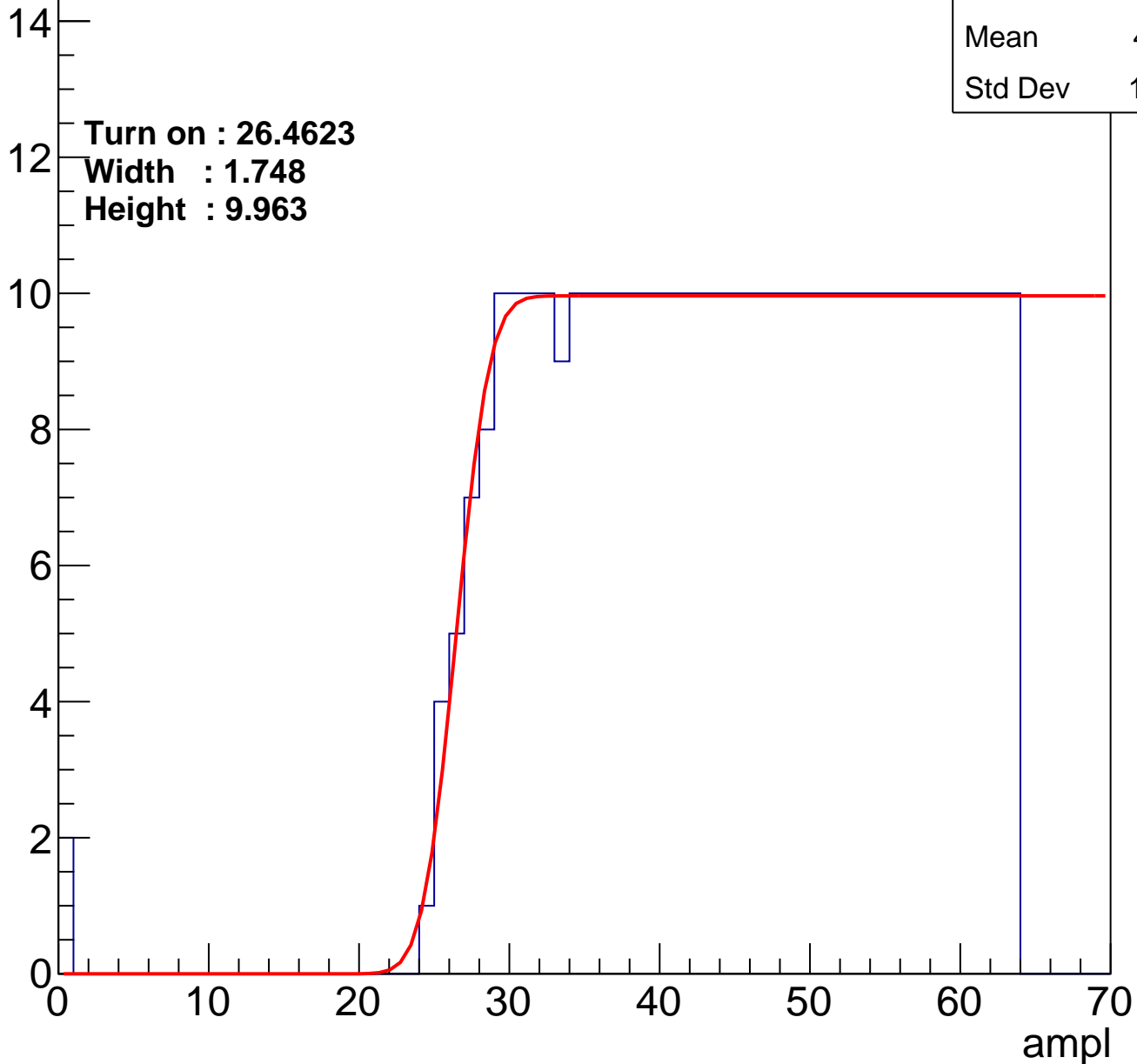
Entries	376
Mean	44.51
Std Dev	11.33

Turn on : 26.4623

Width : 1.748

Height : 9.963

Entry



B0L000S, U15-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.08
Std Dev	12.2

Turn on : 27.2037

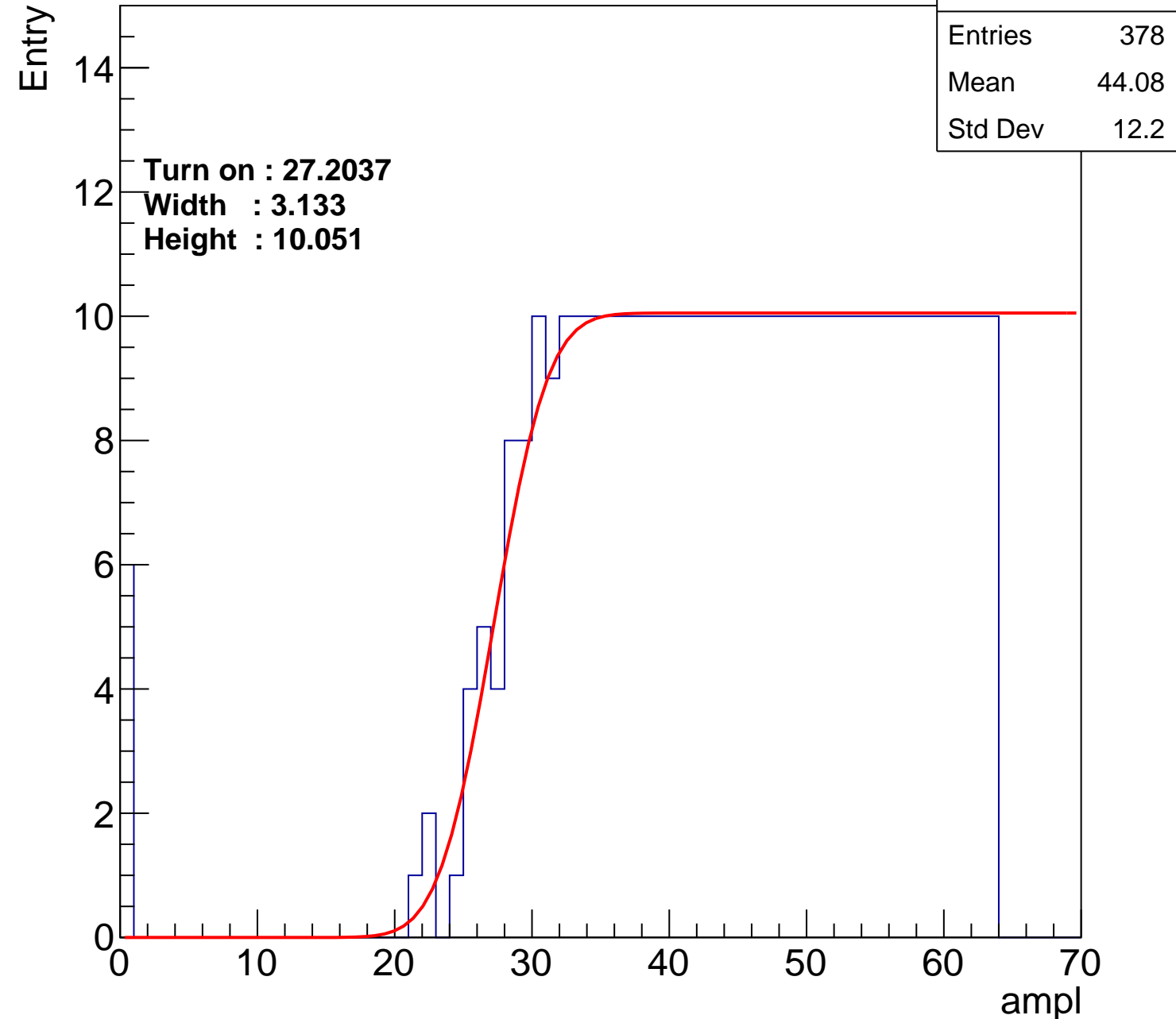
Width : 3.133

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch21

calib_packv5_042523_0143.root, FC#5, port B1

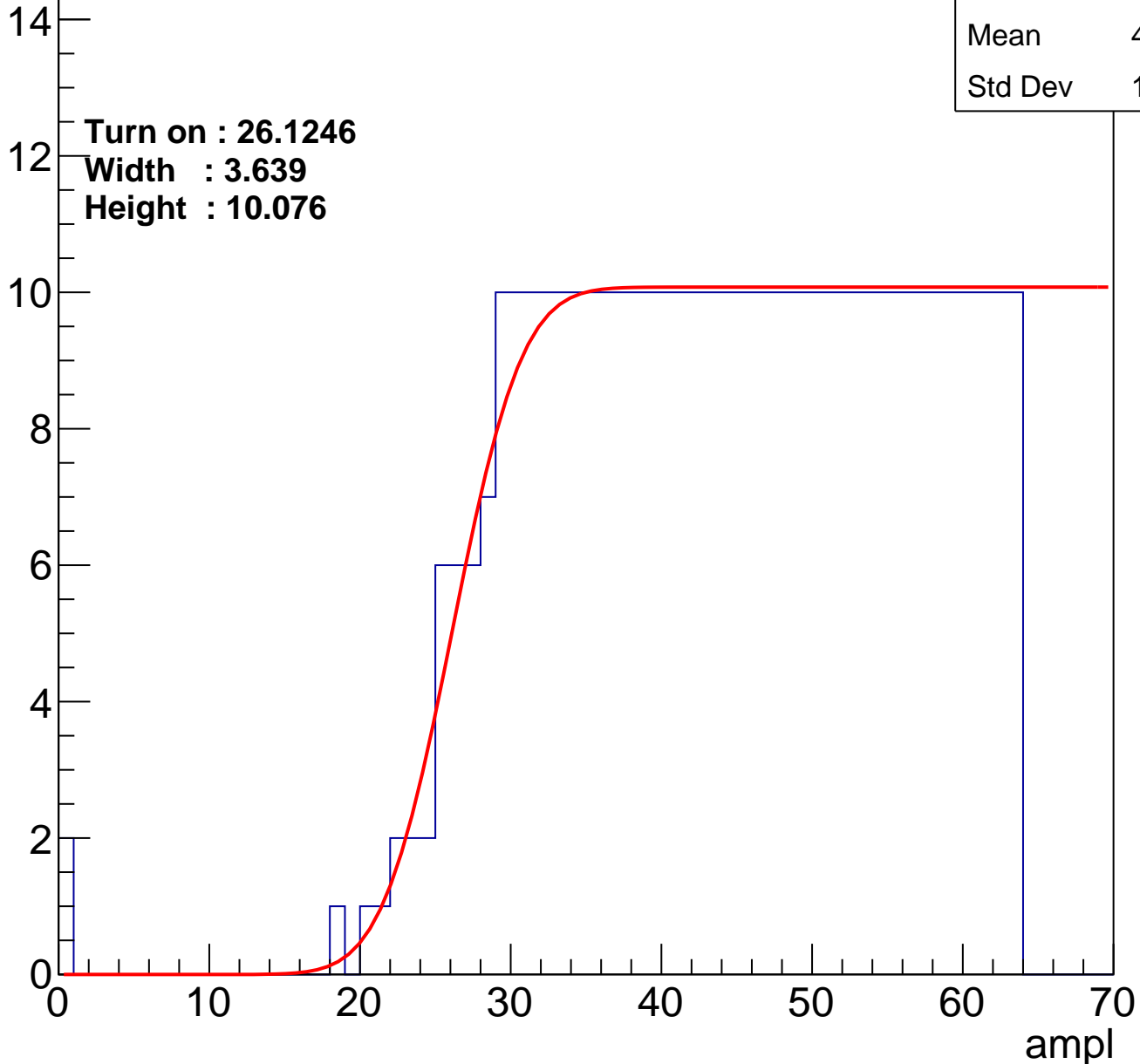
Entries	386
Mean	43.94
Std Dev	11.72

Turn on : 26.1246

Width : 3.639

Height : 10.076

Entry



B0L000S, U15-ch22

calib_packv5_042523_0143.root, FC#5, port B1

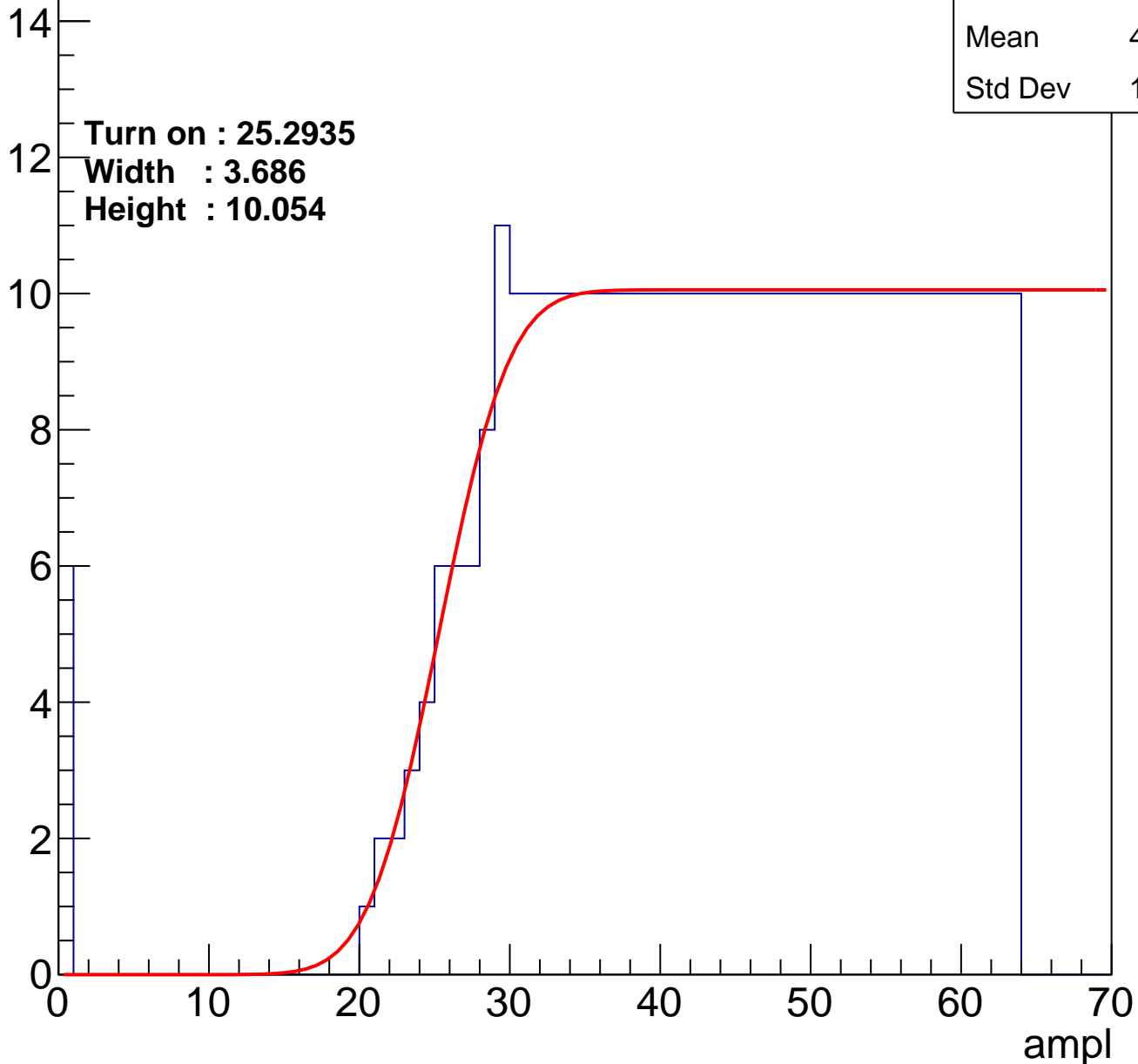
Entries	395
Mean	43.27
Std Dev	12.54

Turn on : 25.2935

Width : 3.686

Height : 10.054

Entry



B0L000S, U15-ch23

calib_packv5_042523_0143.root, FC#5, port B1

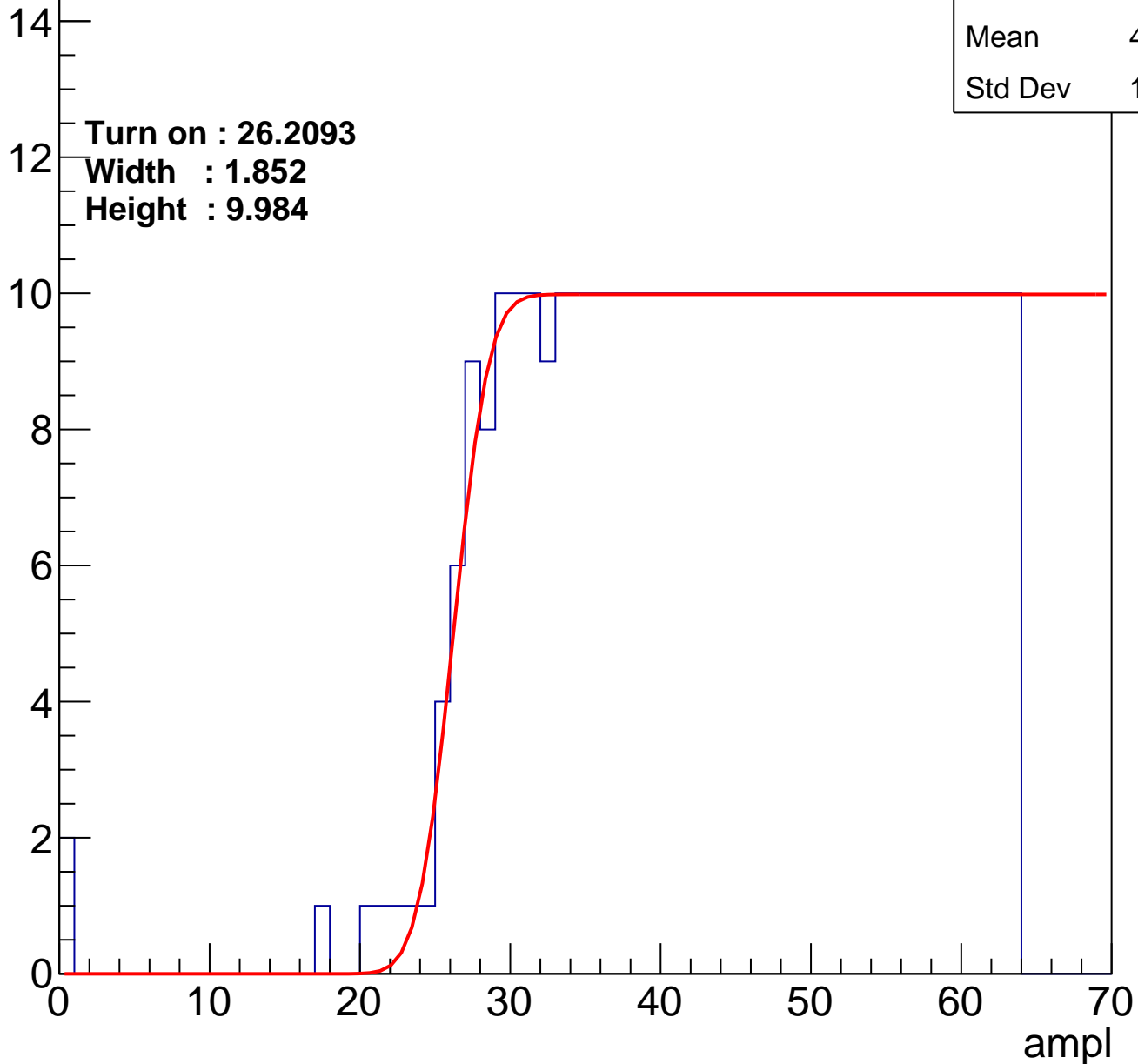
Entries	384
Mean	44.06
Std Dev	11.64

Turn on : 26.2093

Width : 1.852

Height : 9.984

Entry



B0L000S, U15-ch24

calib_packv5_042523_0143.root, FC#5, port B1

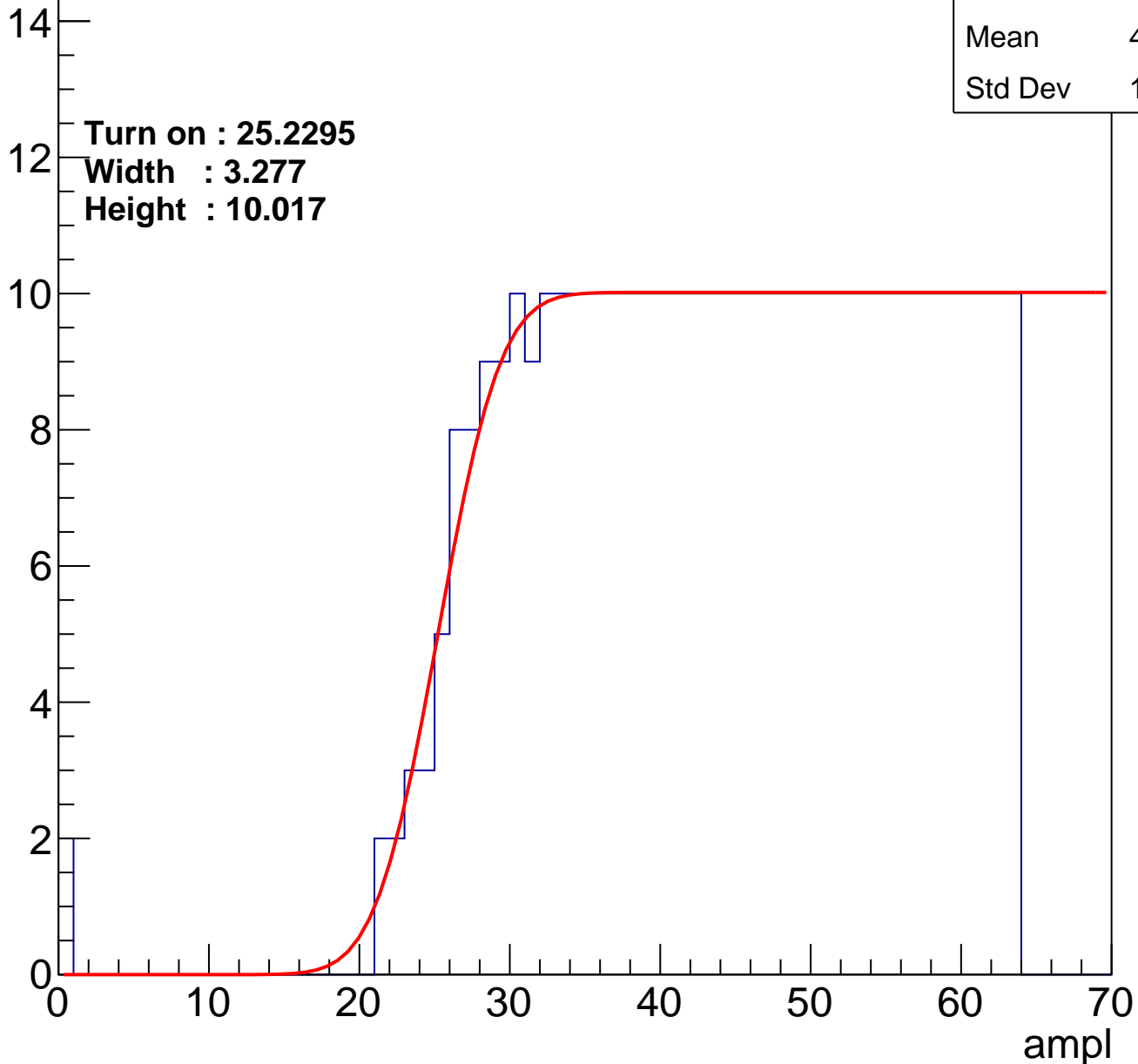
Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 25.2295

Width : 3.277

Height : 10.017

Entry



B0L000S, U15-ch25

calib_packv5_042523_0143.root, FC#5, port B1

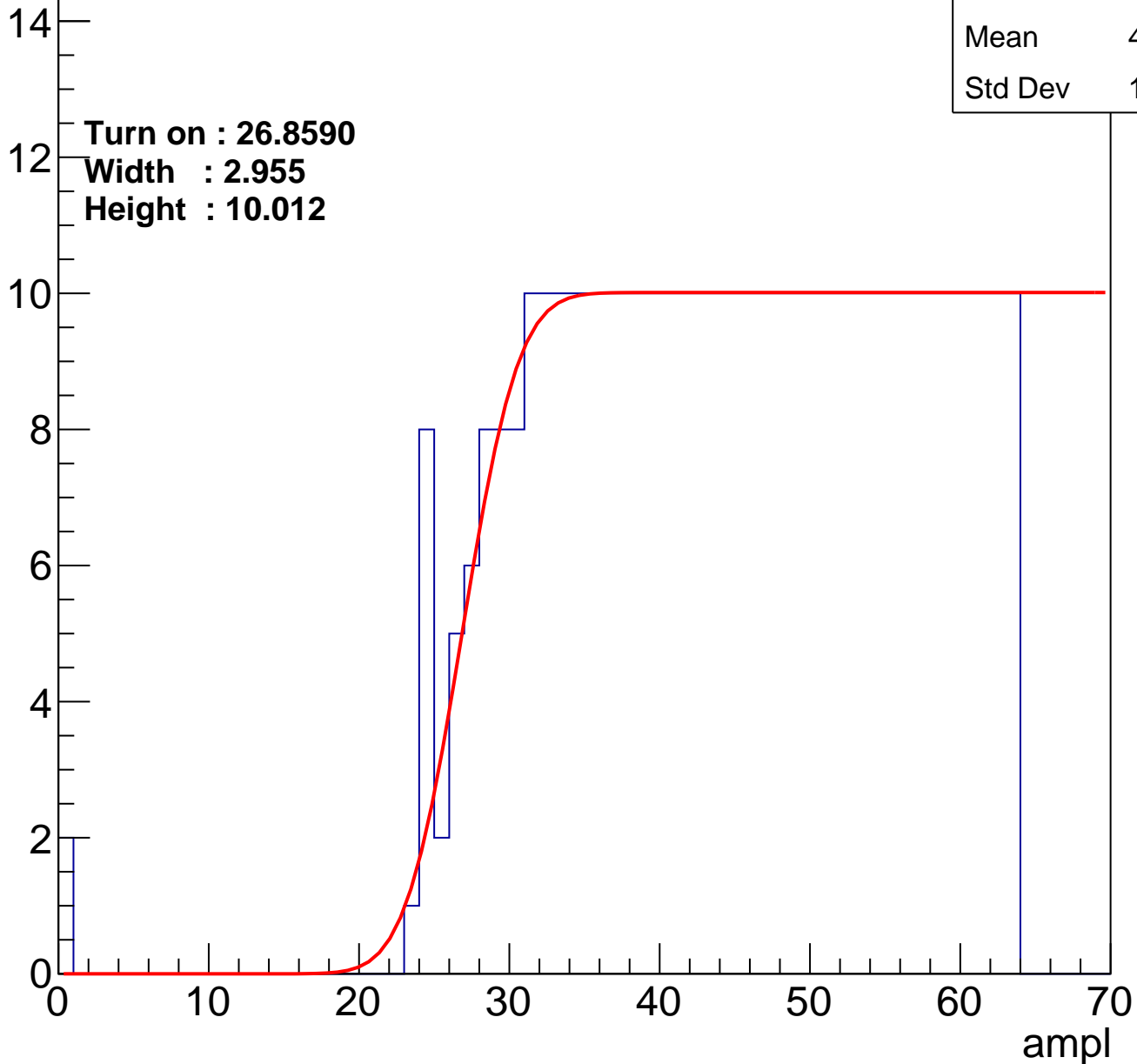
Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.8590

Width : 2.955

Height : 10.012

Entry



B0L000S, U15-ch26

calib_packv5_042523_0143.root, FC#5, port B1

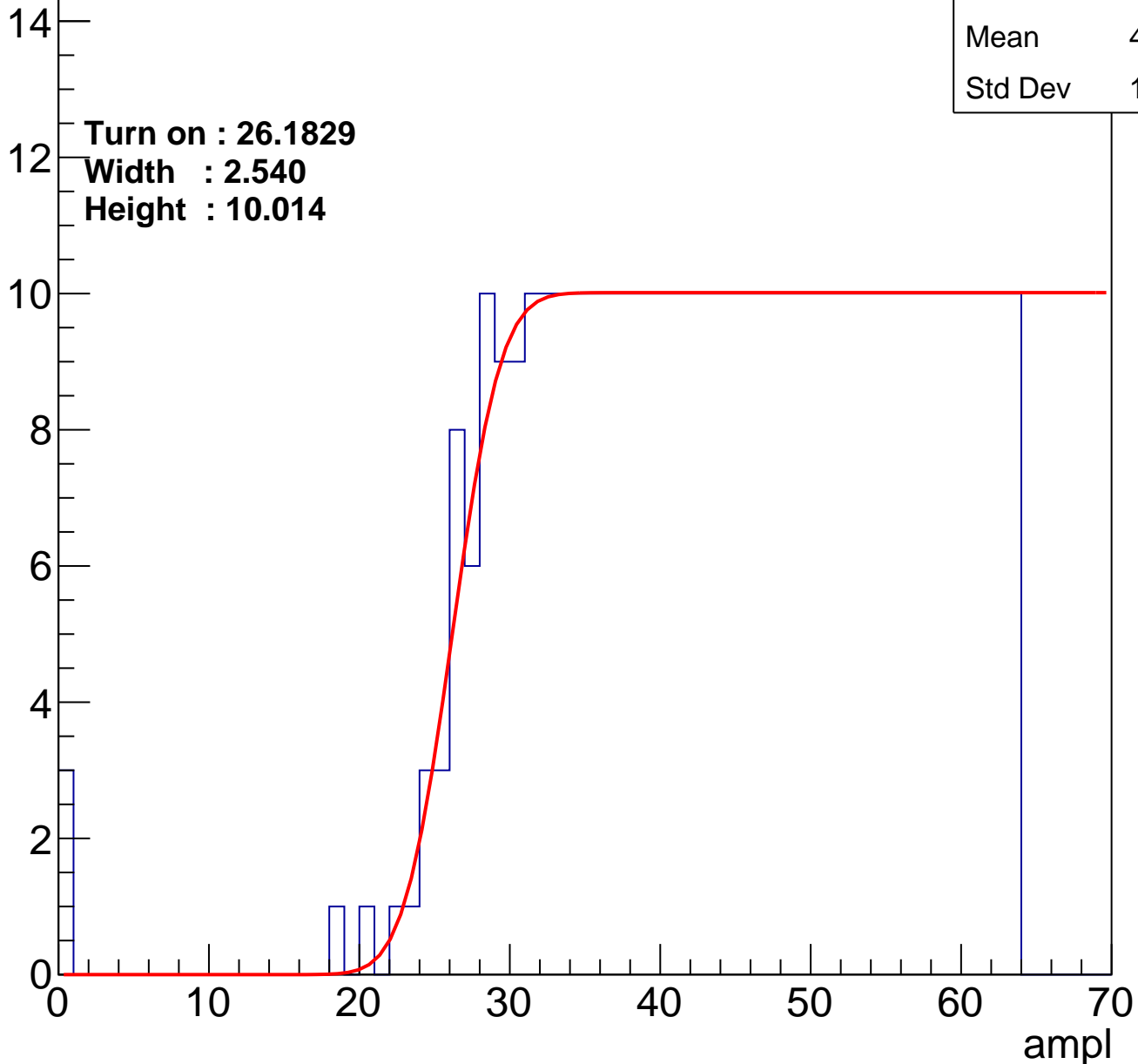
Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 26.1829

Width : 2.540

Height : 10.014

Entry



B0L000S, U15-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.5601

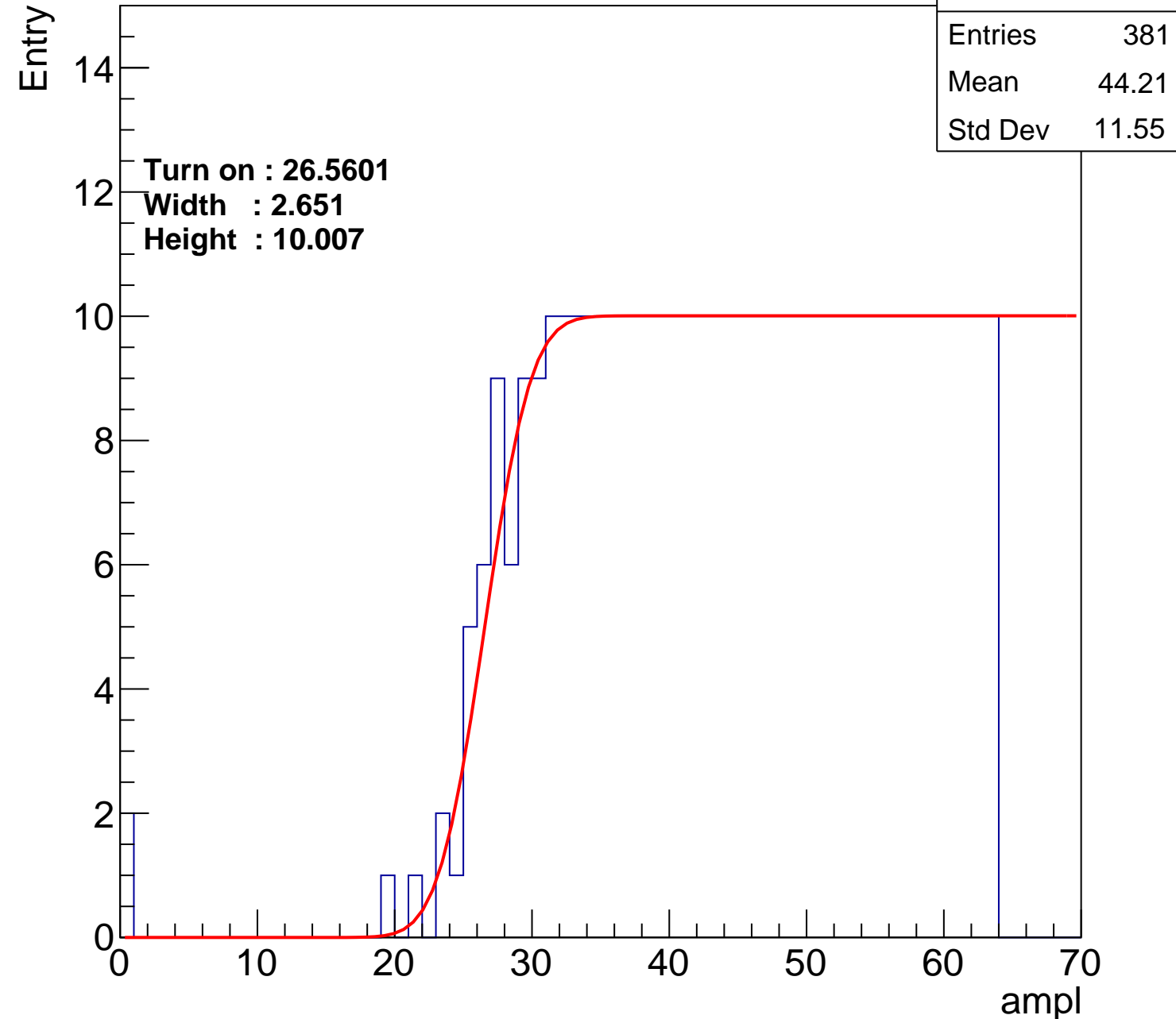
Width : 2.651

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch28

calib_packv5_042523_0143.root, FC#5, port B1

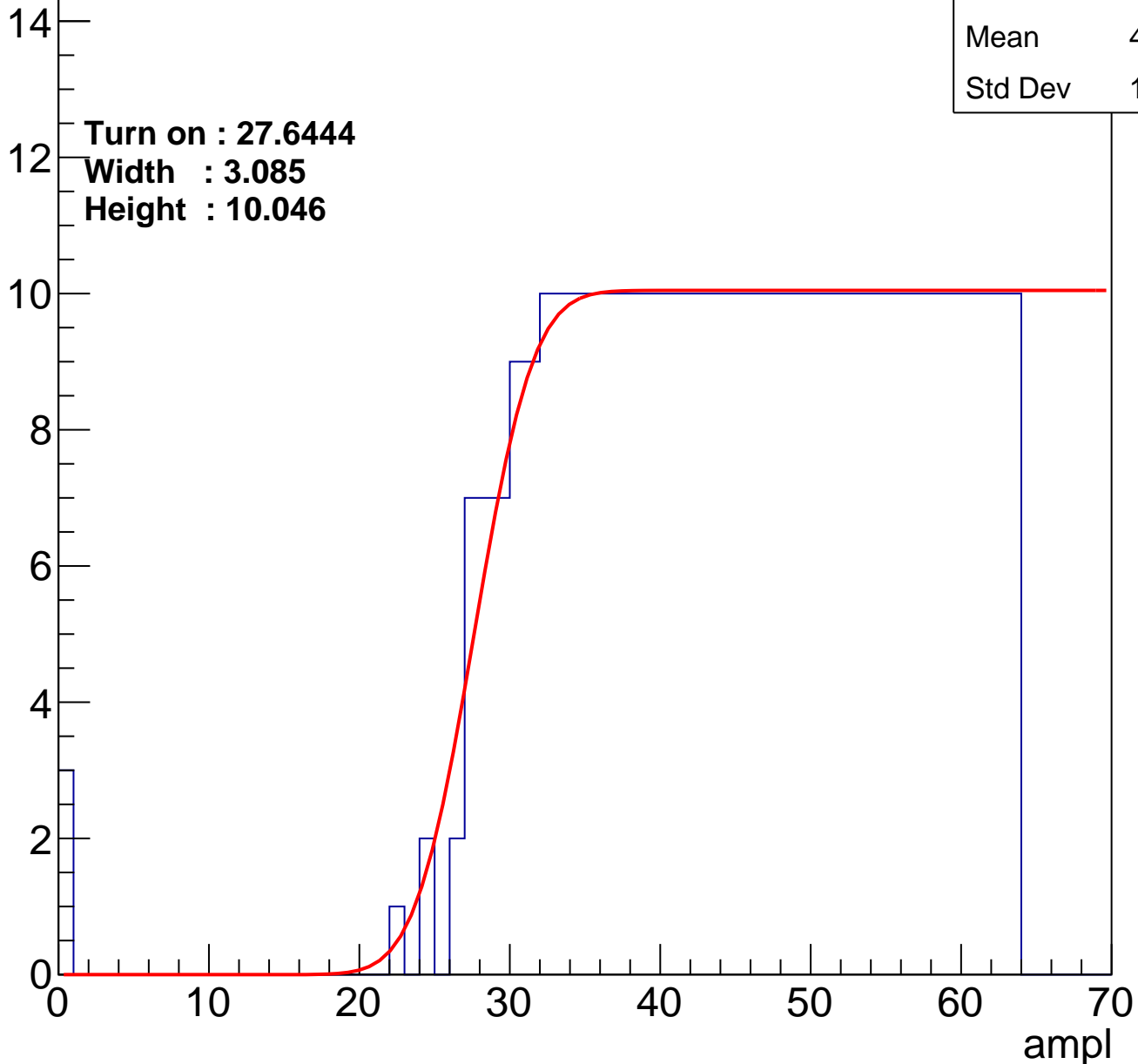
Entry

Entries	367
Mean	44.85
Std Dev	11.36

Turn on : 27.6444

Width : 3.085

Height : 10.046



B0L000S, U15-ch29

calib_packv5_042523_0143.root, FC#5, port B1

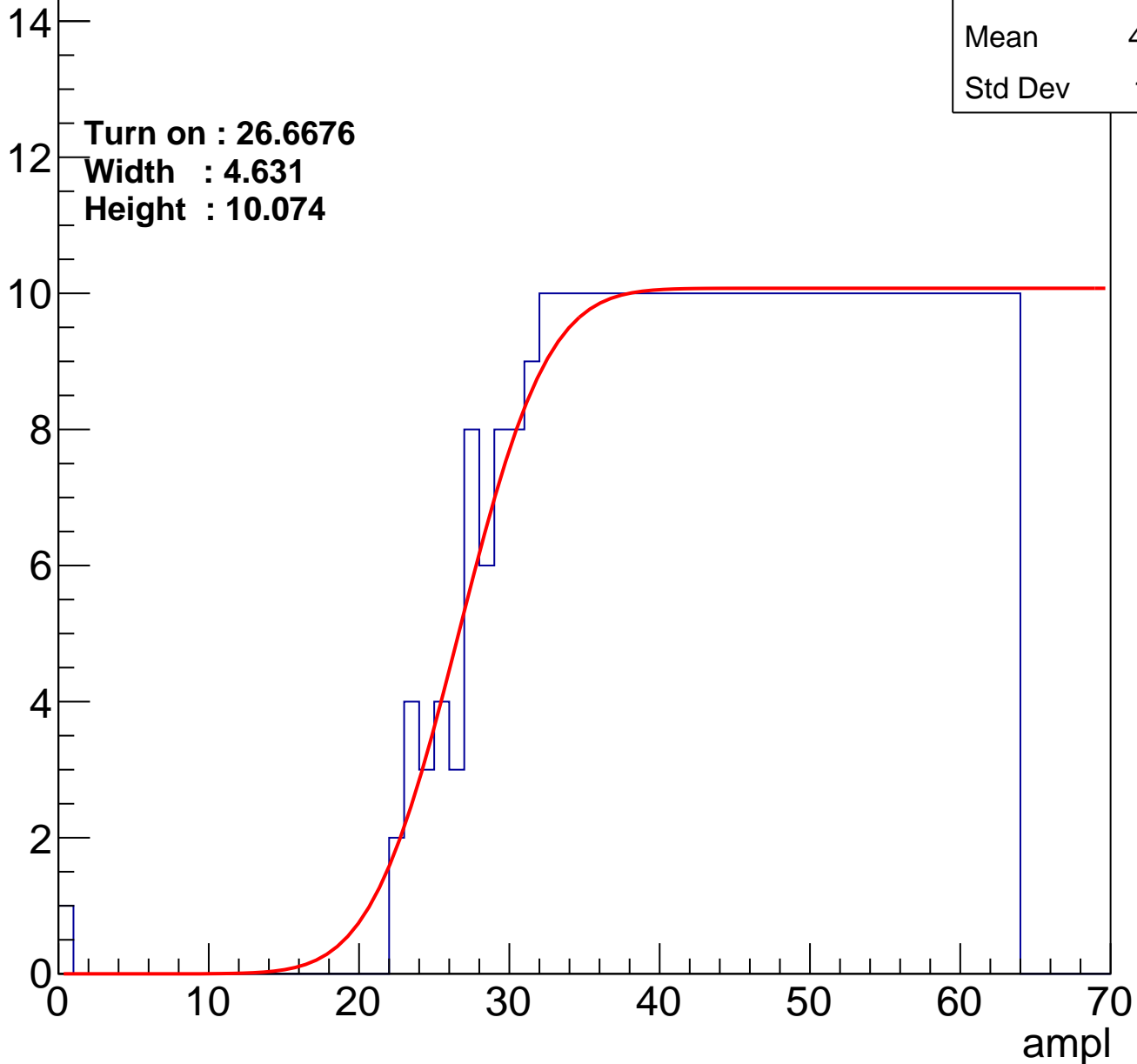
Entries	376
Mean	44.47
Std Dev	11.31

Turn on : 26.6676

Width : 4.631

Height : 10.074

Entry



B0L000S, U15-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.1
Std Dev	11.75

Turn on : 26.1482

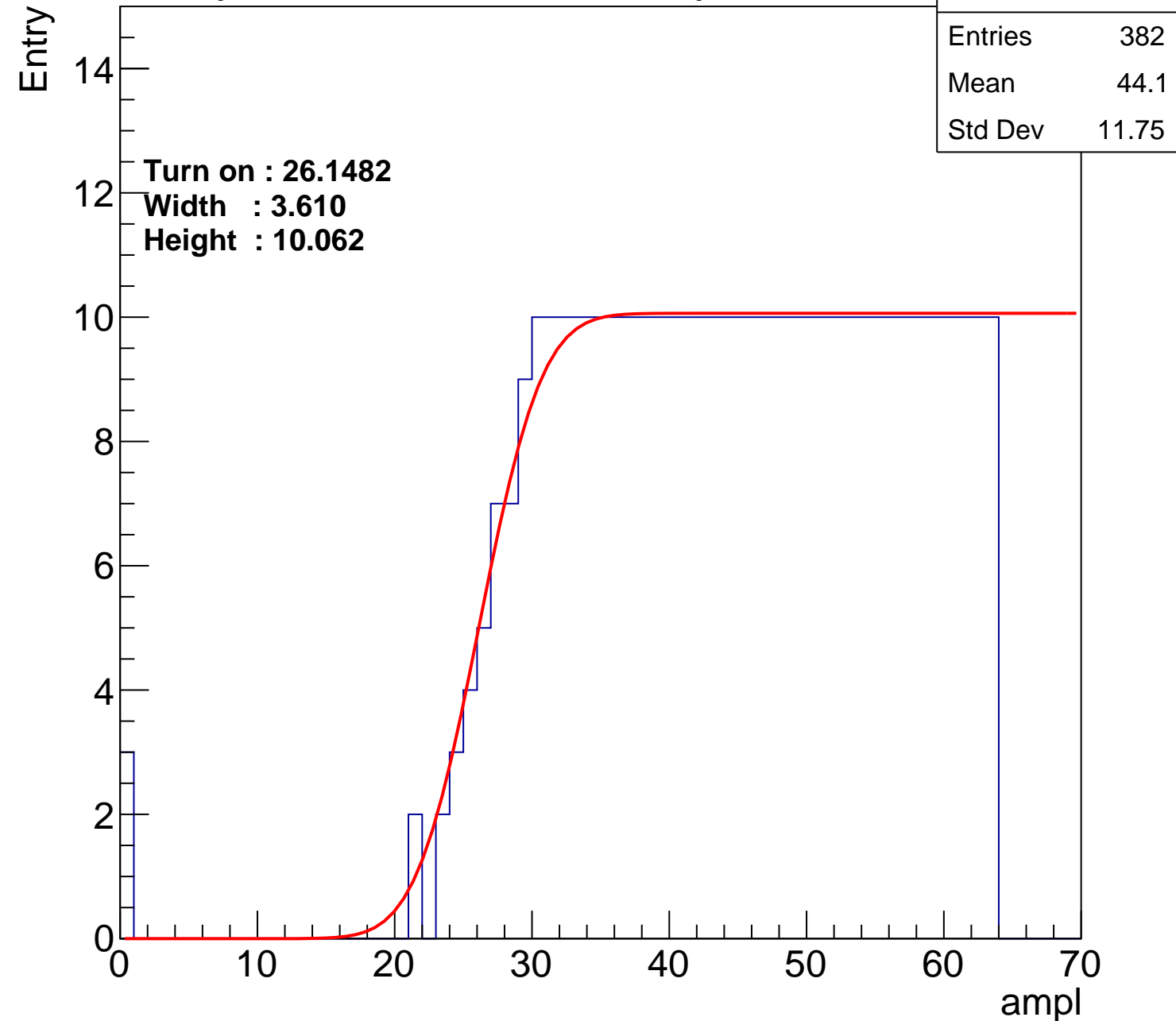
Width : 3.610

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch31

calib_packv5_042523_0143.root, FC#5, port B1

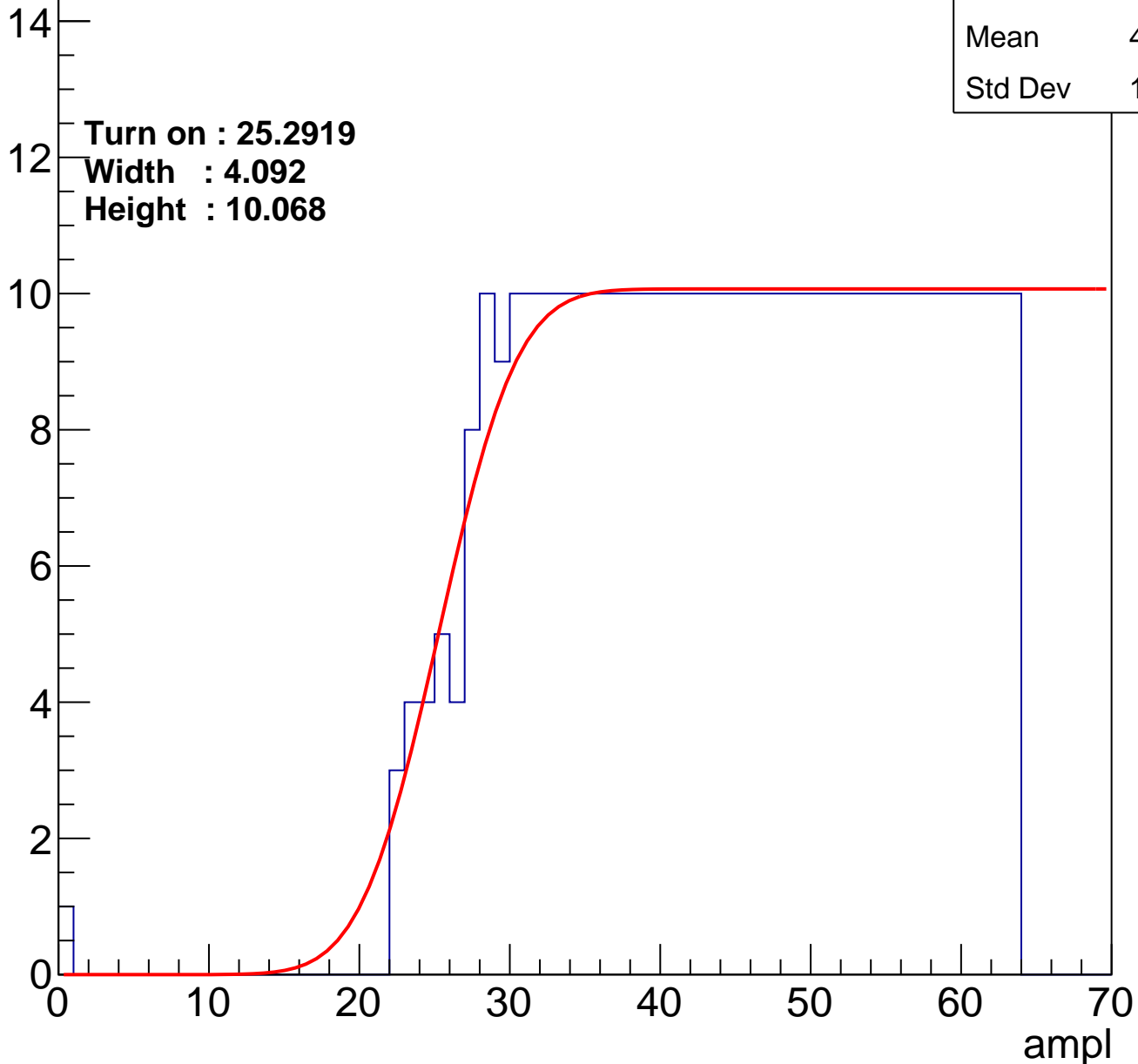
Entries	388
Mean	43.94
Std Dev	11.53

Turn on : 25.2919

Width : 4.092

Height : 10.068

Entry



B0L000S, U15-ch32

calib_packv5_042523_0143.root, FC#5, port B1

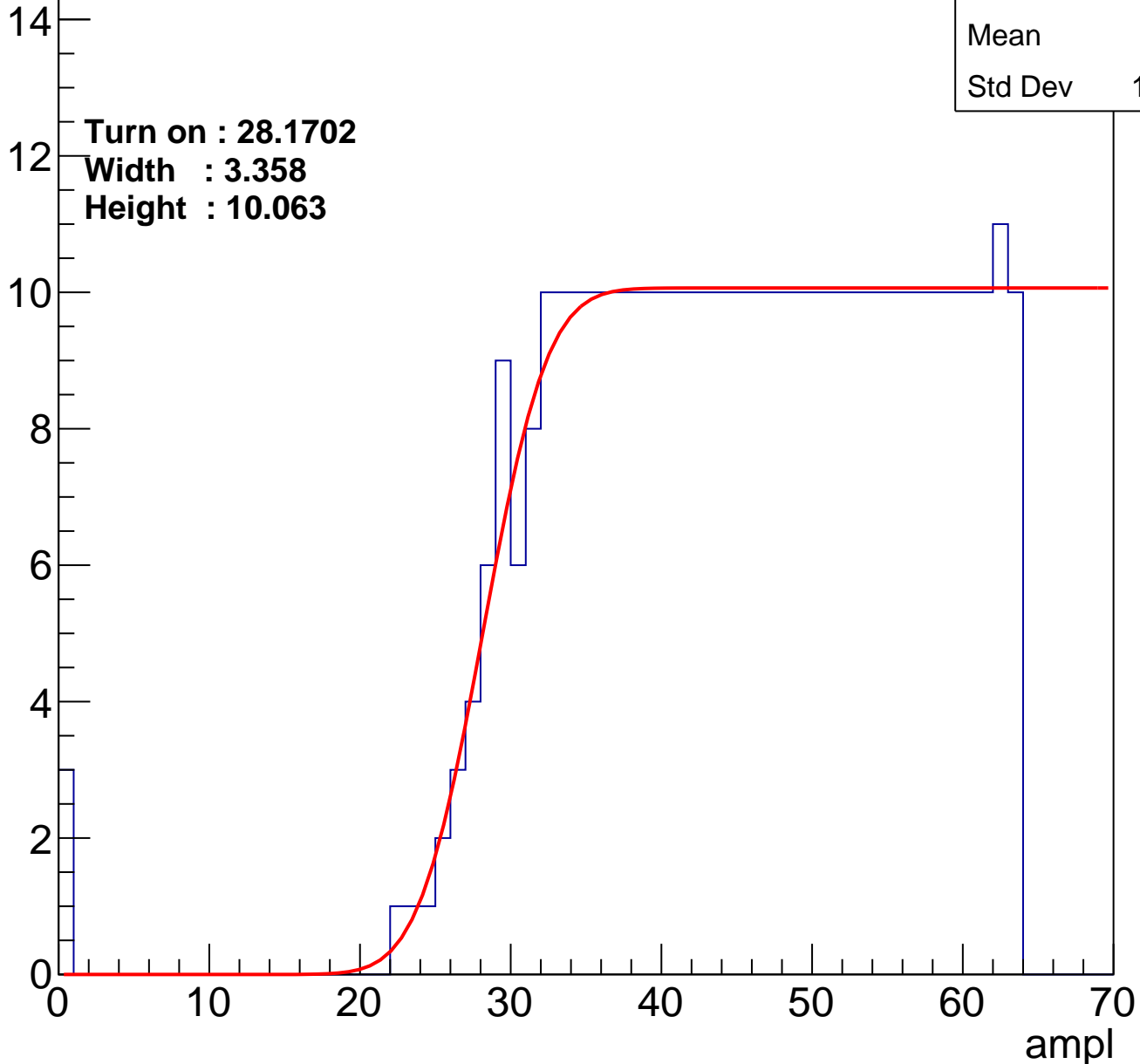
Entries	365
Mean	45
Std Dev	11.38

Turn on : 28.1702

Width : 3.358

Height : 10.063

Entry



B0L000S, U15-ch33

calib_packv5_042523_0143.root, FC#5, port B1

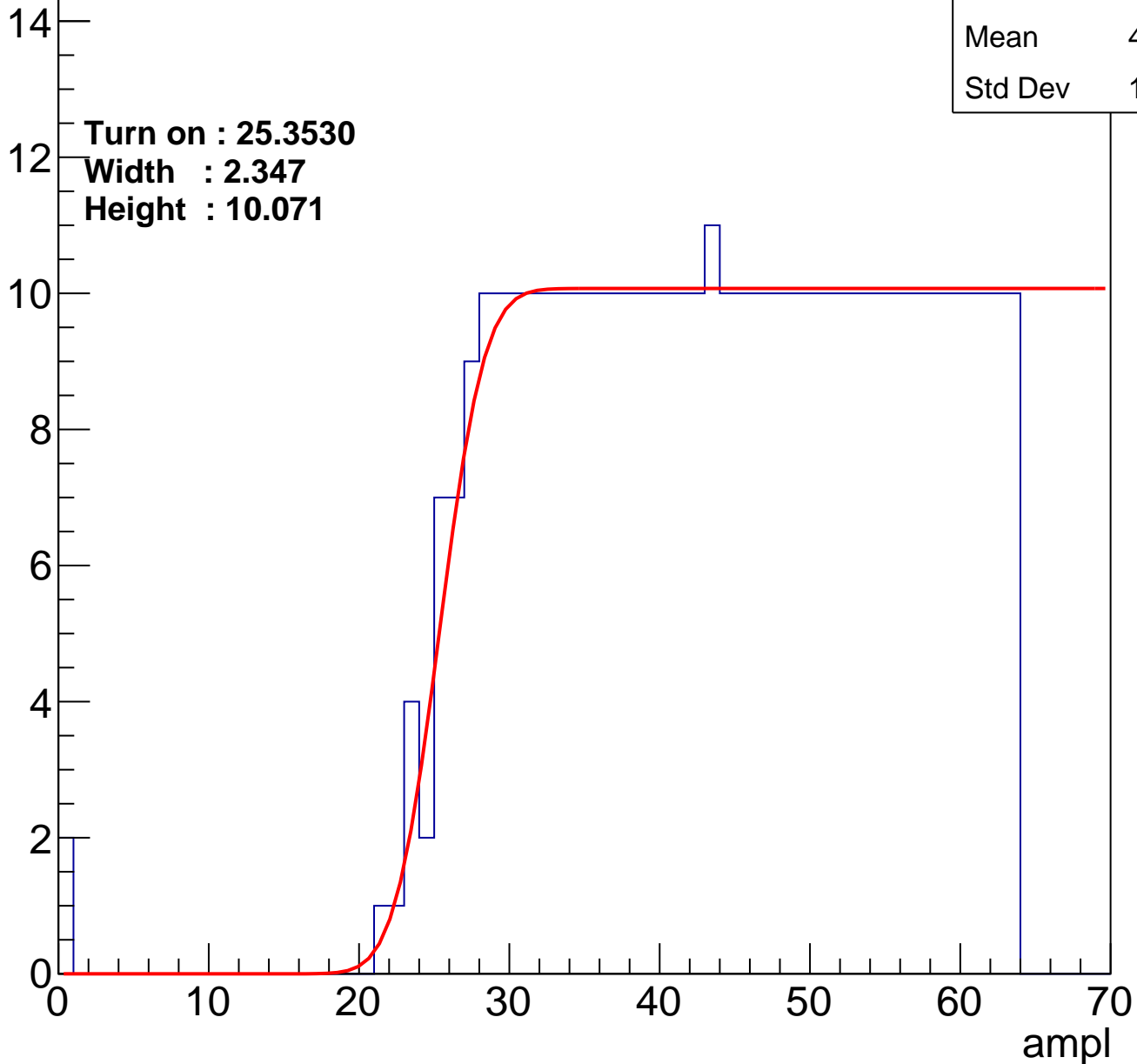
Entries	394
Mean	43.67
Std Dev	11.76

Turn on : 25.3530

Width : 2.347

Height : 10.071

Entry



B0L000S, U15-ch34

calib_packv5_042523_0143.root, FC#5, port B1

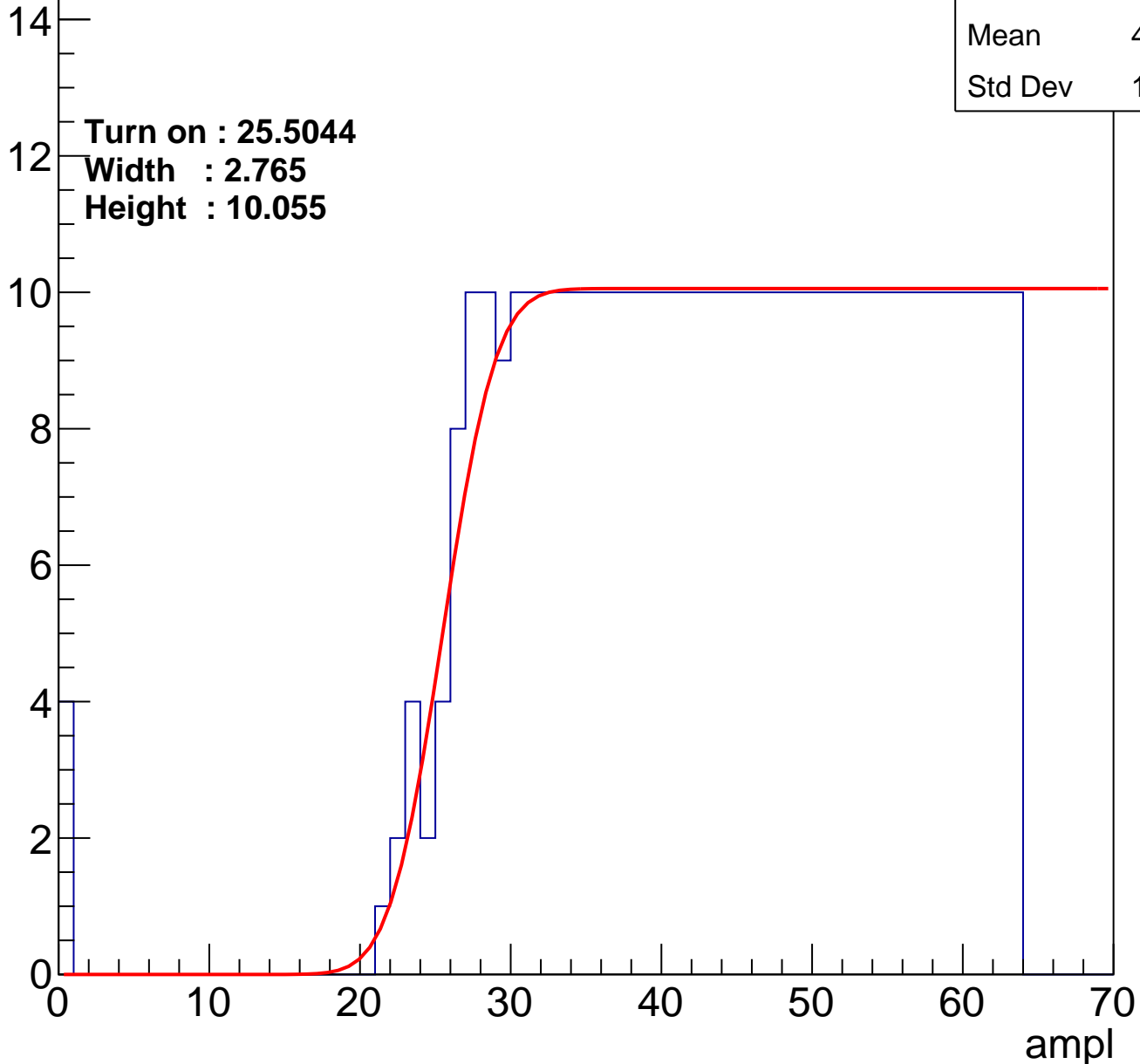
Entries	394
Mean	43.49
Std Dev	12.14

Turn on : 25.5044

Width : 2.765

Height : 10.055

Entry



B0L000S, U15-ch35

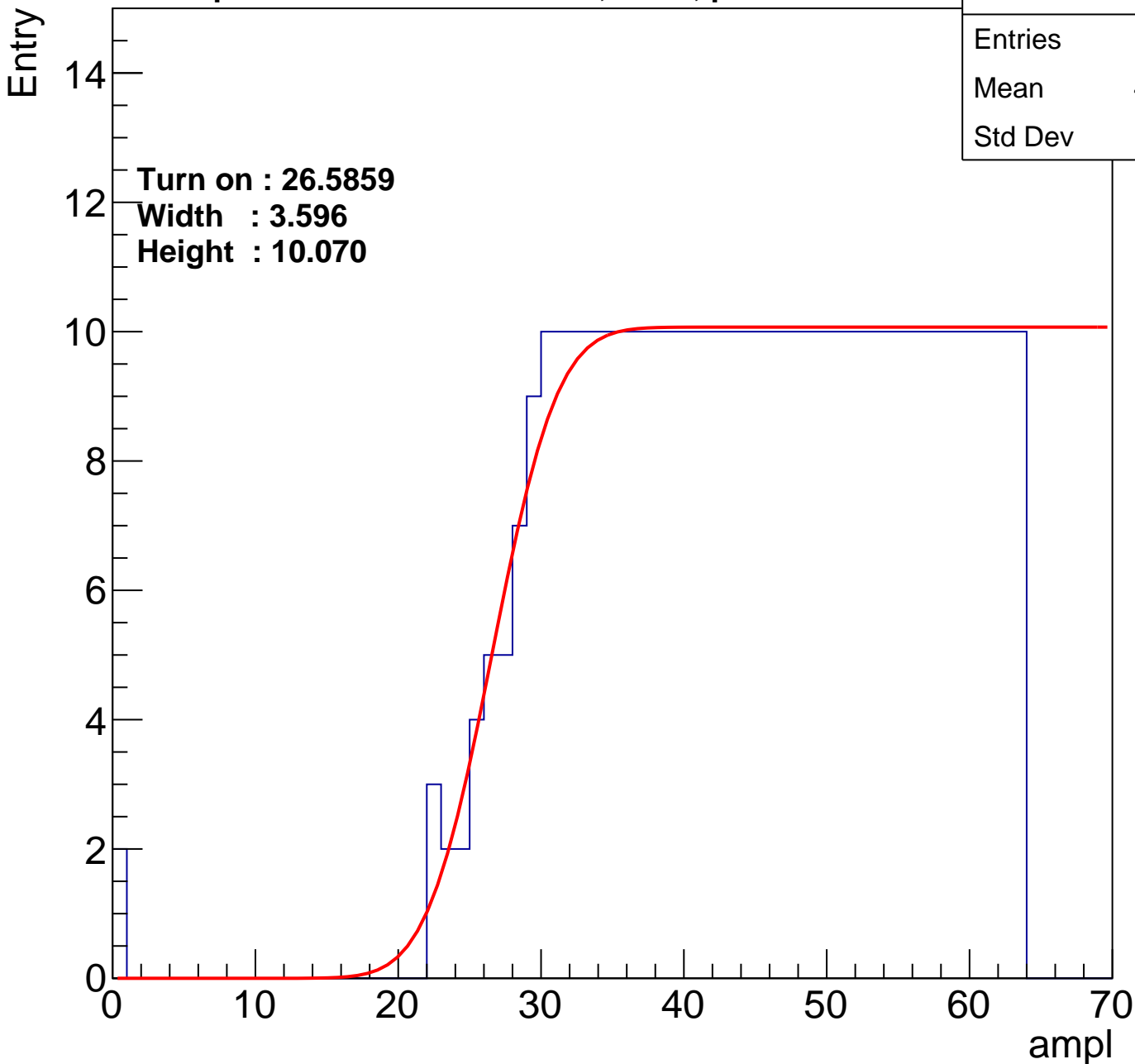
calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.31
Std Dev	11.5

Turn on : 26.5859

Width : 3.596

Height : 10.070



B0L000S, U15-ch36

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.6639

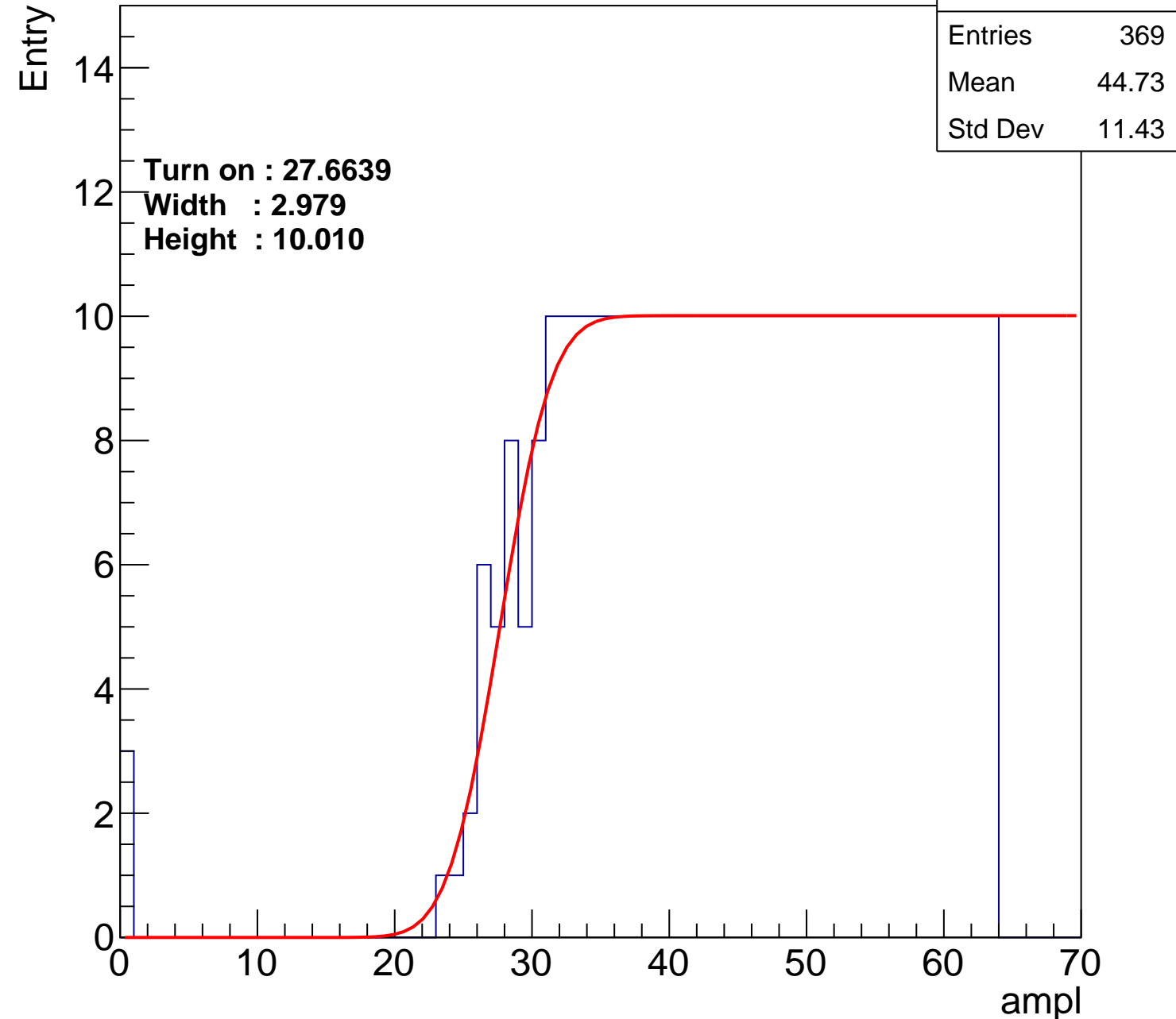
Width : 2.979

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	399
Mean	43.38
Std Dev	11.87

Turn on : 24.3272

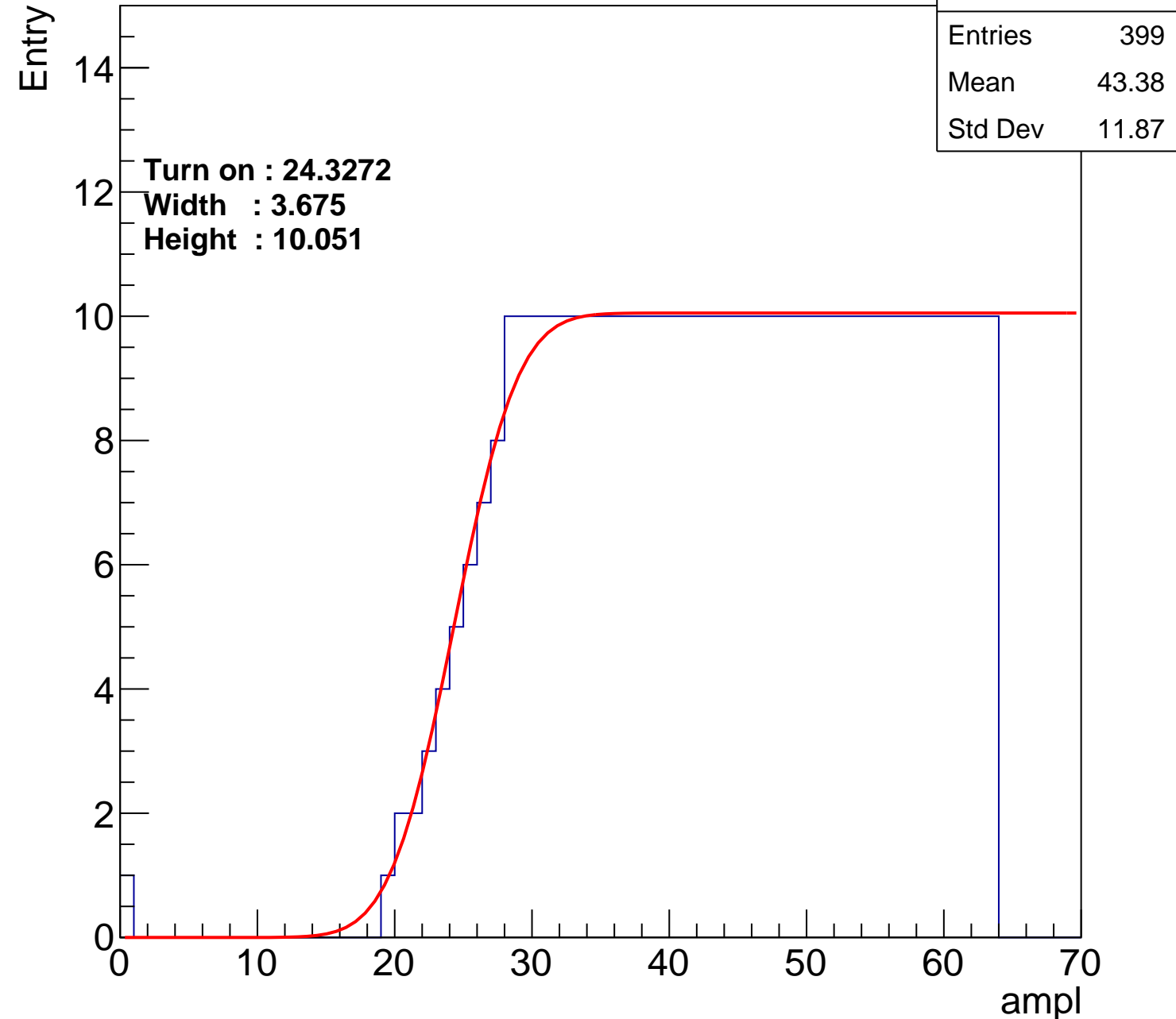
Width : 3.675

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch38

calib_packv5_042523_0143.root, FC#5, port B1

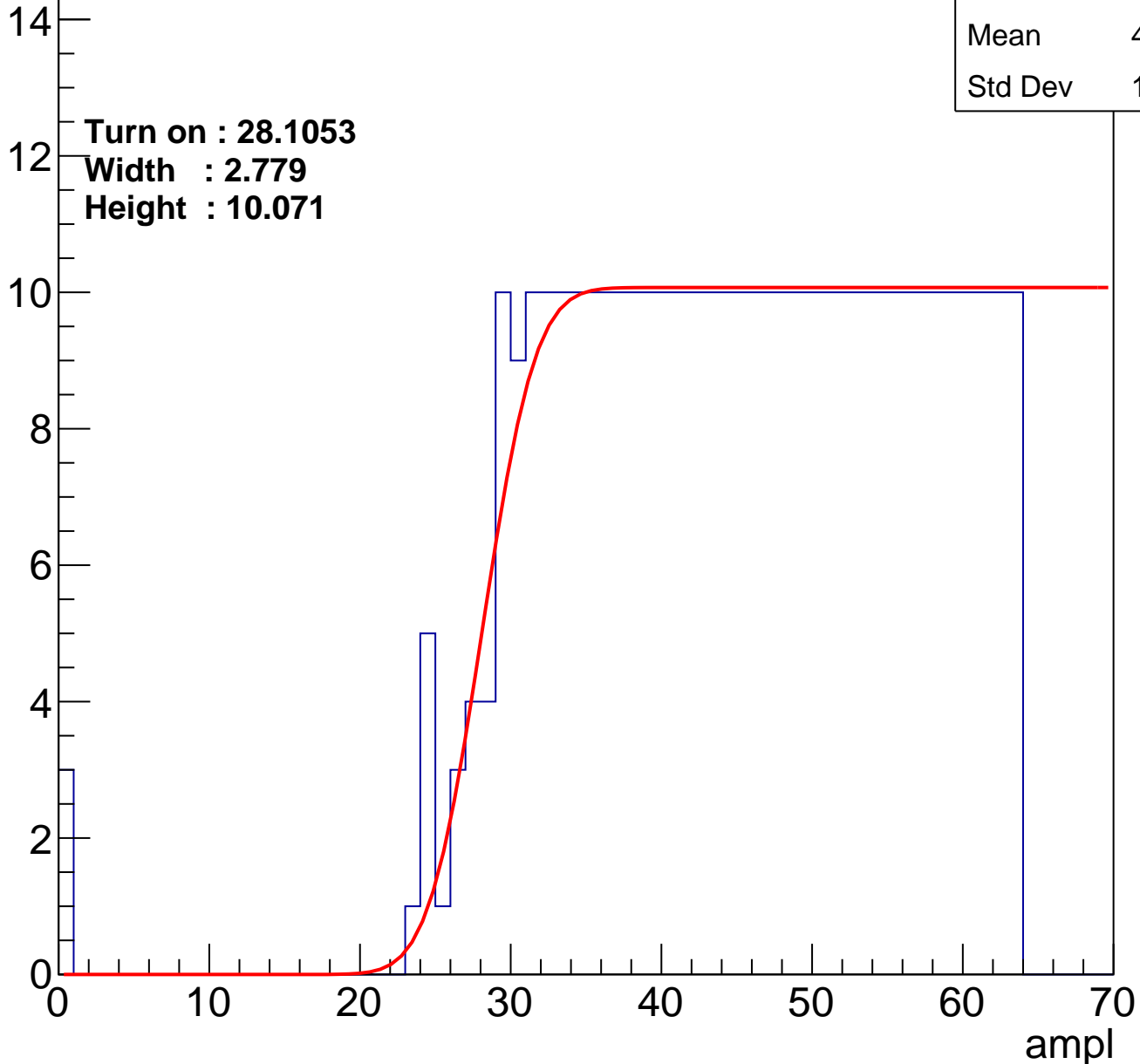
Entries	370
Mean	44.69
Std Dev	11.45

Turn on : 28.1053

Width : 2.779

Height : 10.071

Entry



B0L000S, U15-ch39

calib_packv5_042523_0143.root, FC#5, port B1

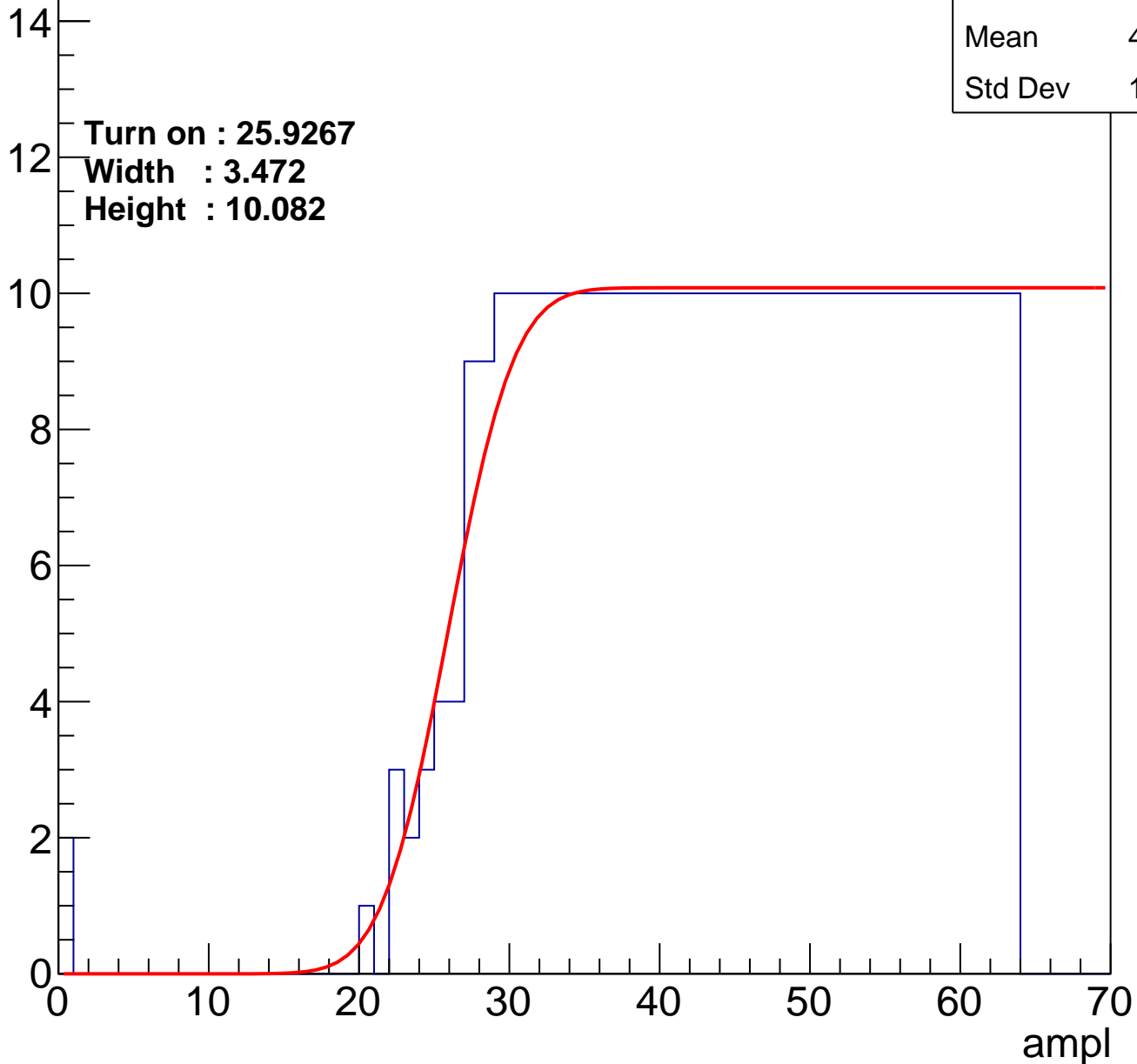
Entries	387
Mean	43.94
Std Dev	11.67

Turn on : 25.9267

Width : 3.472

Height : 10.082

Entry



B0L000S, U15-ch40

calib_packv5_042523_0143.root, FC#5, port B1

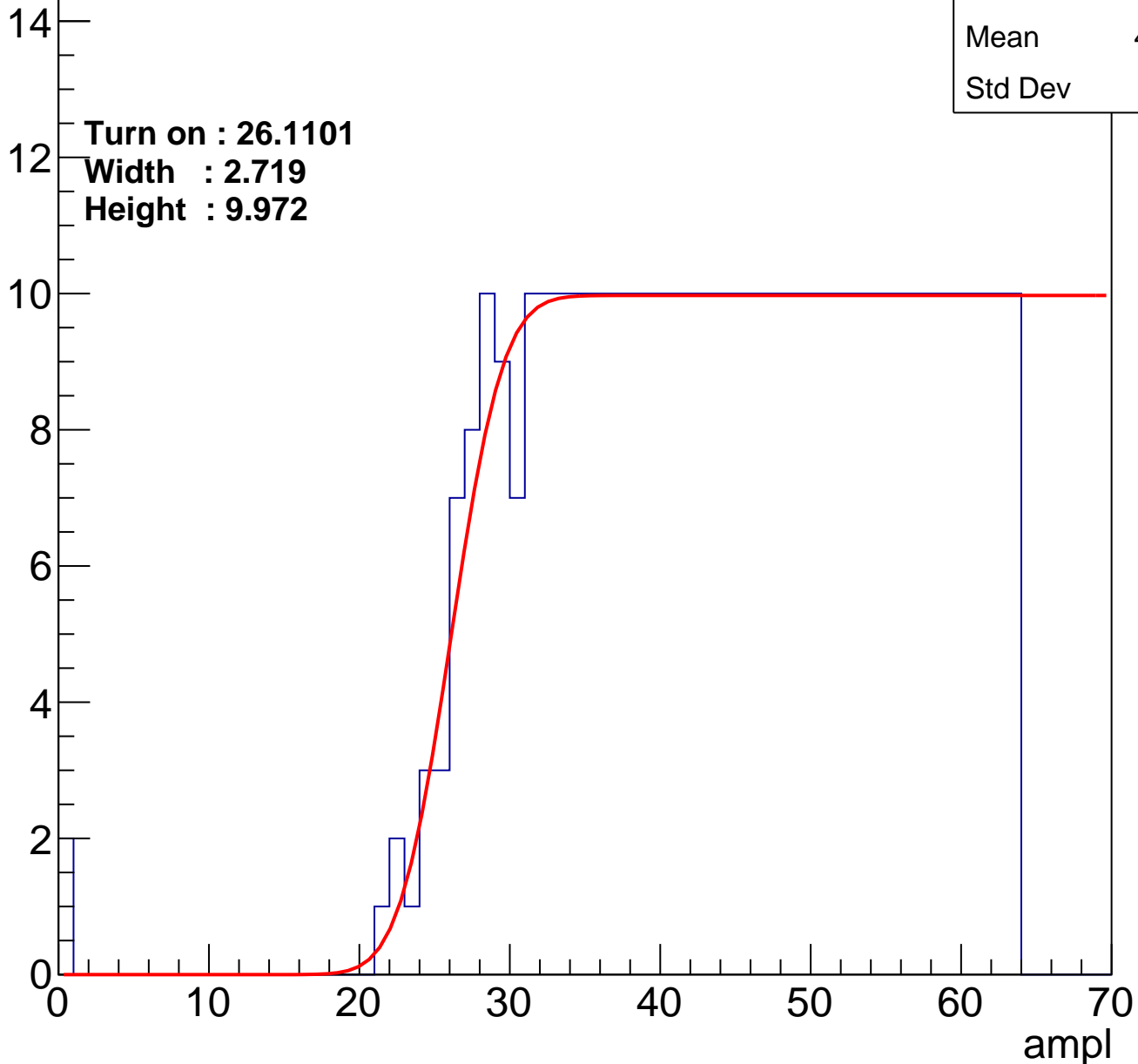
Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 26.1101

Width : 2.719

Height : 9.972

Entry



B0L000S, U15-ch41

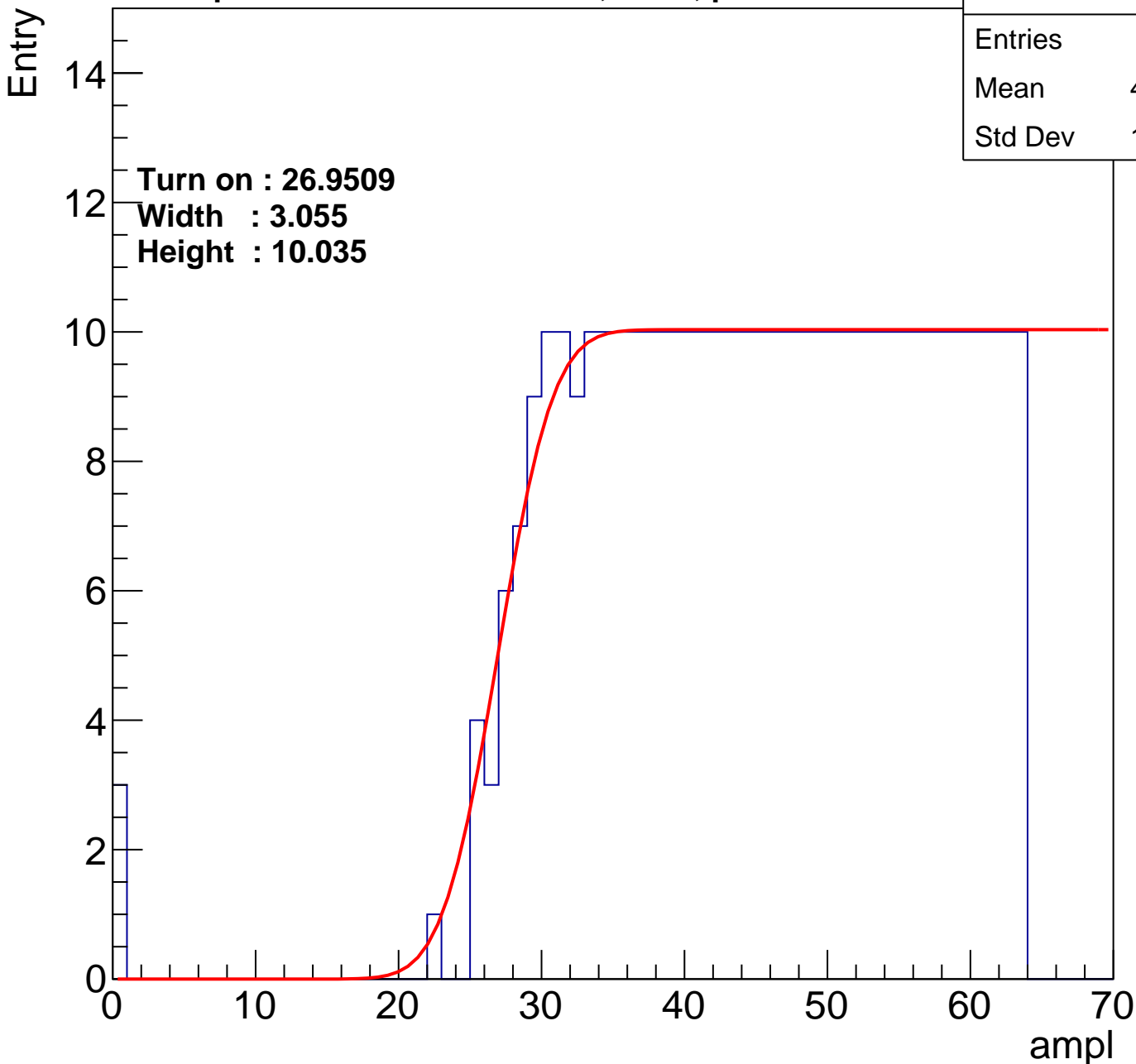
calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.62
Std Dev	11.46

Turn on : 26.9509

Width : 3.055

Height : 10.035



B0L000S, U15-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.81
Std Dev	12.13

Turn on : 27.0044

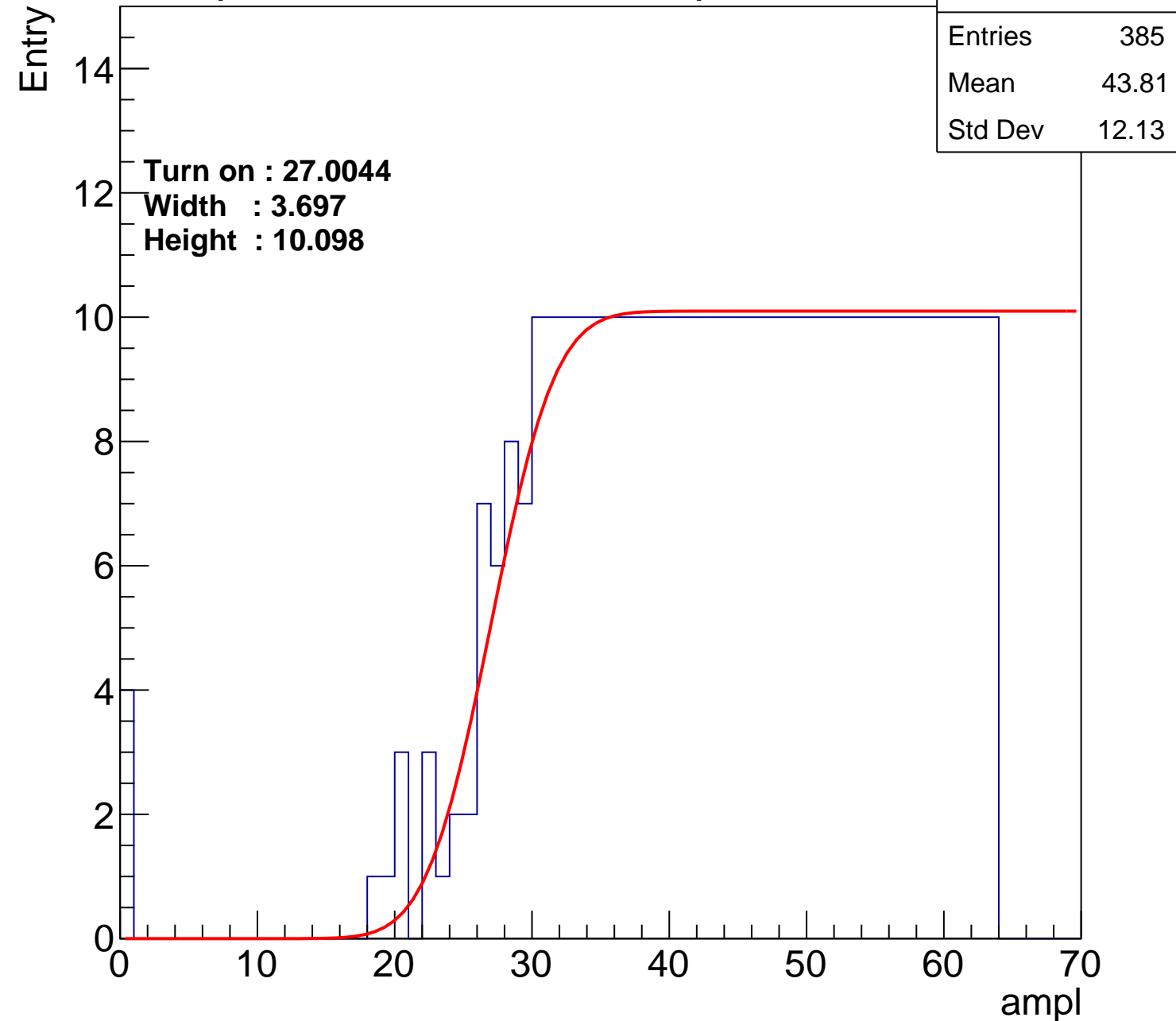
Width : 3.697

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 26.5790

Width : 3.518

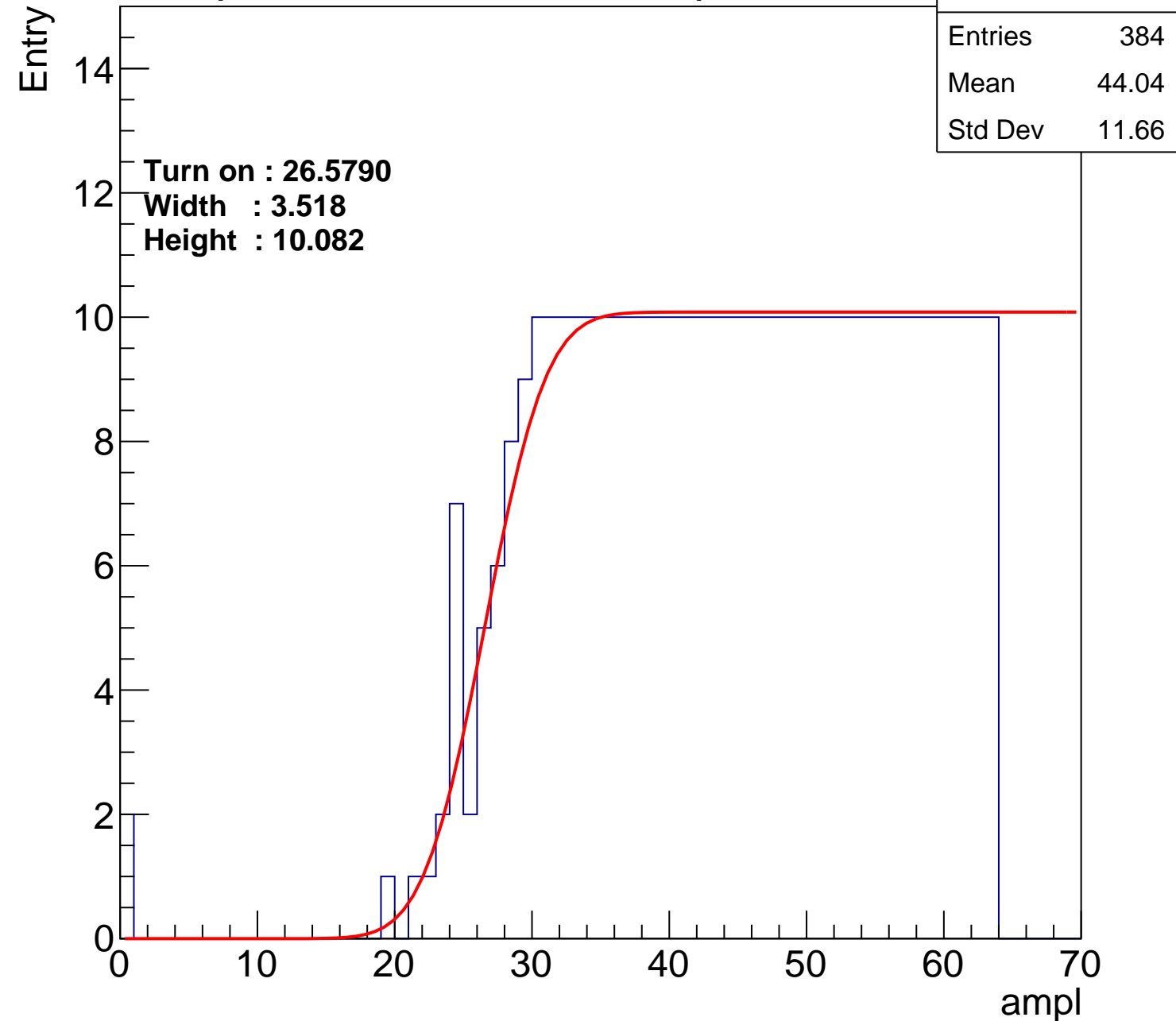
Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U15-ch44

calib_packv5_042523_0143.root, FC#5, port B1

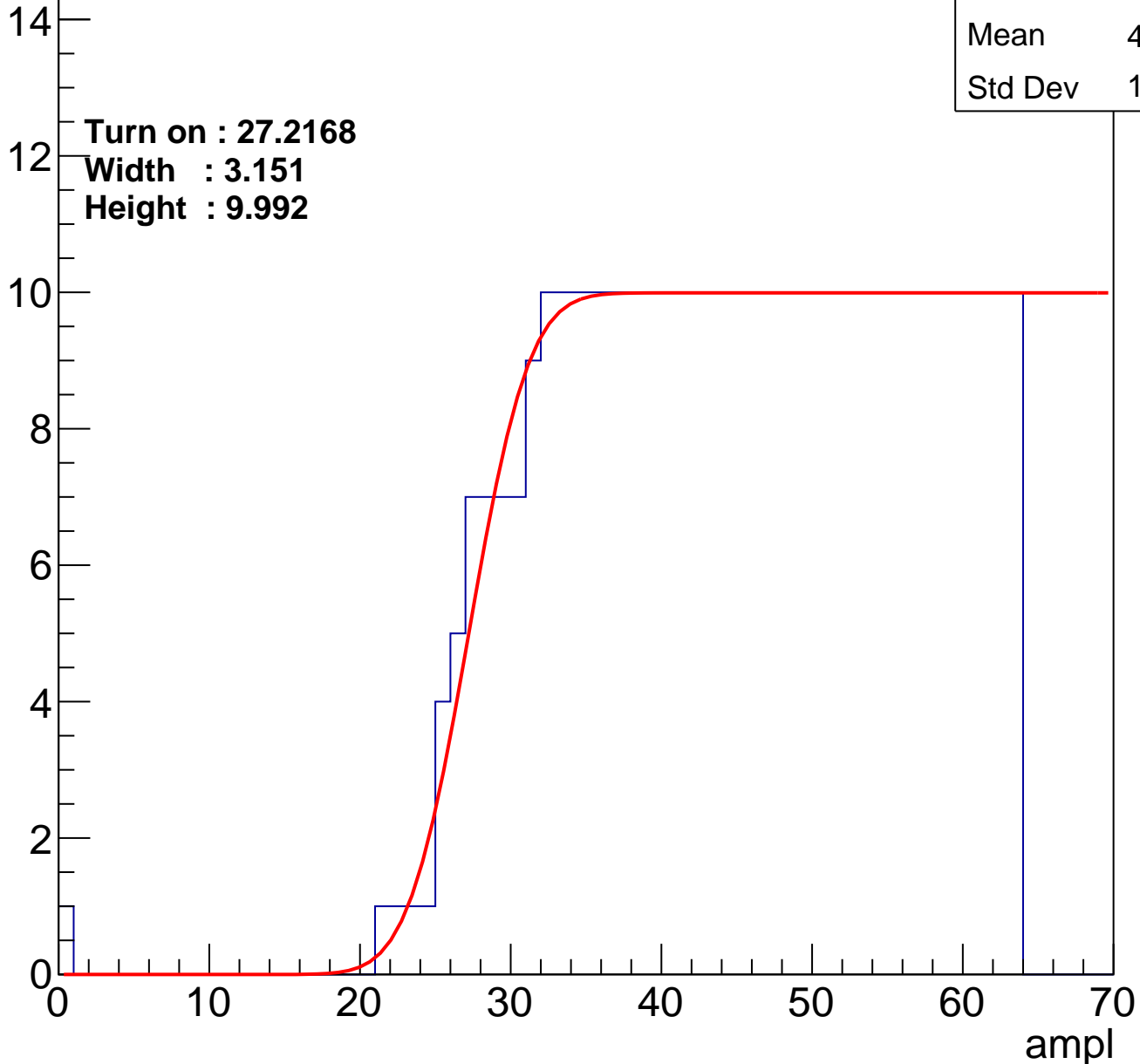
Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.2168

Width : 3.151

Height : 9.992

Entry



B0L000S, U15-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.16
Std Dev	11.89

Turn on : 27.0334

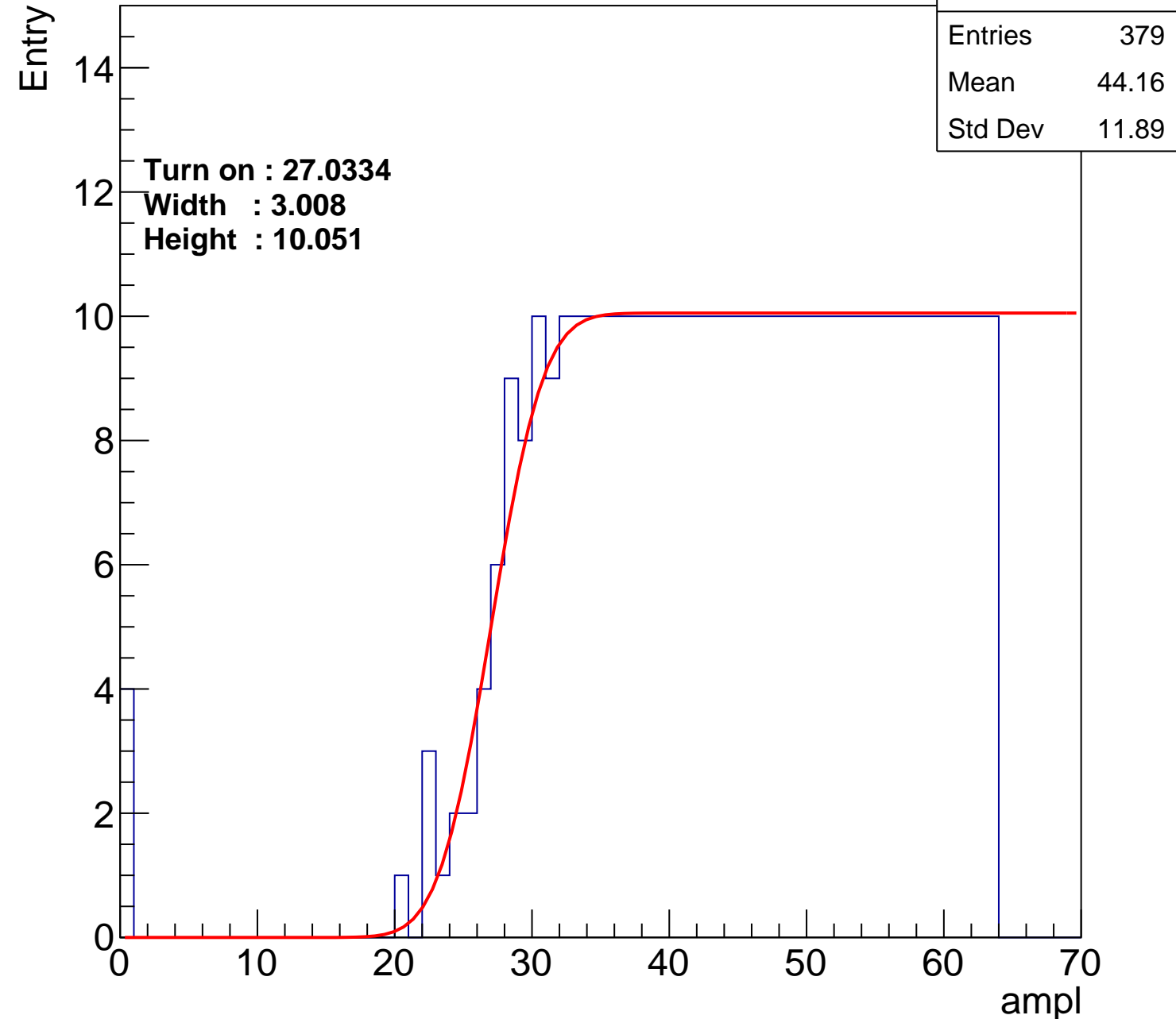
Width : 3.008

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch46

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.07
Std Dev	12.19

Turn on : 26.7563

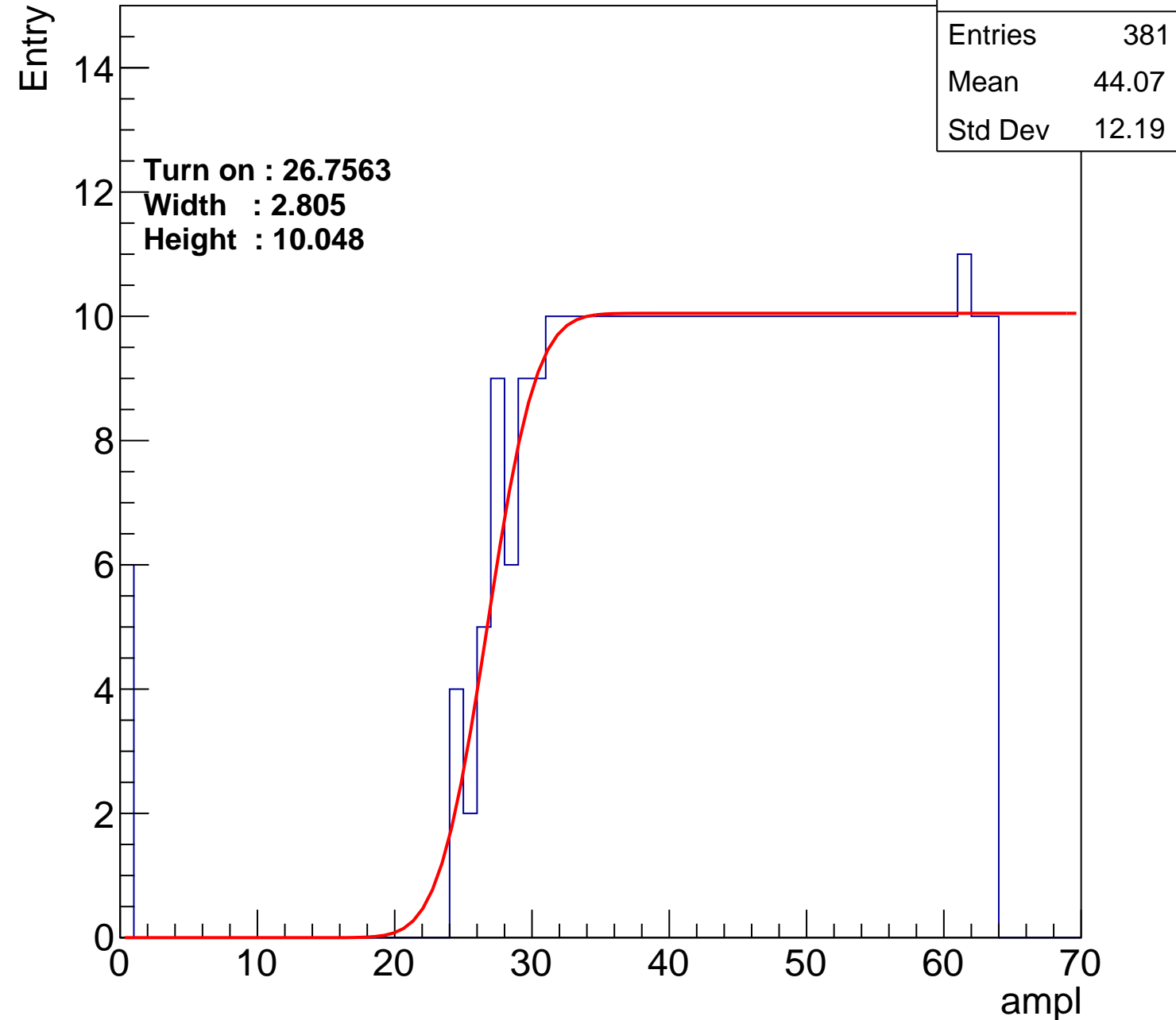
Width : 2.805

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch47

calib_packv5_042523_0143.root, FC#5, port B1

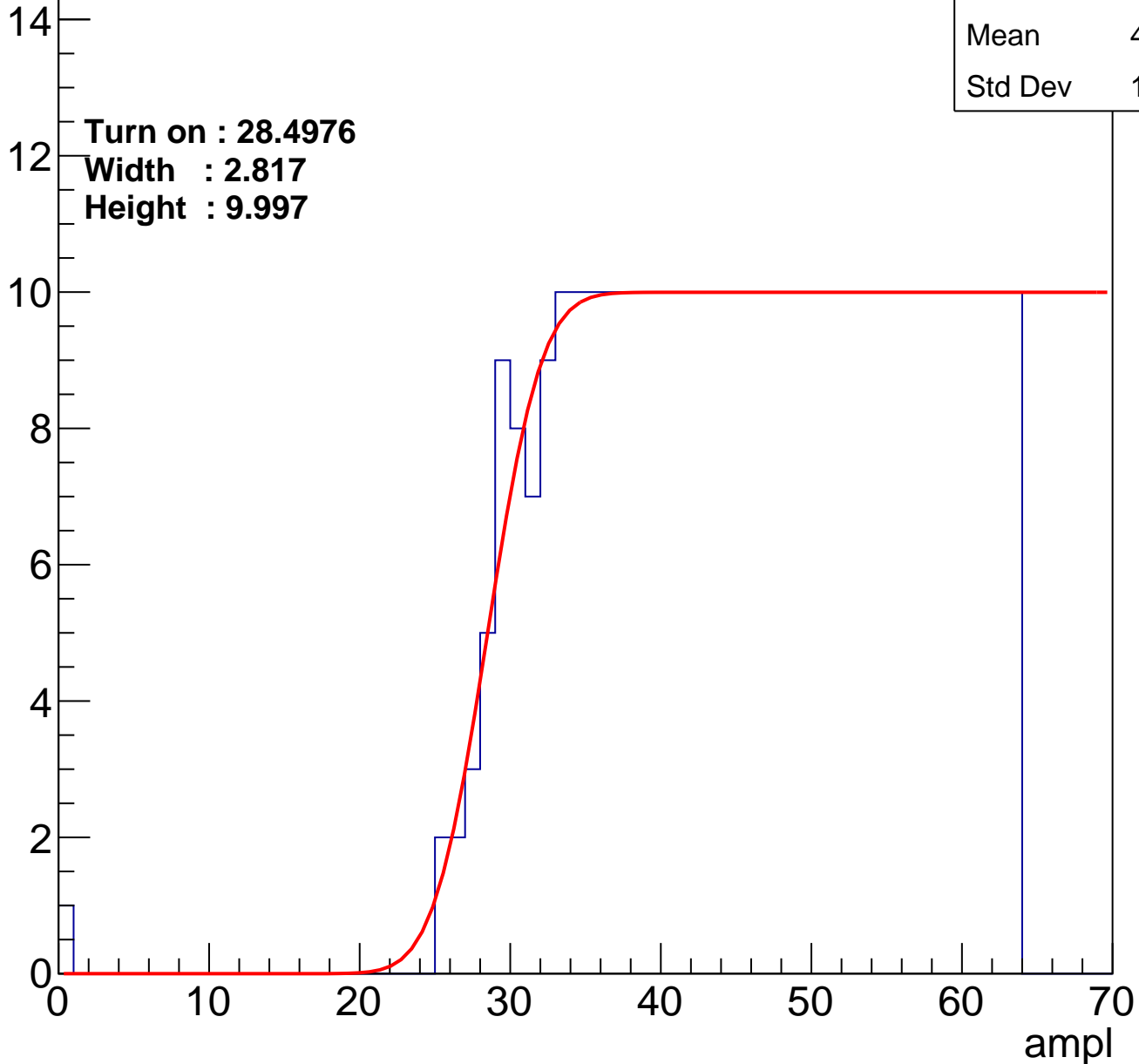
Entries	356
Mean	45.53
Std Dev	10.66

Turn on : 28.4976

Width : 2.817

Height : 9.997

Entry



B0L000S, U15-ch48

calib_packv5_042523_0143.root, FC#5, port B1

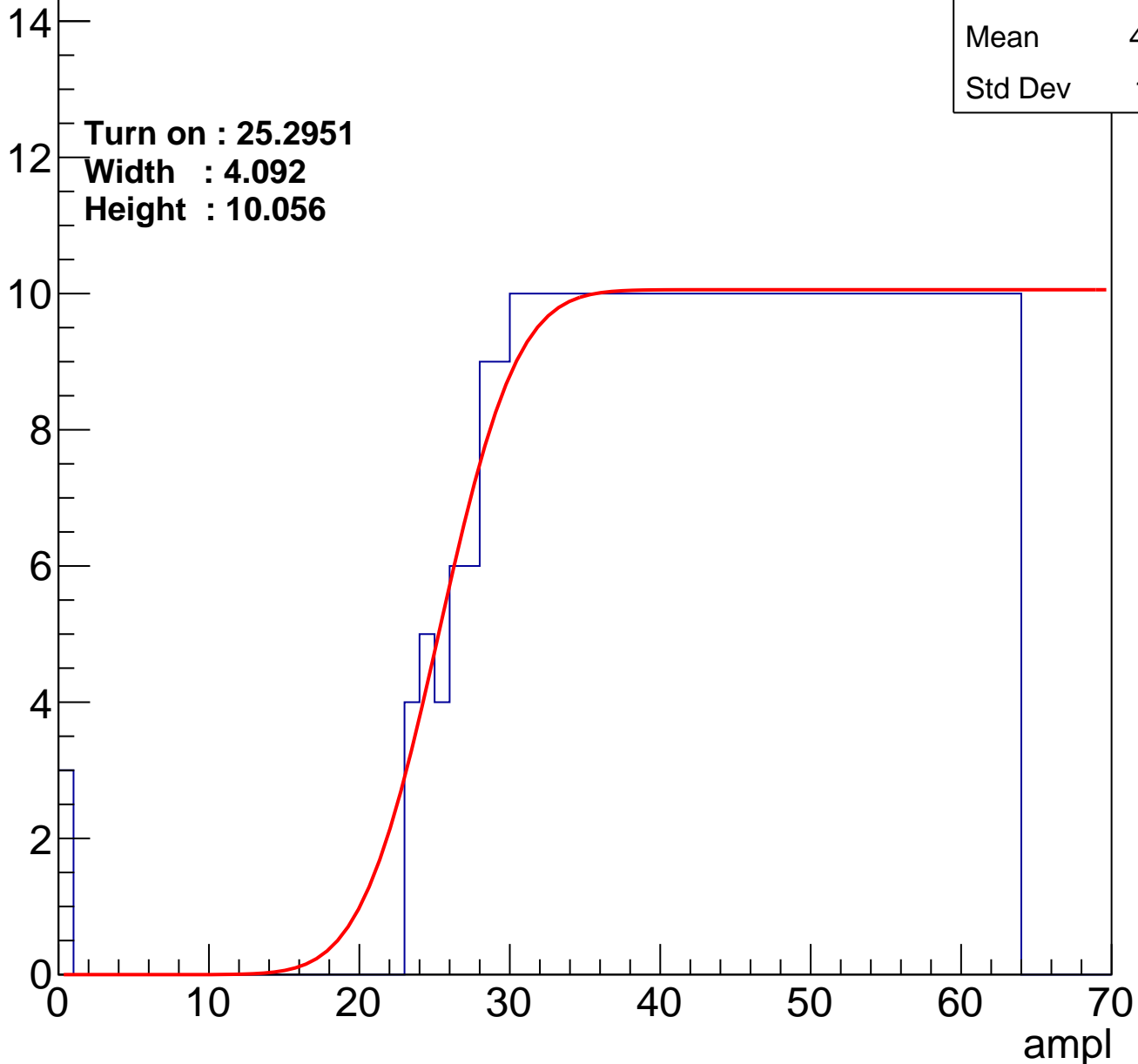
Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 25.2951

Width : 4.092

Height : 10.056

Entry



B0L000S, U15-ch49

calib_packv5_042523_0143.root, FC#5, port B1

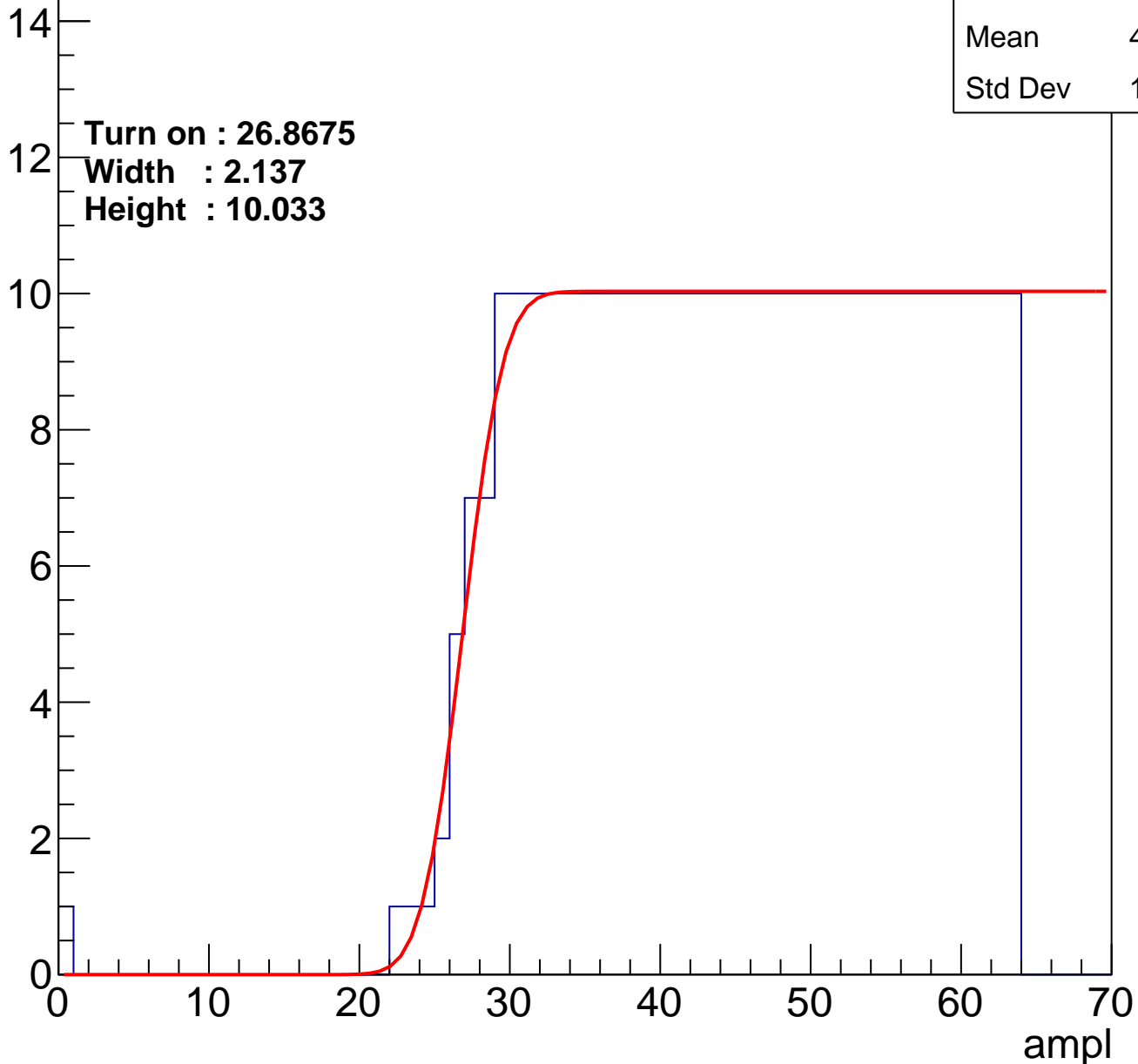
Entries	375
Mean	44.62
Std Dev	11.12

Turn on : 26.8675

Width : 2.137

Height : 10.033

Entry



B0L000S, U15-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 26.3480

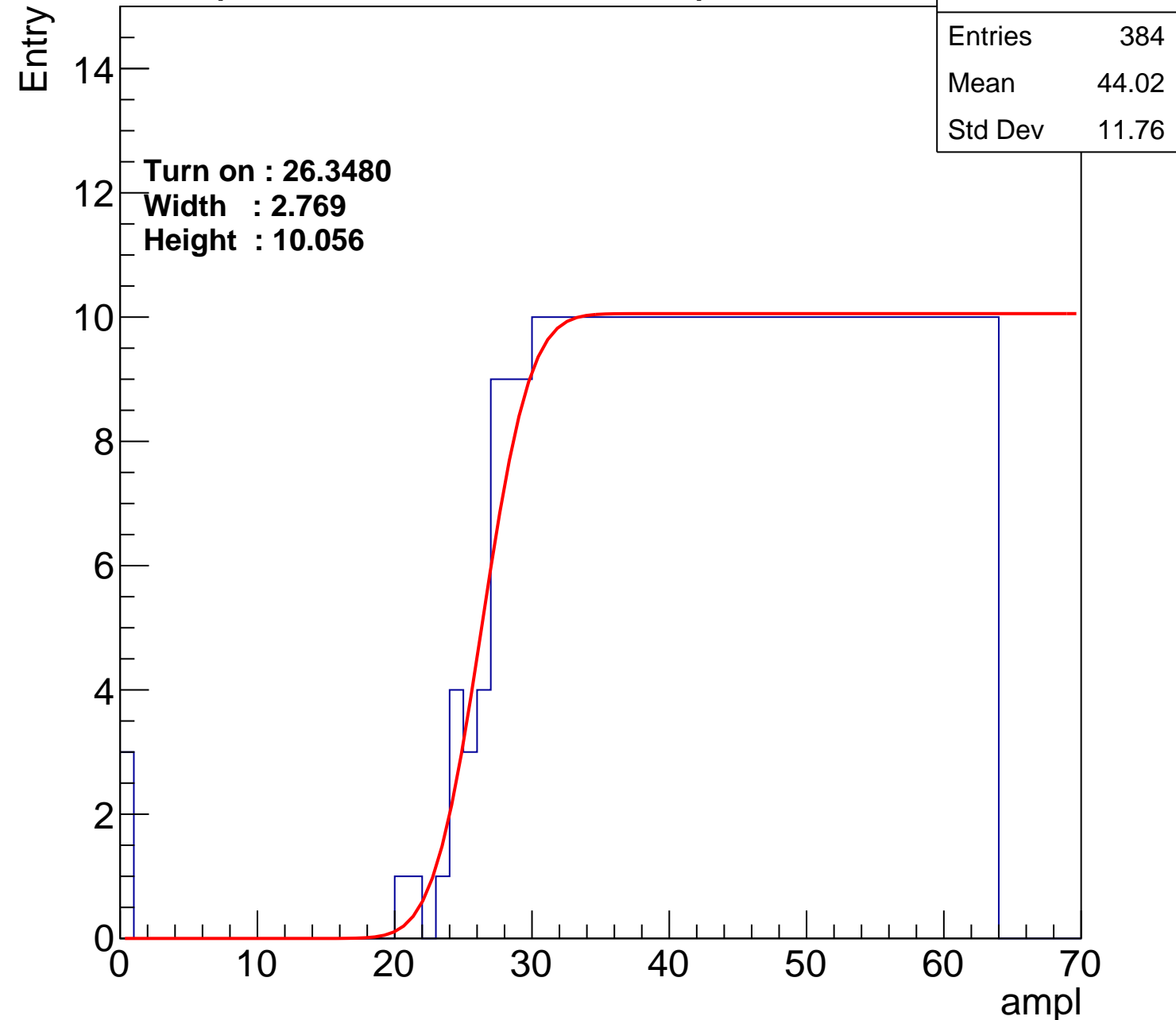
Width : 2.769

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
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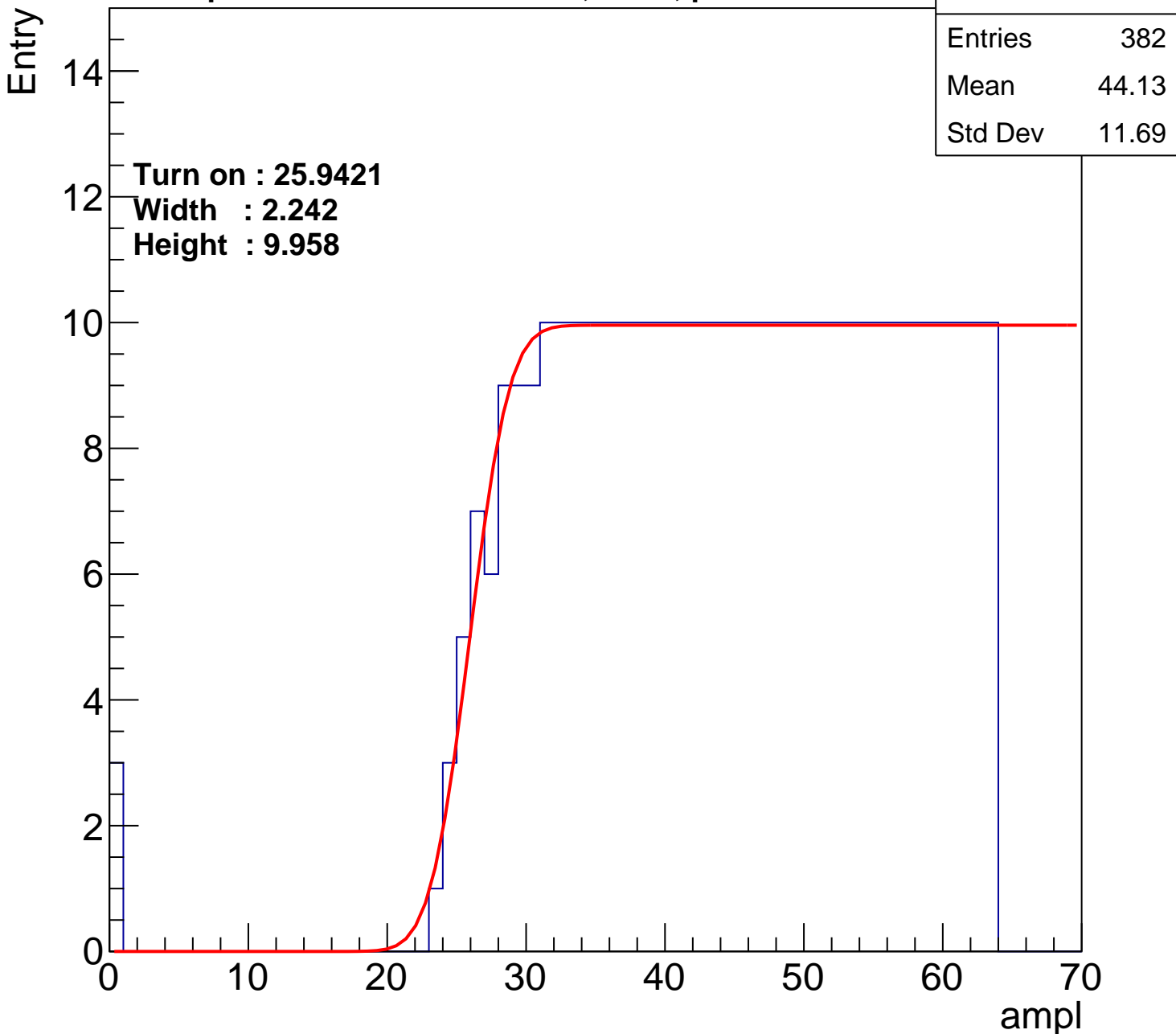
Mean	44.13
------	-------

Std Dev	11.69
---------	-------

Turn on : 25.9421

Width : 2.242

Height : 9.958



B0L000S, U15-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.12
Std Dev	11.89

Turn on : 26.4603

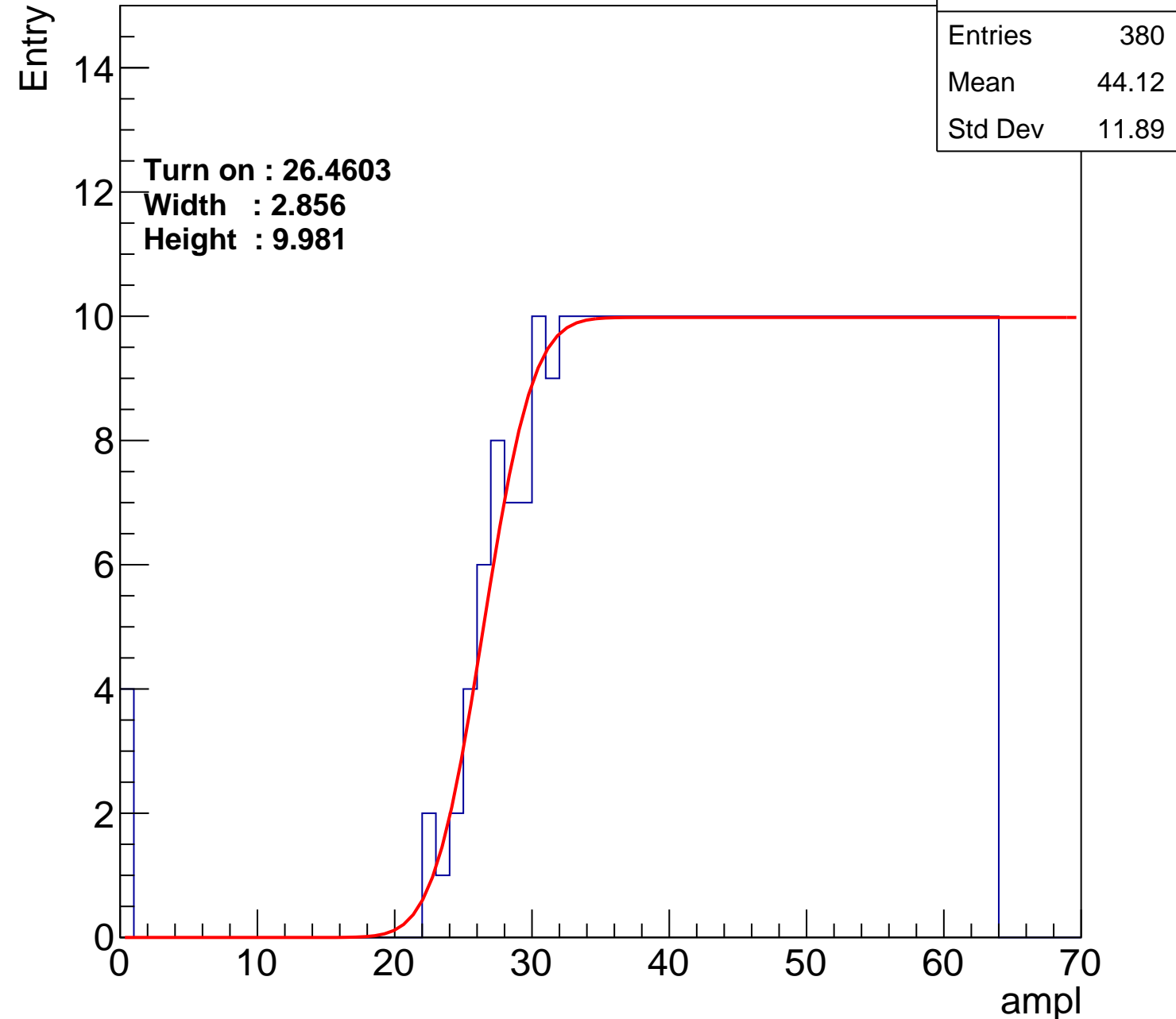
Width : 2.856

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch53

calib_packv5_042523_0143.root, FC#5, port B1

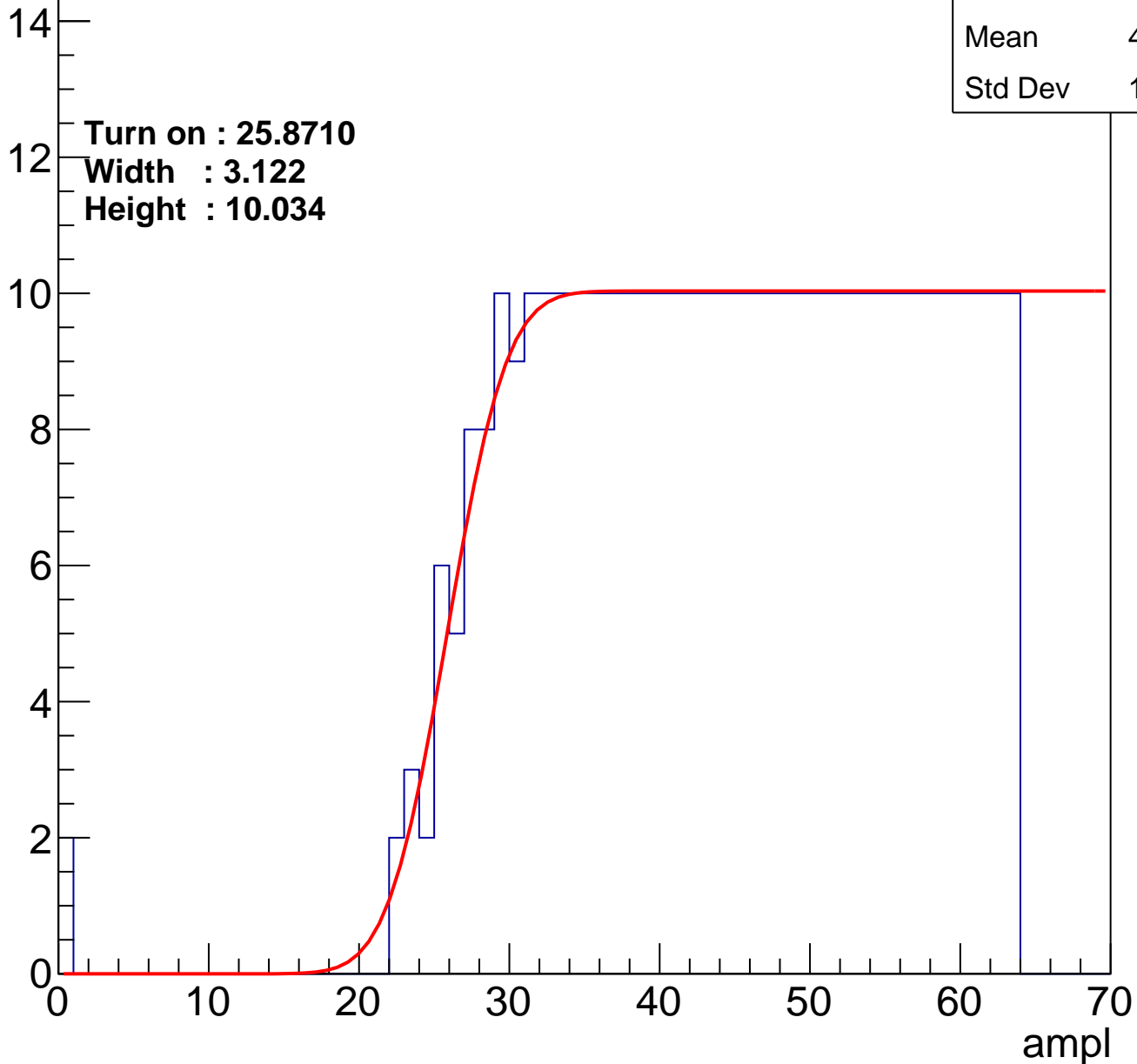
Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.8710

Width : 3.122

Height : 10.034

Entry



B0L000S, U15-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.96
Std Dev	11.94

Turn on : 25.9400

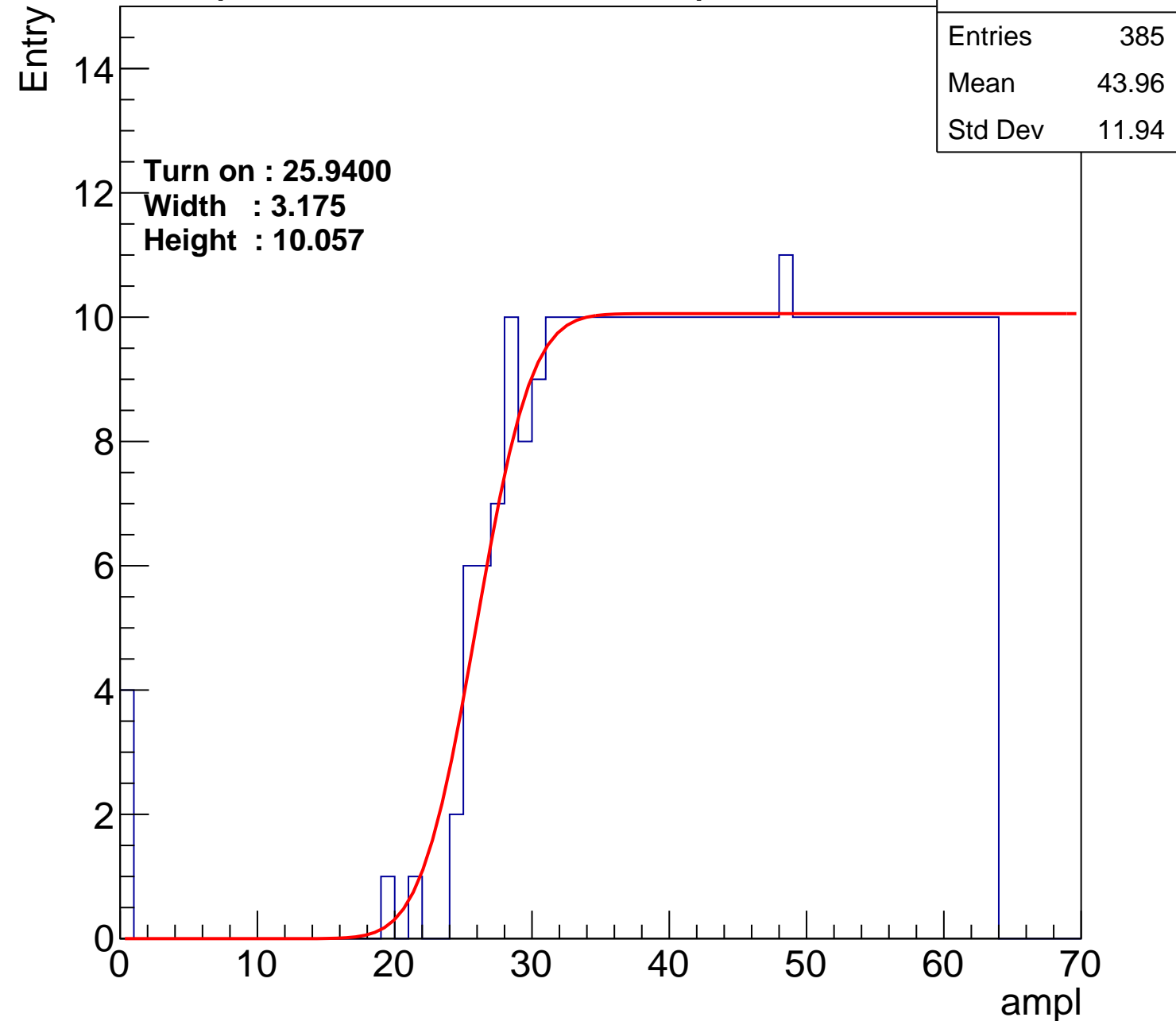
Width : 3.175

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch55

calib_packv5_042523_0143.root, FC#5, port B1

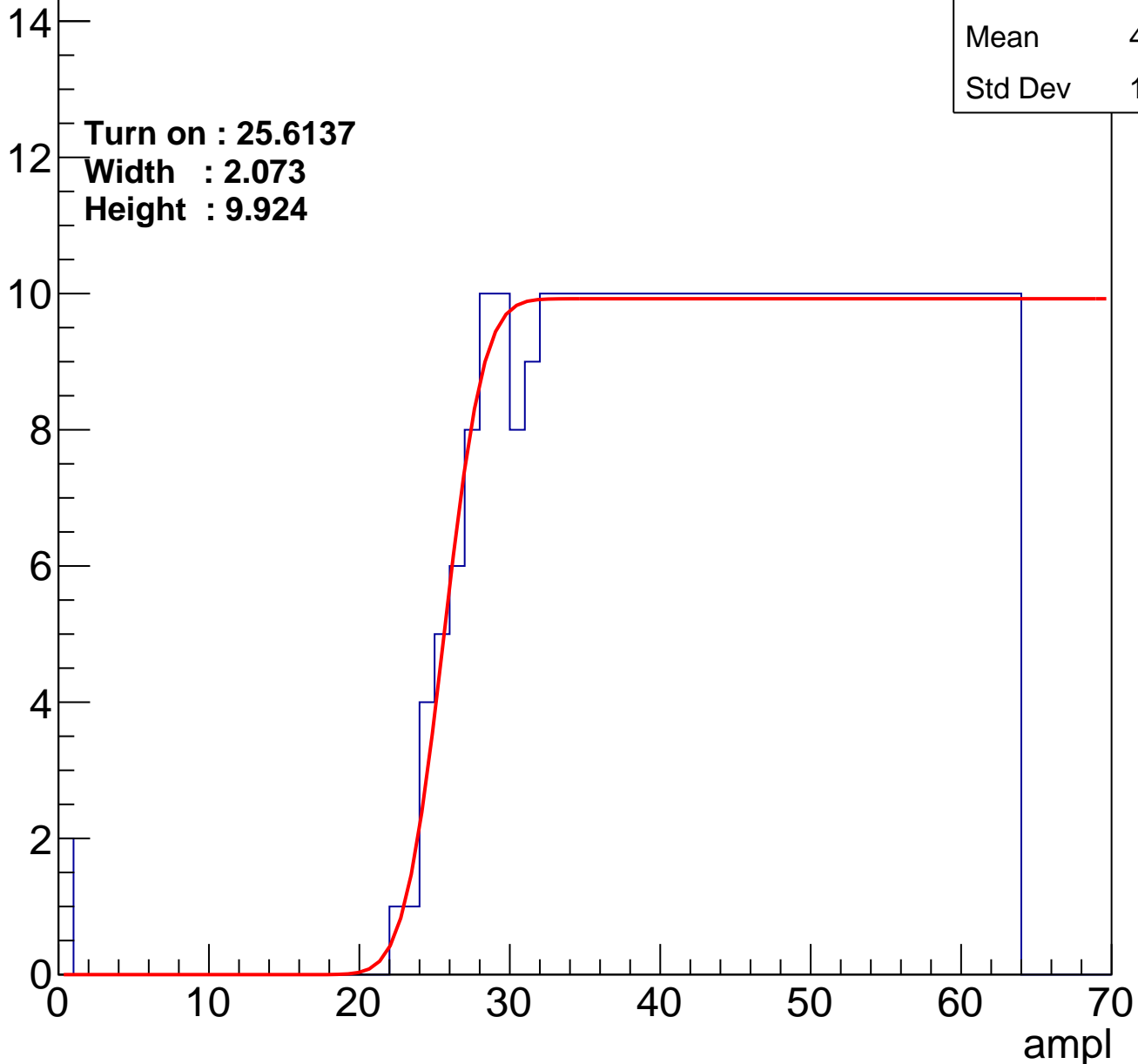
Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.6137

Width : 2.073

Height : 9.924

Entry



B0L000S, U15-ch56

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.67
Std Dev	11.47

Turn on : 27.5129

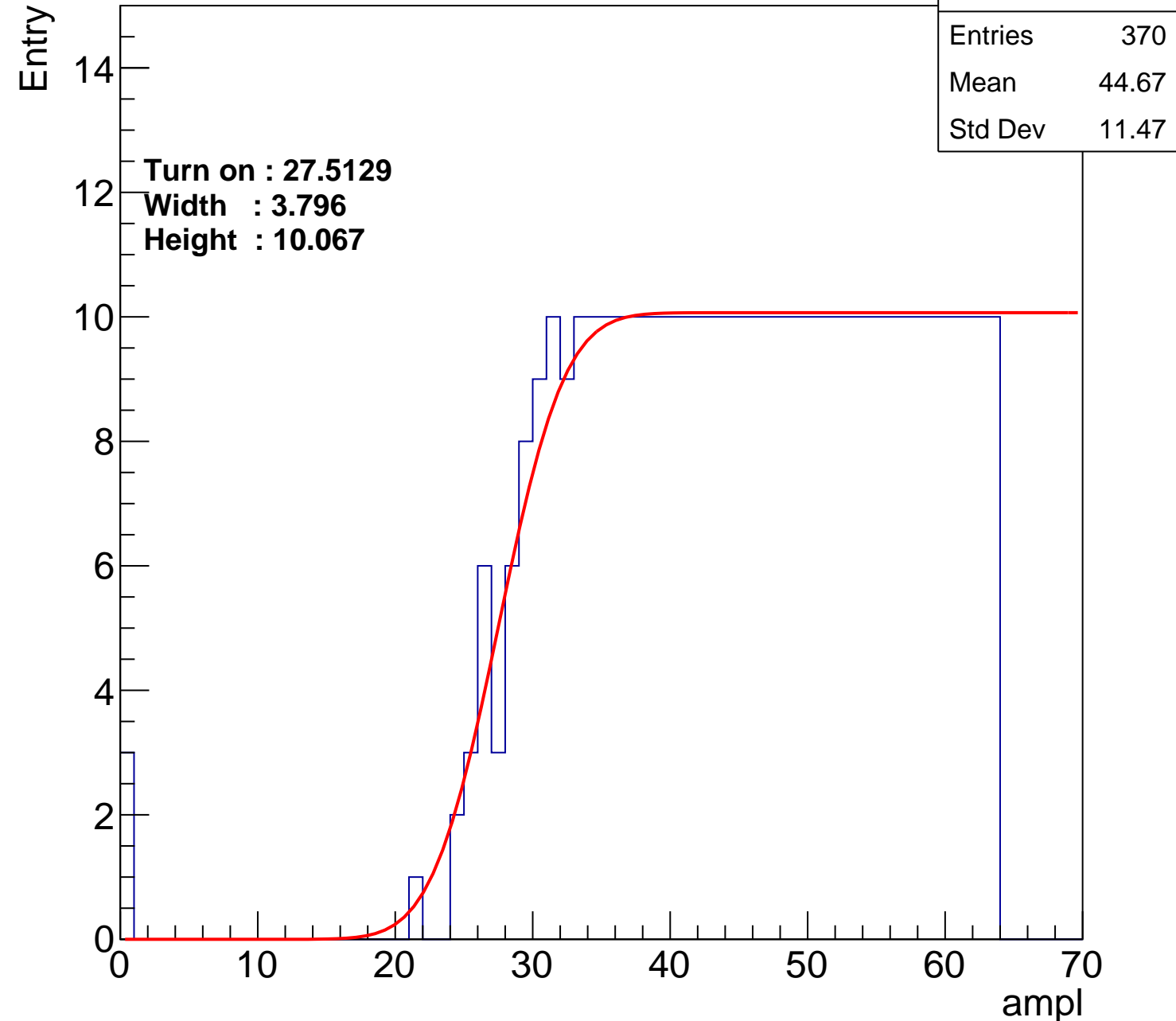
Width : 3.796

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.5543

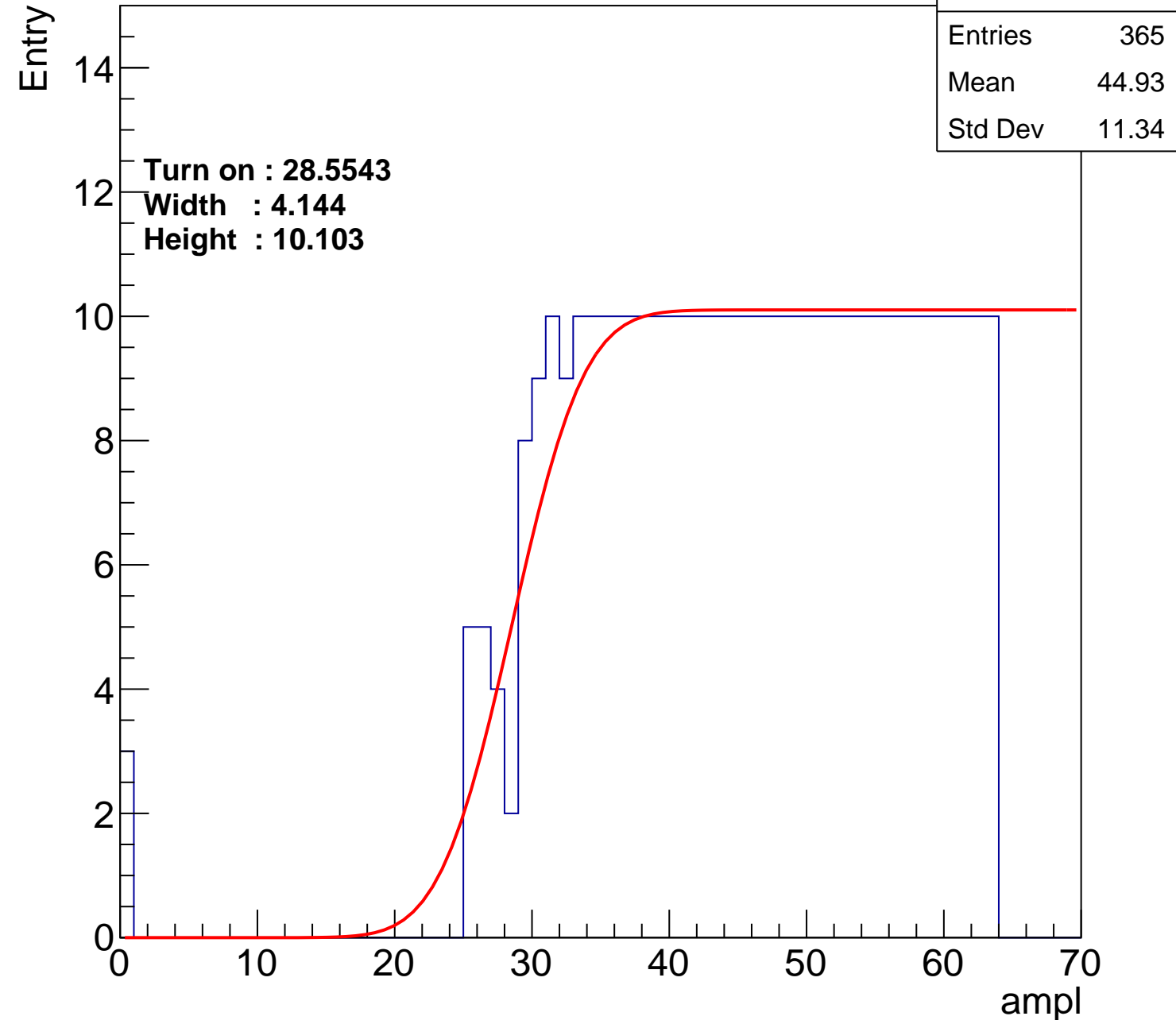
Width : 4.144

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch58

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.72
Std Dev	11.69

Turn on : 25.4471

Width : 3.537

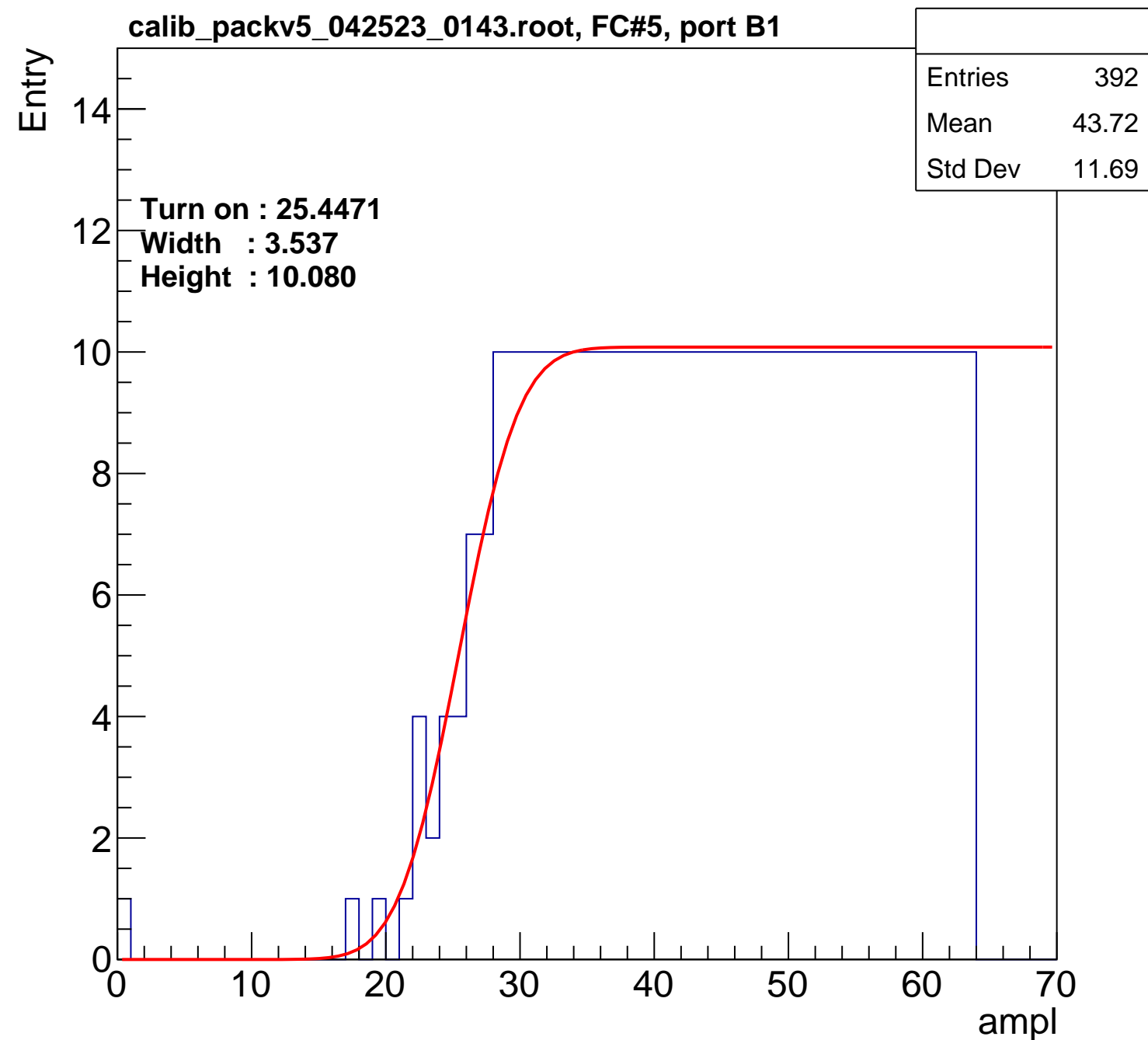
Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U15-ch59

calib_packv5_042523_0143.root, FC#5, port B1

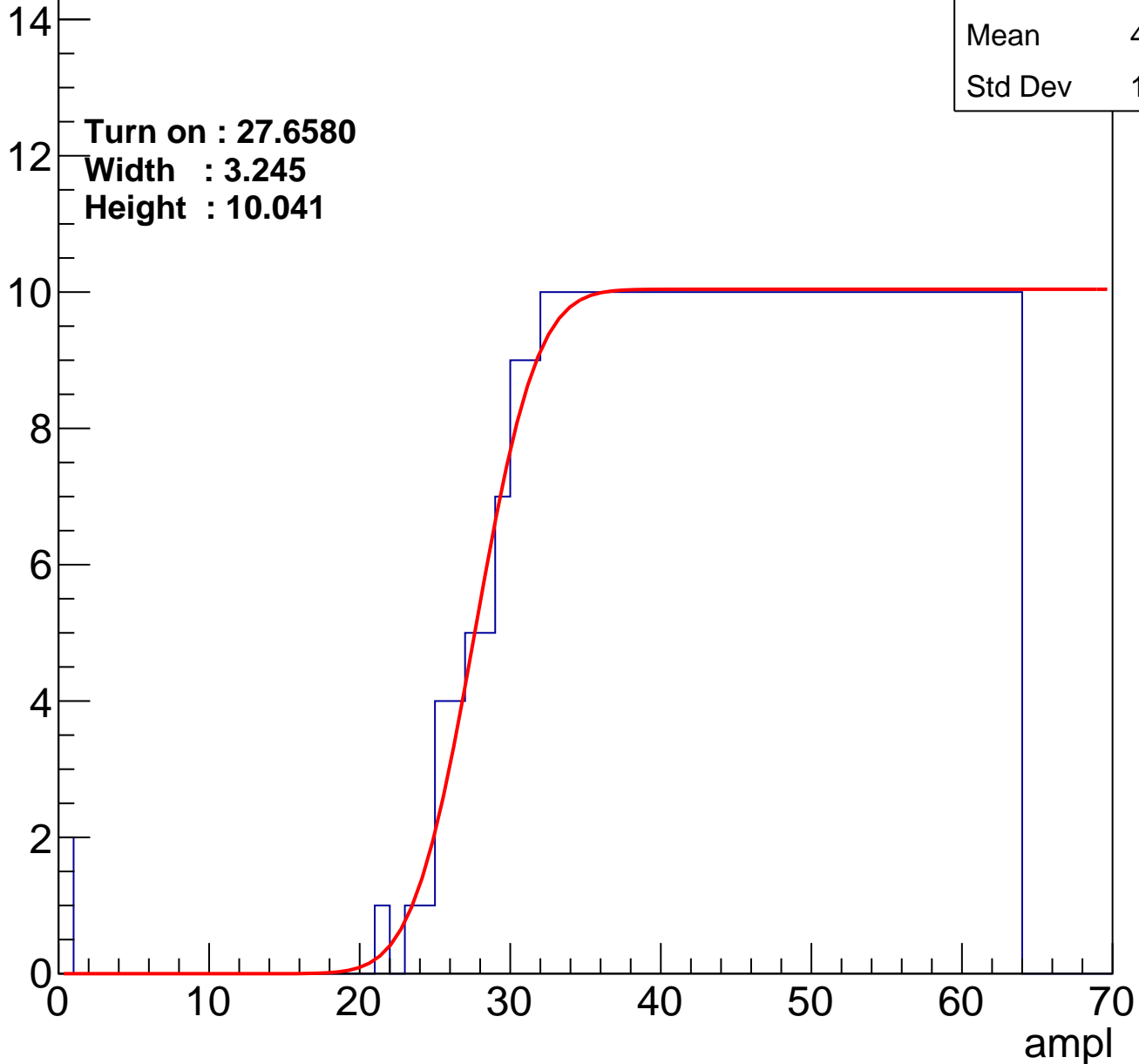
Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.6580

Width : 3.245

Height : 10.041

Entry



B0L000S, U15-ch60

calib_packv5_042523_0143.root, FC#5, port B1

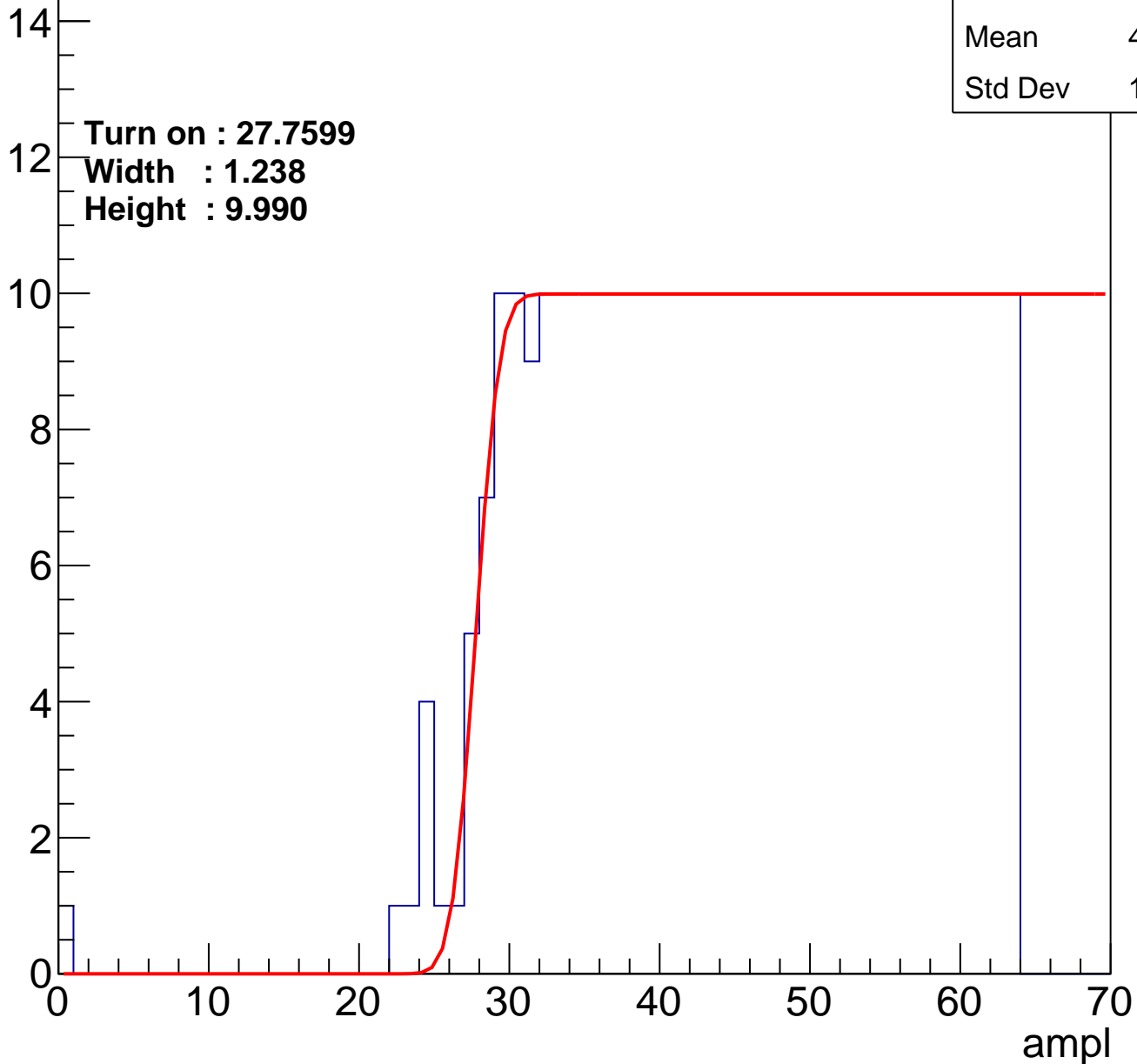
Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 27.7599

Width : 1.238

Height : 9.990

Entry



B0L000S, U15-ch61

calib_packv5_042523_0143.root, FC#5, port B1

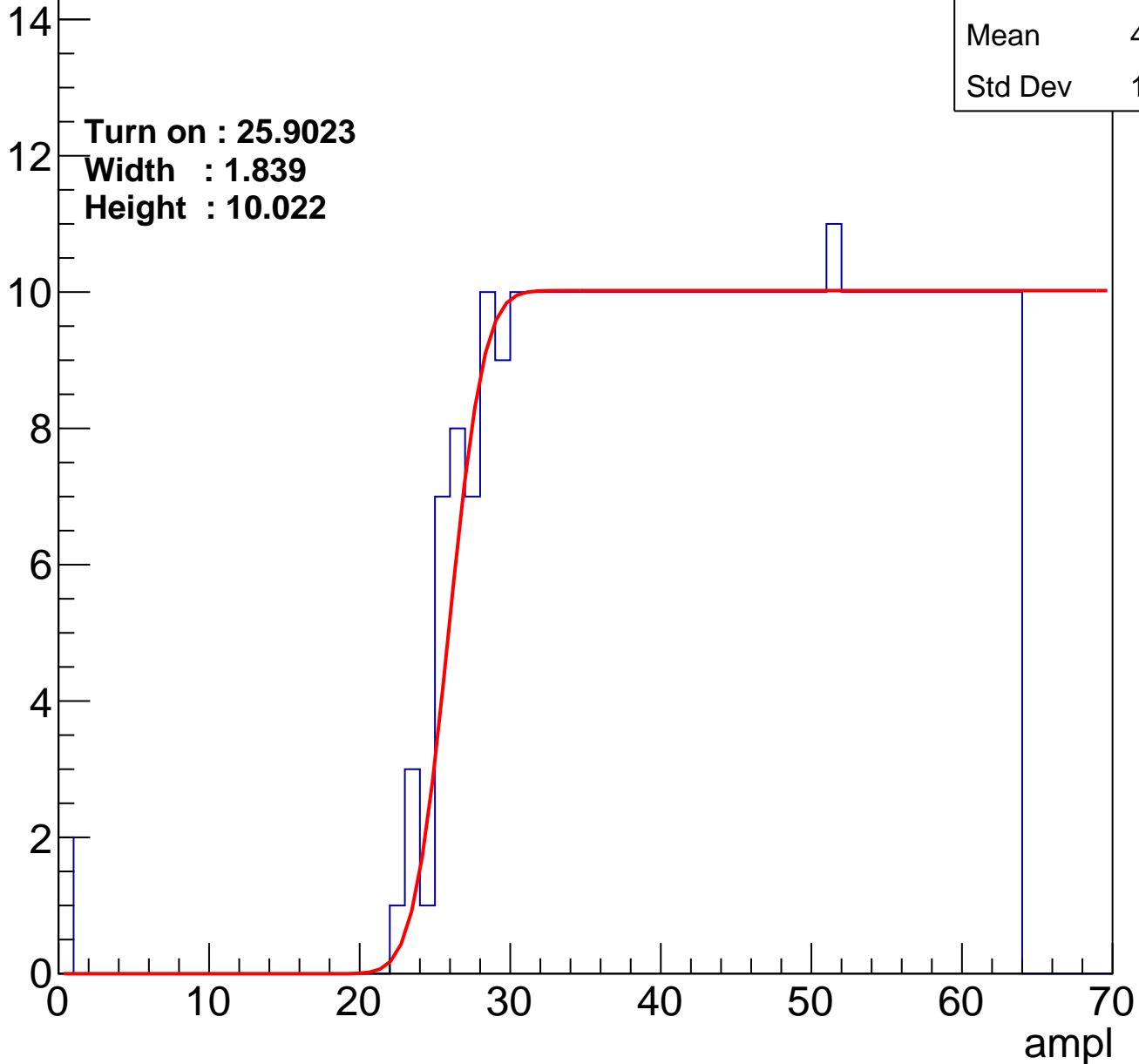
Entries	389
Mean	43.93
Std Dev	11.64

Turn on : 25.9023

Width : 1.839

Height : 10.022

Entry



B0L000S, U15-ch62

calib_packv5_042523_0143.root, FC#5, port B1

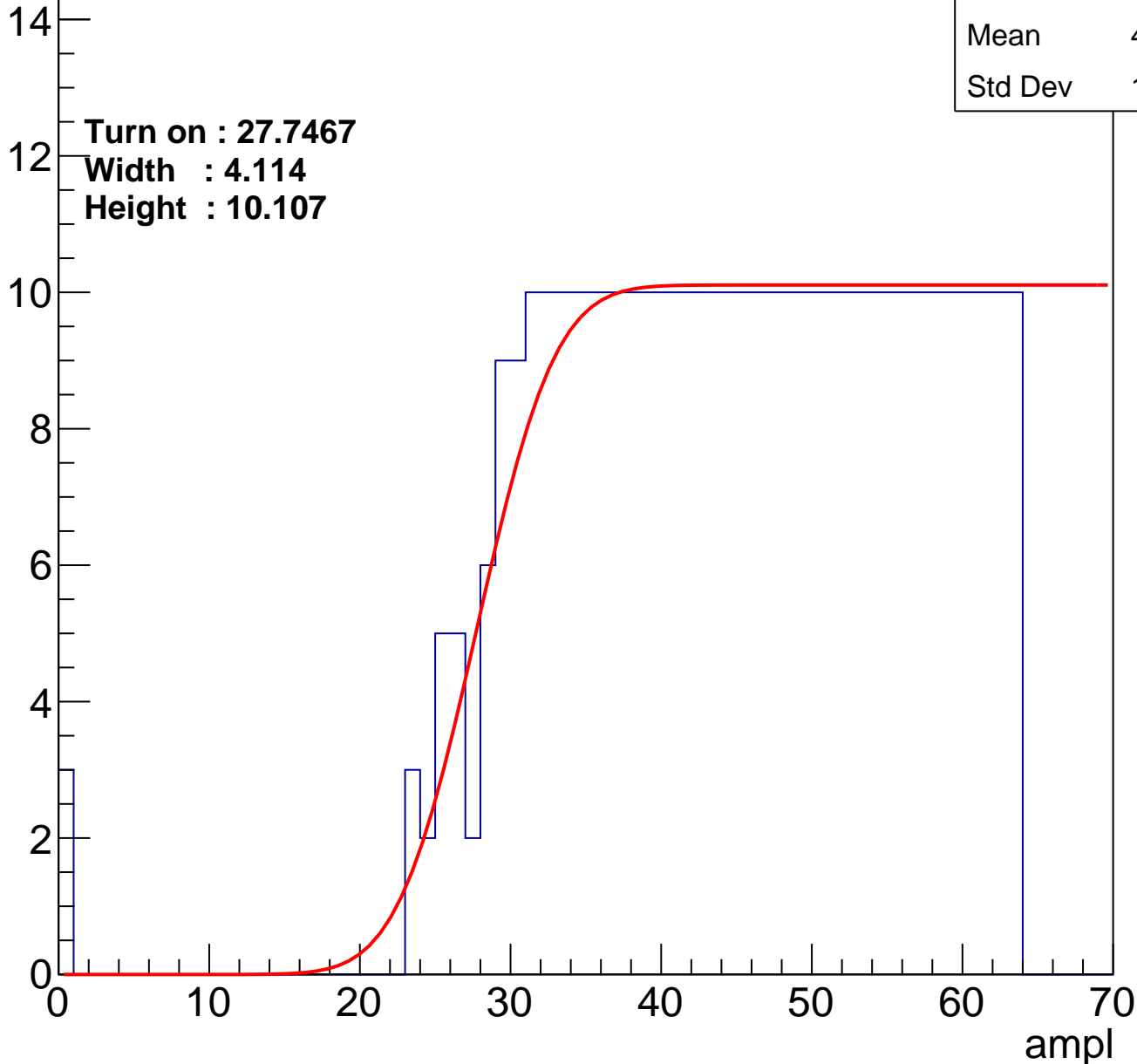
Entries	374
Mean	44.48
Std Dev	11.57

Turn on : 27.7467

Width : 4.114

Height : 10.107

Entry



B0L000S, U15-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	43.89
Std Dev	12.52

Turn on : 26.8541

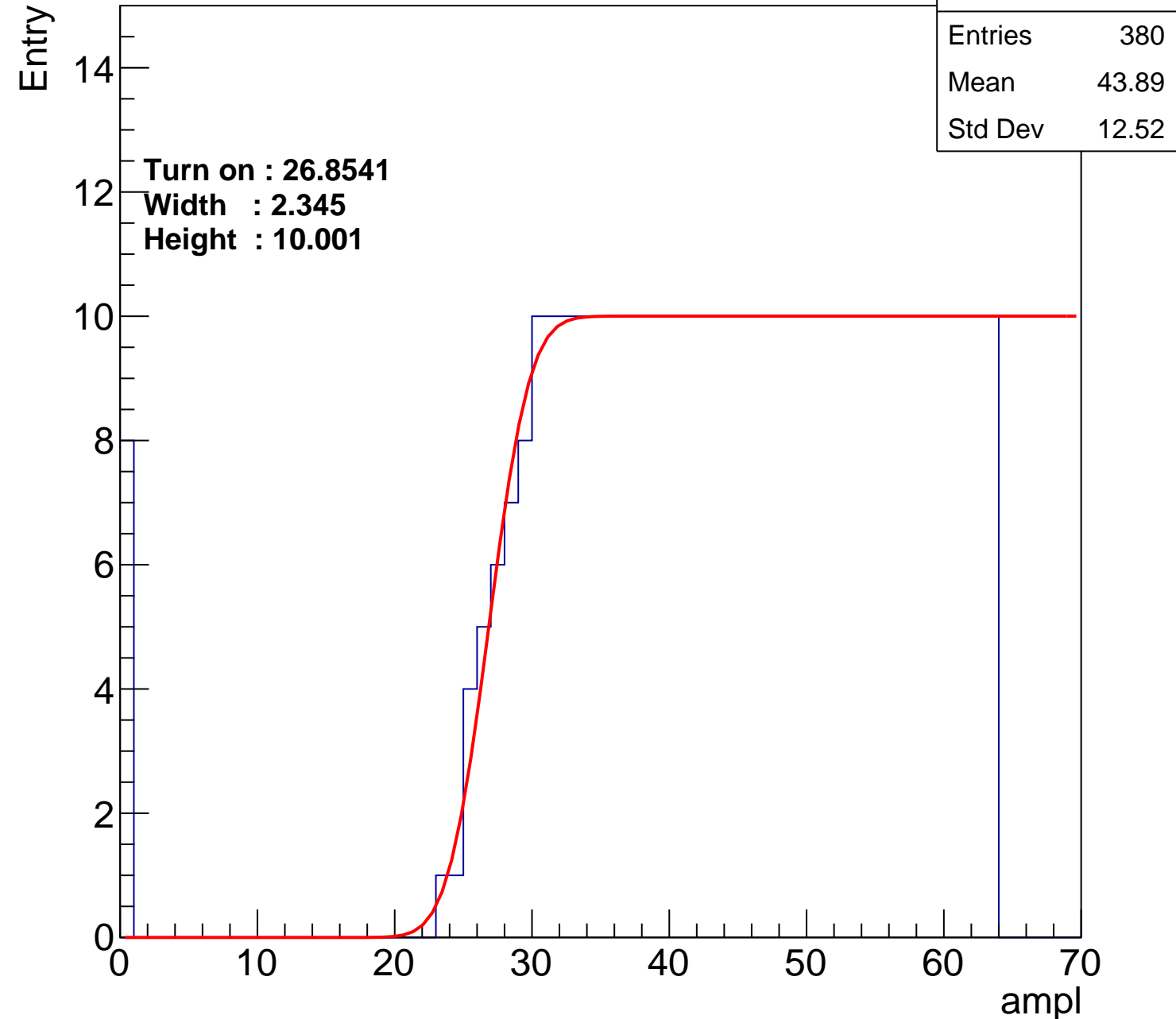
Width : 2.345

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch64

calib_packv5_042523_0143.root, FC#5, port B1

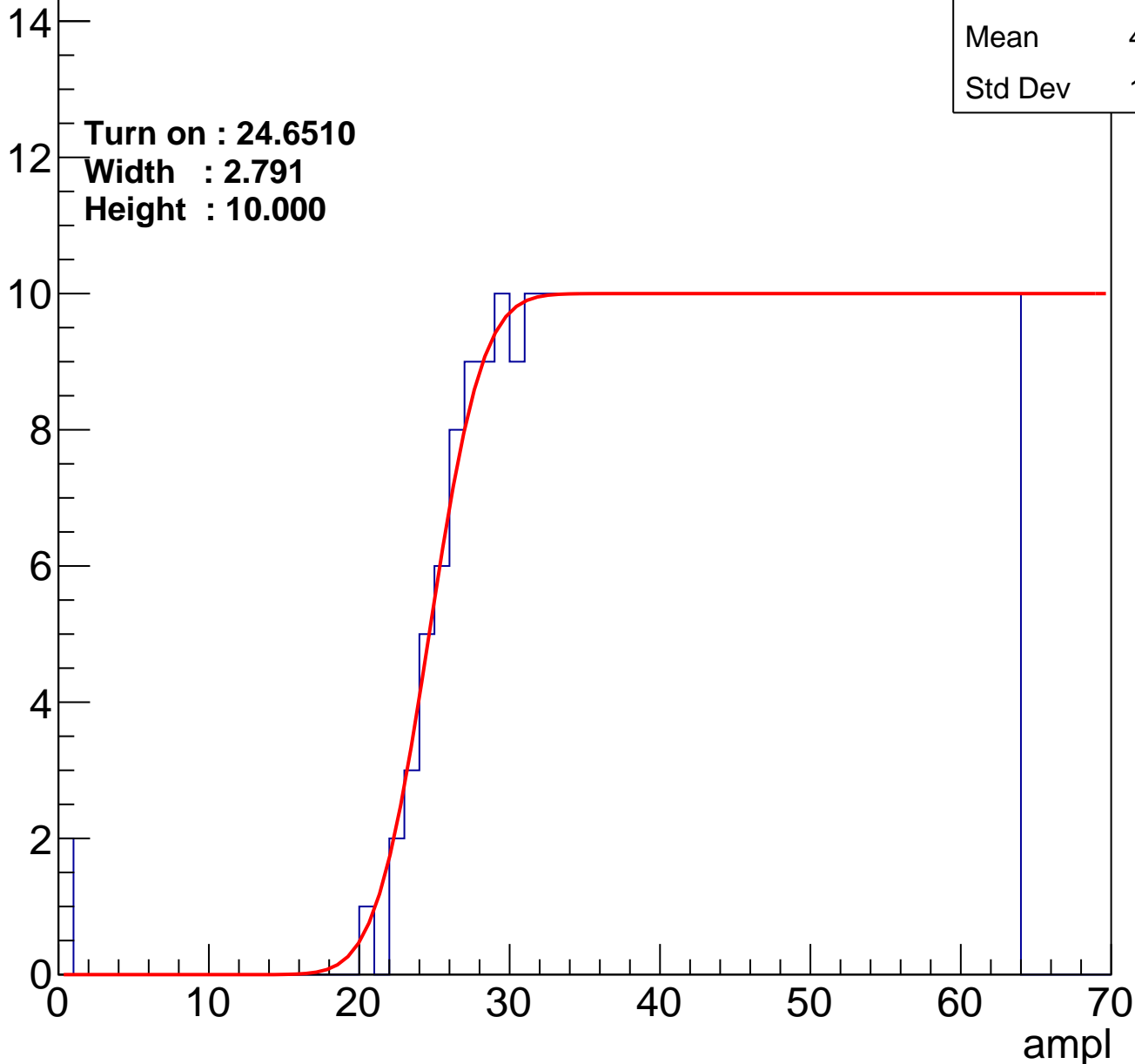
Entries	394
Mean	43.59
Std Dev	11.84

Turn on : 24.6510

Width : 2.791

Height : 10.000

Entry



B0L000S, U15-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.57
Std Dev	11.25

Turn on : 26.1446

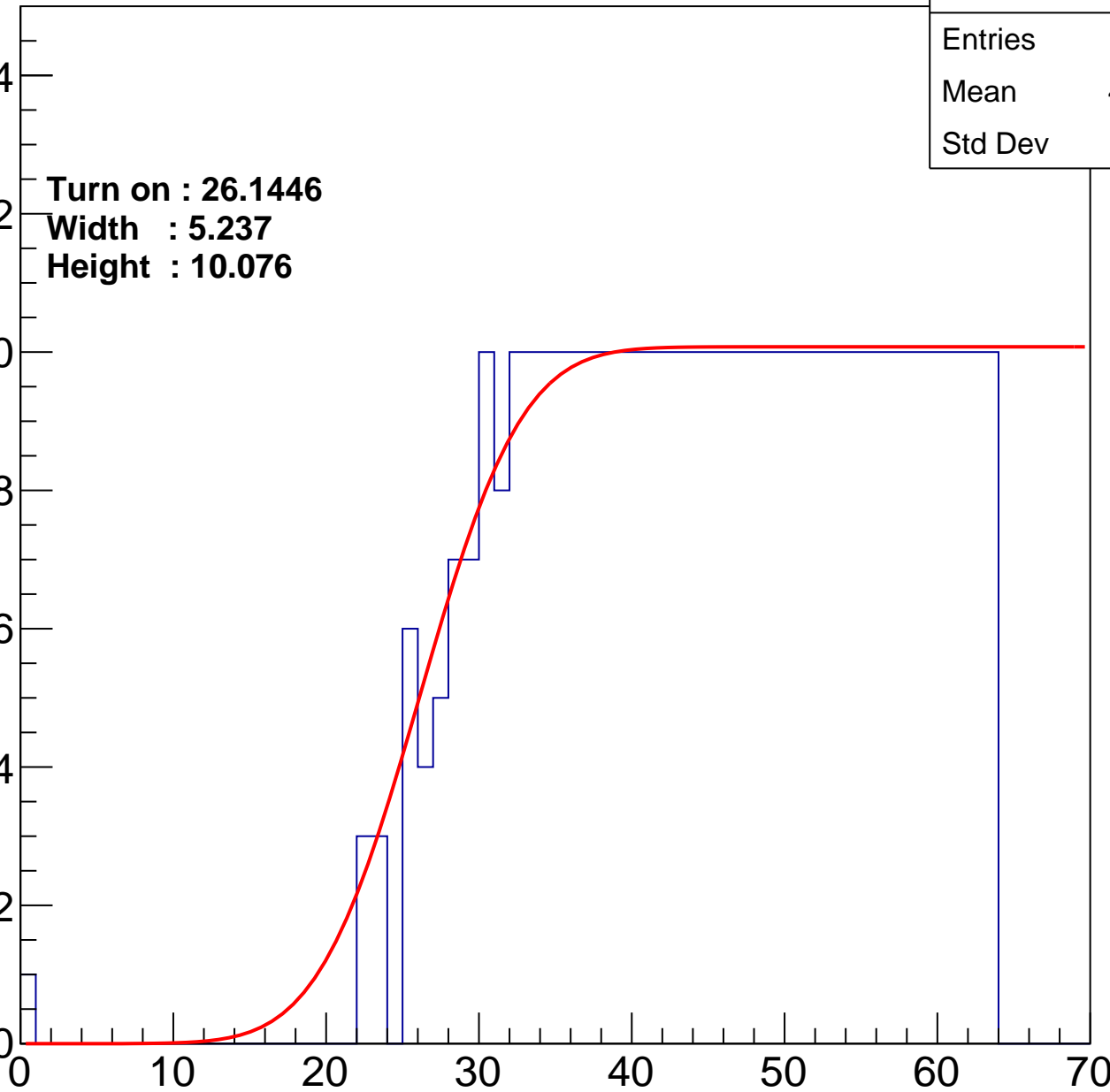
Width : 5.237

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch66

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.44
Std Dev	11.63

Turn on : 26.6987

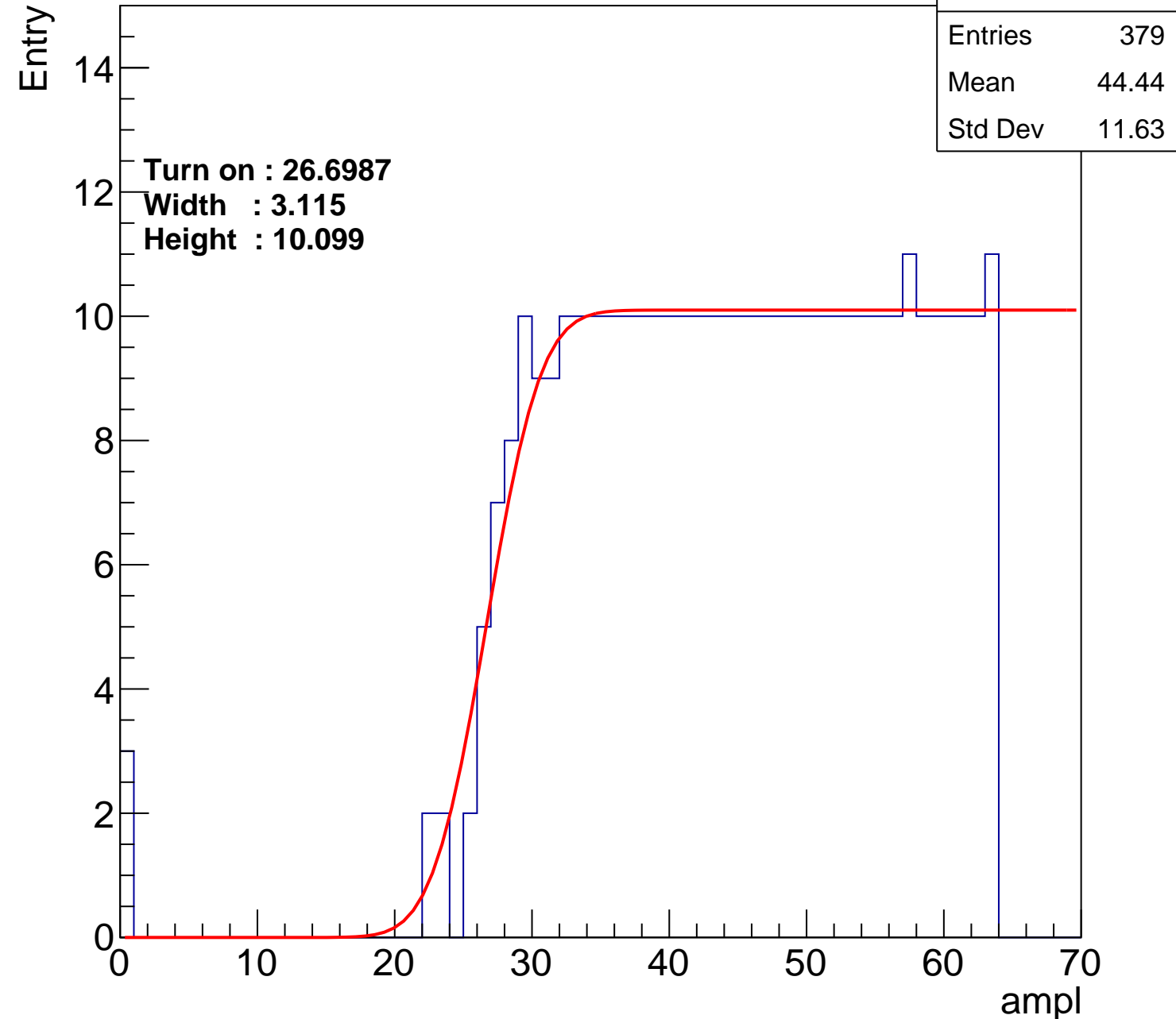
Width : 3.115

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.54
Std Dev	11.26

Turn on : 26.5474

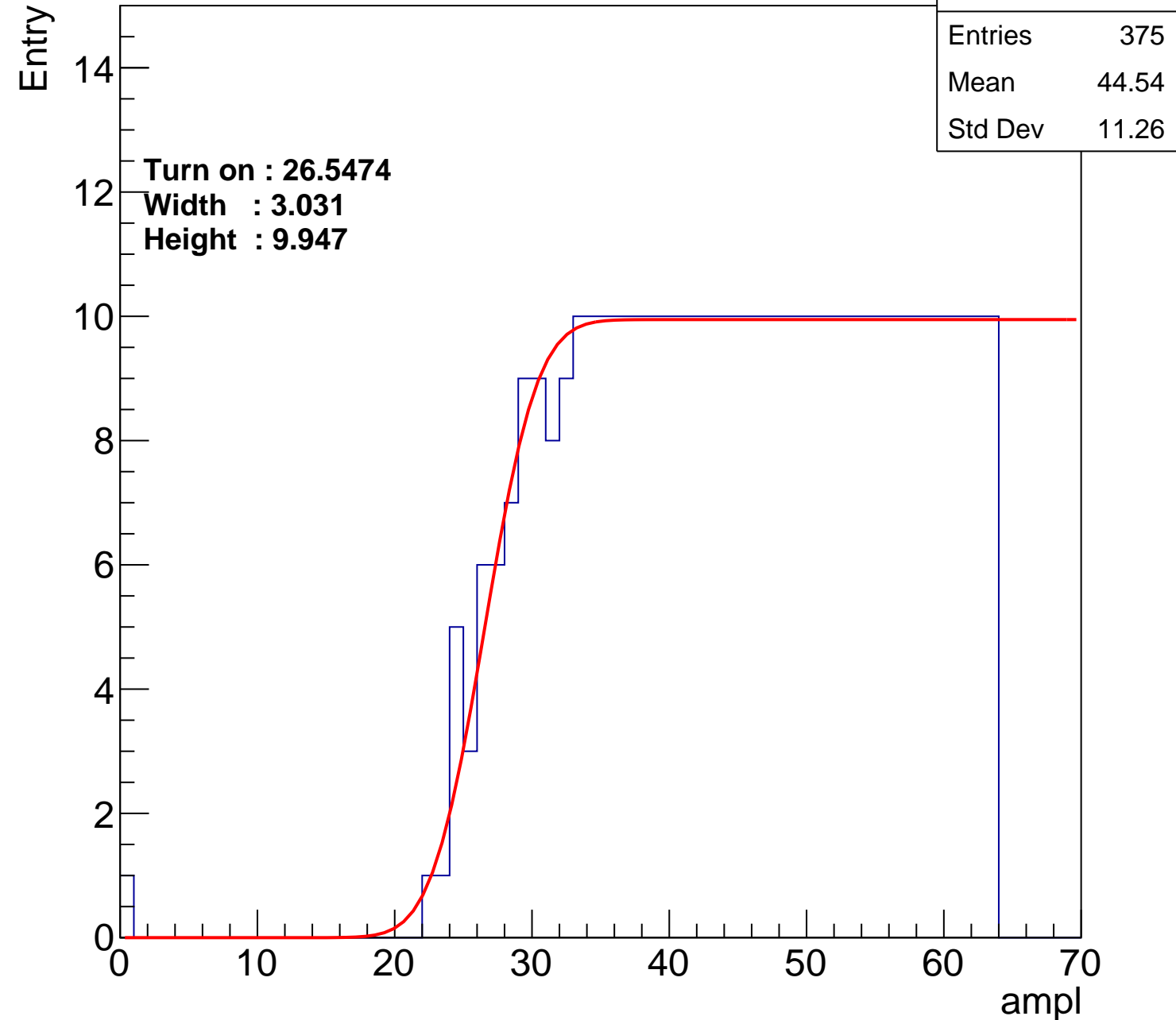
Width : 3.031

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.09
Std Dev	10.9

Turn on : 27.4521

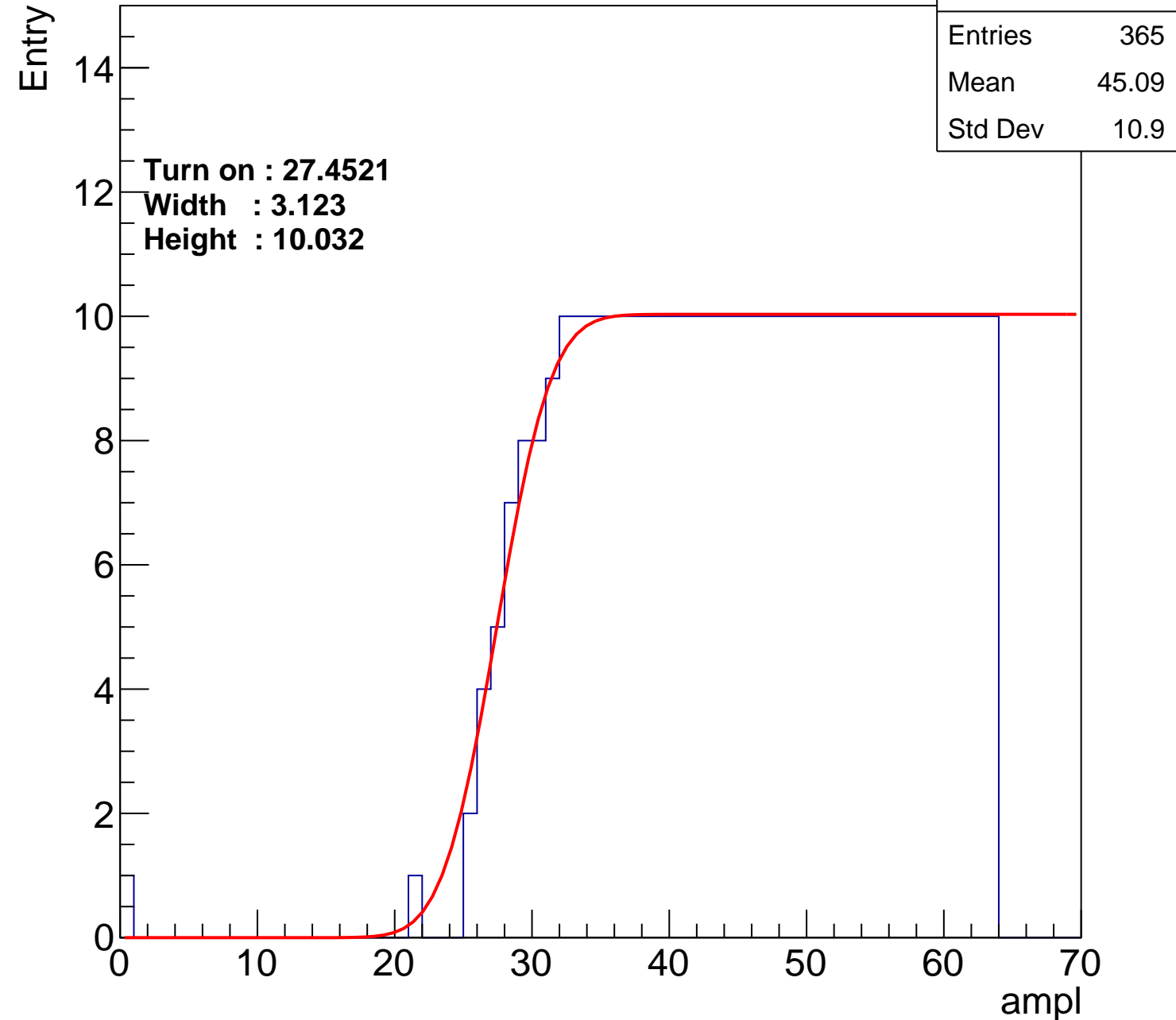
Width : 3.123

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.86
Std Dev	11.8

Turn on : 25.7327

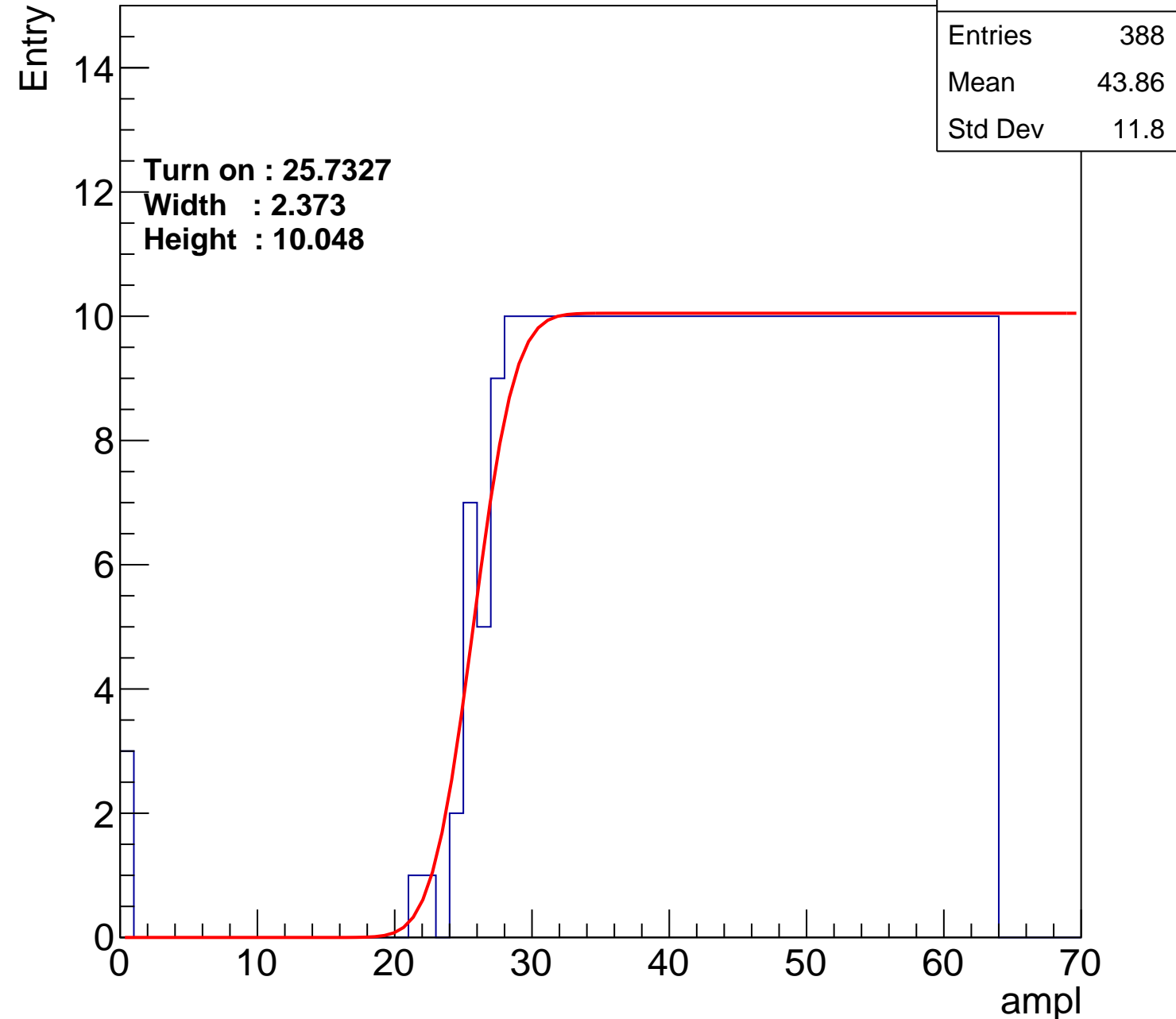
Width : 2.373

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch70

calib_packv5_042523_0143.root, FC#5, port B1

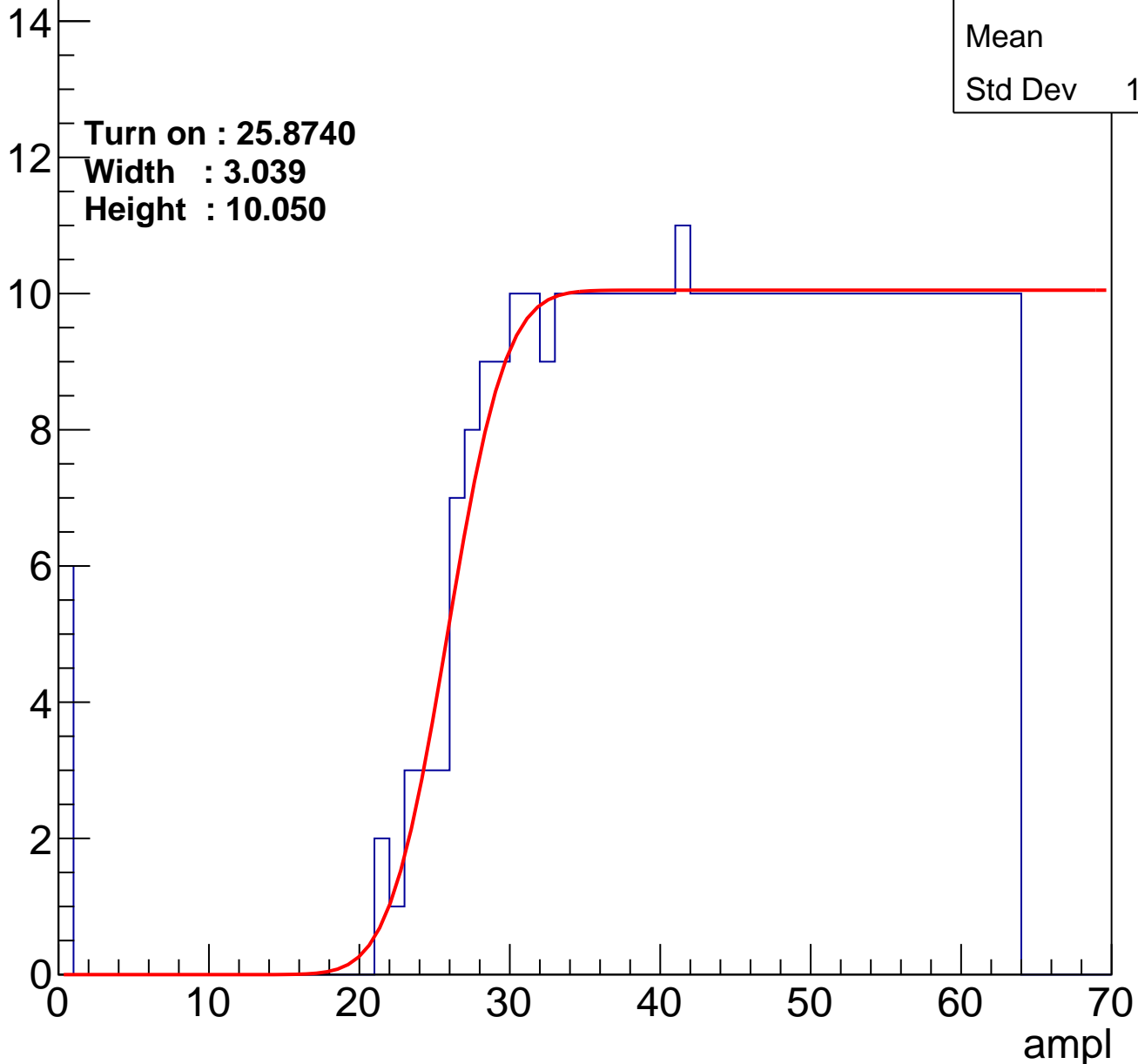
Entries	391
Mean	43.5
Std Dev	12.42

Turn on : 25.8740

Width : 3.039

Height : 10.050

Entry



B0L000S, U15-ch71

calib_packv5_042523_0143.root, FC#5, port B1

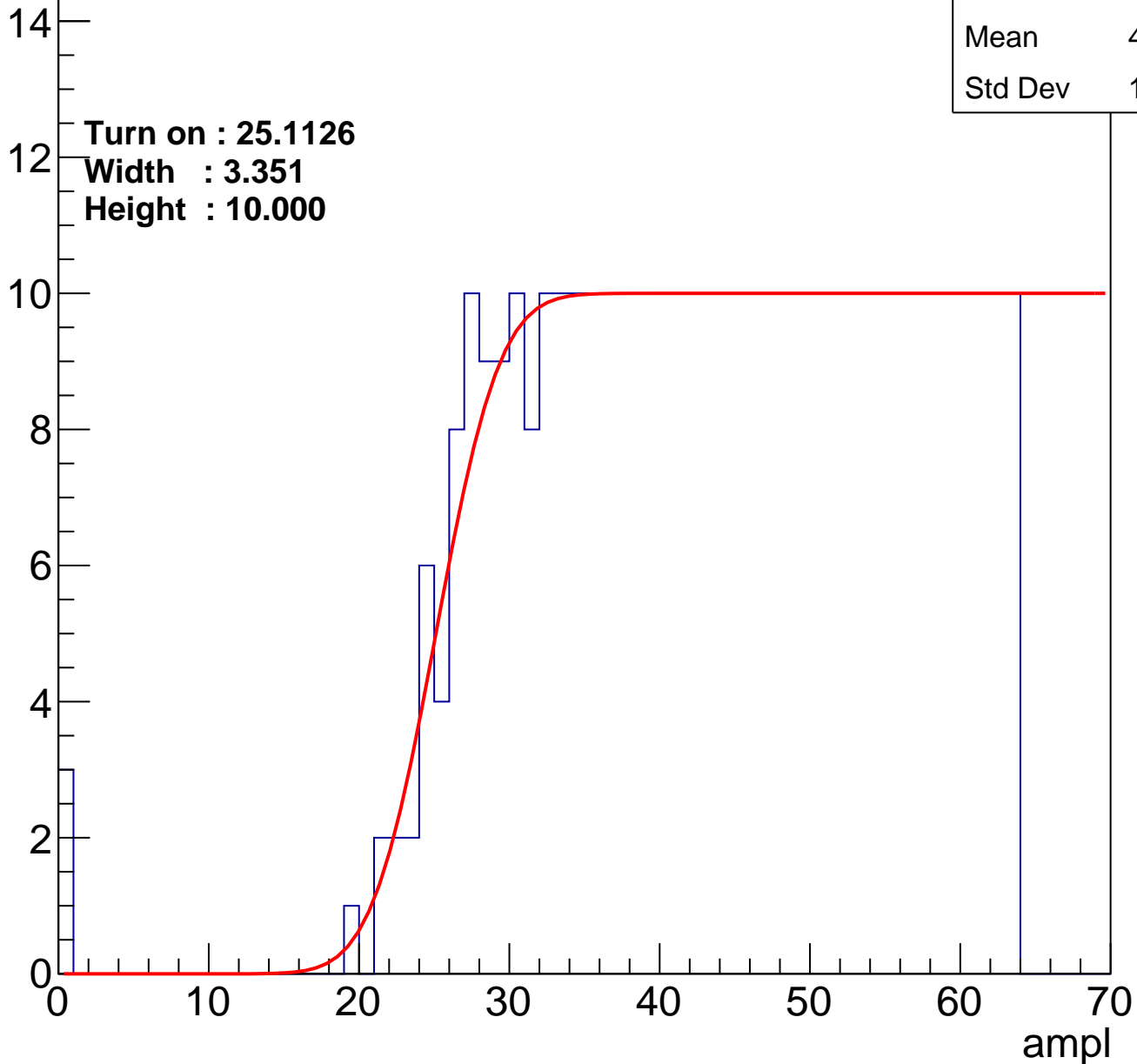
Entries	394
Mean	43.49
Std Dev	12.07

Turn on : 25.1126

Width : 3.351

Height : 10.000

Entry



B0L000S, U15-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.74
Std Dev	12.18

Turn on : 25.9596

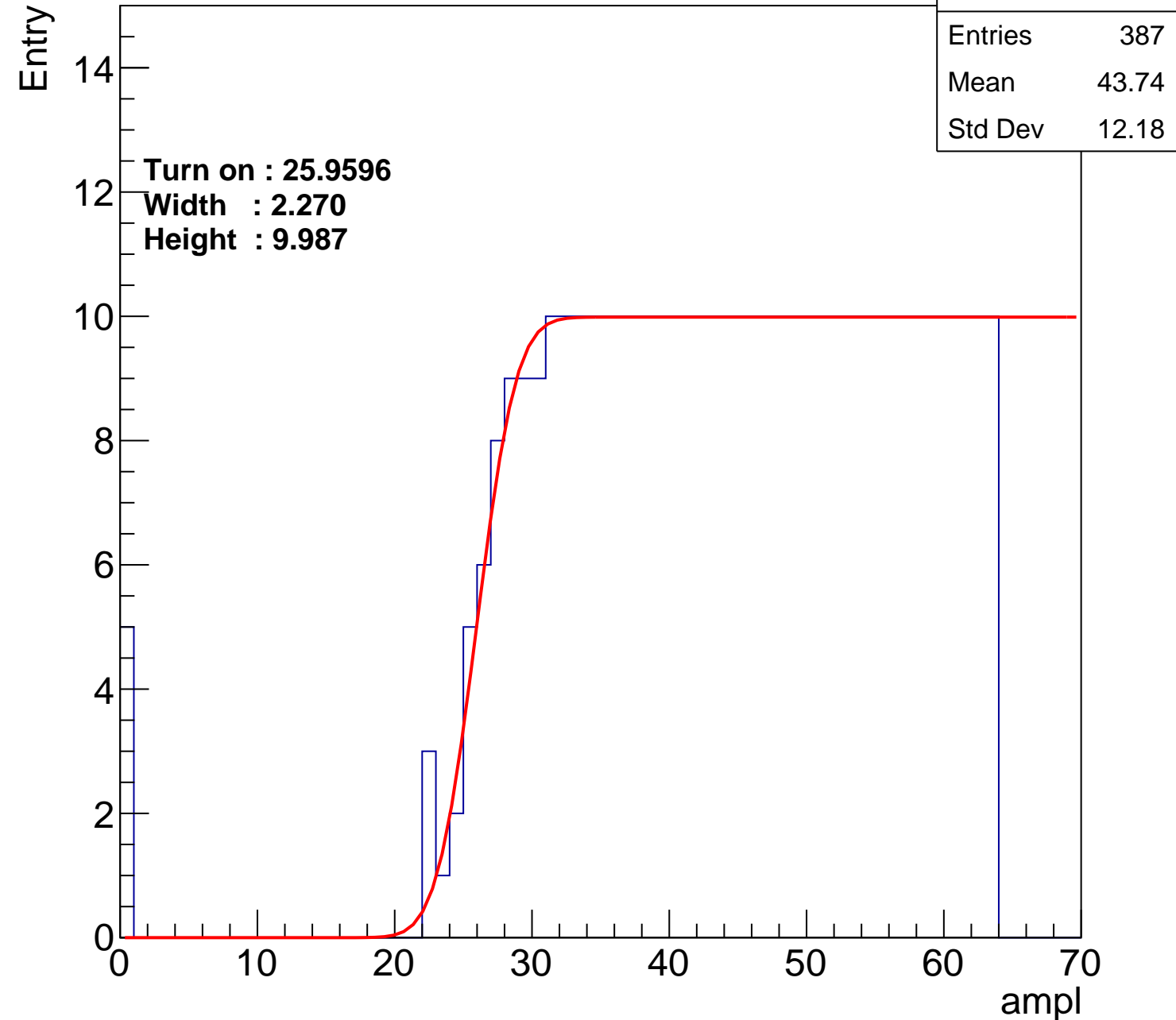
Width : 2.270

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch73

calib_packv5_042523_0143.root, FC#5, port B1

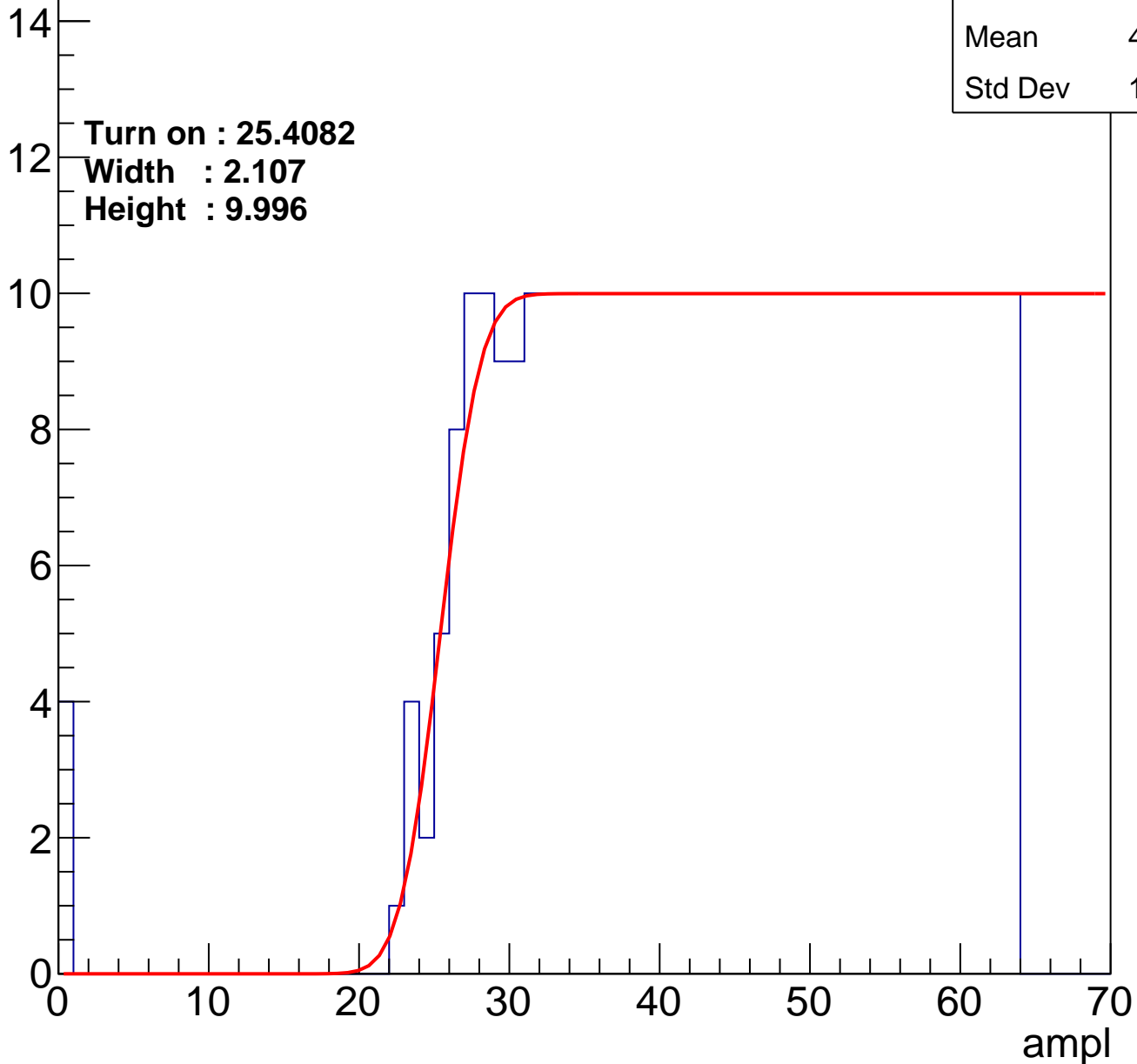
Entries	392
Mean	43.59
Std Dev	12.09

Turn on : 25.4082

Width : 2.107

Height : 9.996

Entry



B0L000S, U15-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	398
Mean	43.46
Std Dev	11.79

Turn on : 25.3510

Width : 2.419

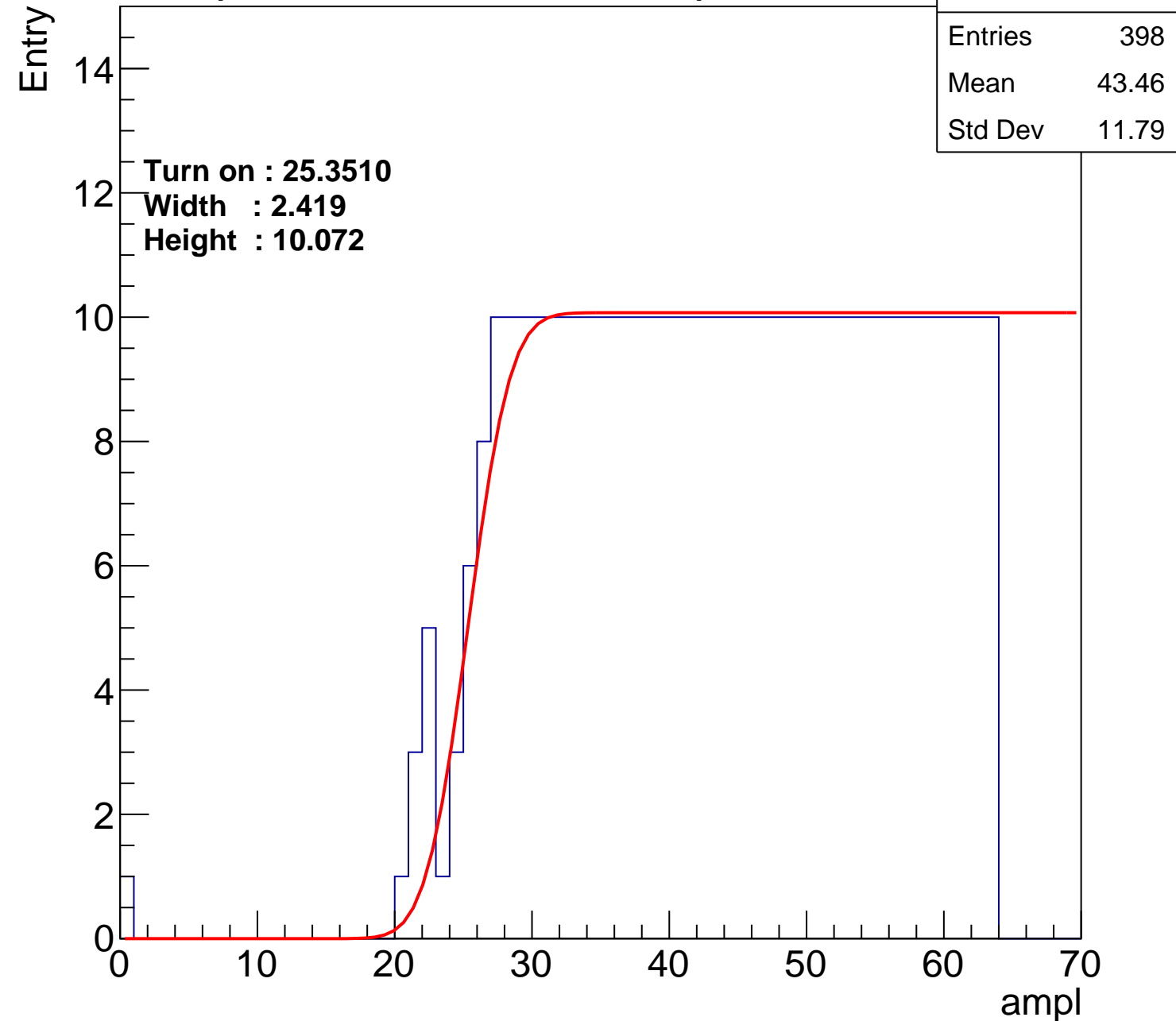
Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U15-ch75

calib_packv5_042523_0143.root, FC#5, port B1

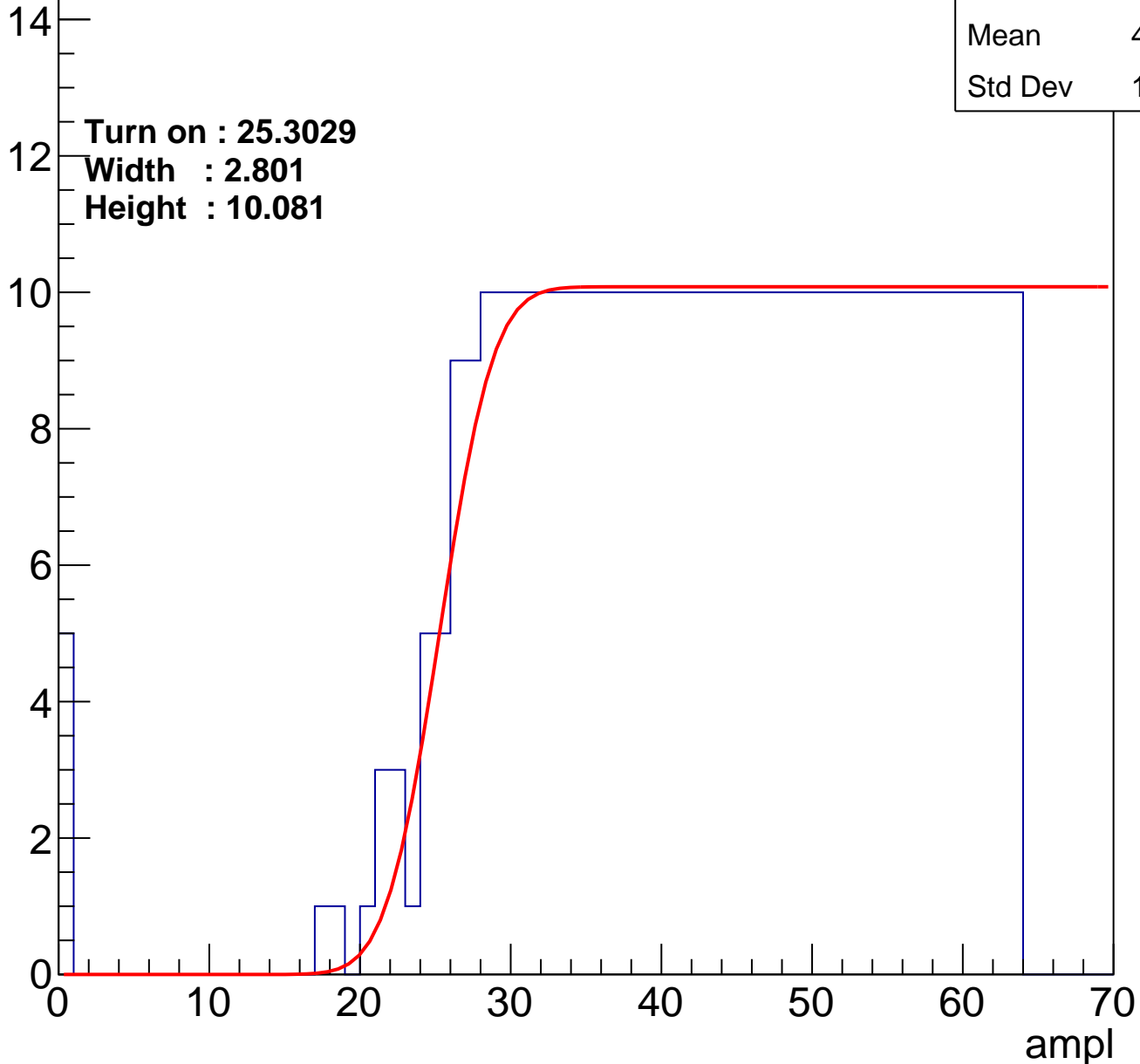
Entries	403
Mean	42.95
Std Dev	12.57

Turn on : 25.3029

Width : 2.801

Height : 10.081

Entry



B0L000S, U15-ch76

calib_packv5_042523_0143.root, FC#5, port B1

Entries	397
Mean	43.47
Std Dev	11.95

Turn on : 24.9737

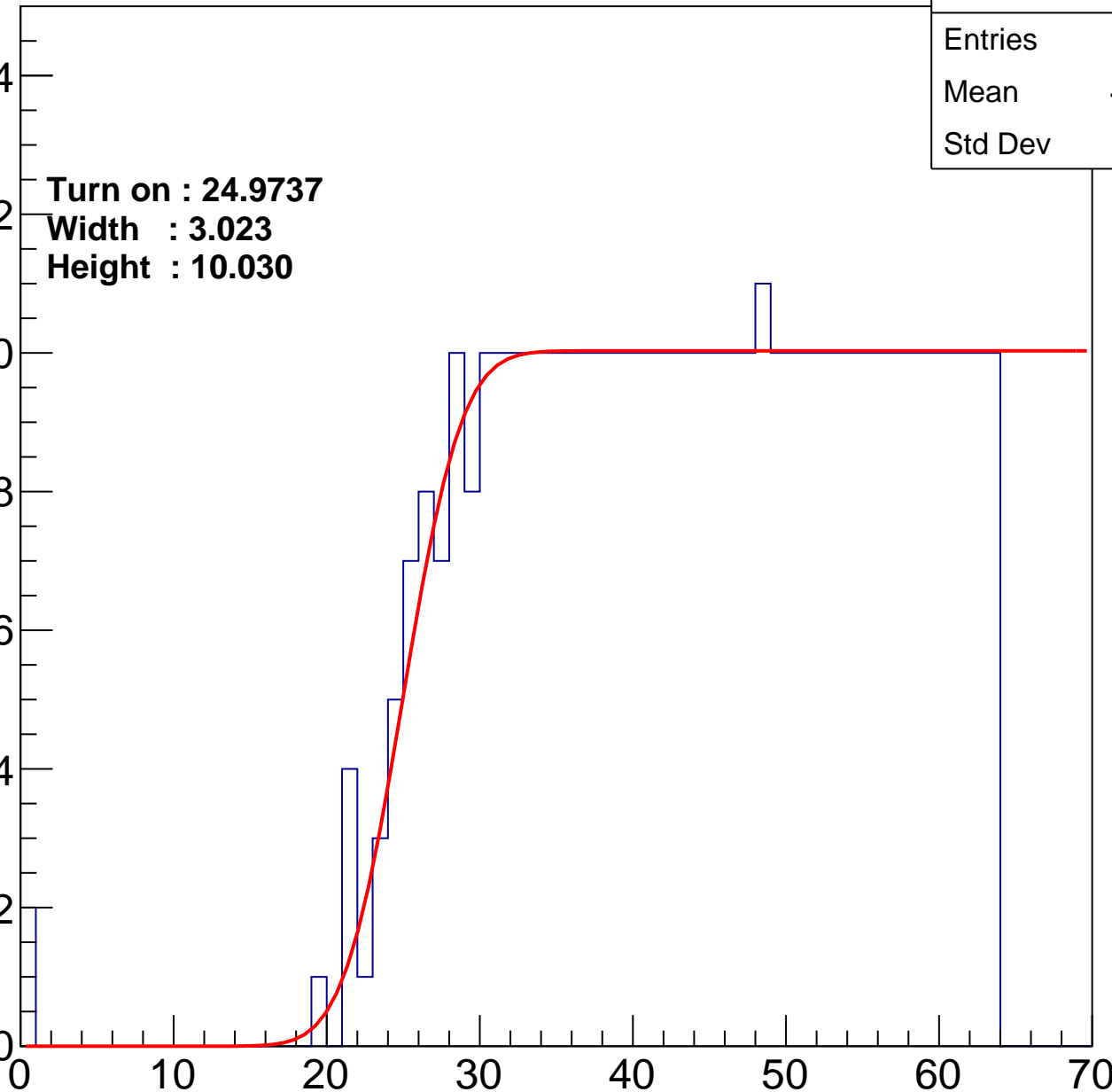
Width : 3.023

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch77

calib_packv5_042523_0143.root, FC#5, port B1

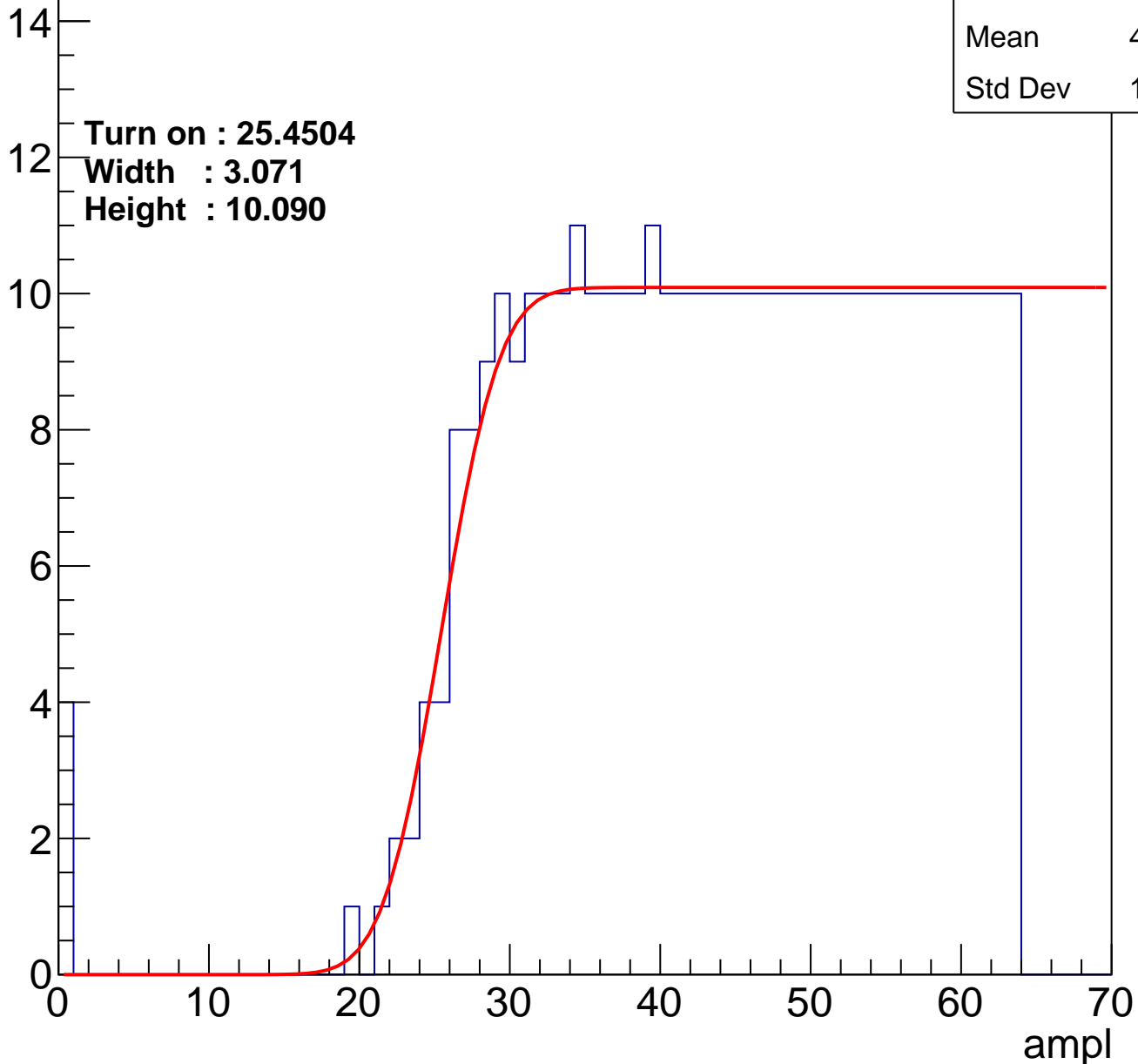
Entries	394
Mean	43.52
Std Dev	12.13

Turn on : 25.4504

Width : 3.071

Height : 10.090

Entry



B0L000S, U15-ch78

calib_packv5_042523_0143.root, FC#5, port B1

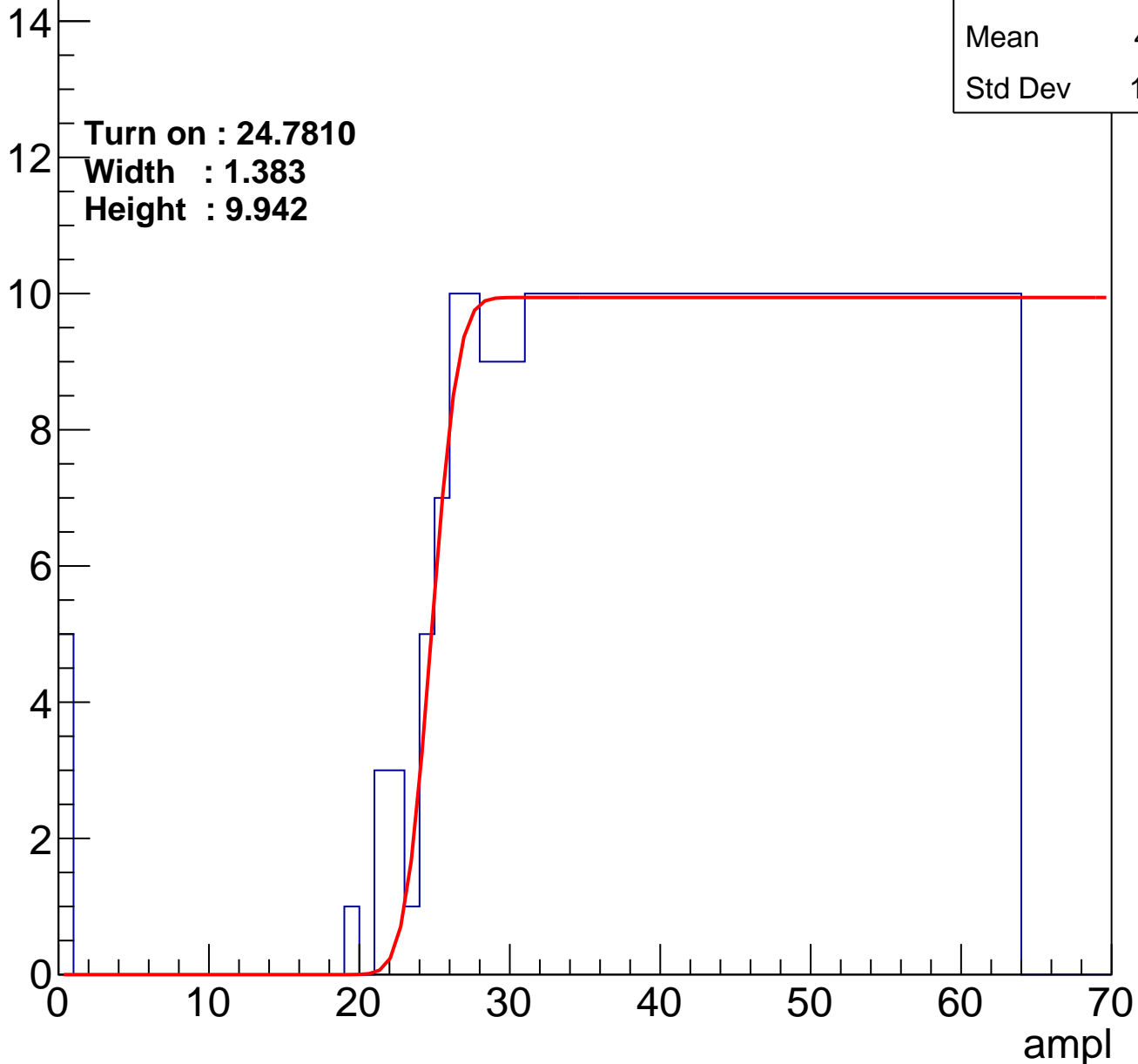
Entries	402
Mean	43.01
Std Dev	12.52

Turn on : 24.7810

Width : 1.383

Height : 9.942

Entry



B0L000S, U15-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.21
Std Dev	11.41

Turn on : 25.9594

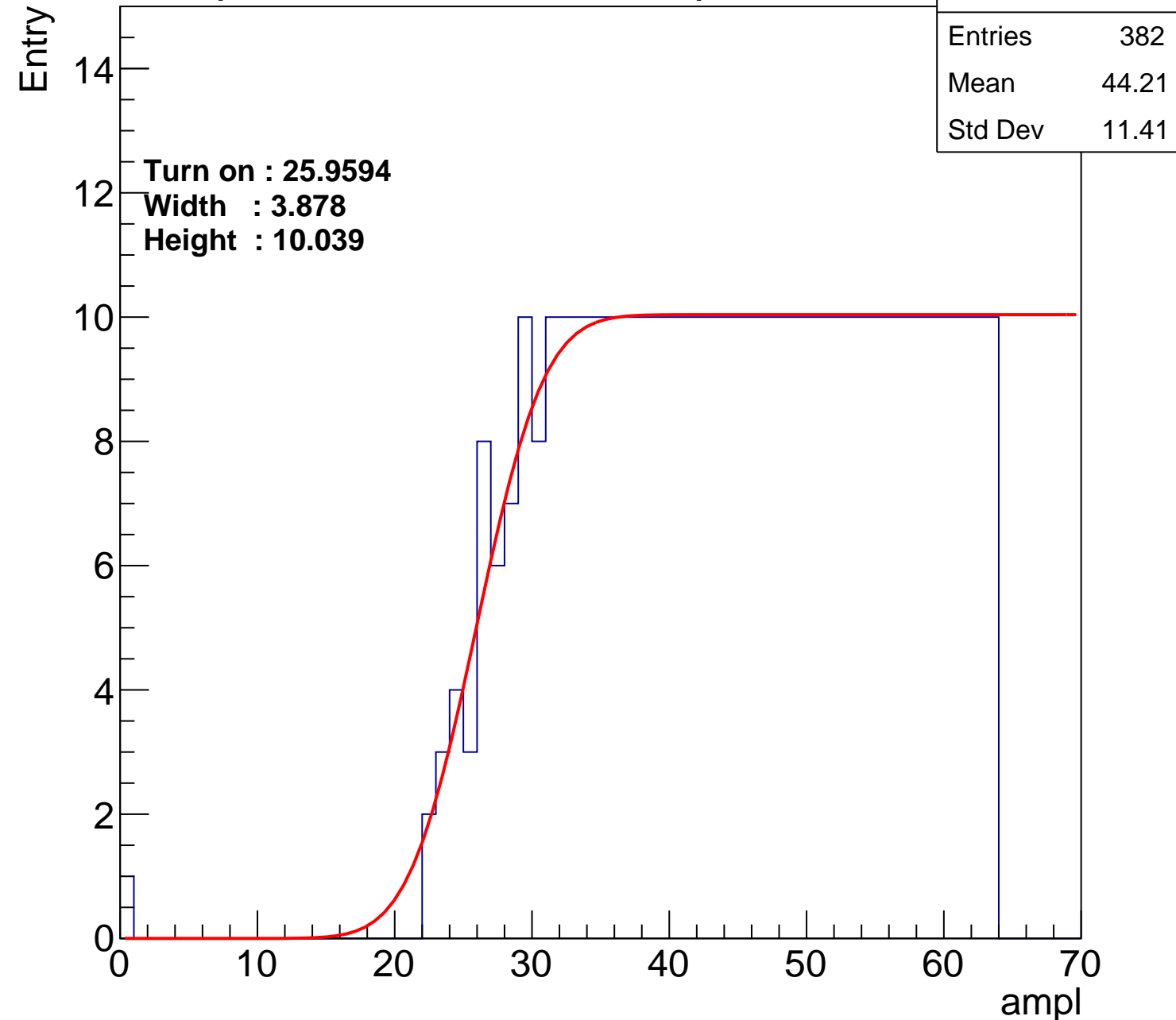
Width : 3.878

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch80

calib_packv5_042523_0143.root, FC#5, port B1

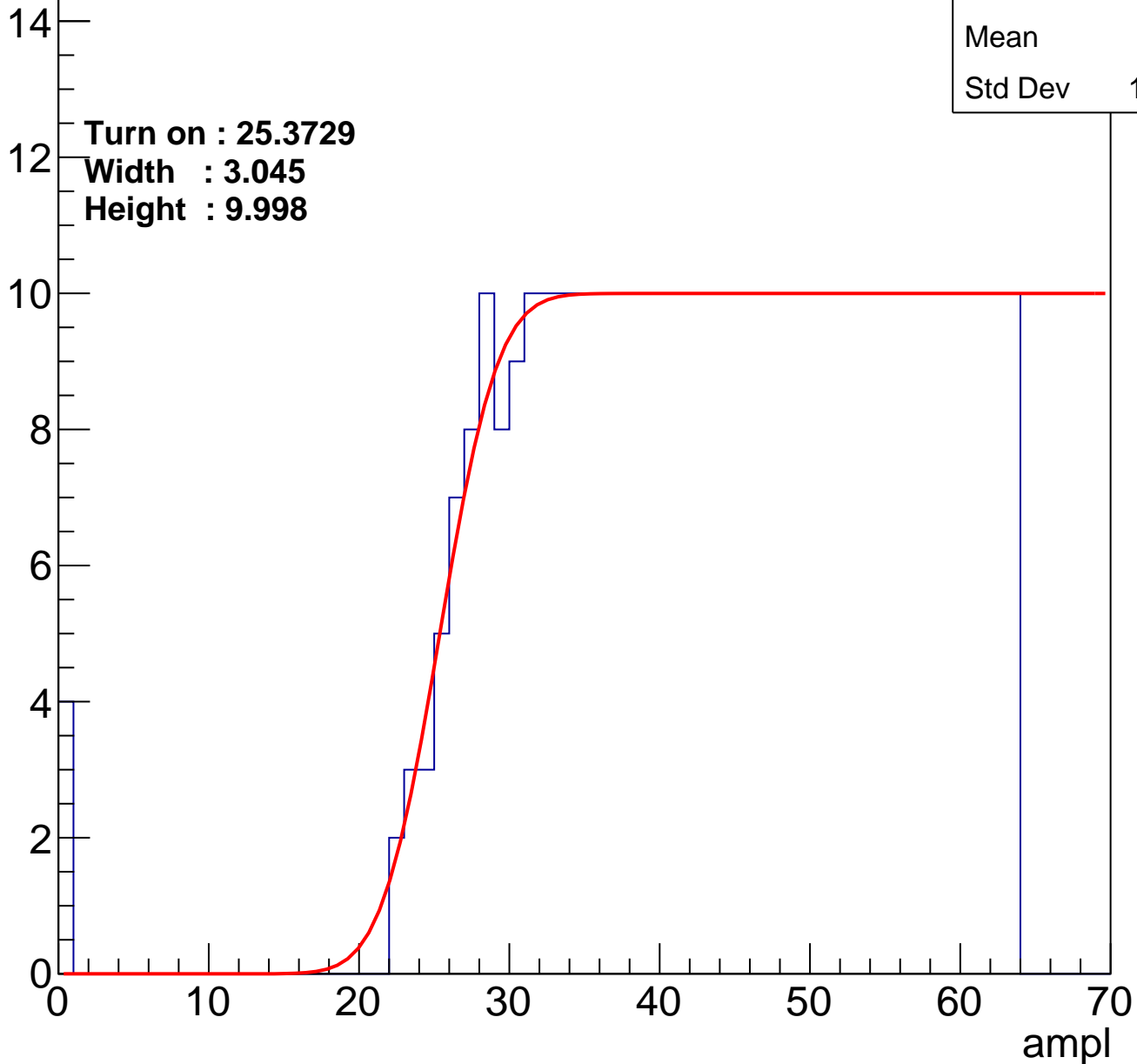
Entries	389
Mean	43.7
Std Dev	12.06

Turn on : 25.3729

Width : 3.045

Height : 9.998

Entry



B0L000S, U15-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.12
Std Dev	11.31

Turn on : 28.8308

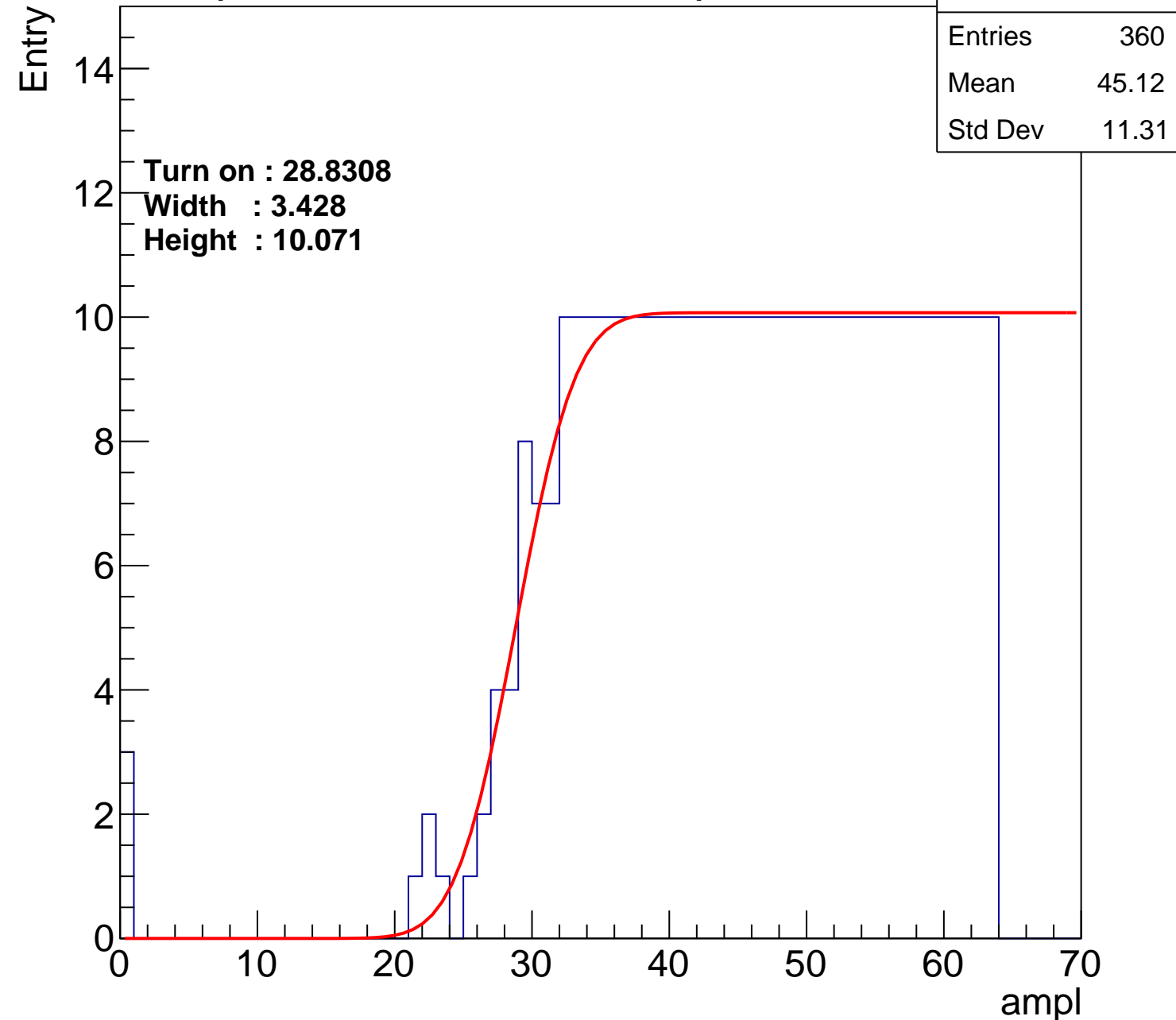
Width : 3.428

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch82

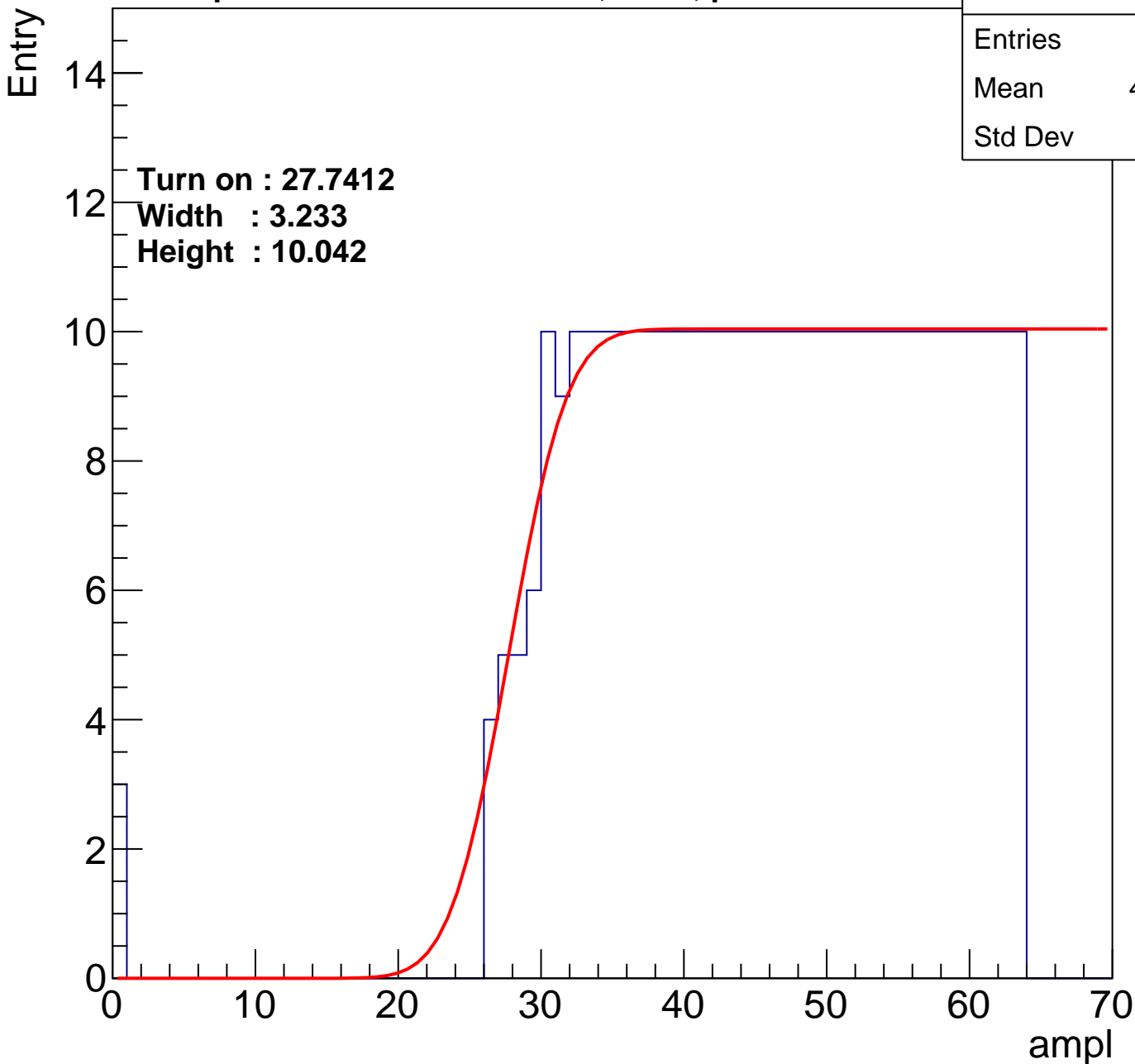
calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.12
Std Dev	11.2

Turn on : 27.7412

Width : 3.233

Height : 10.042



B0L000S, U15-ch83

calib_packv5_042523_0143.root, FC#5, port B1

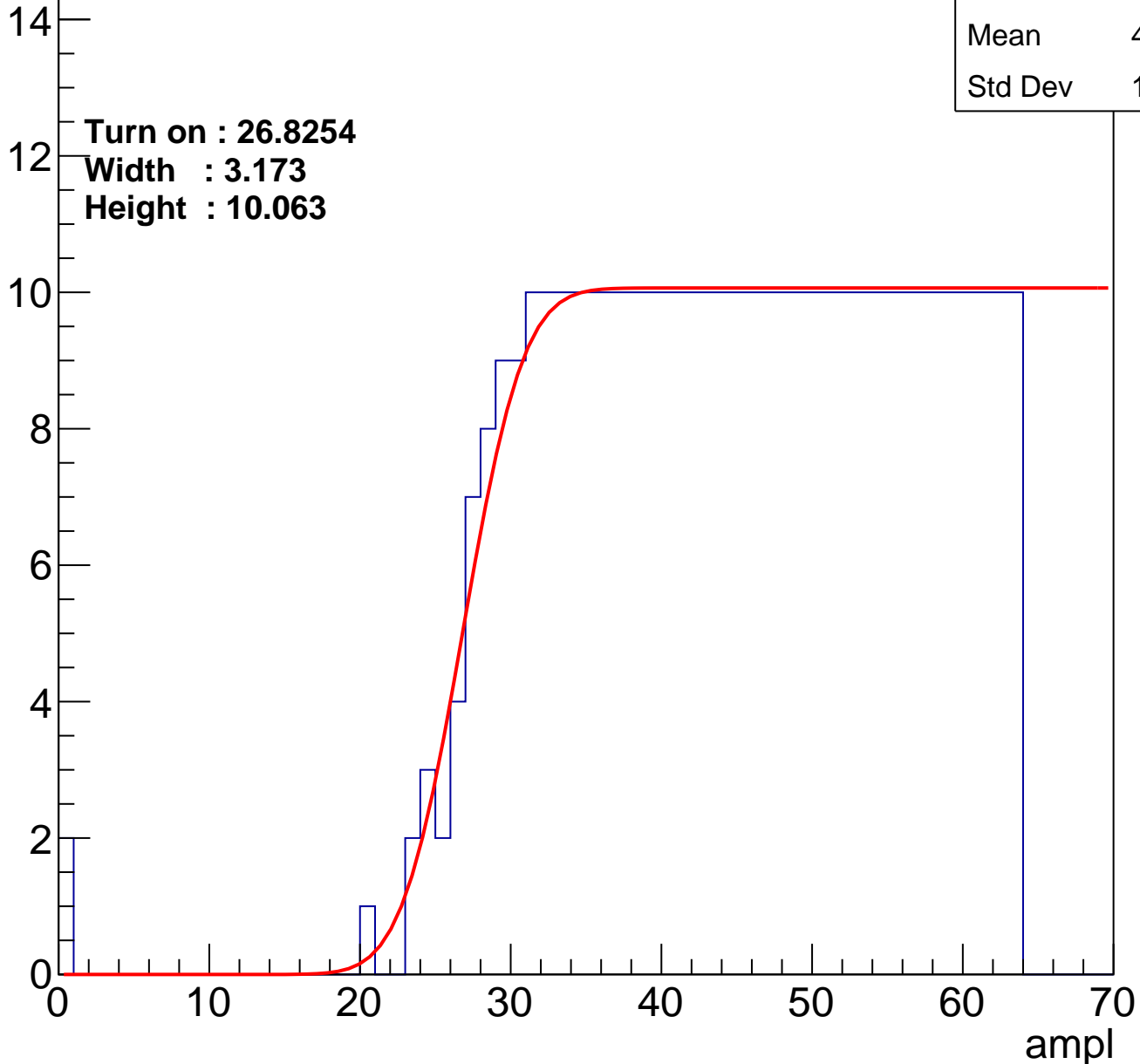
Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.8254

Width : 3.173

Height : 10.063

Entry



B0L000S, U15-ch84

calib_packv5_042523_0143.root, FC#5, port B1

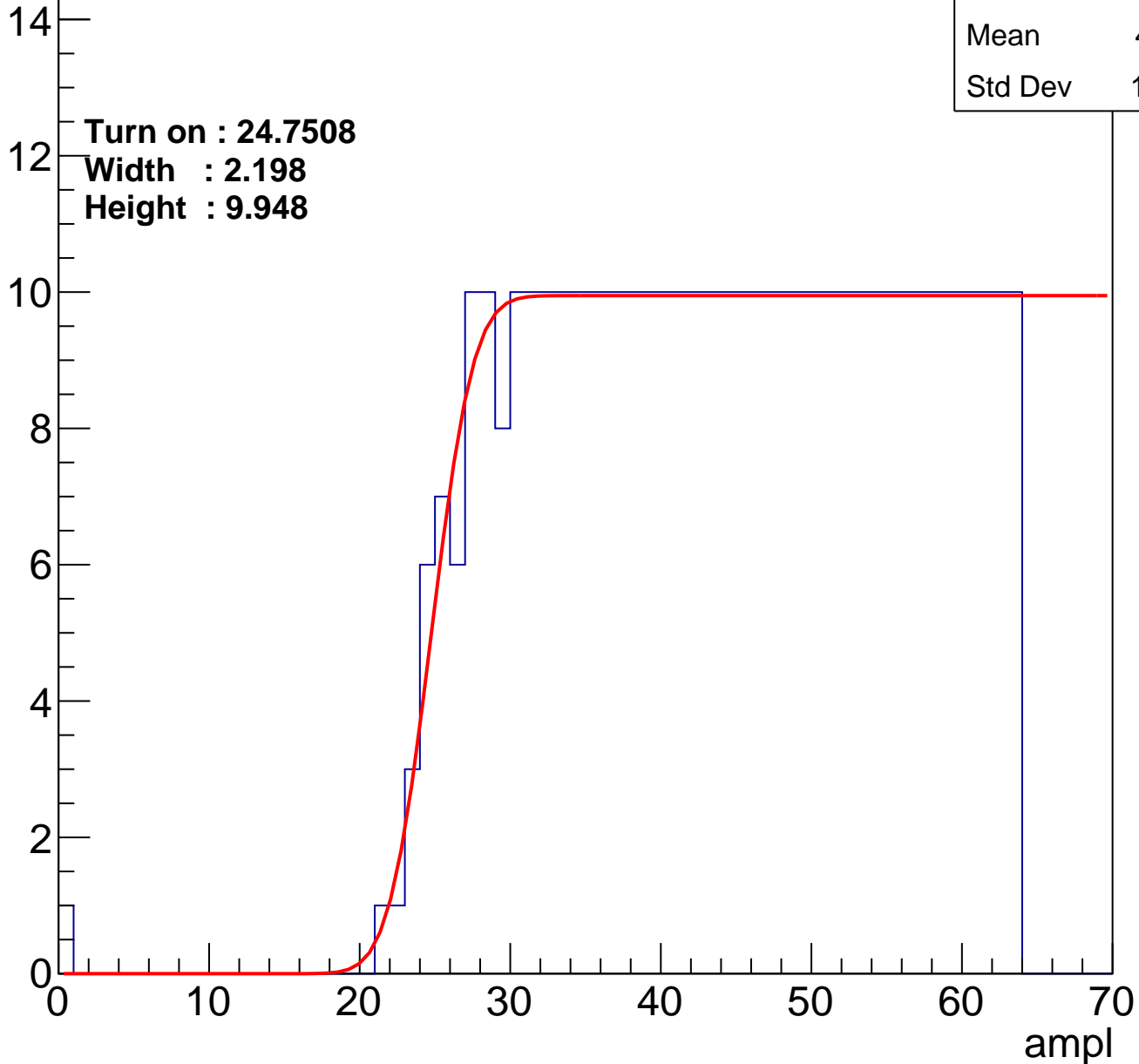
Entries	393
Mean	43.71
Std Dev	11.64

Turn on : 24.7508

Width : 2.198

Height : 9.948

Entry



B0L000S, U15-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.32
Std Dev	11.34

Turn on : 26.0494

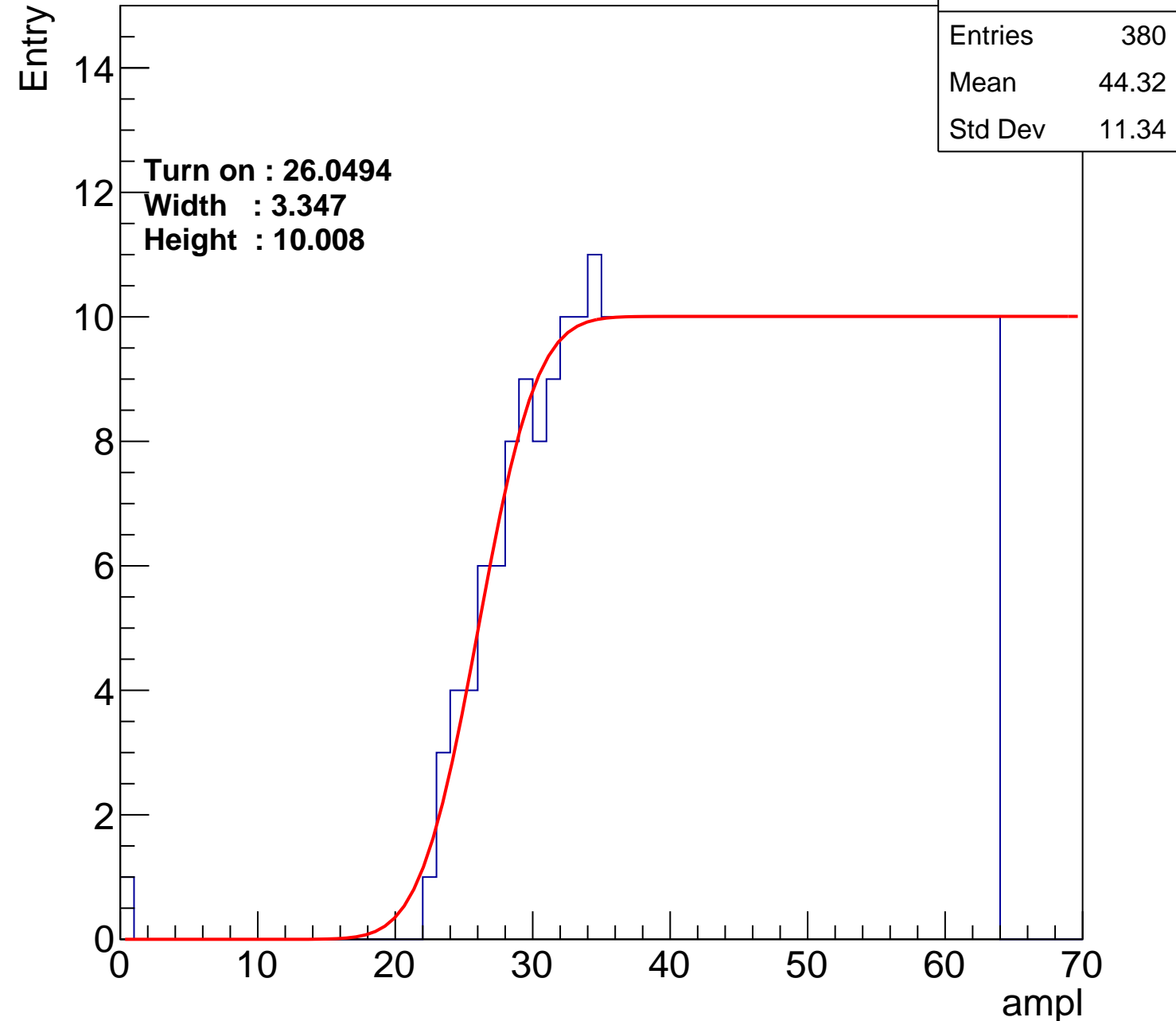
Width : 3.347

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.69
Std Dev	11.96

Turn on : 28.4365

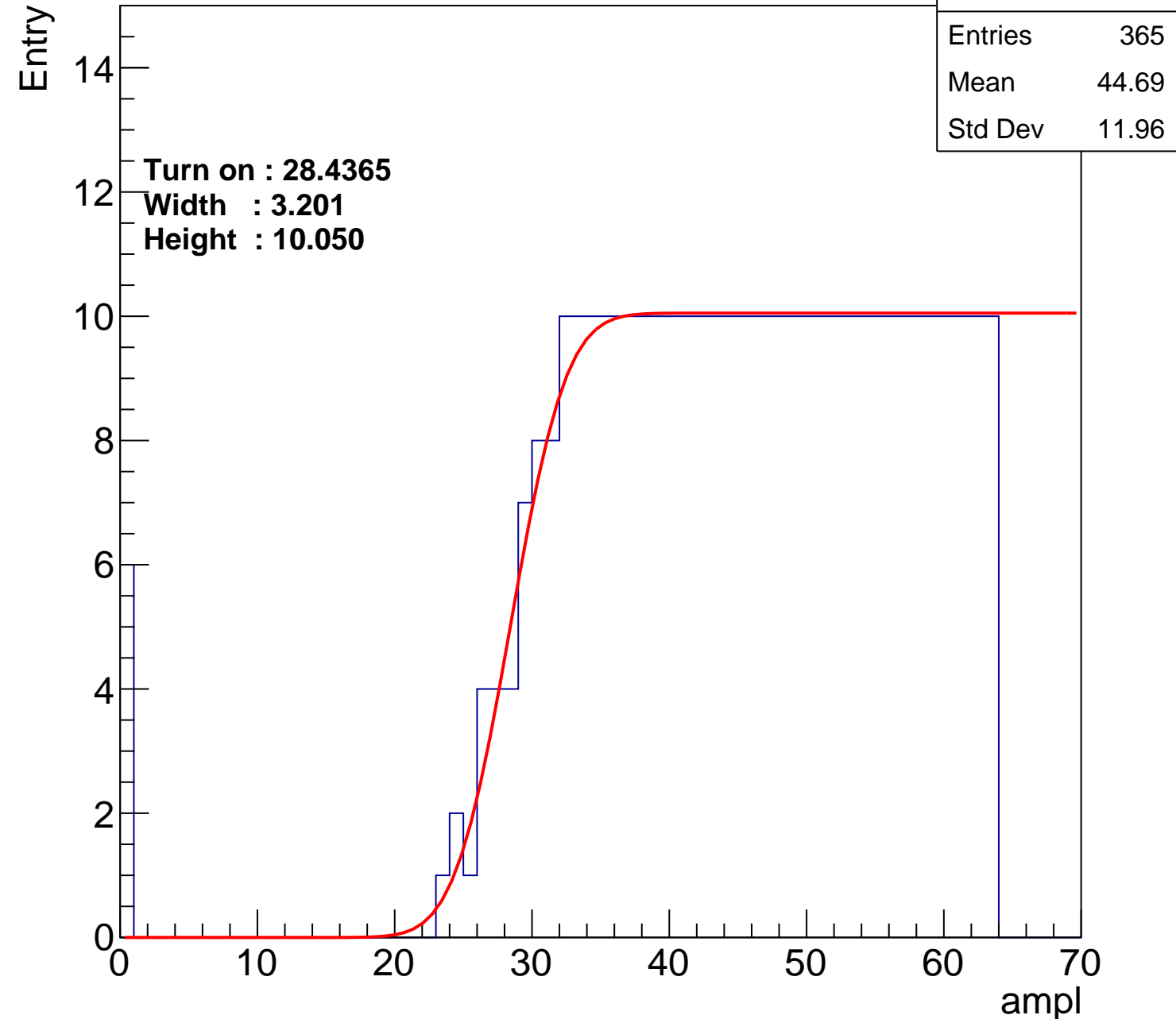
Width : 3.201

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.2433

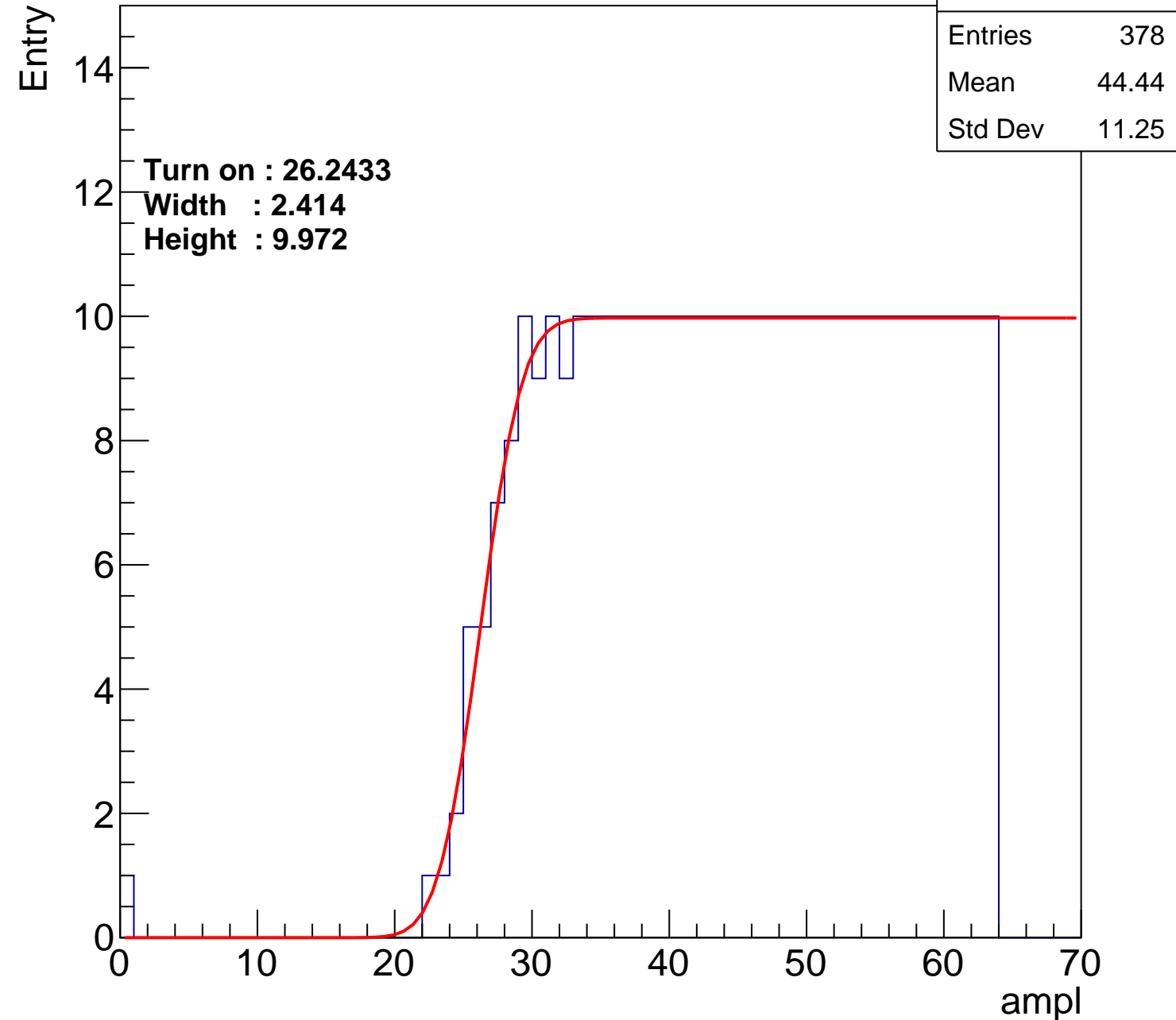
Width : 2.414

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.99
Std Dev	11.62

Turn on : 26.5268

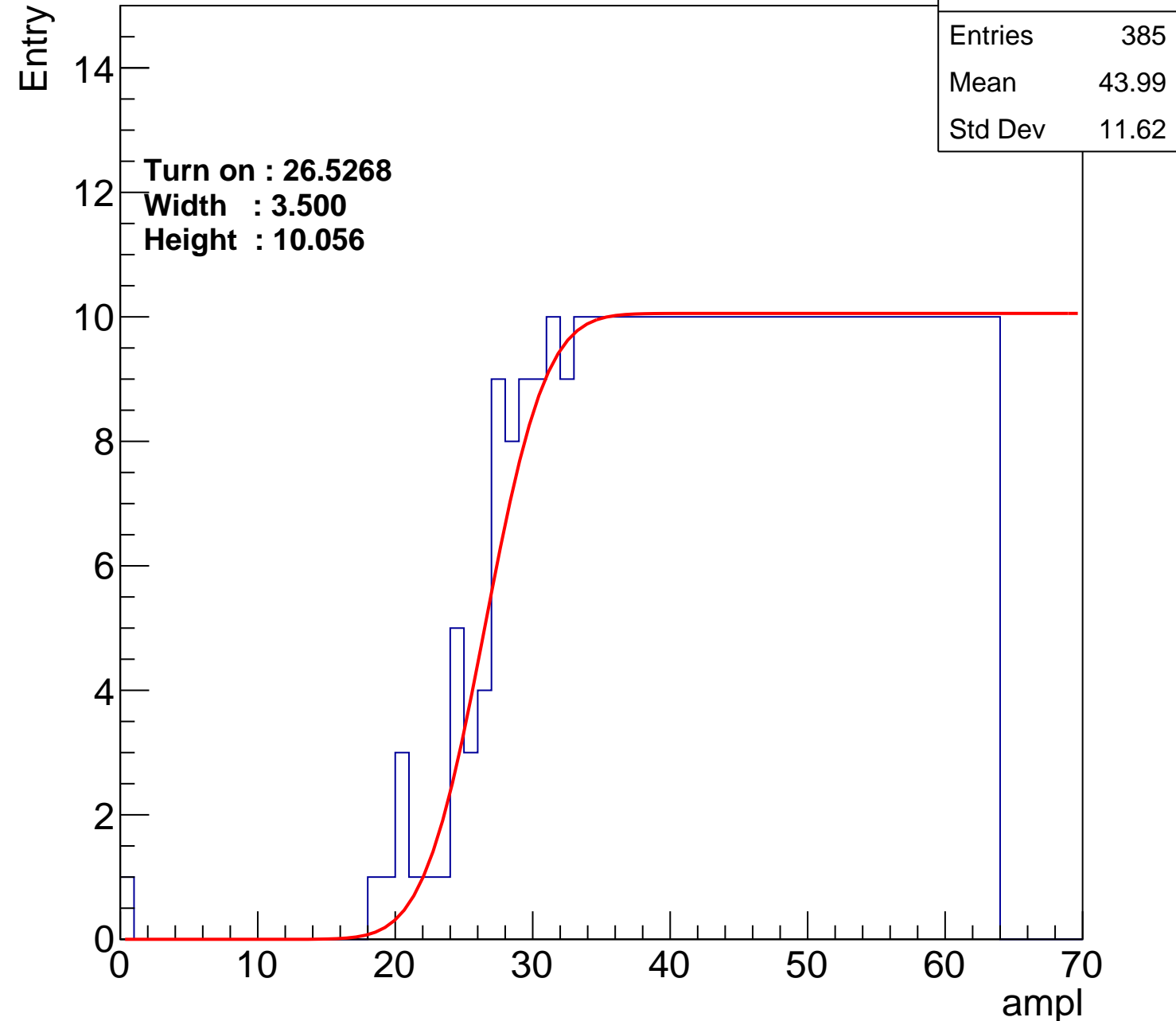
Width : 3.500

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 26.6915

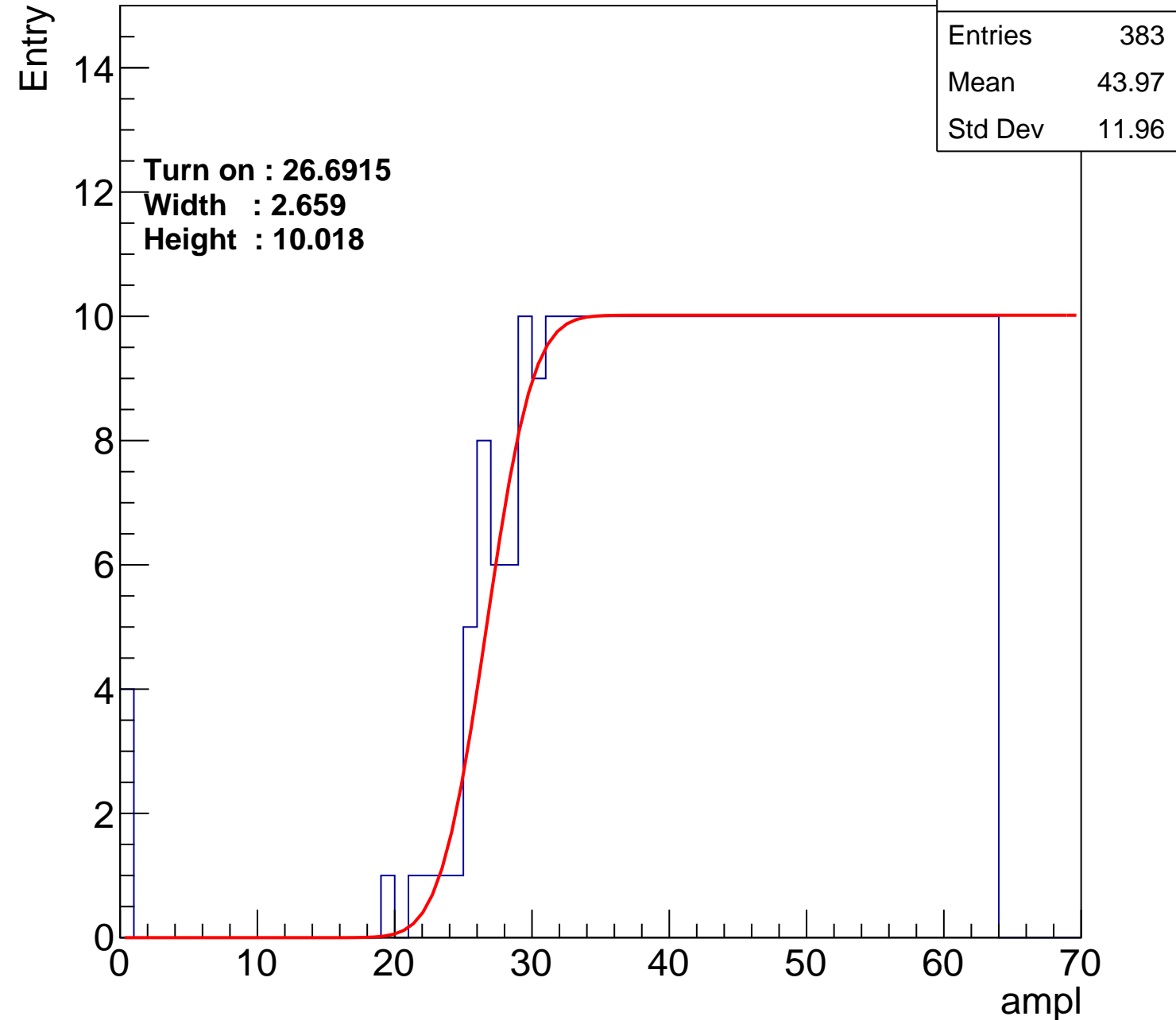
Width : 2.659

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch90

calib_packv5_042523_0143.root, FC#5, port B1

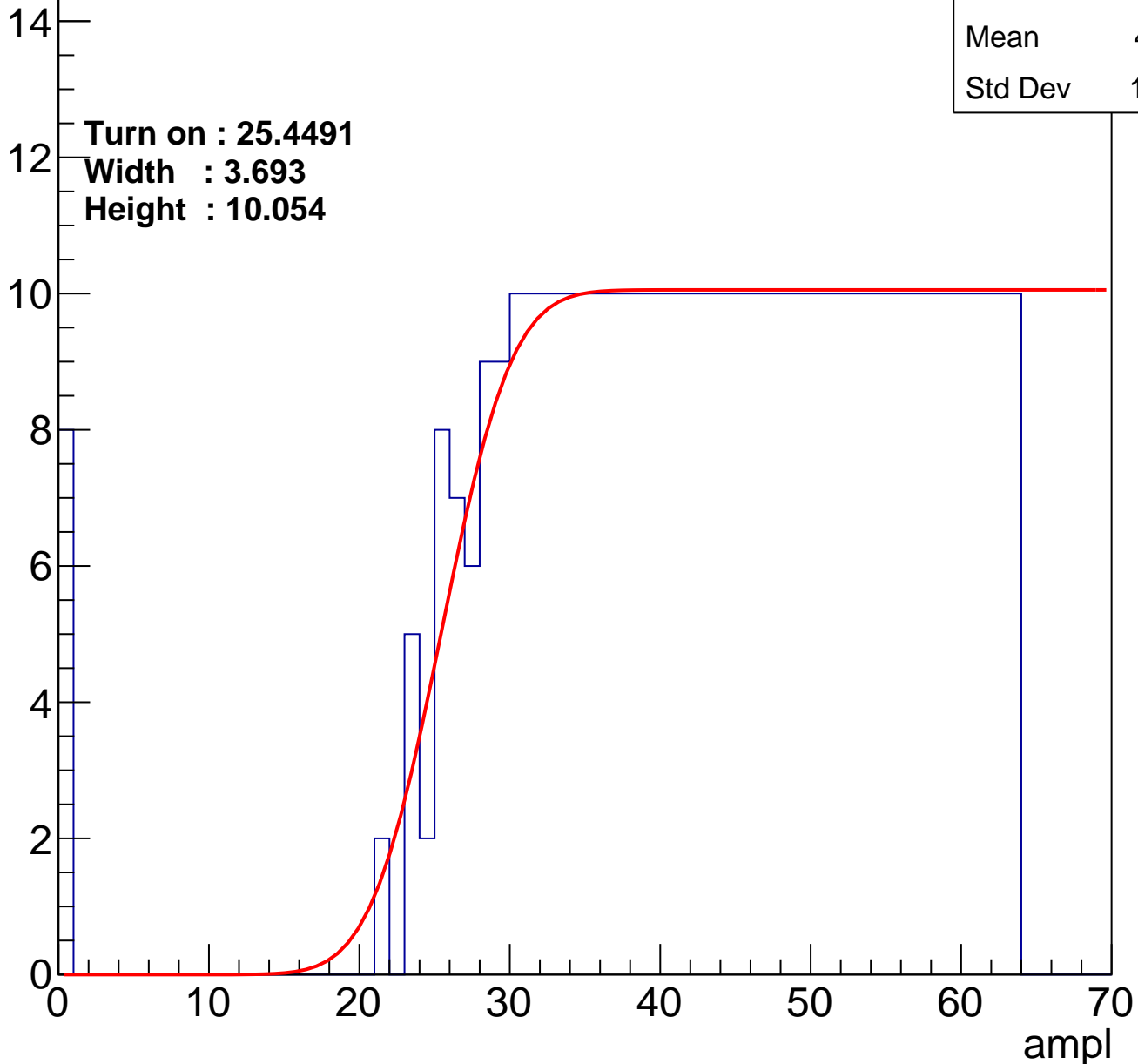
Entries	396
Mean	43.11
Std Dev	12.84

Turn on : 25.4491

Width : 3.693

Height : 10.054

Entry



B0L000S, U15-ch91

calib_packv5_042523_0143.root, FC#5, port B1

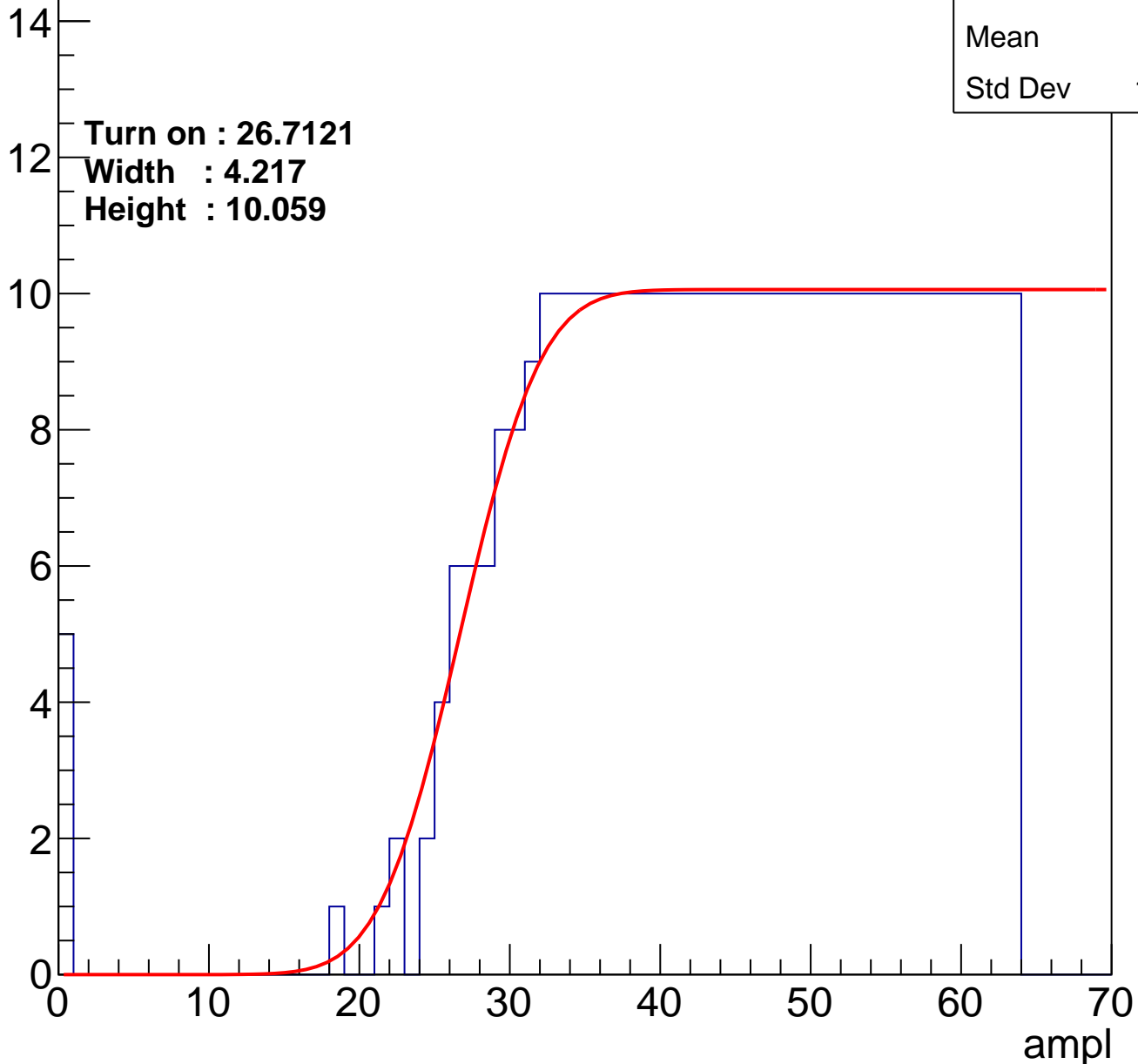
Entries	378
Mean	44.1
Std Dev	12.11

Turn on : 26.7121

Width : 4.217

Height : 10.059

Entry



B0L000S, U15-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.28
Std Dev	11.55

Turn on : 26.7838

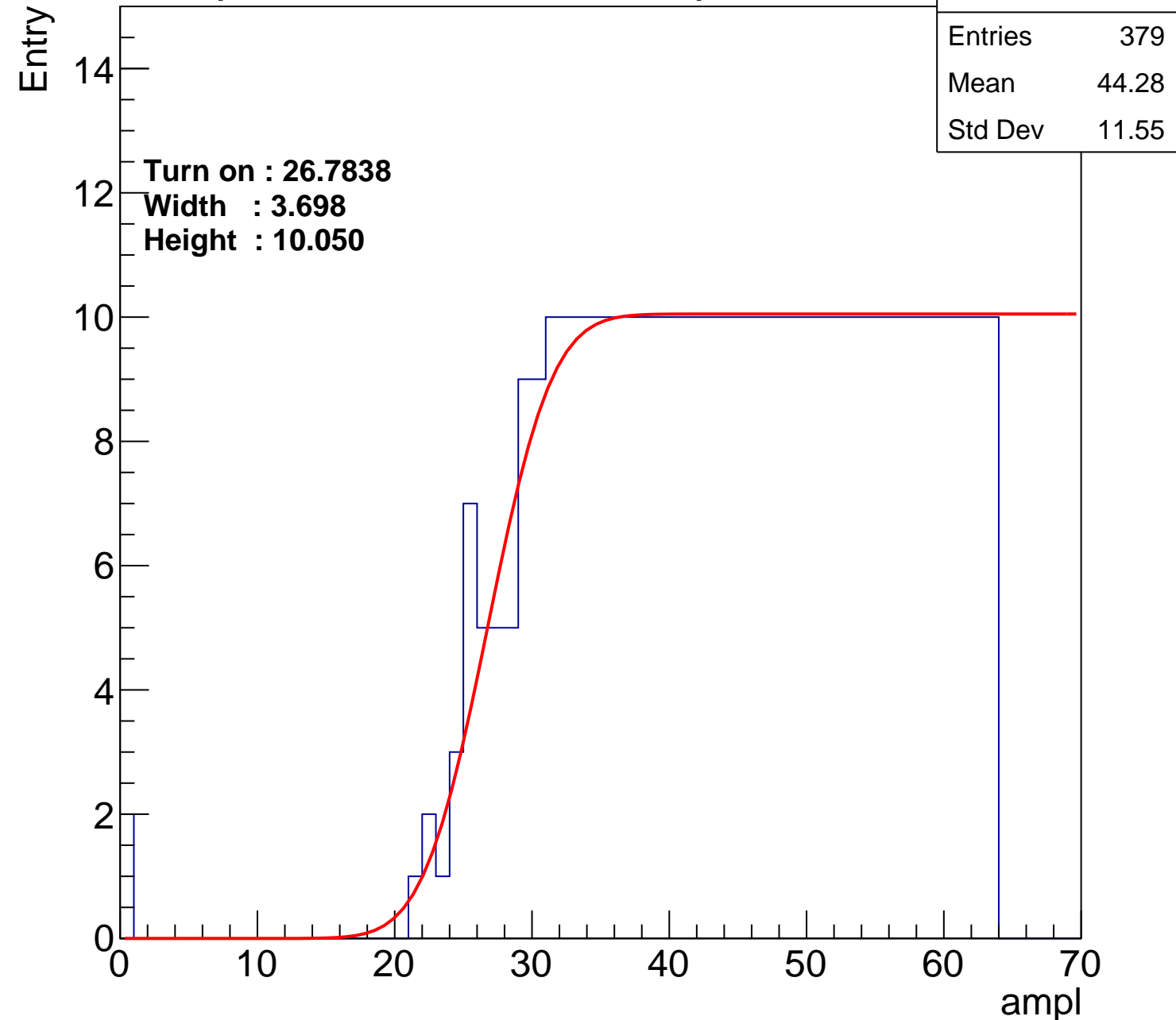
Width : 3.698

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.3245
Width : 3.073
Height : 10.040



B0L000S, U15-ch94

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.6213

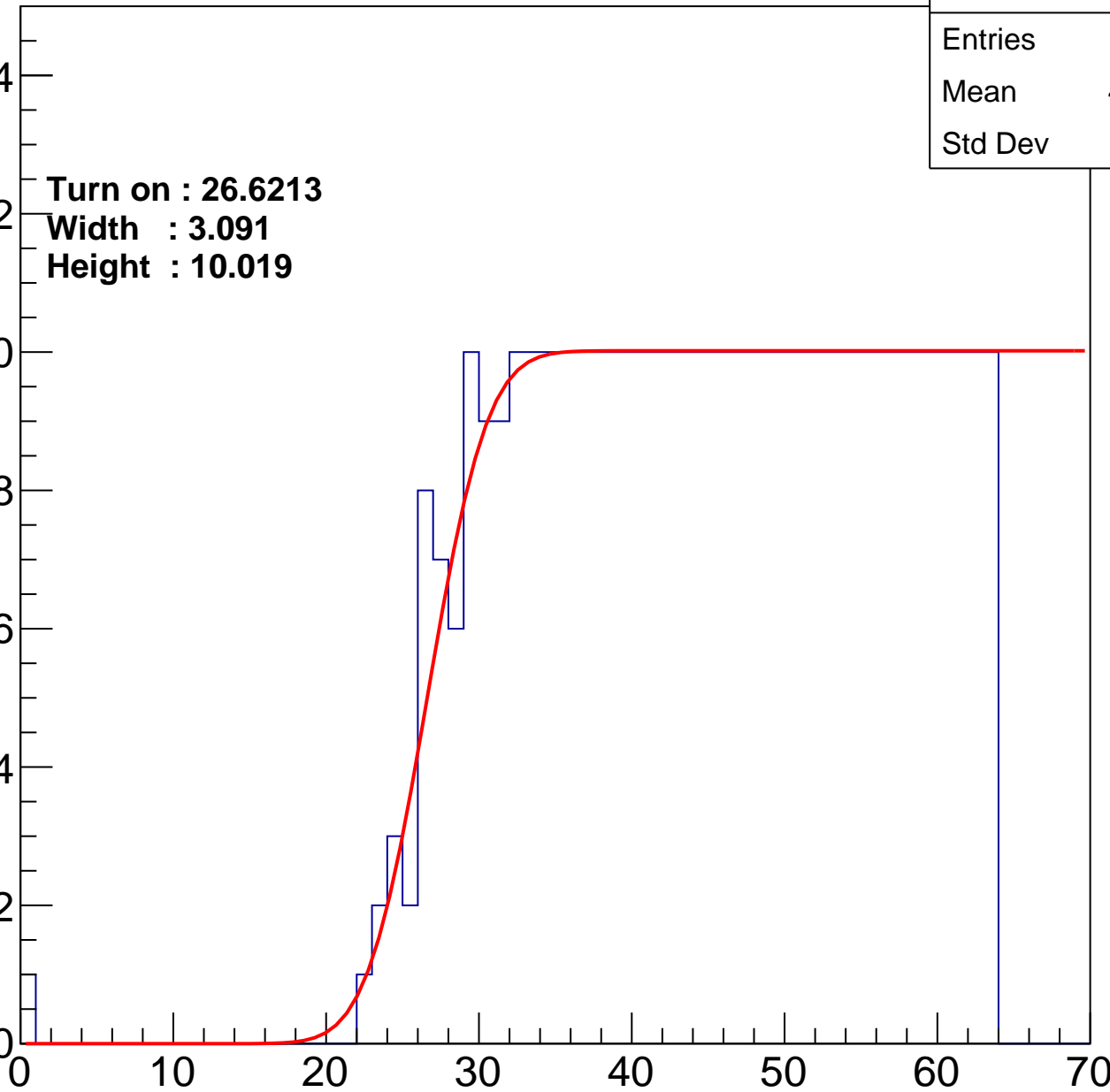
Width : 3.091

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.68
Std Dev	12.34

Turn on : 26.4947

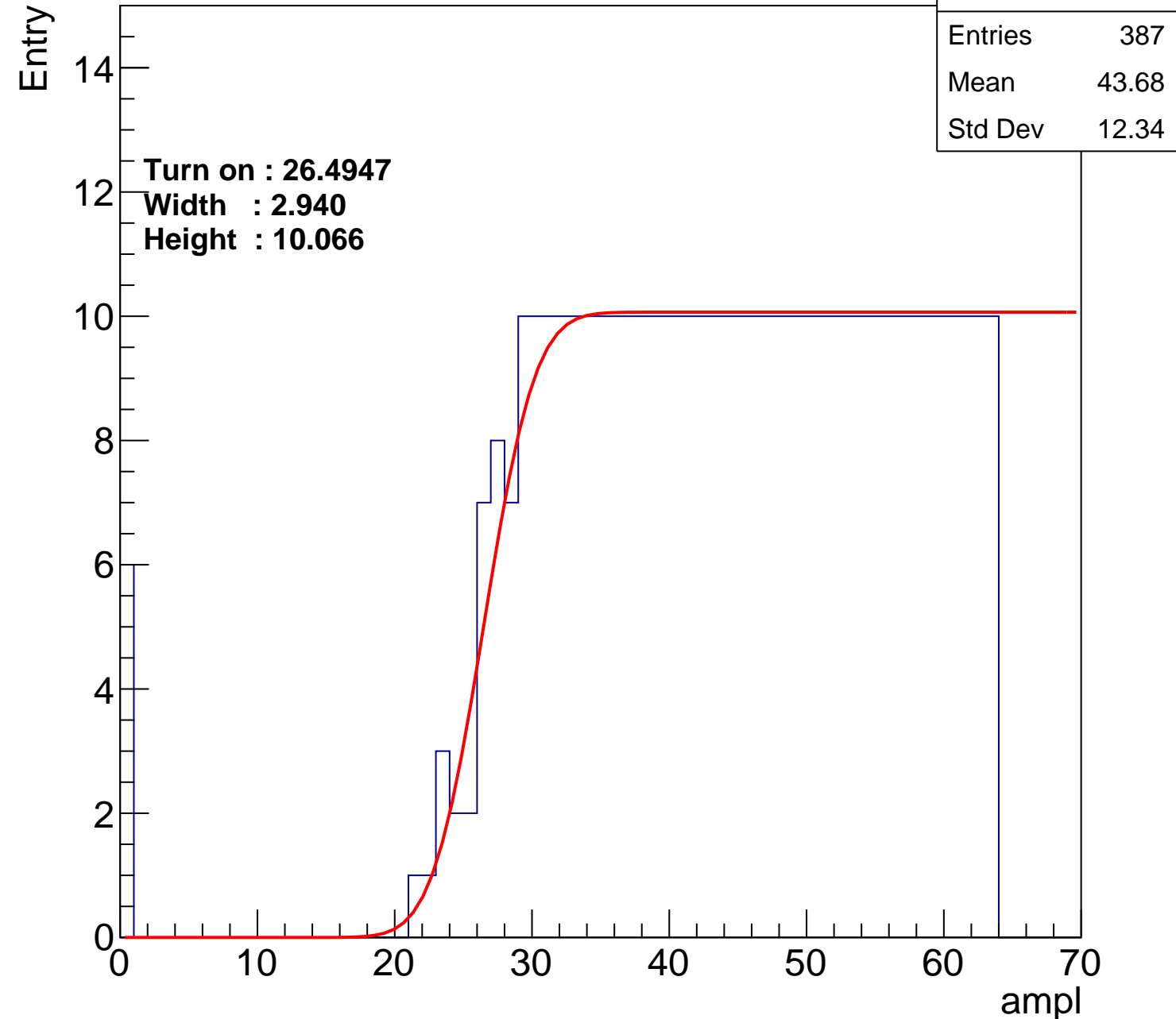
Width : 2.940

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	396
Mean	43.3
Std Dev	12.51

Turn on : 25.6173

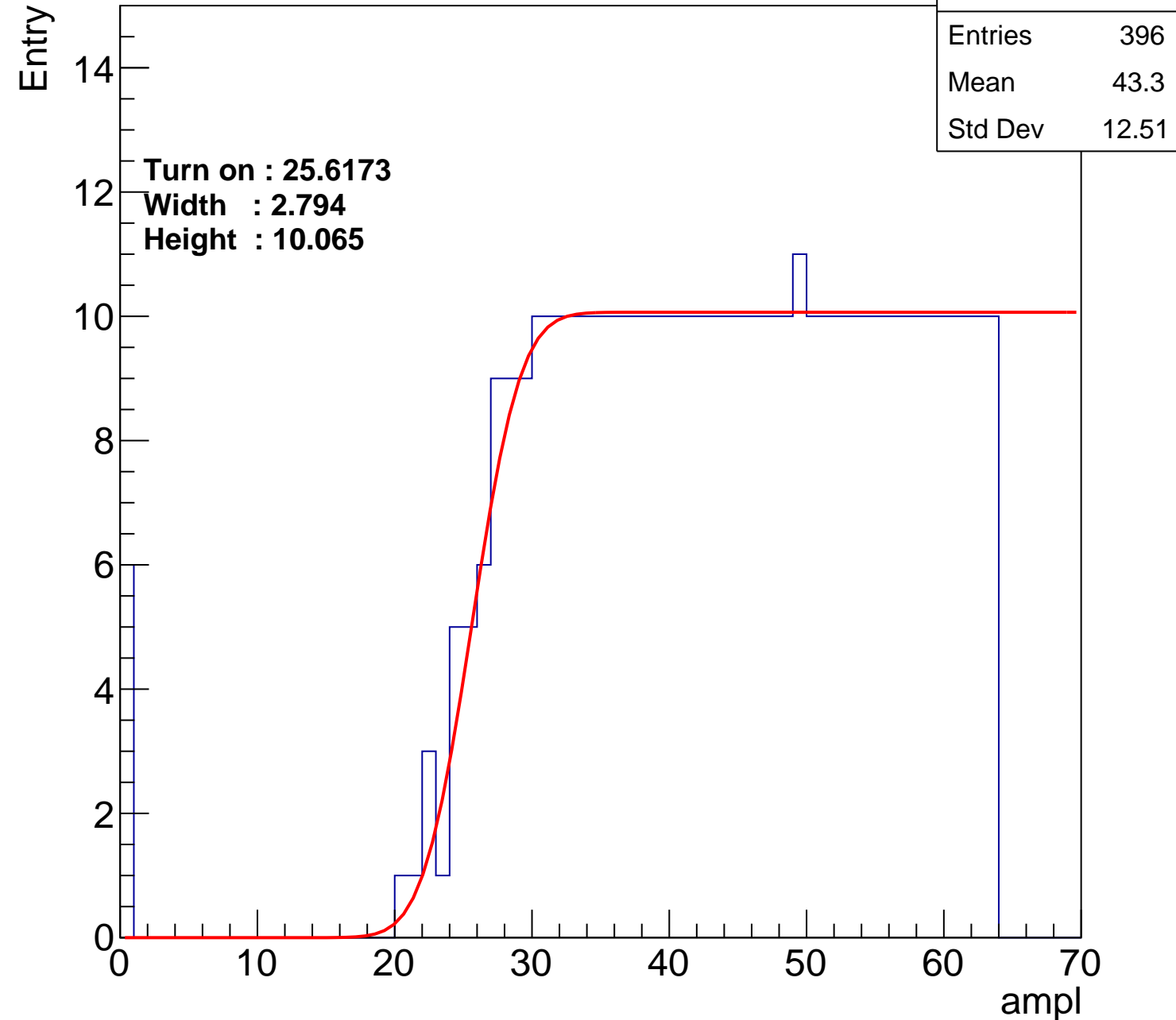
Width : 2.794

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.19
Std Dev	12.15

Turn on : 27.6126

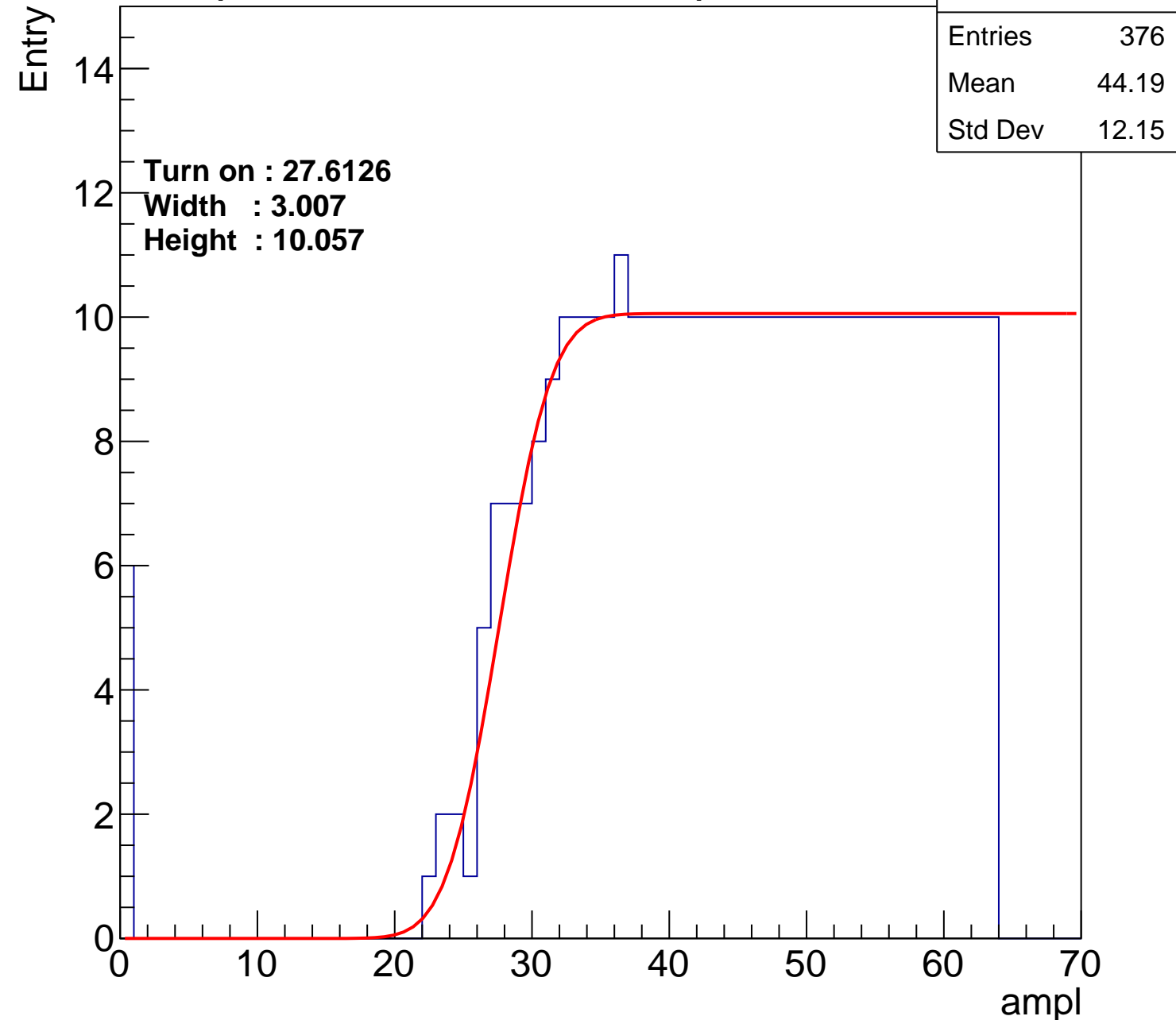
Width : 3.007

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch98

calib_packv5_042523_0143.root, FC#5, port B1

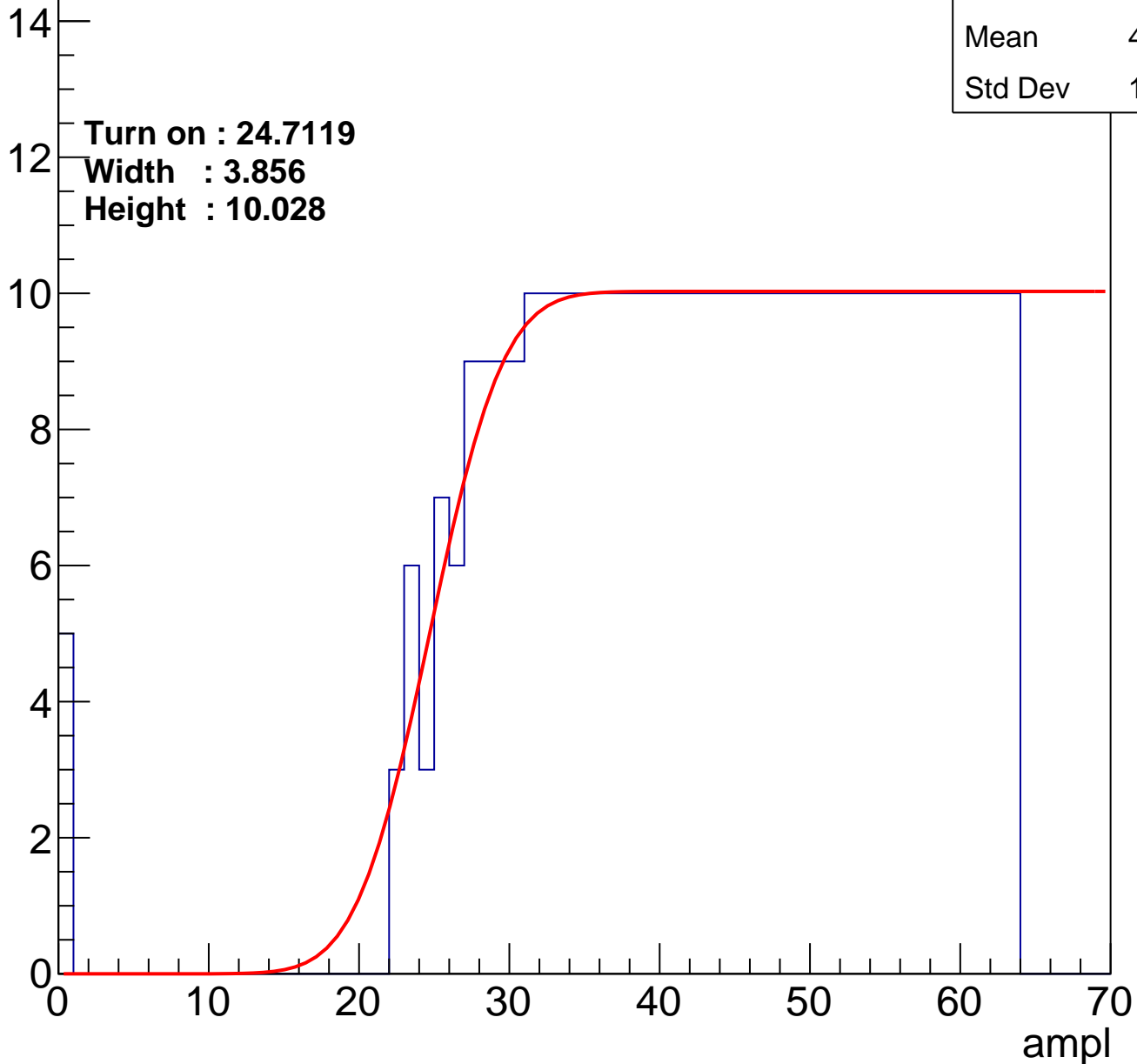
Entries	396
Mean	43.29
Std Dev	12.39

Turn on : 24.7119

Width : 3.856

Height : 10.028

Entry



B0L000S, U15-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 26.0844

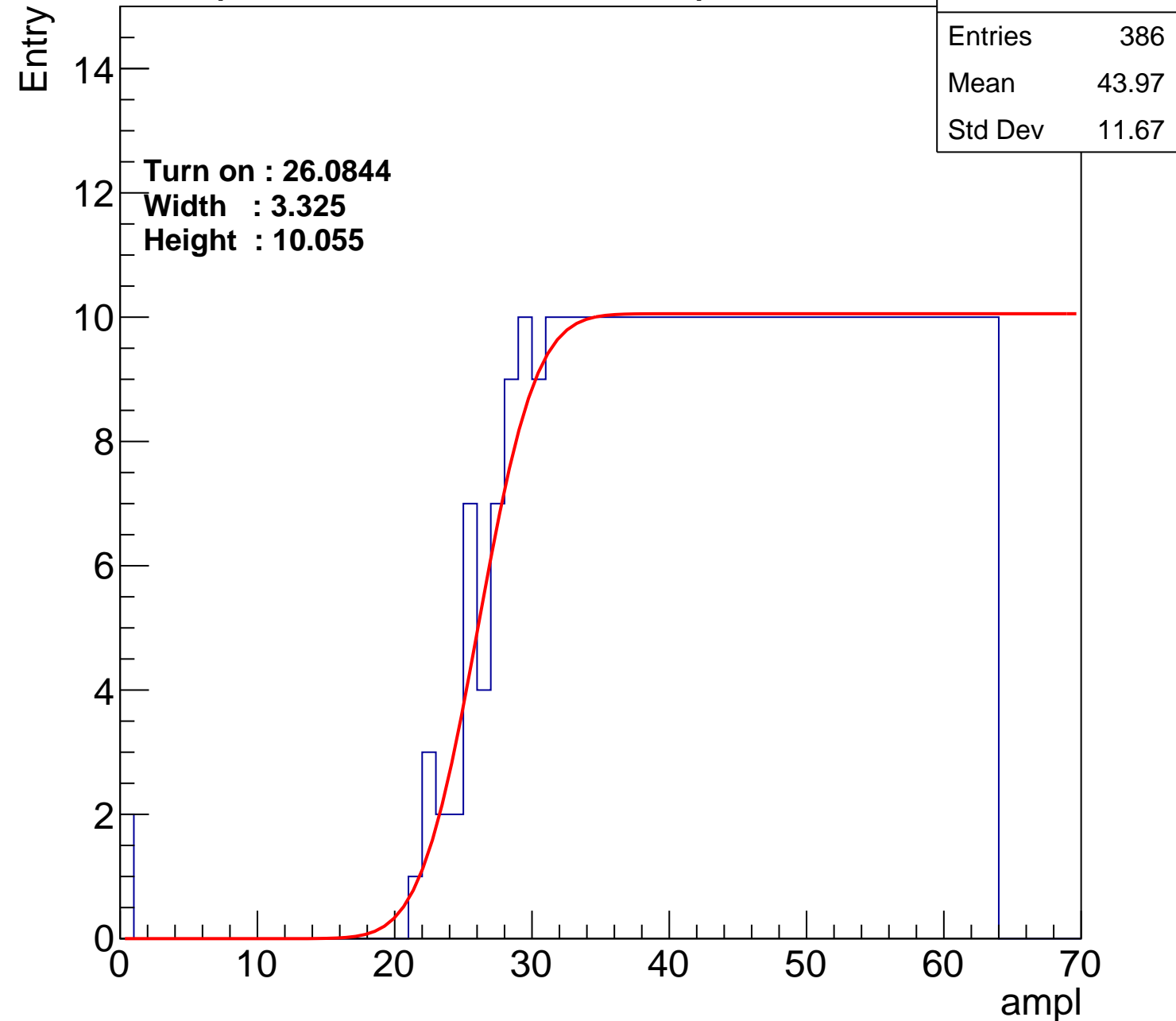
Width : 3.325

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch100

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	43.91
Std Dev	12.13

Turn on : 26.0821

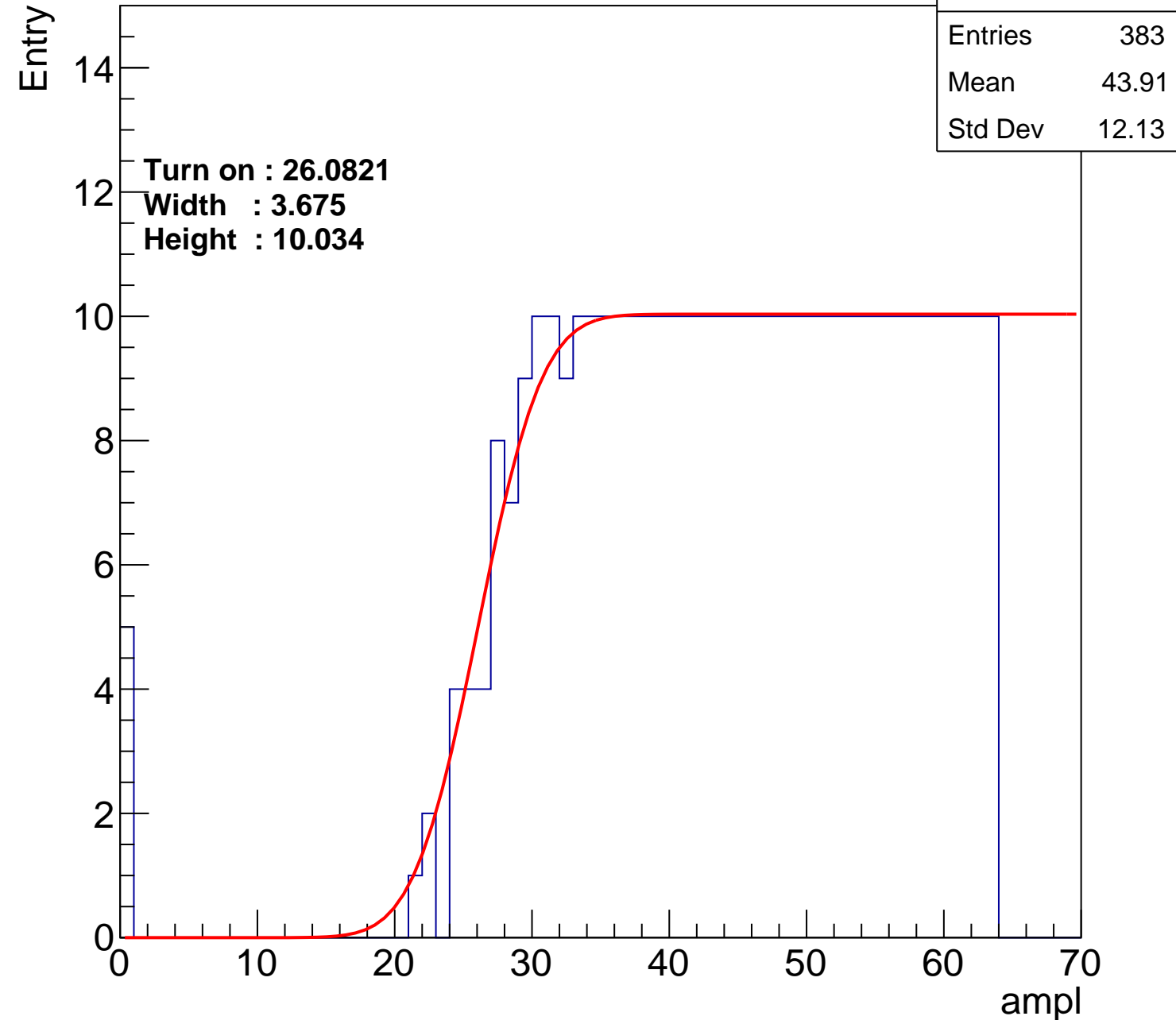
Width : 3.675

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch101

calib_packv5_042523_0143.root, FC#5, port B1

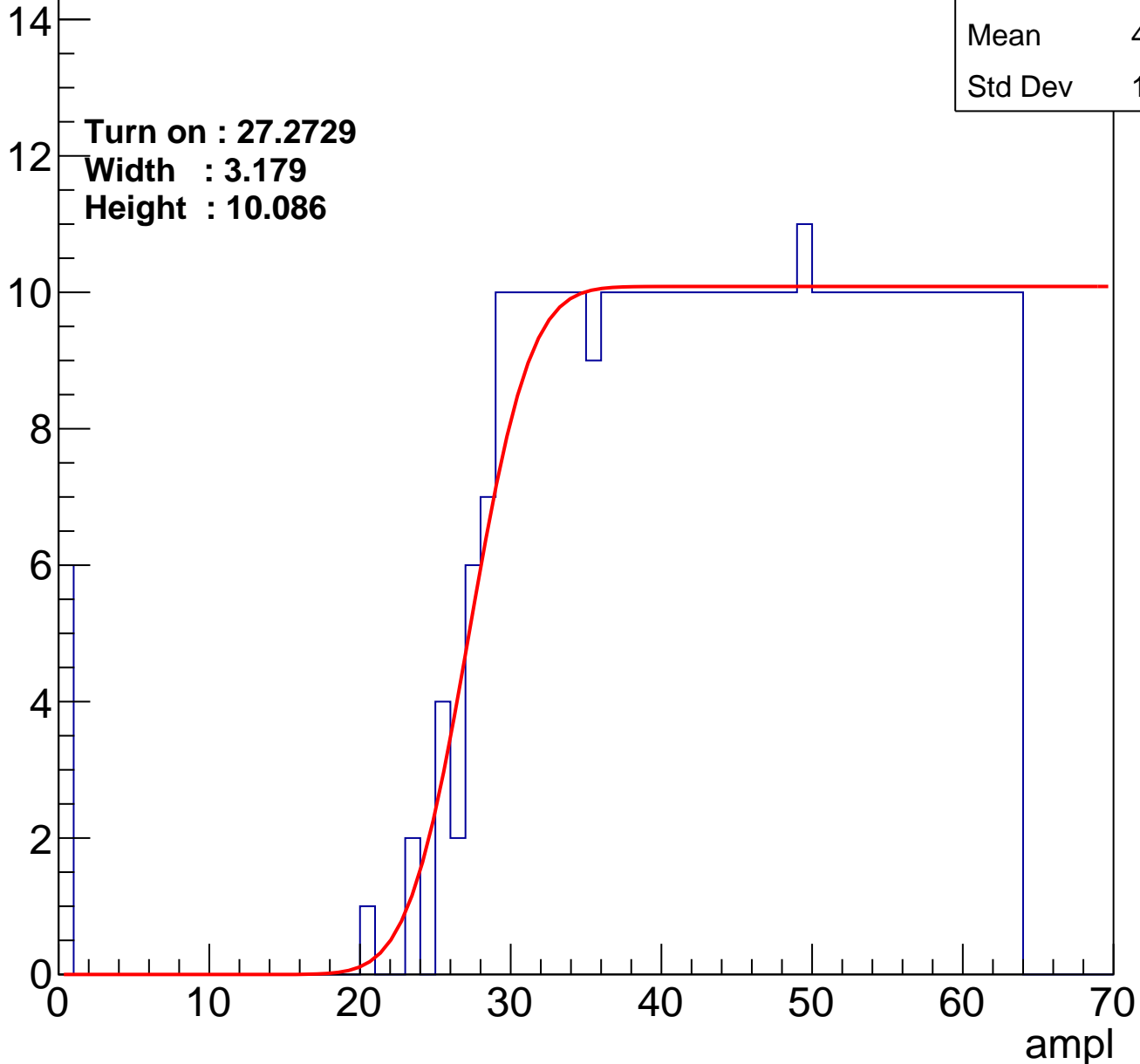
Entries	378
Mean	44.15
Std Dev	12.14

Turn on : 27.2729

Width : 3.179

Height : 10.086

Entry



B0L000S, U15-ch102

calib_packv5_042523_0143.root, FC#5, port B1

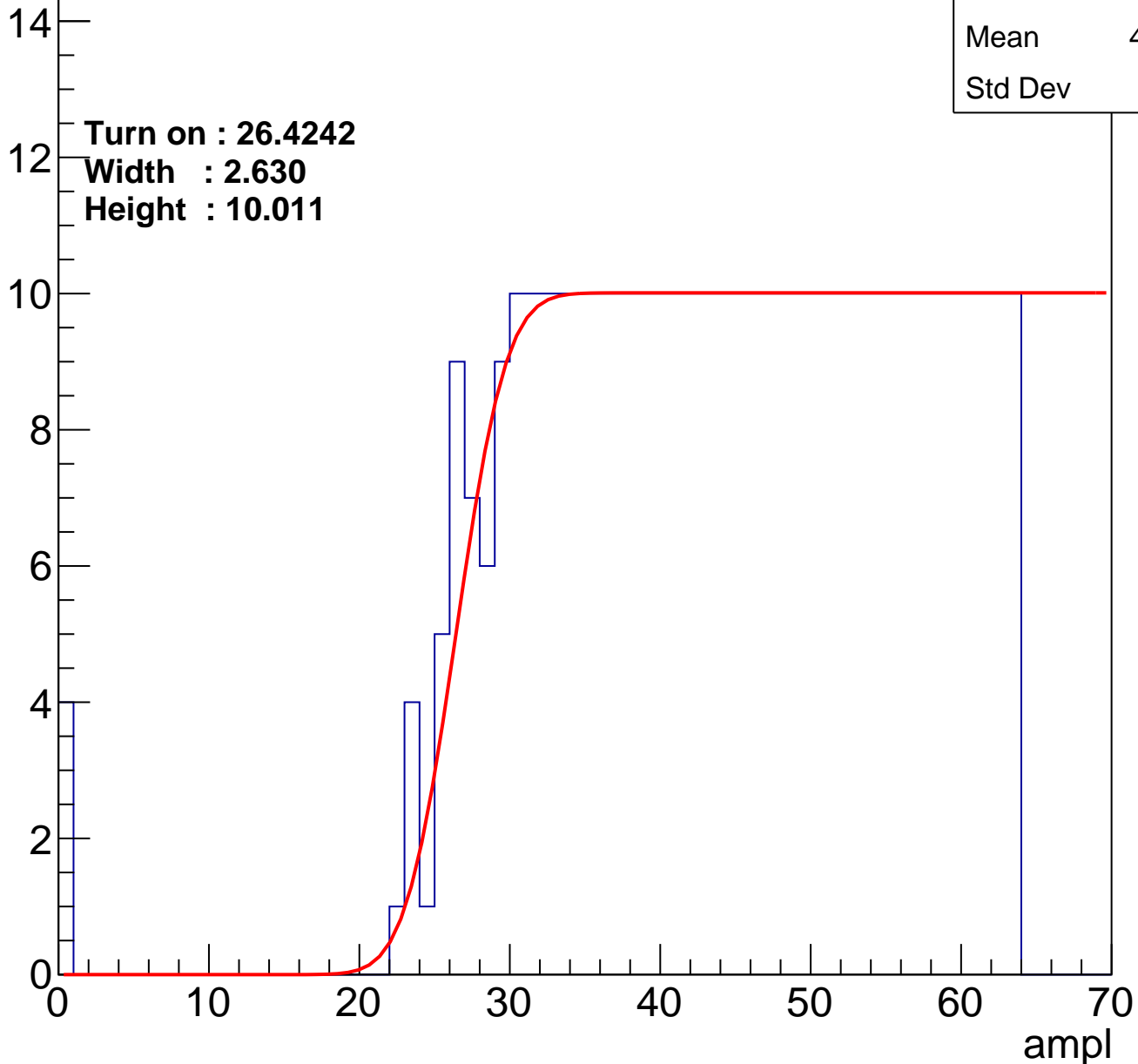
Entries	386
Mean	43.85
Std Dev	12

Turn on : 26.4242

Width : 2.630

Height : 10.011

Entry



B0L000S, U15-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.14
Std Dev	12.03

Turn on : 26.2734

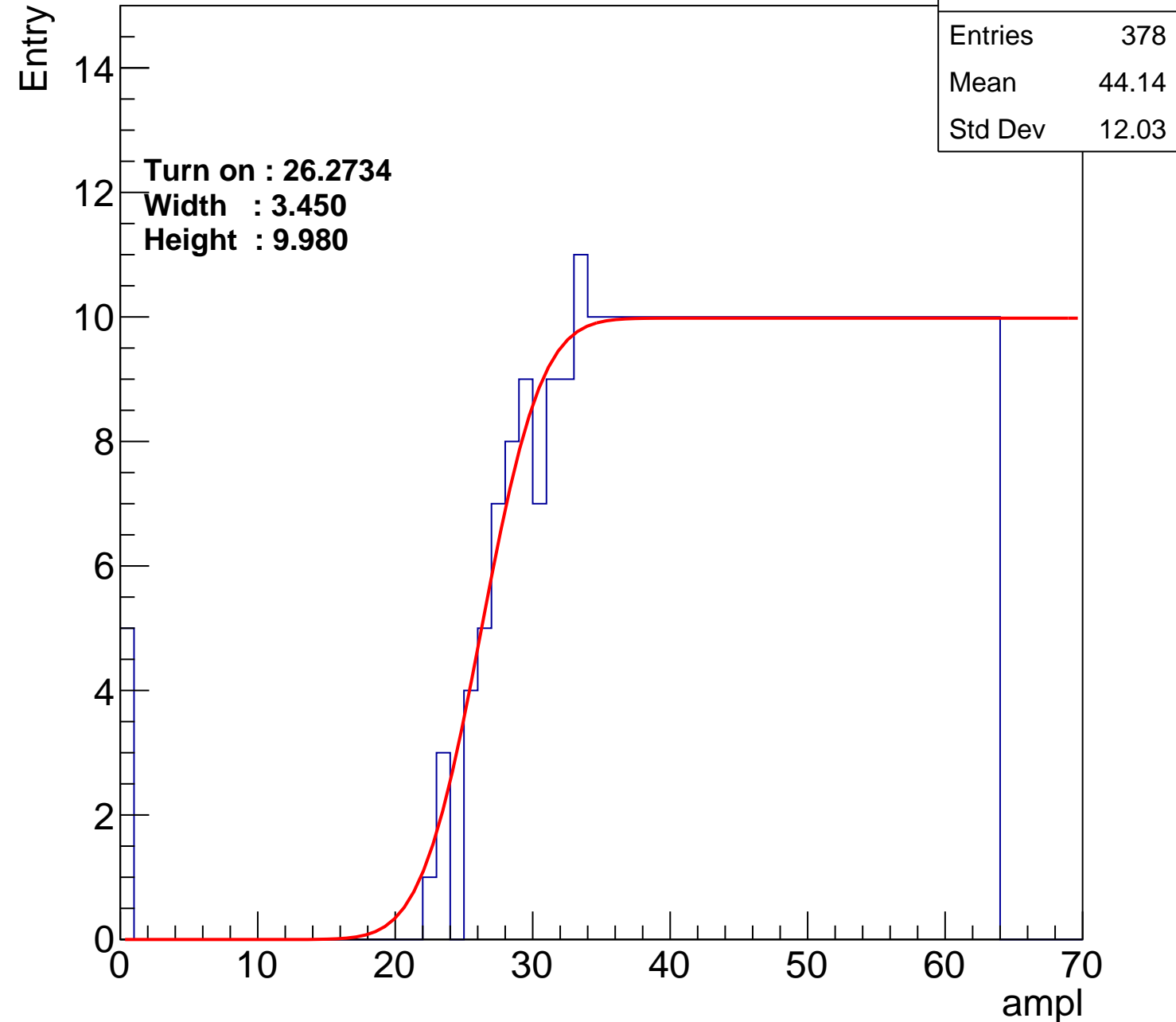
Width : 3.450

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.46
Std Dev	11.46

Turn on : 26.2856

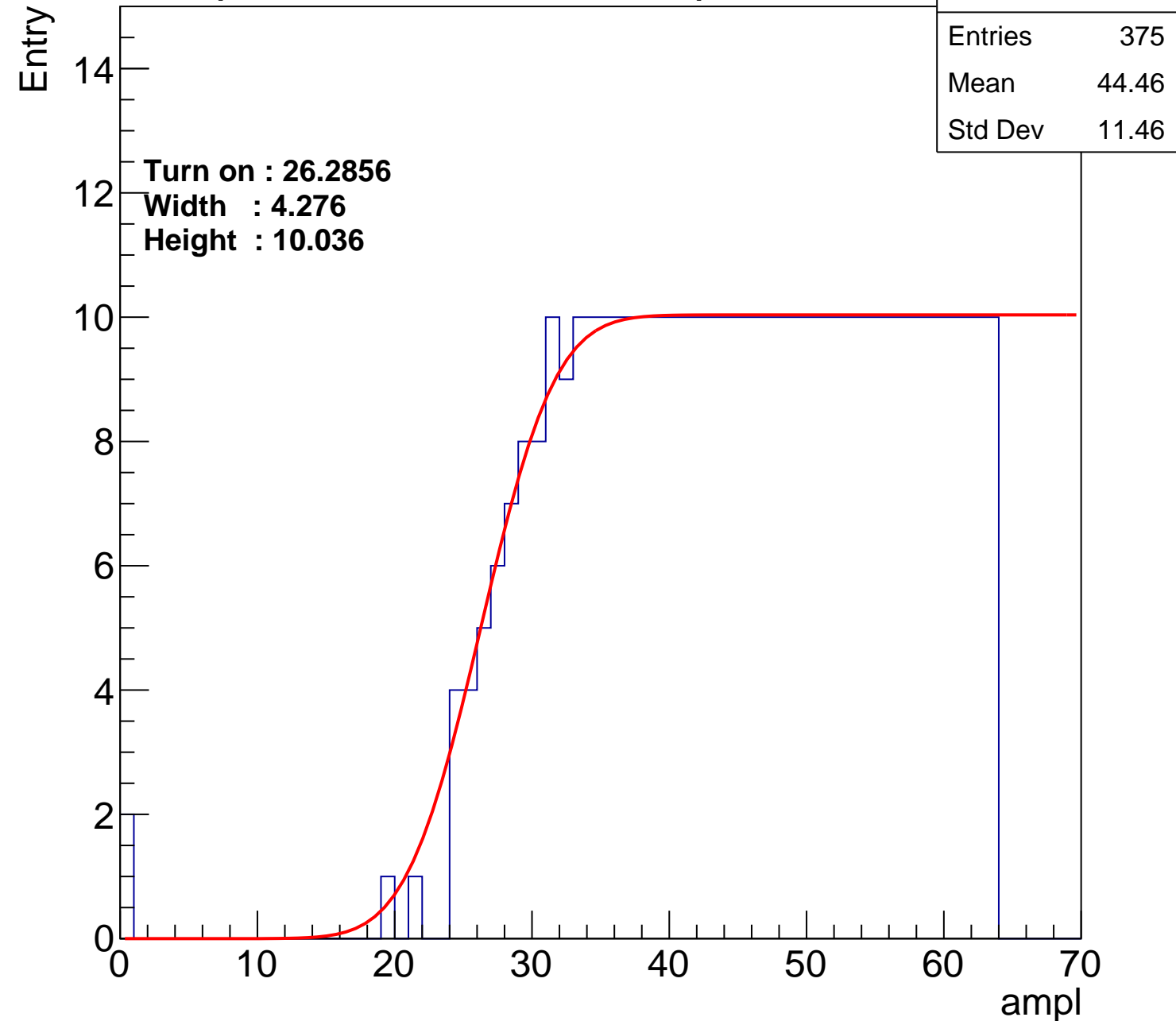
Width : 4.276

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	398
Mean	43.33
Std Dev	12.11

Turn on : 25.2040

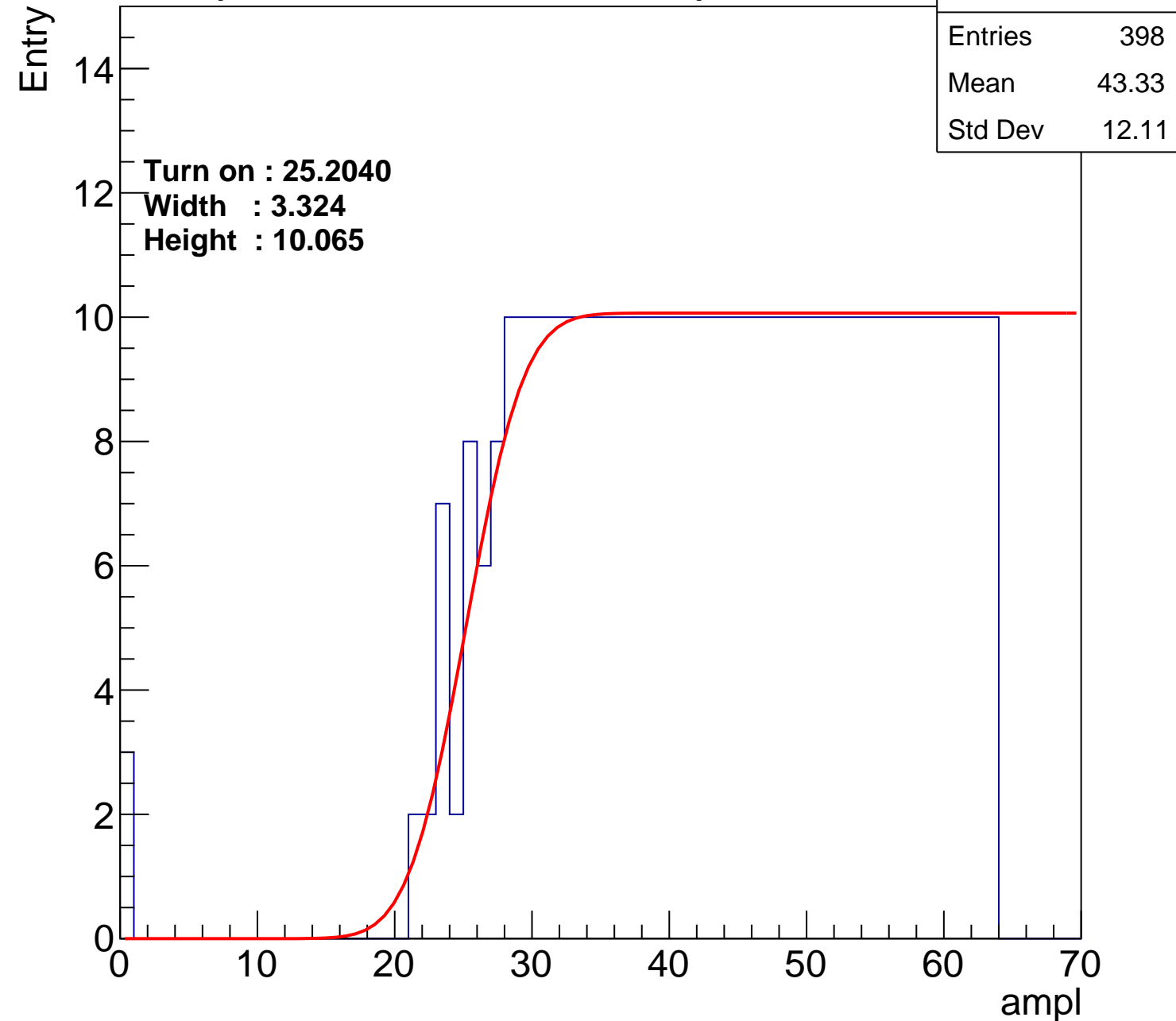
Width : 3.324

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.3512

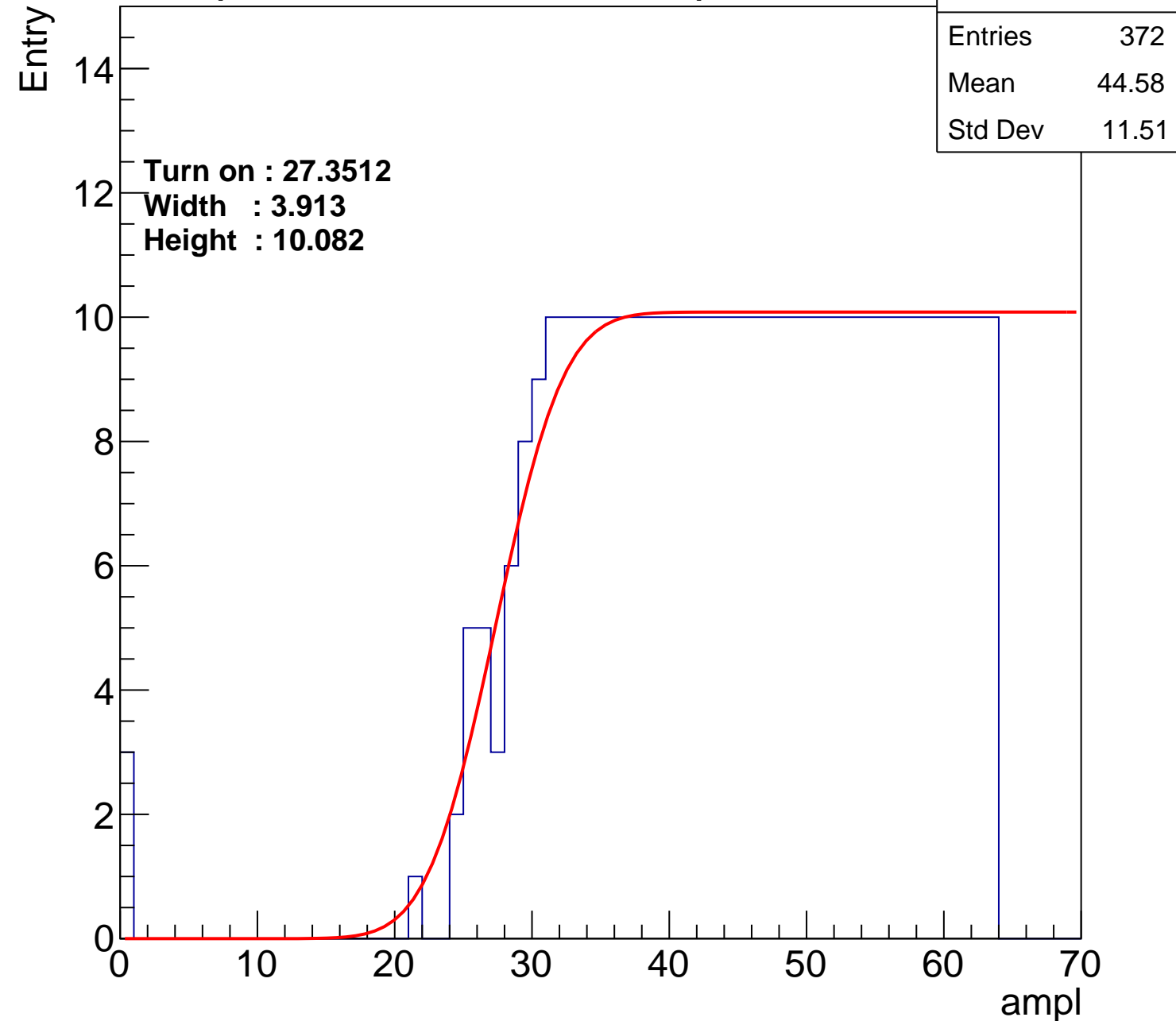
Width : 3.913

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.52
Std Dev	12.42

Turn on : 25.0299

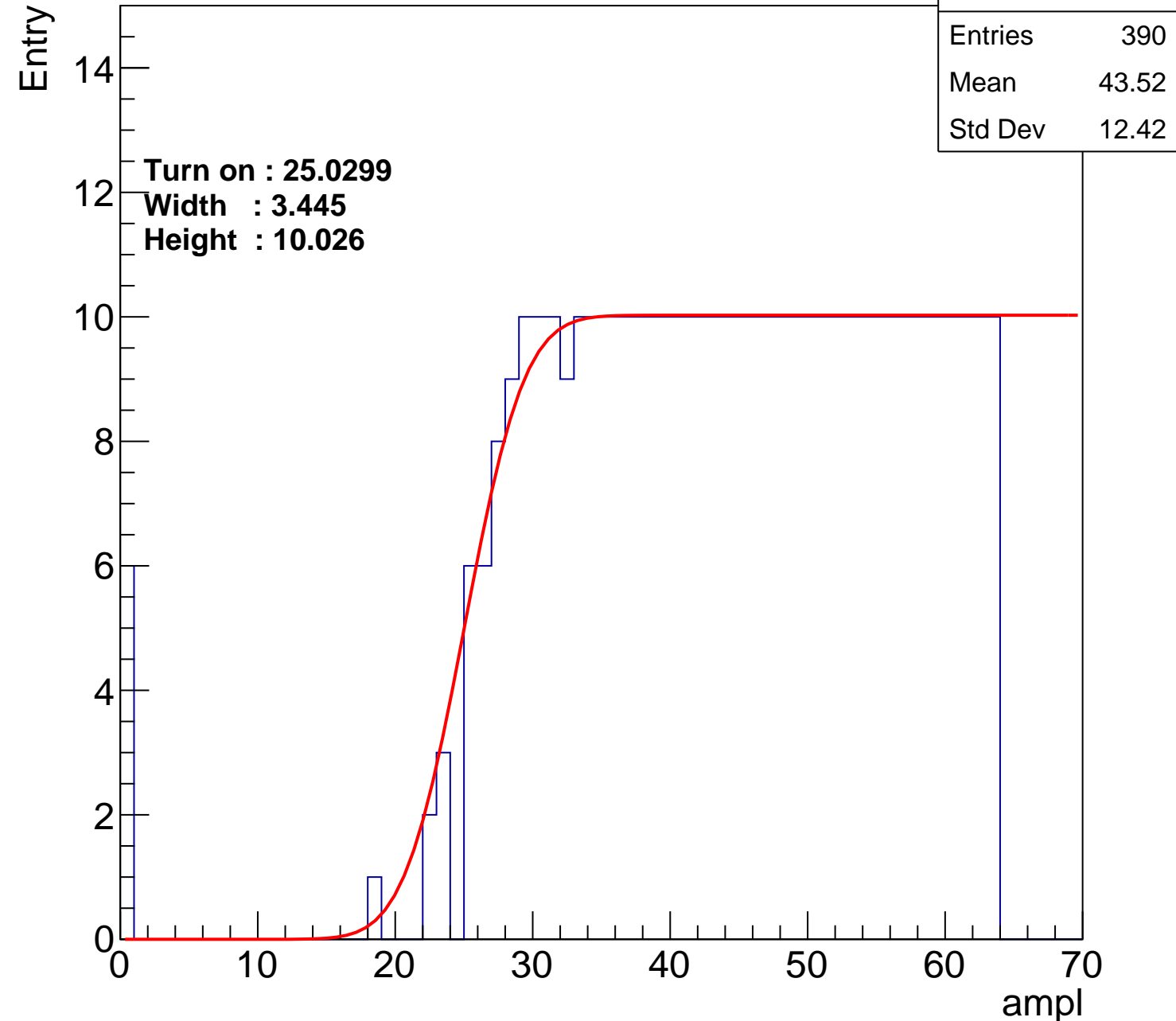
Width : 3.445

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.19
Std Dev	11.89

Turn on : 26.9754

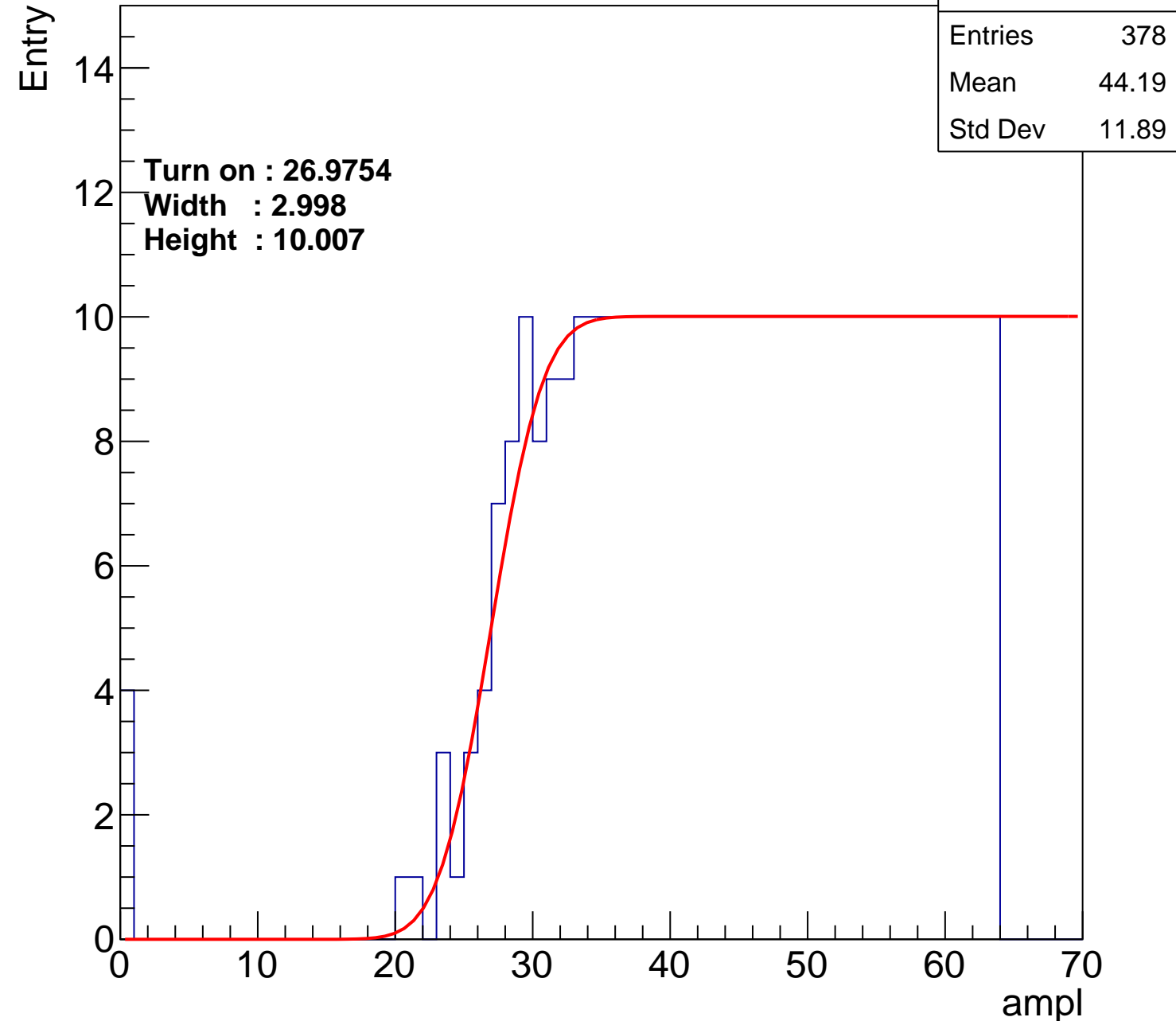
Width : 2.998

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 27.3242

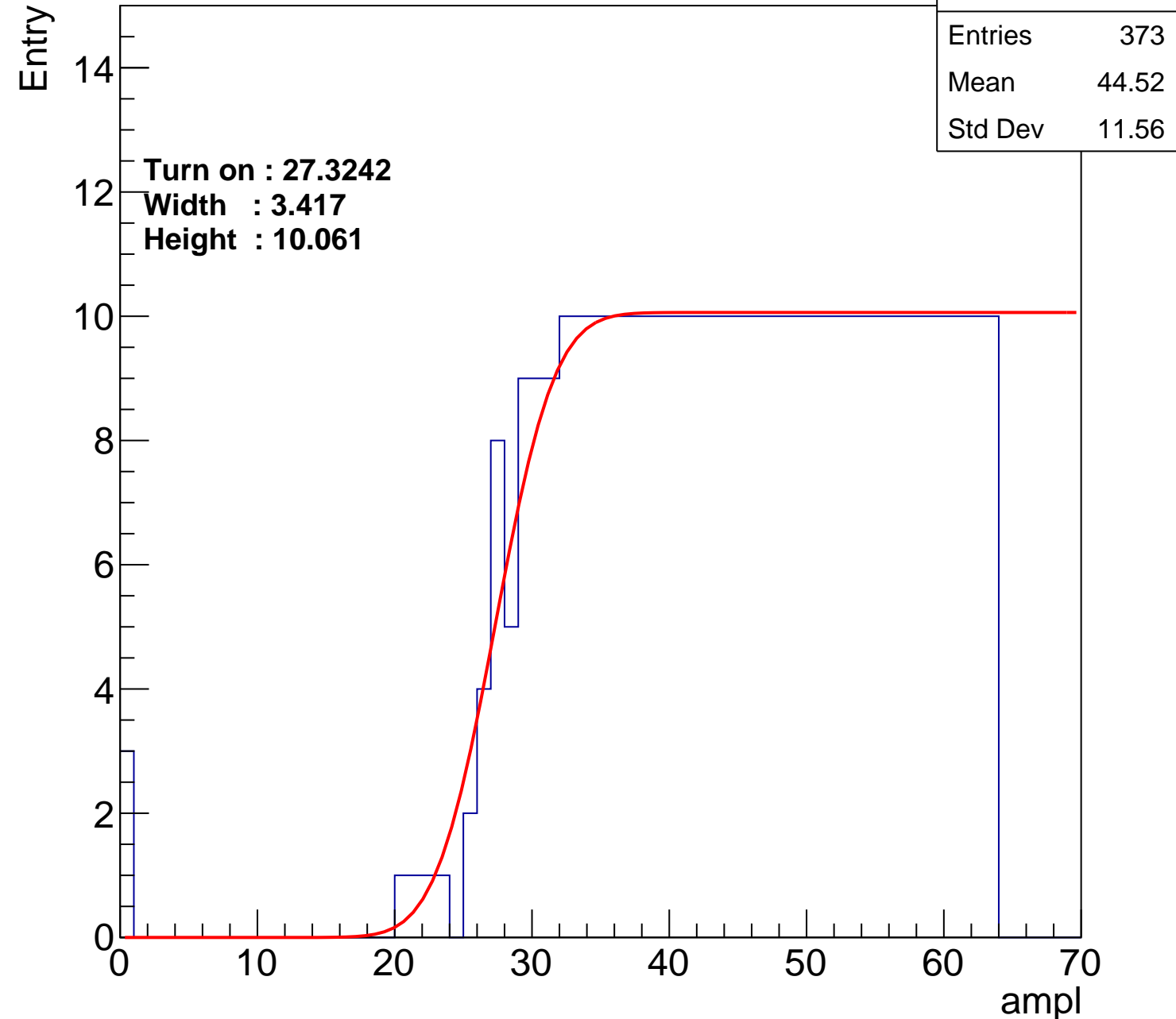
Width : 3.417

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 25.6591

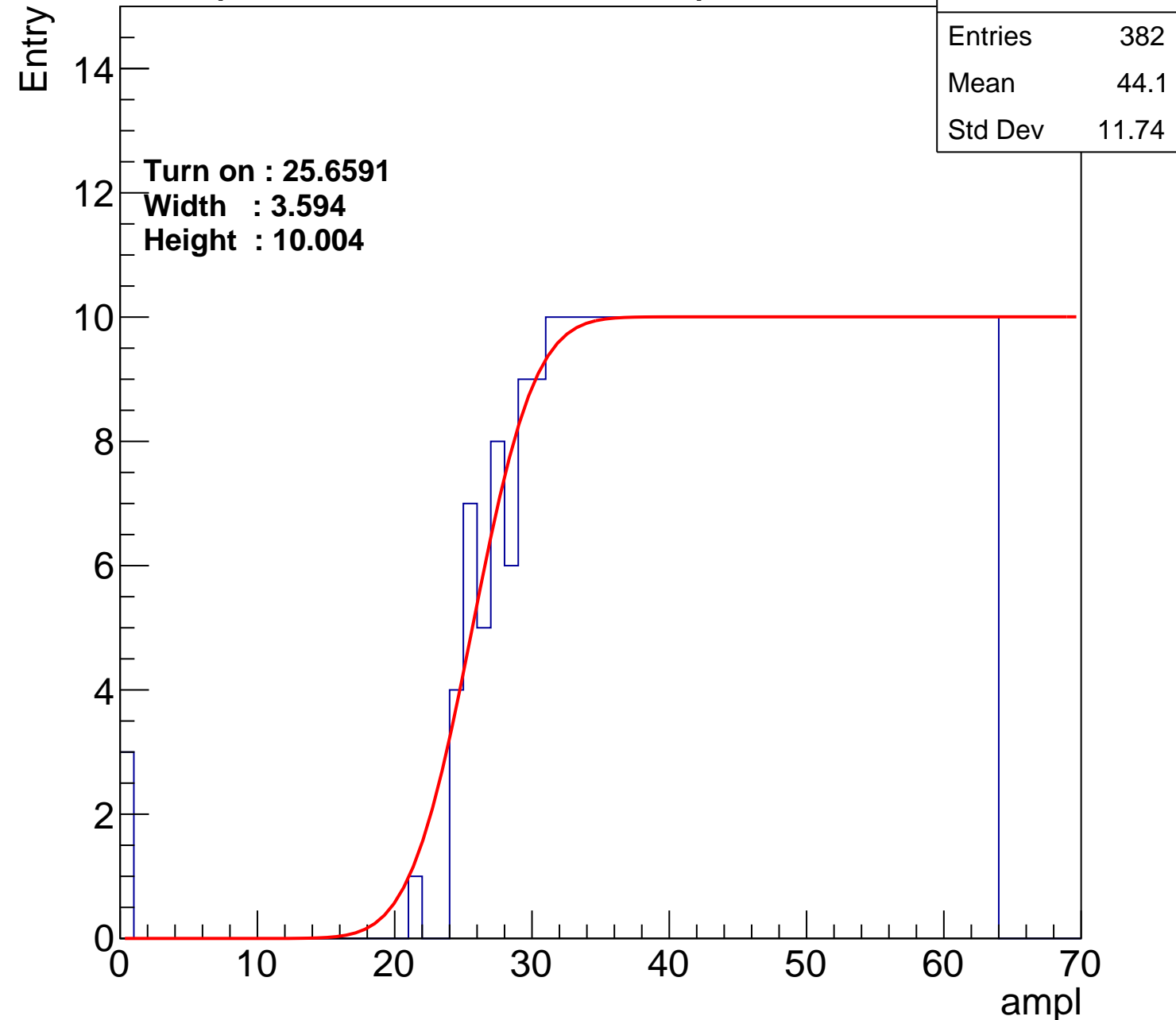
Width : 3.594

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.2576

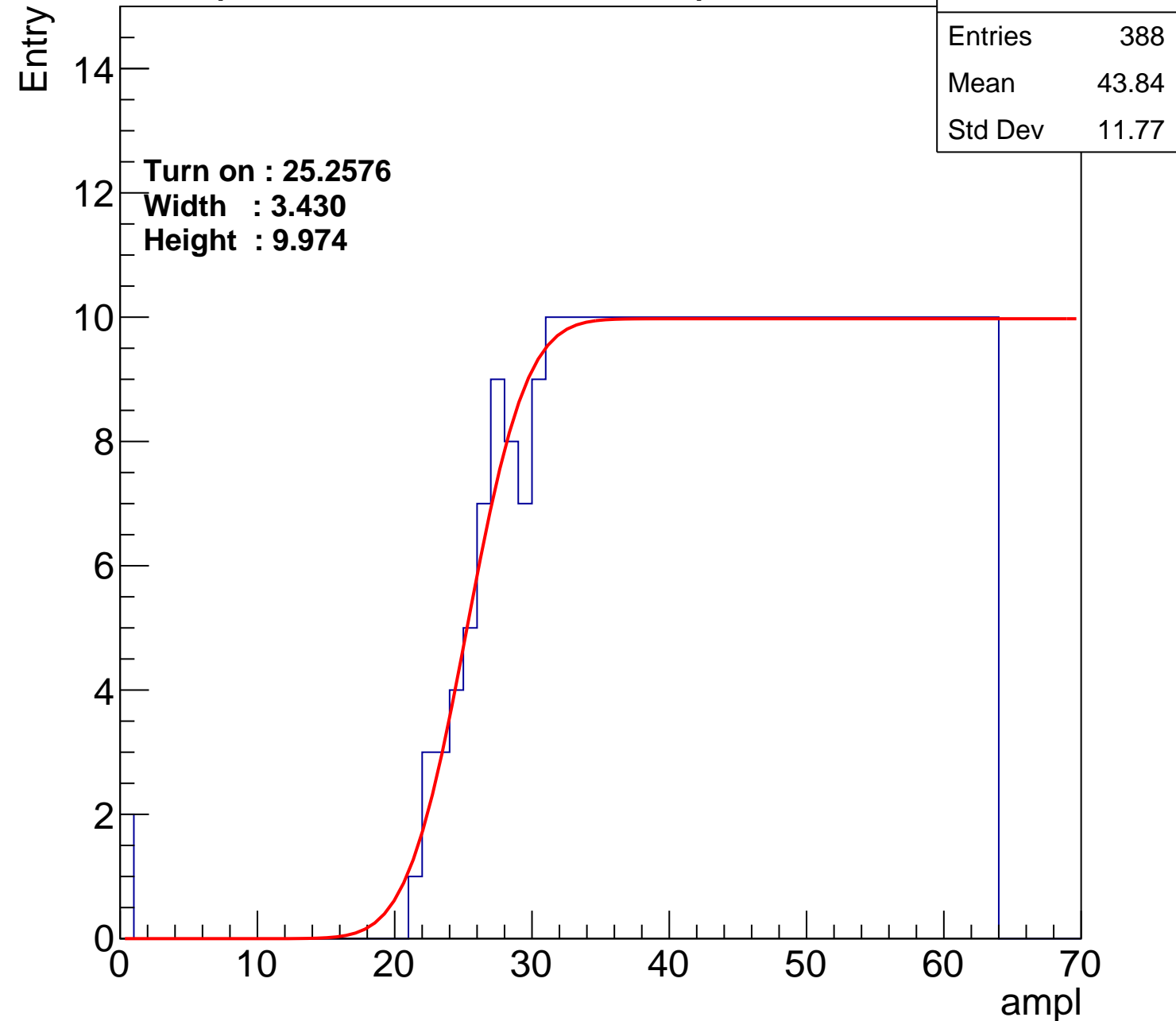
Width : 3.430

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.34
Std Dev	11.54

Turn on : 26.7622

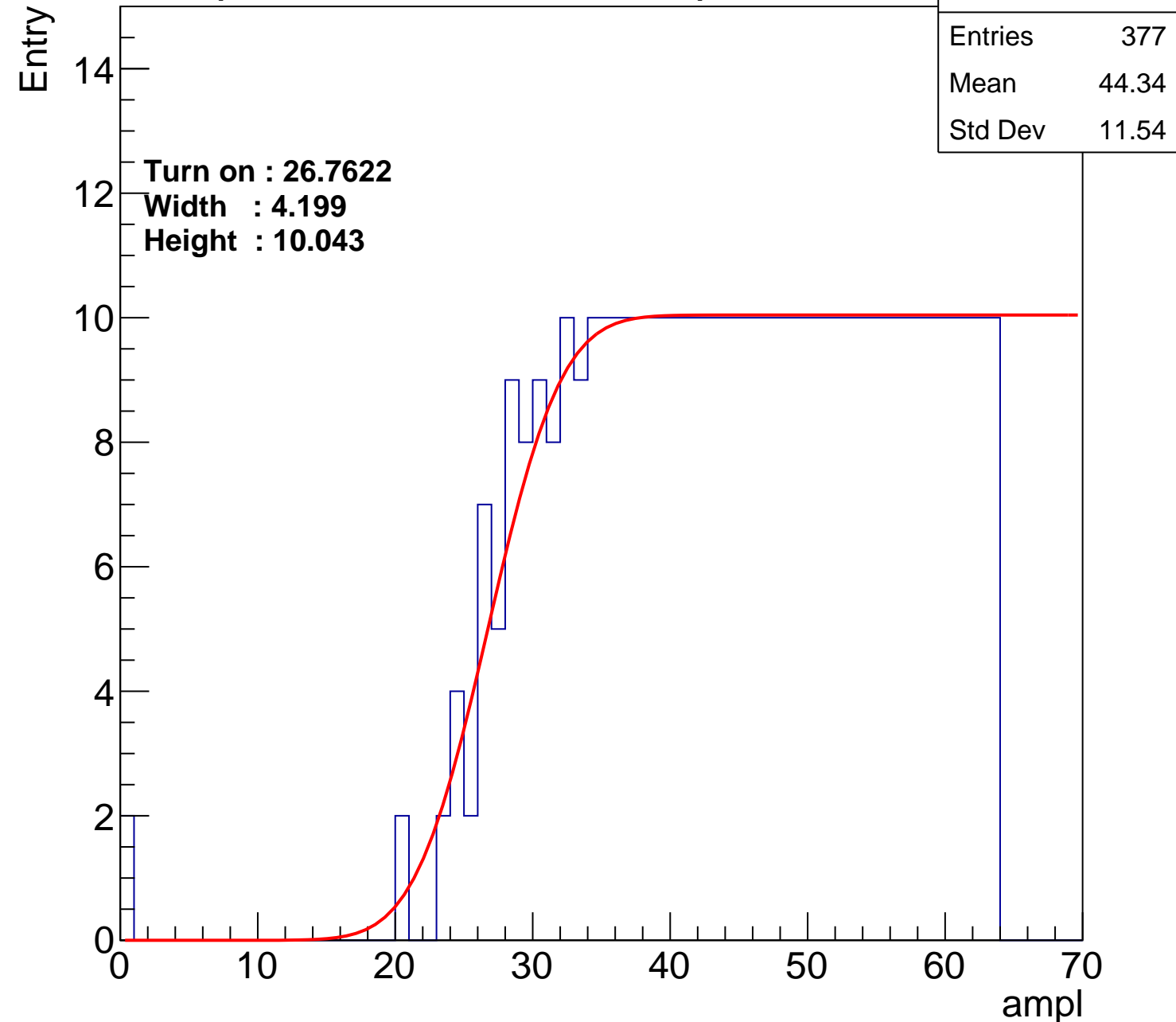
Width : 4.199

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	400
Mean	43.36
Std Dev	11.98

Turn on : 24.3968

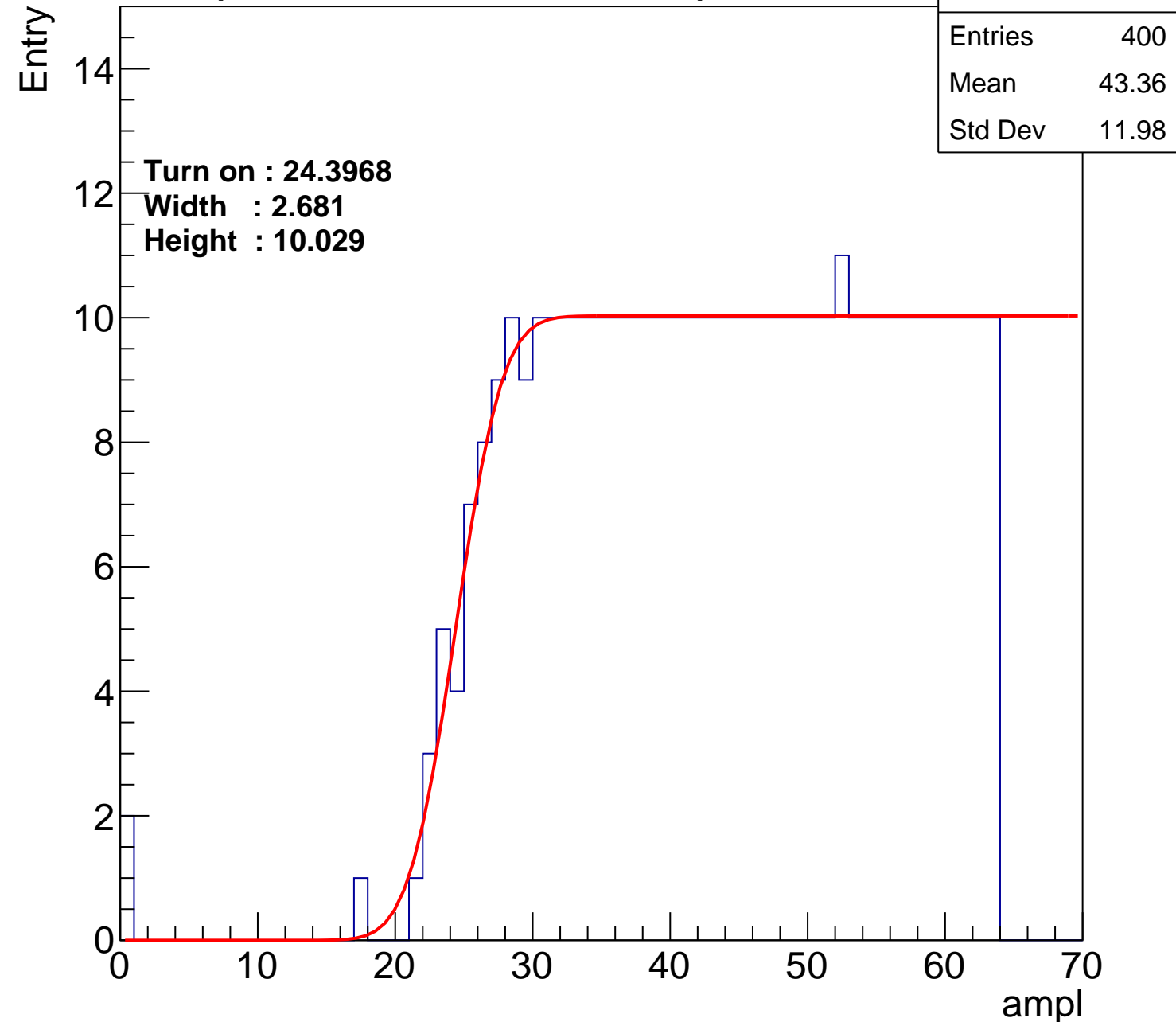
Width : 2.681

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.94
Std Dev	11.1

Turn on : 28.0164

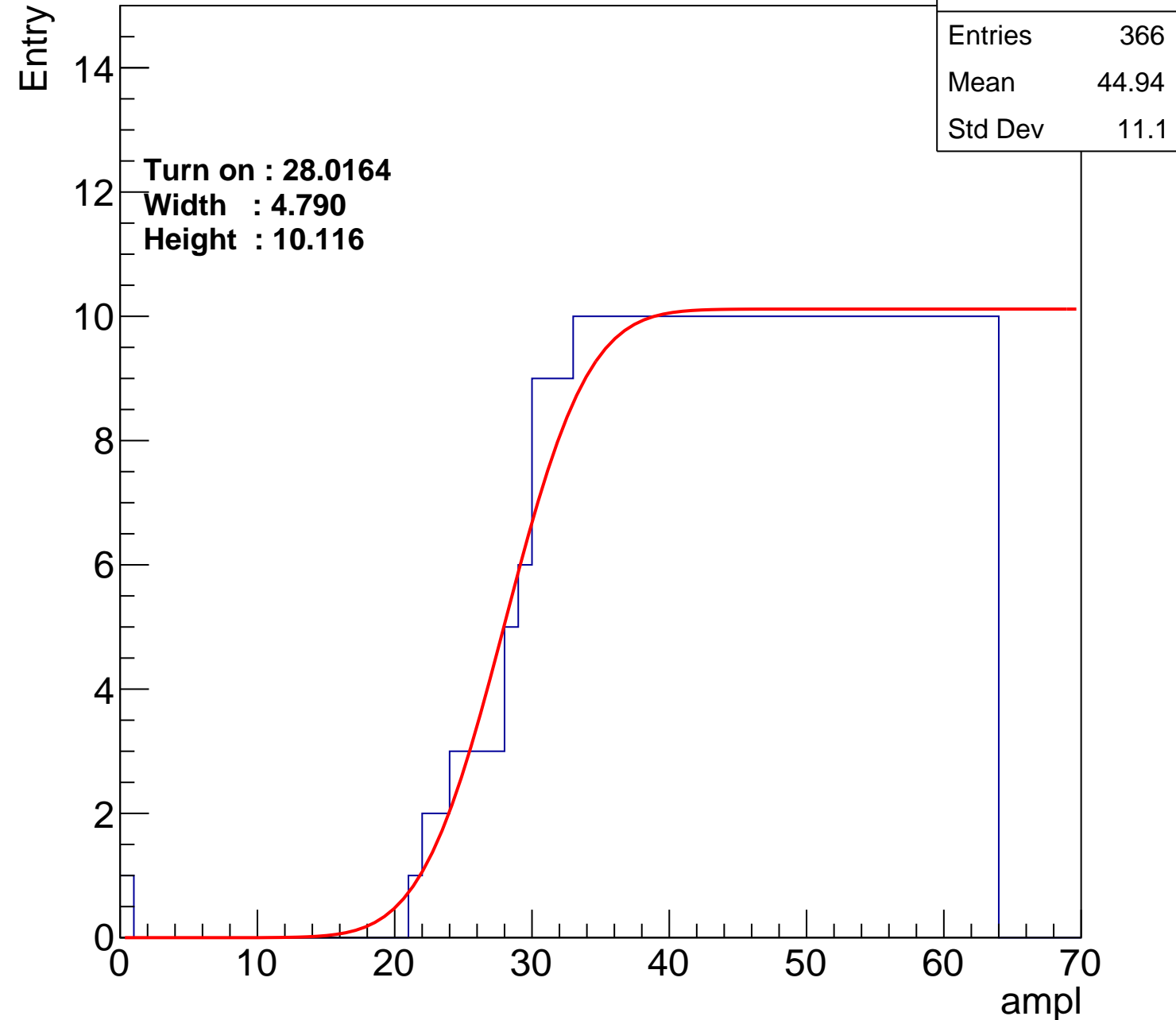
Width : 4.790

Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.87
Std Dev	11.54

Turn on : 25.2667

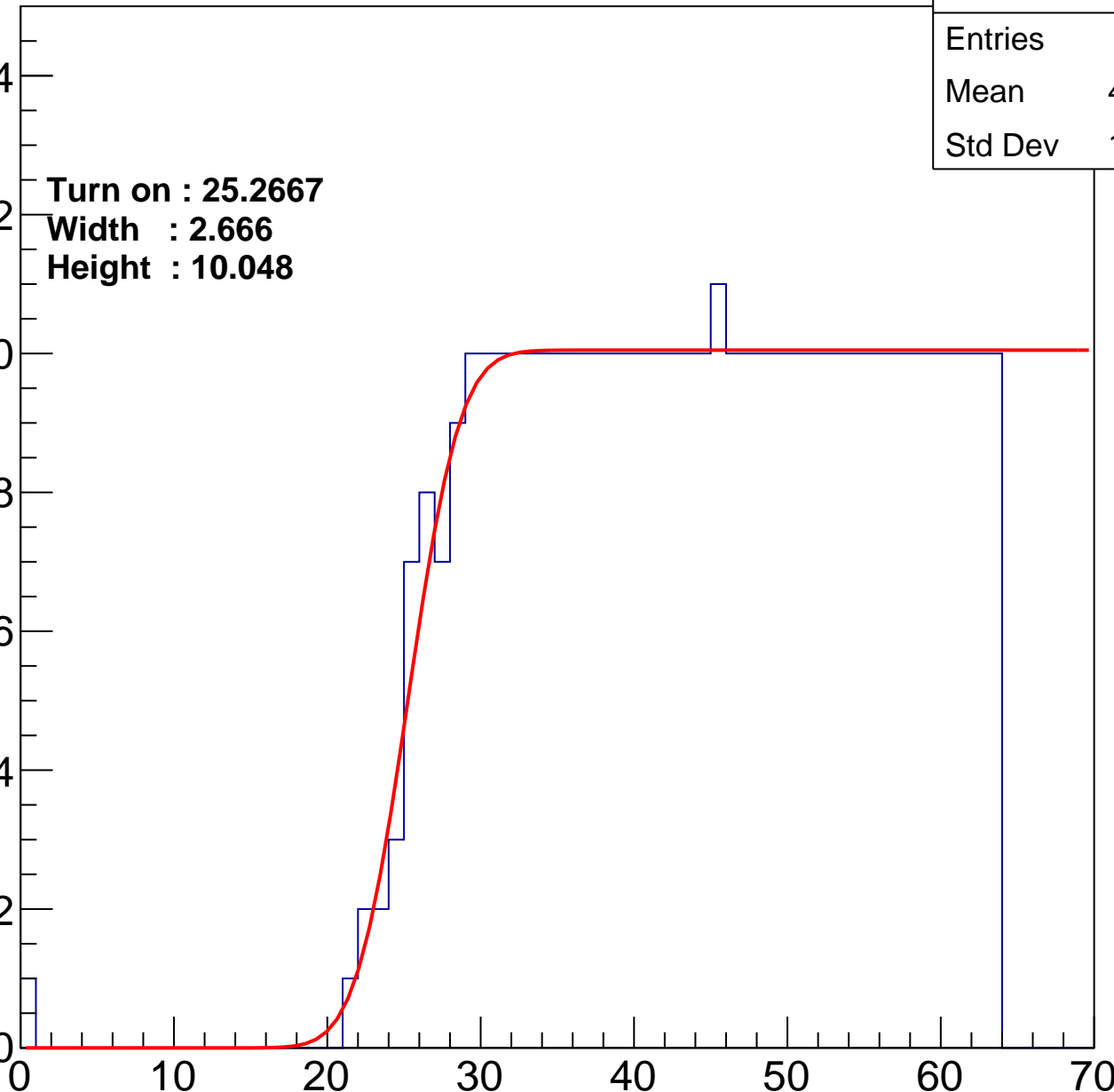
Width : 2.666

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.18
Std Dev	10.91

Turn on : 28.2592

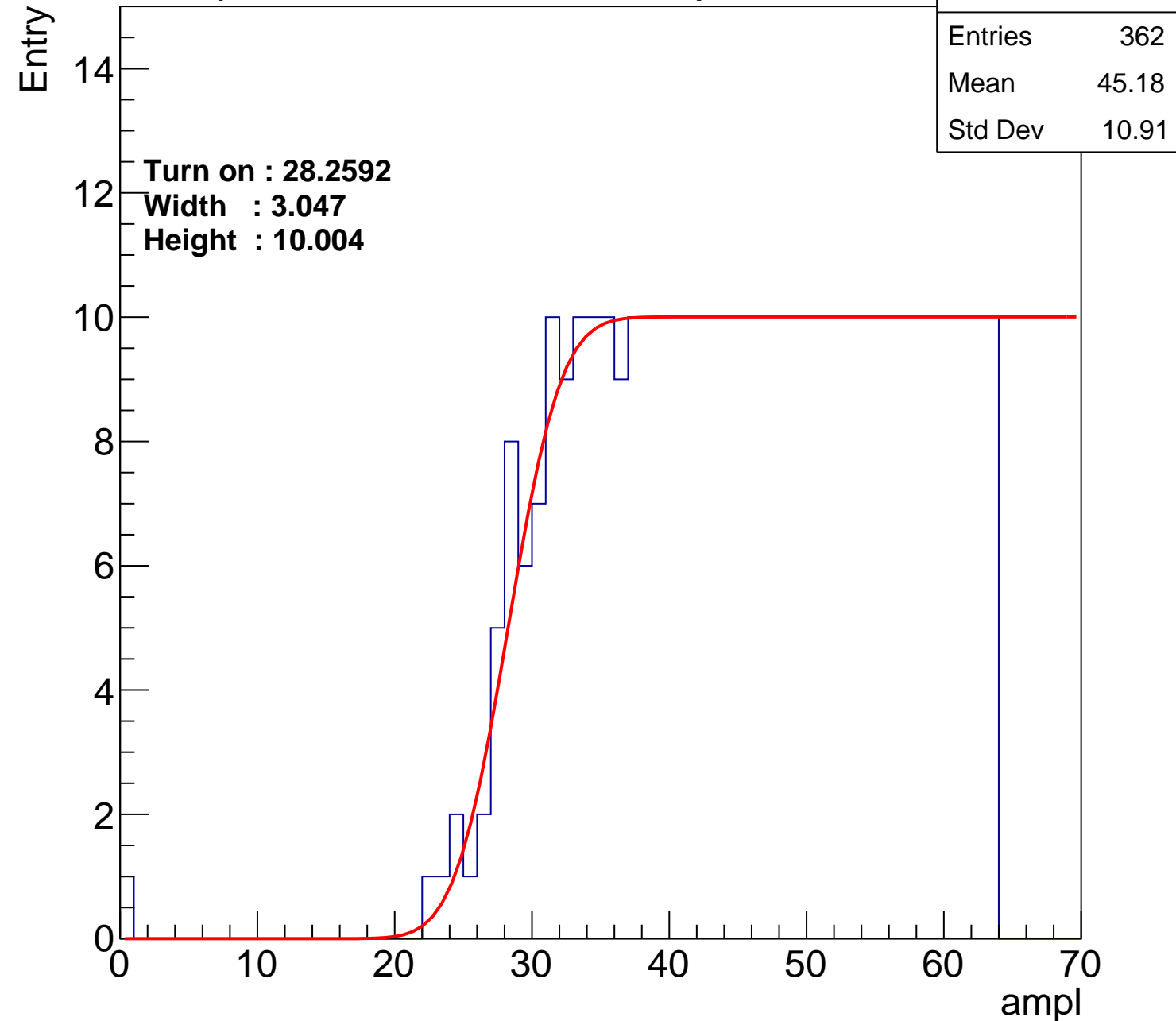
Width : 3.047

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.75
Std Dev	12.16

Turn on : 26.0062

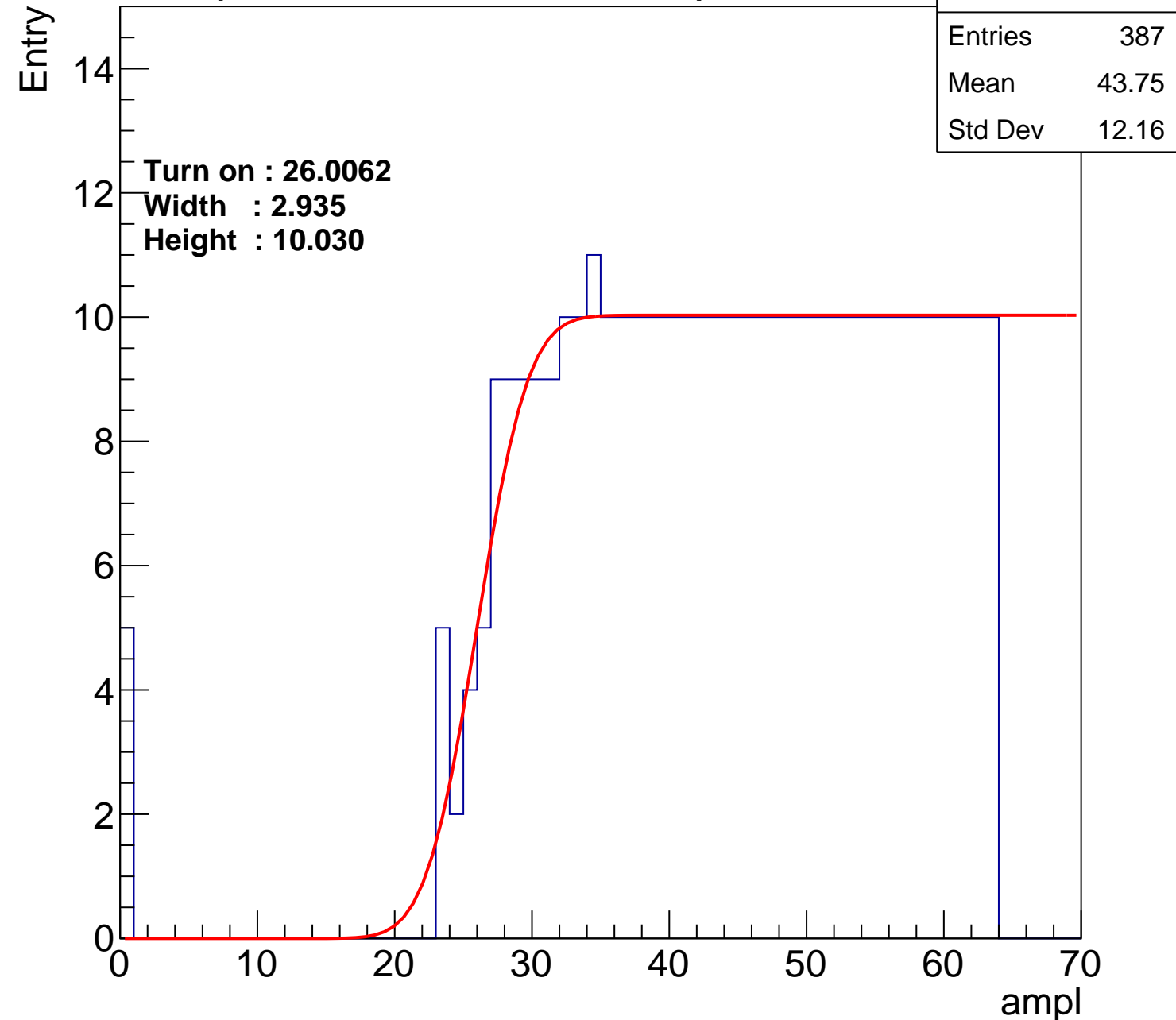
Width : 2.935

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch118

calib_packv5_042523_0143.root, FC#5, port B1

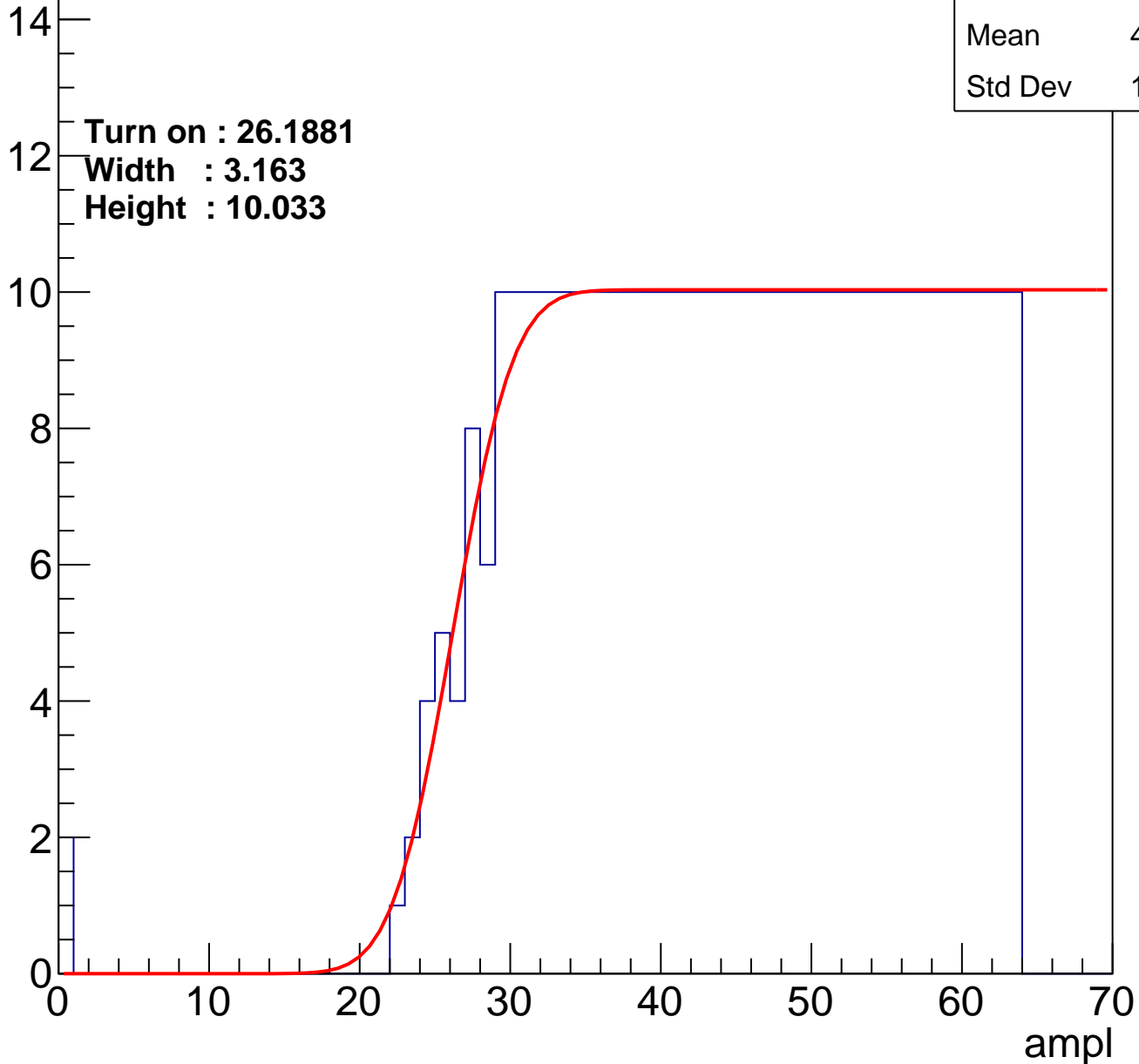
Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.1881

Width : 3.163

Height : 10.033

Entry



B0L000S, U15-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.41
Std Dev	10.72

Turn on : 28.4102

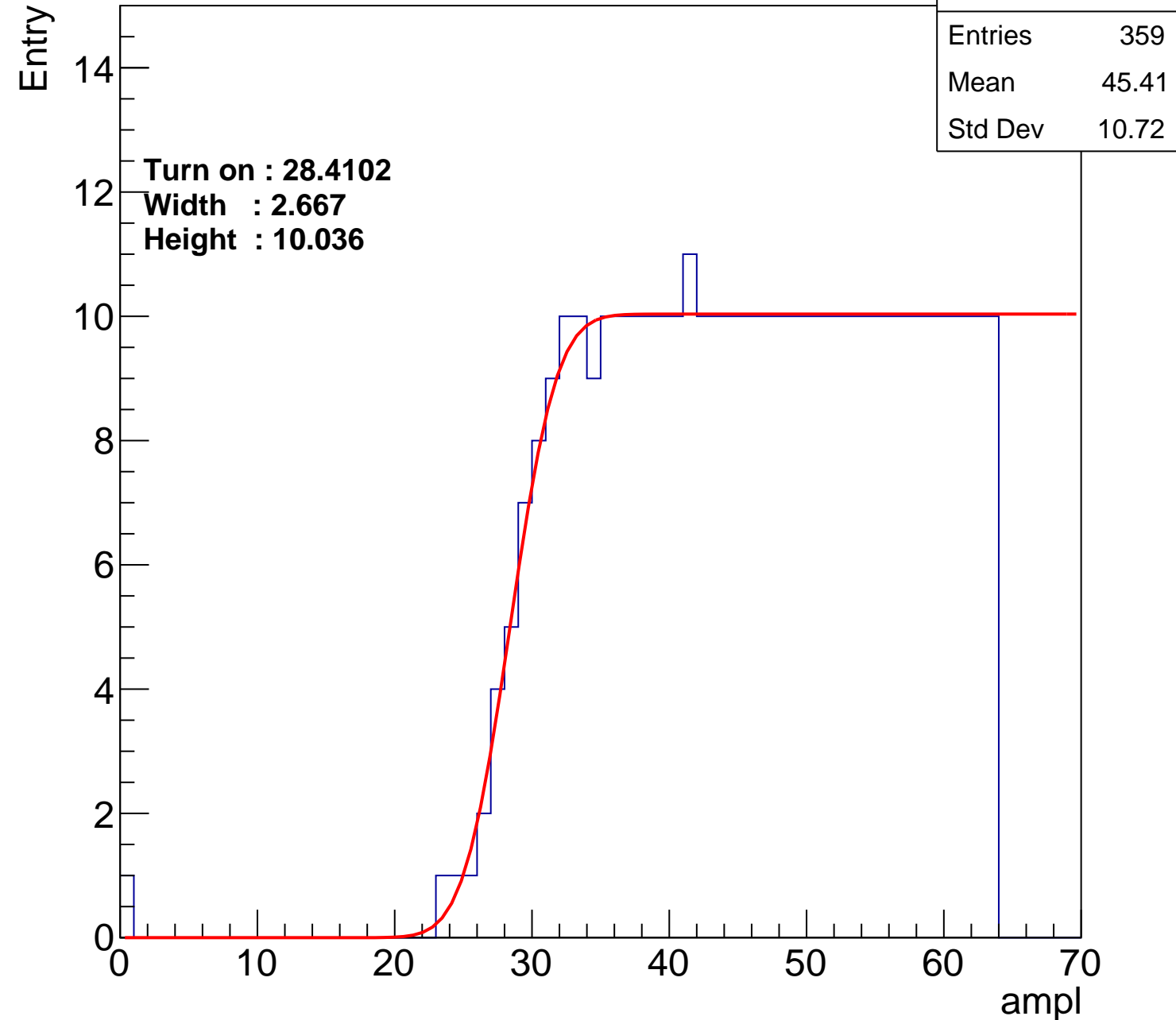
Width : 2.667

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.2896

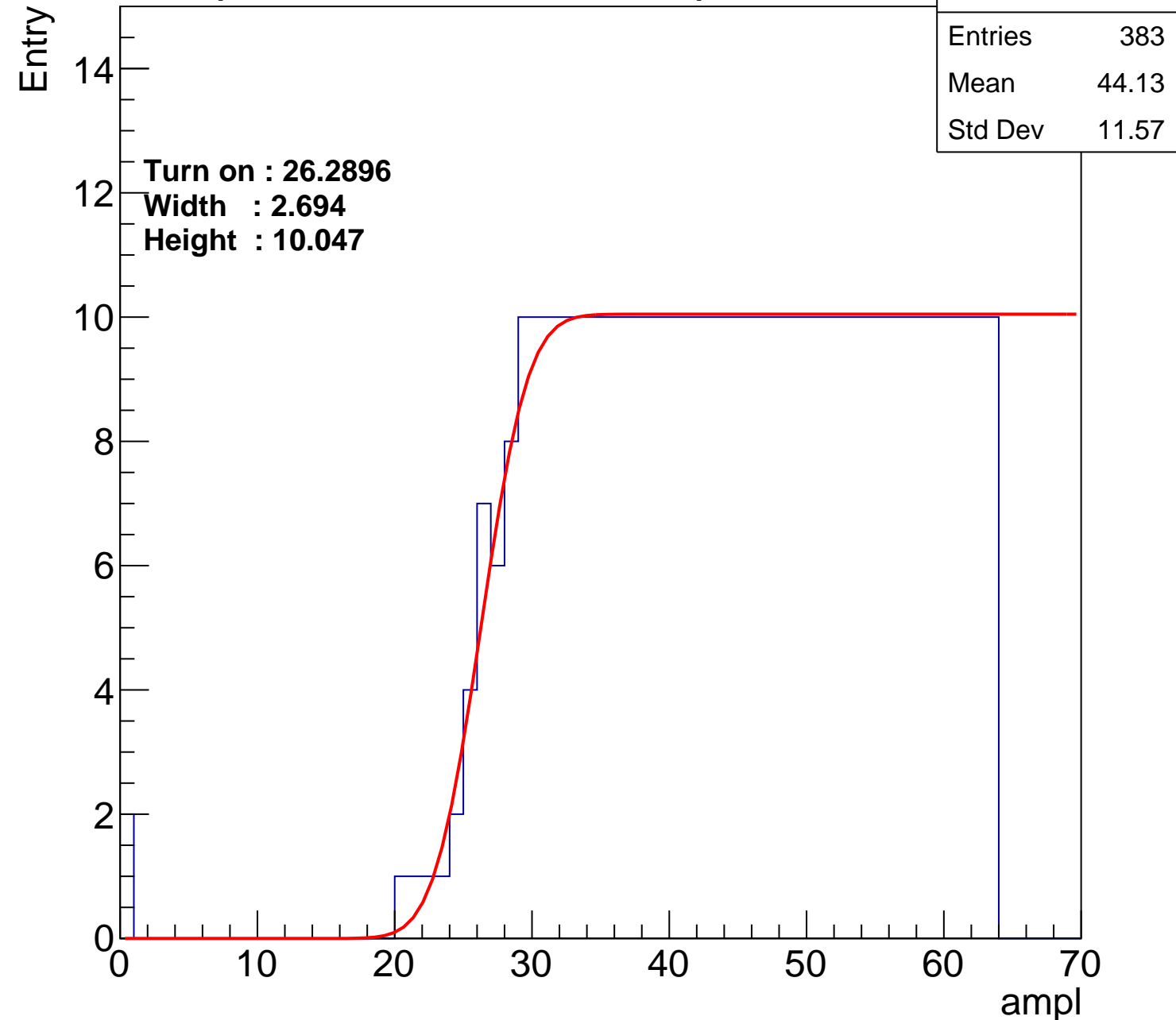
Width : 2.694

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.2070

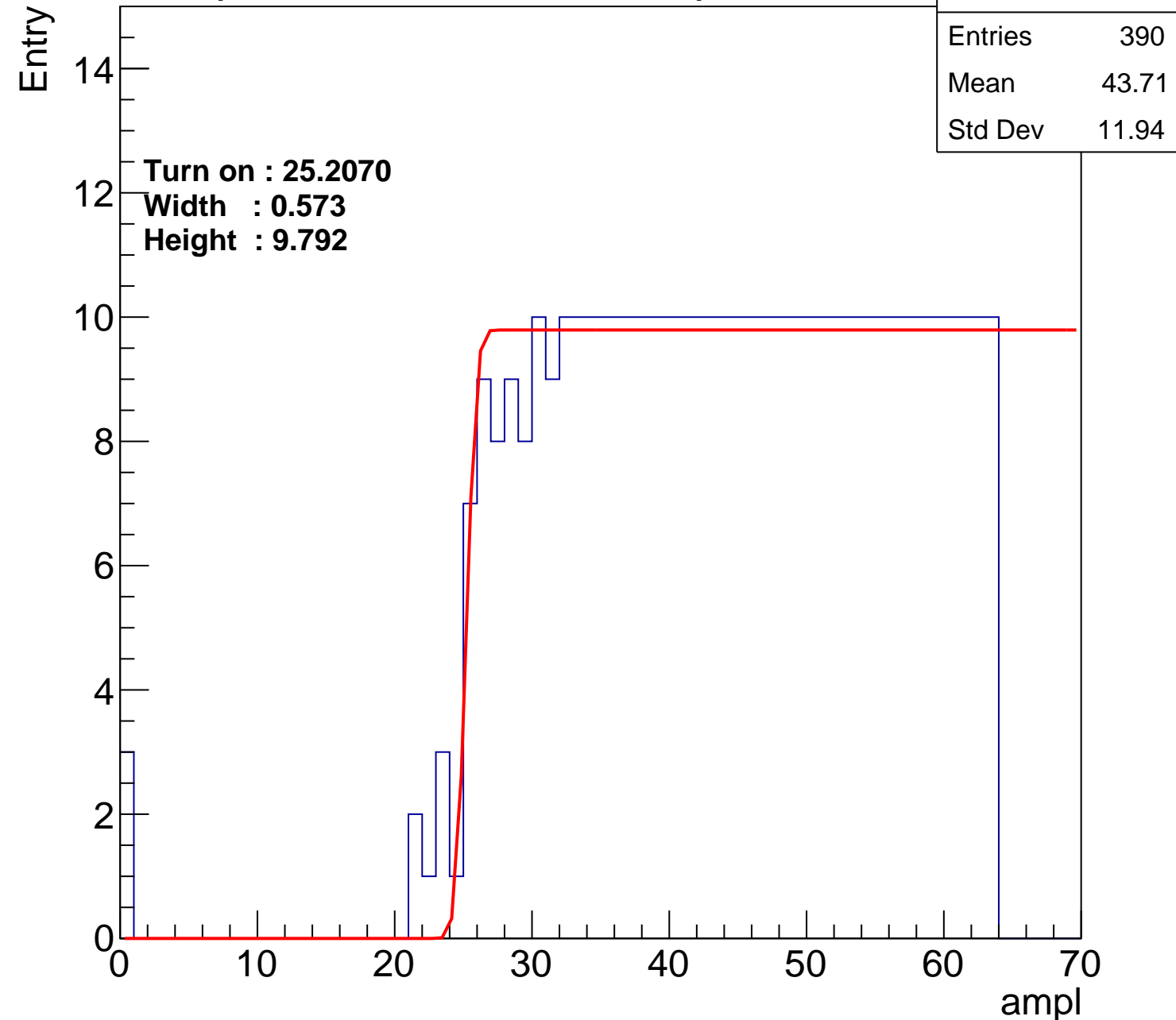
Width : 0.573

Height : 9.792

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.41
Std Dev	12.25

Turn on : 24.4788

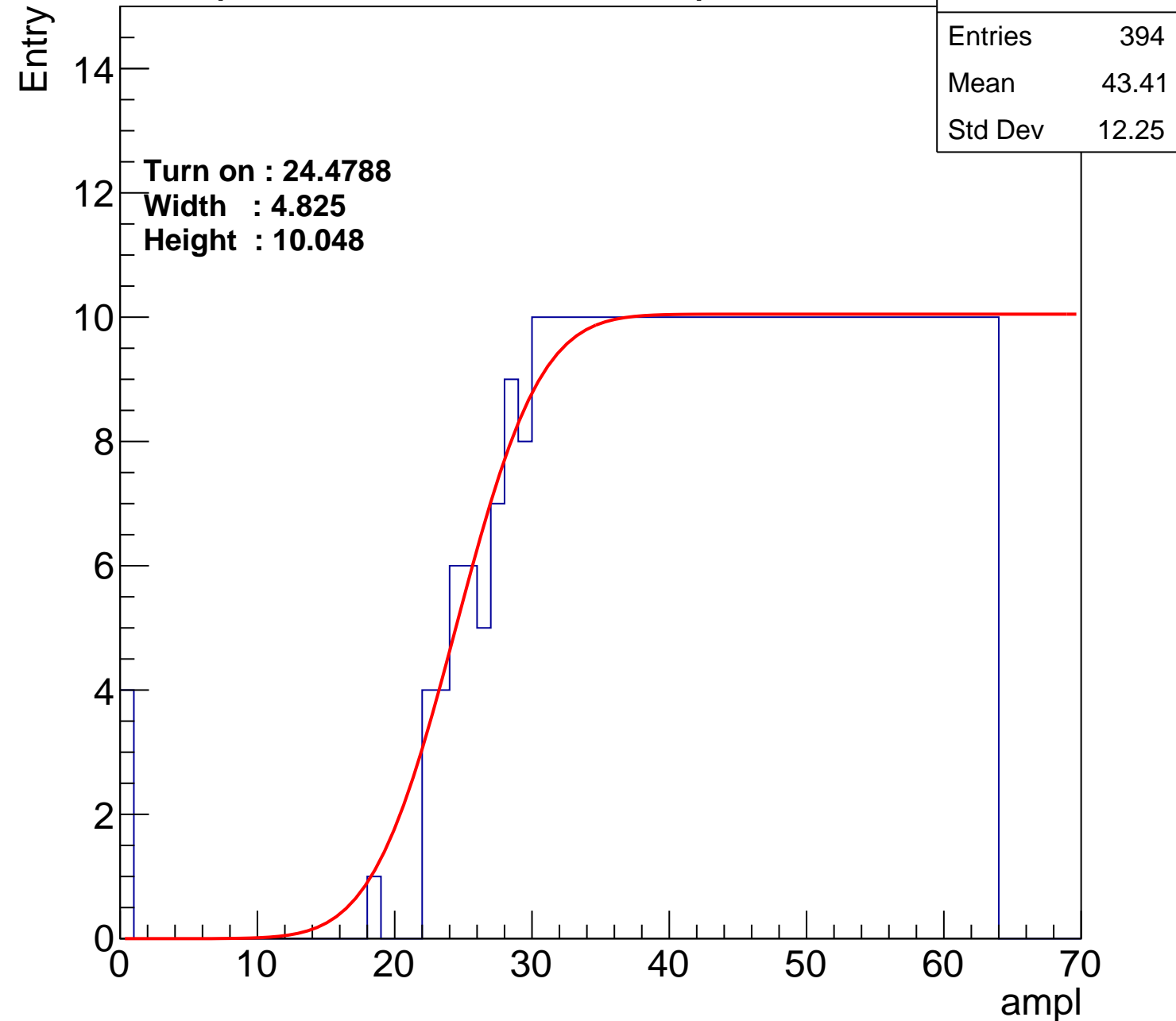
Width : 4.825

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 24.9590

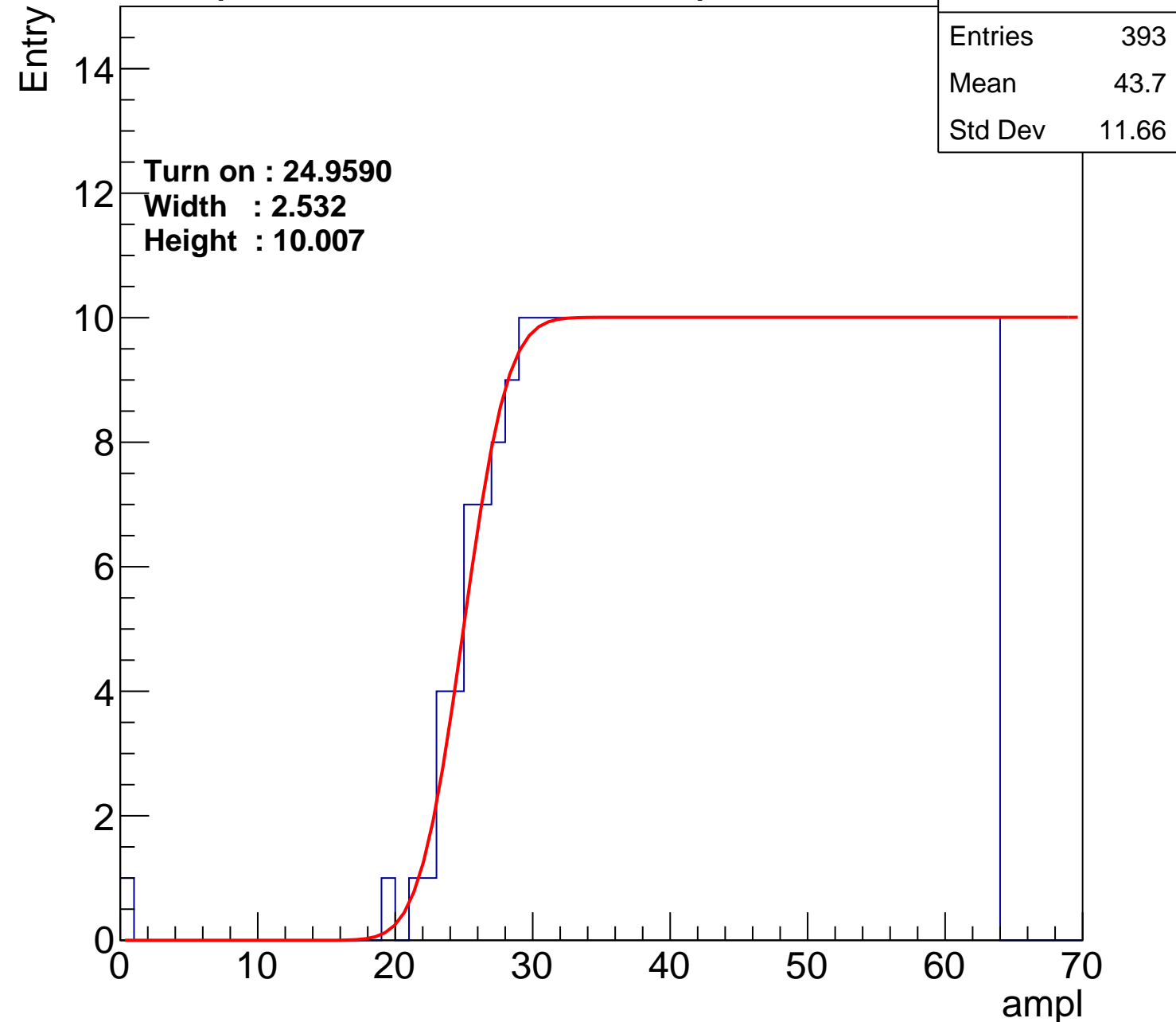
Width : 2.532

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	395
Mean	43.58
Std Dev	11.74

Turn on : 25.4283

Width : 2.897

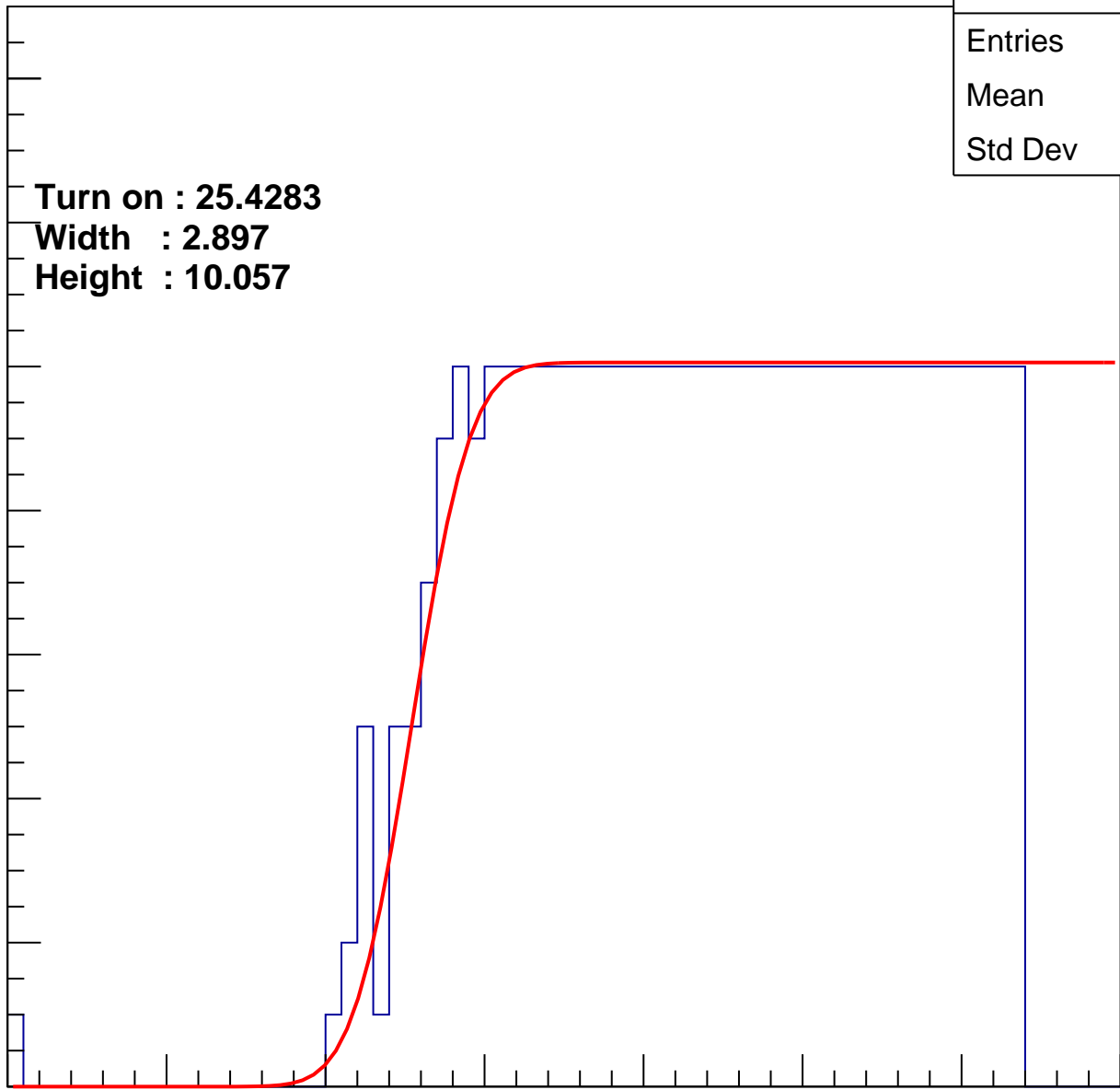
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U15-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 25.5745

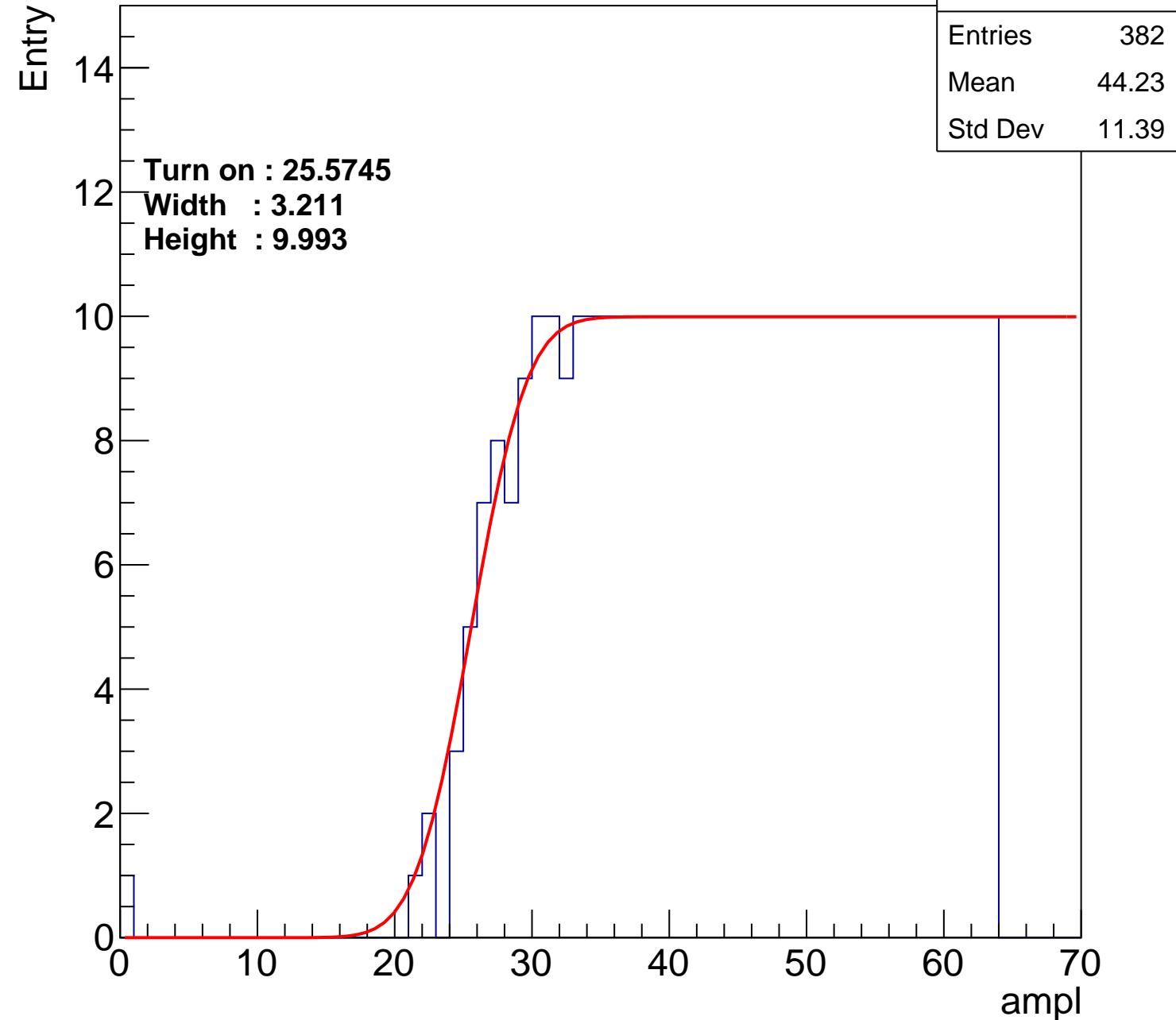
Width : 3.211

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	396
Mean	43.4
Std Dev	12.11

Turn on : 24.7567

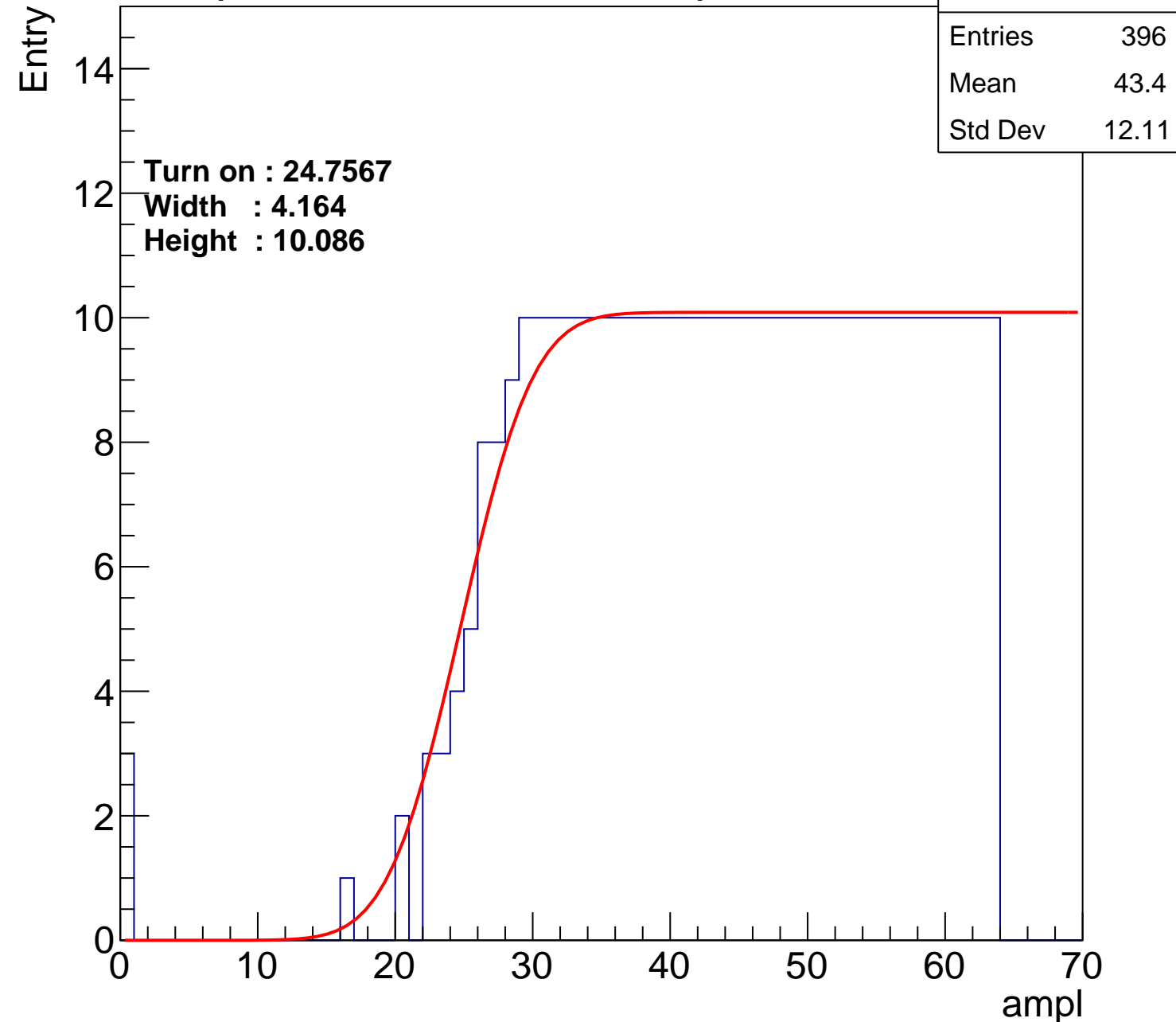
Width : 4.164

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U15-ch127

calib_packv5_042523_0143.root, FC#5, port B1

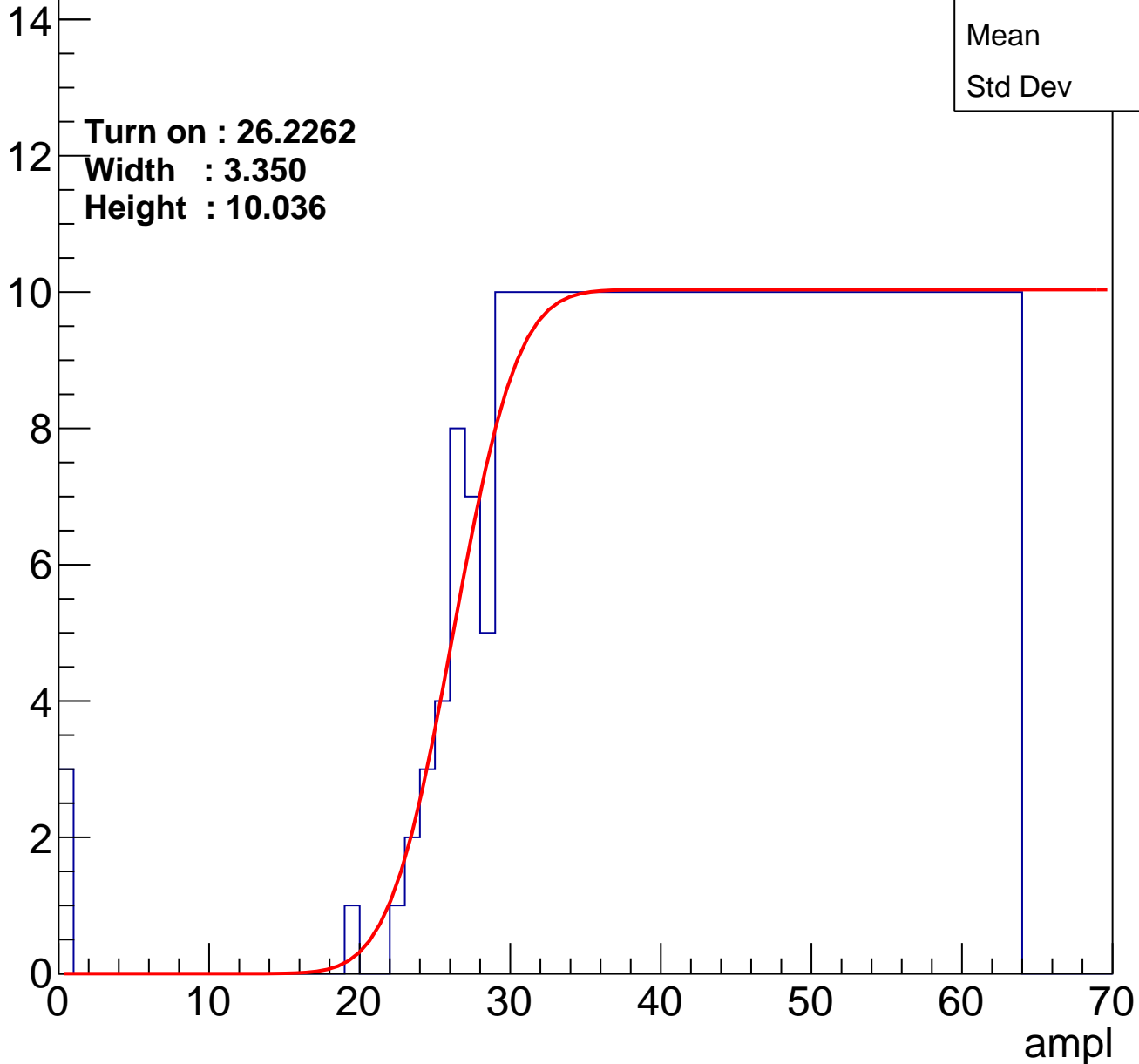
Entries	384
Mean	44
Std Dev	11.8

Turn on : 26.2262

Width : 3.350

Height : 10.036

Entry



B0L000S, U15-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44
Std Dev	11.8

Turn on : 26.2262

Width : 3.350

Height : 10.036

Entry

