

B0L102S, U20-ch0, adc0

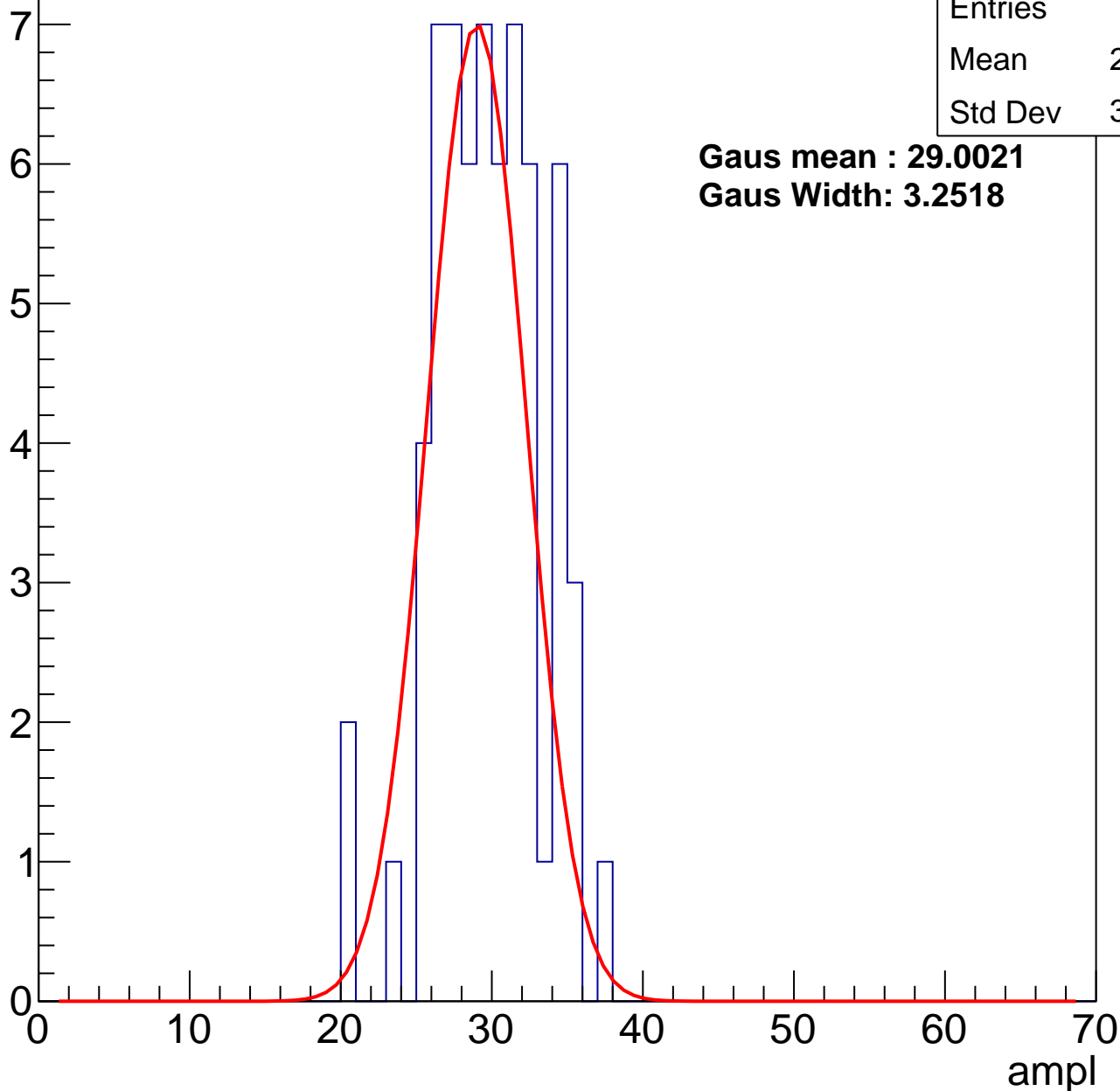
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.27 |
| Std Dev | 3.492 |

Gaus mean : 29.0021

Gaus Width: 3.2518



B0L102S, U20-ch0, adc1

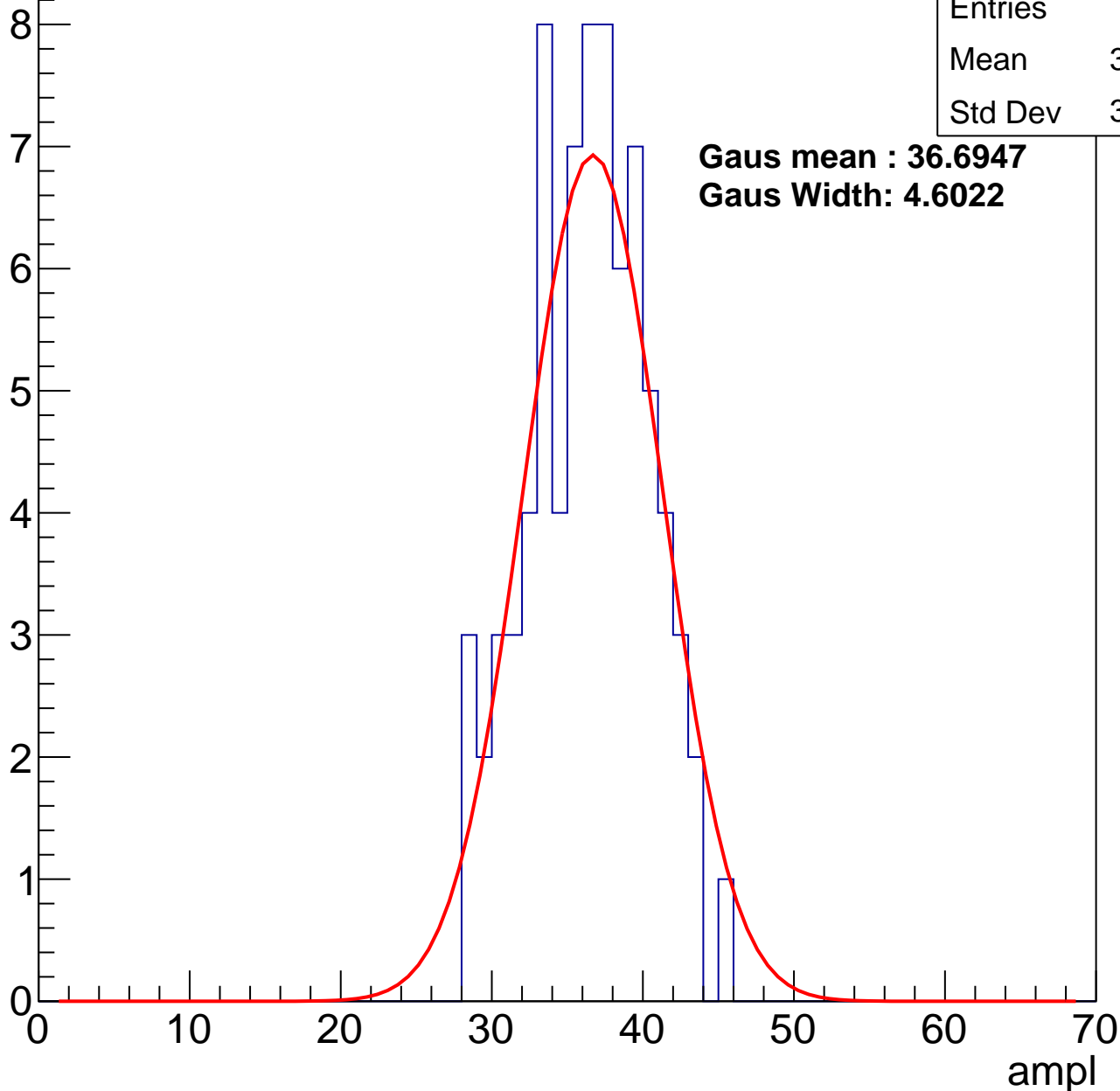
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 35.95 |
| Std Dev | 3.912 |

Gaus mean : 36.6947

Gaus Width: 4.6022



B0L102S, U20-ch0, adc2

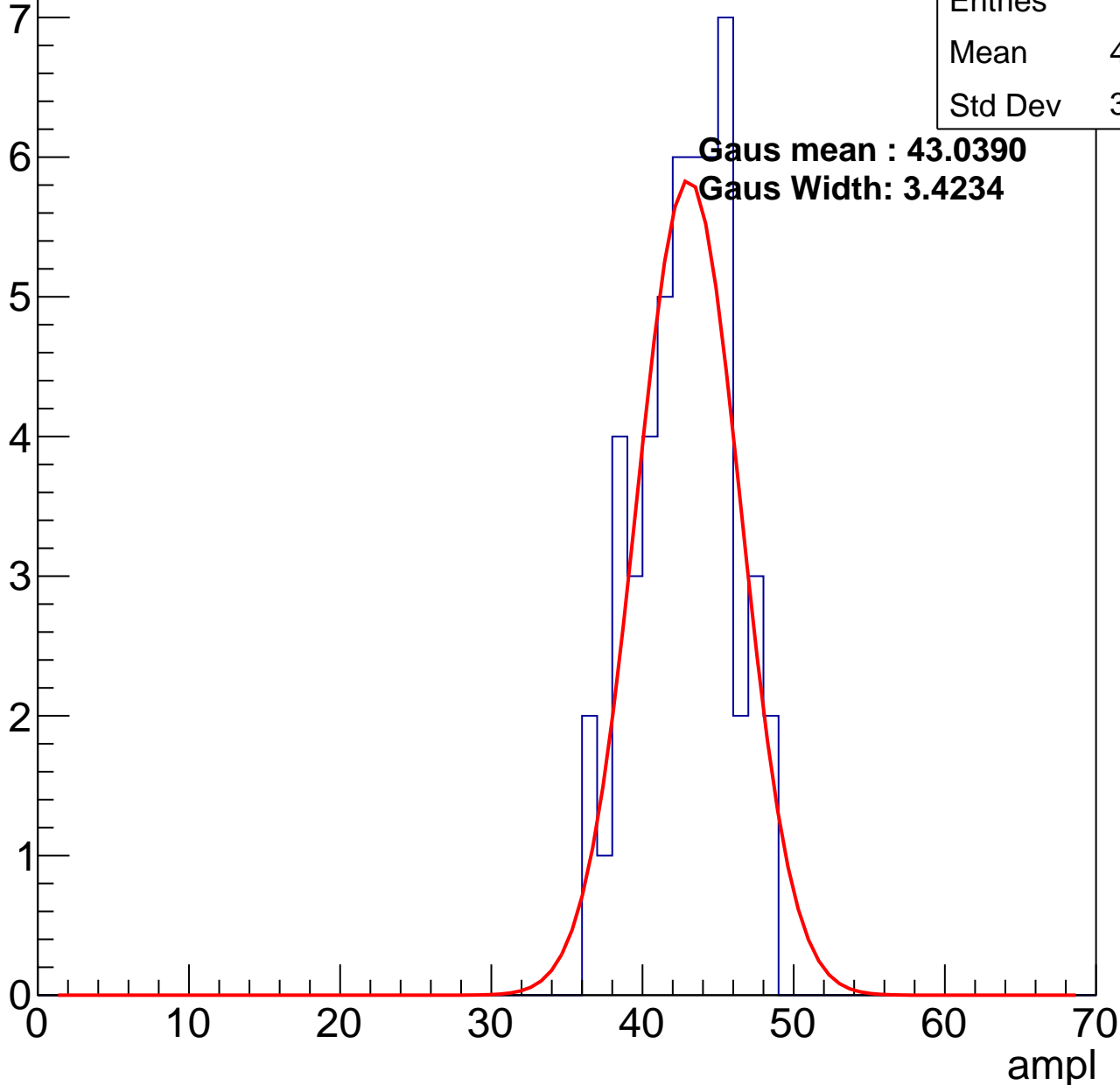
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 42.37 |
| Std Dev | 3.048 |

Gaus mean : 43.0390

Gaus Width: 3.4234

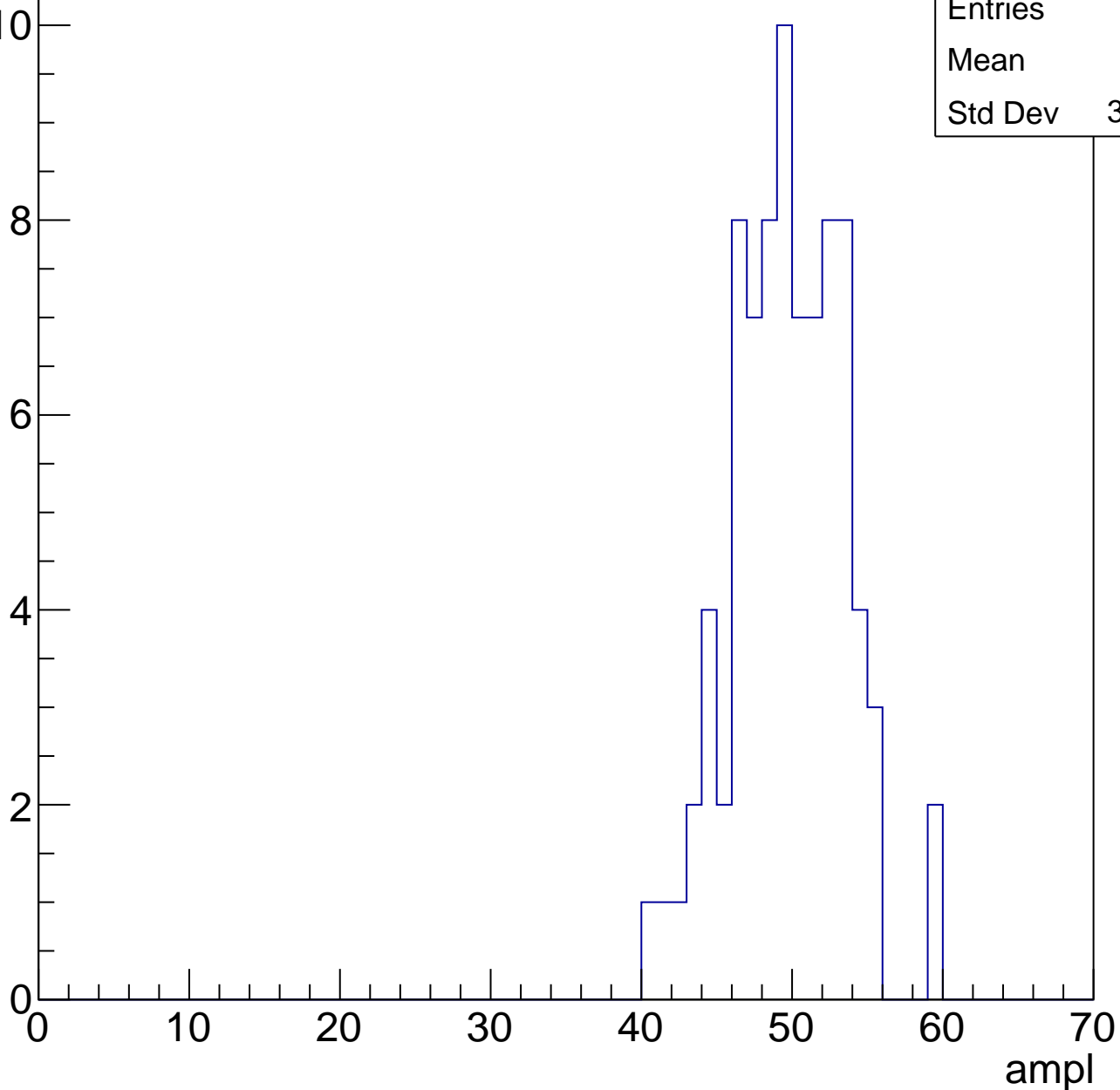


B0L102S, U20-ch0, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 49.3 |
| Std Dev | 3.708 |

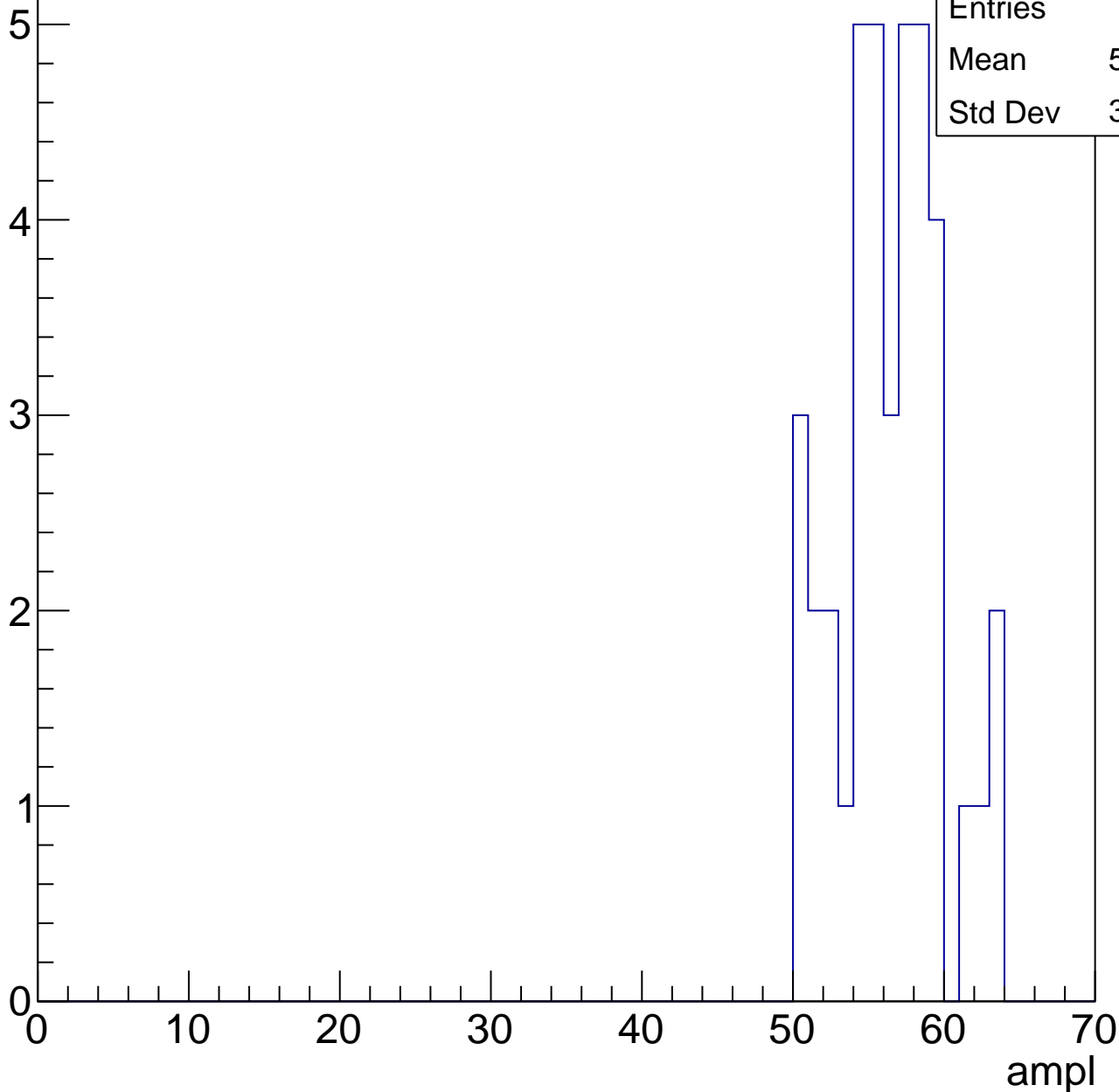


B0L102S, U20-ch0, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 55.95 |
| Std Dev | 3.374 |

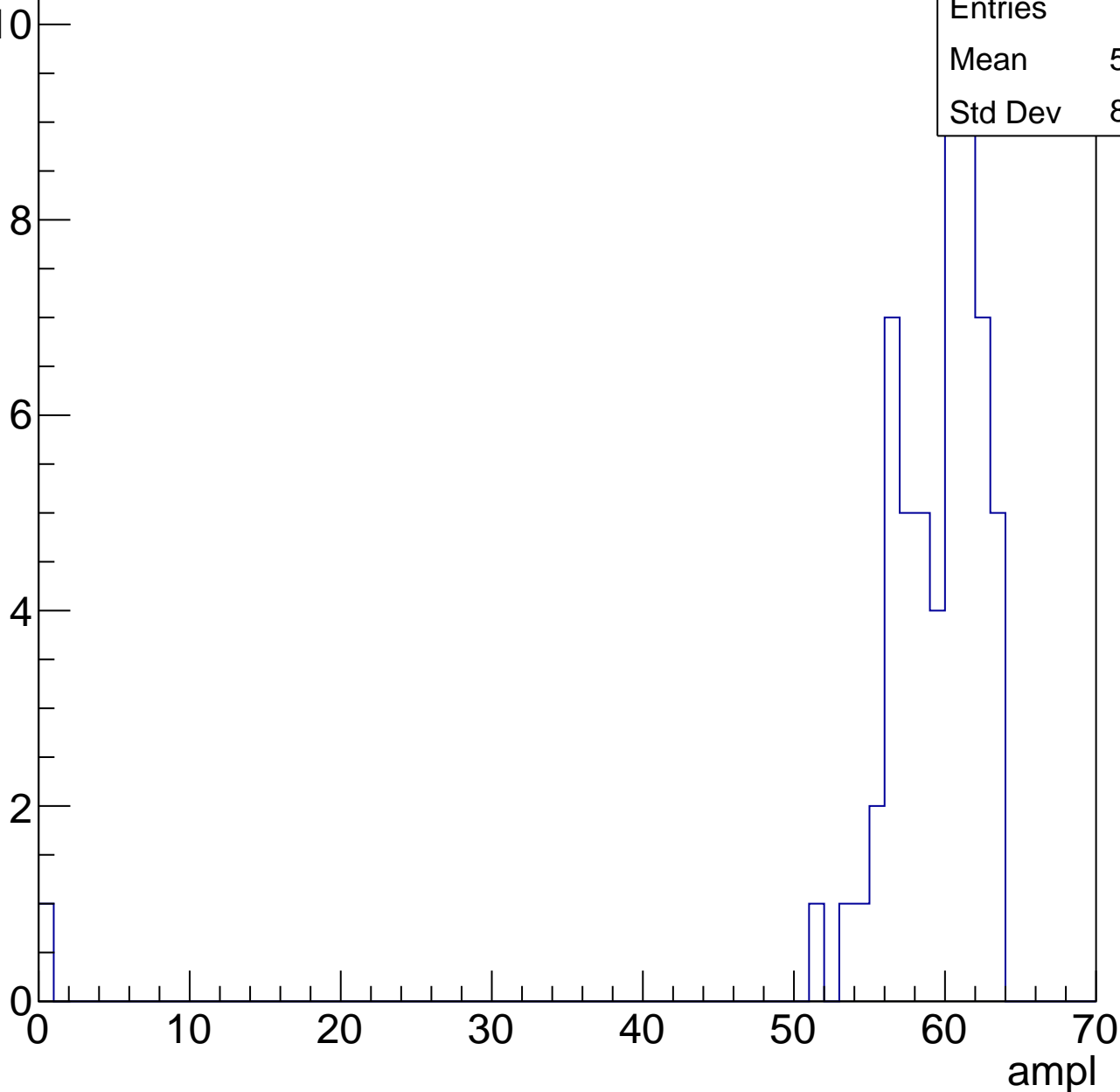


B0L102S, U20-ch0, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

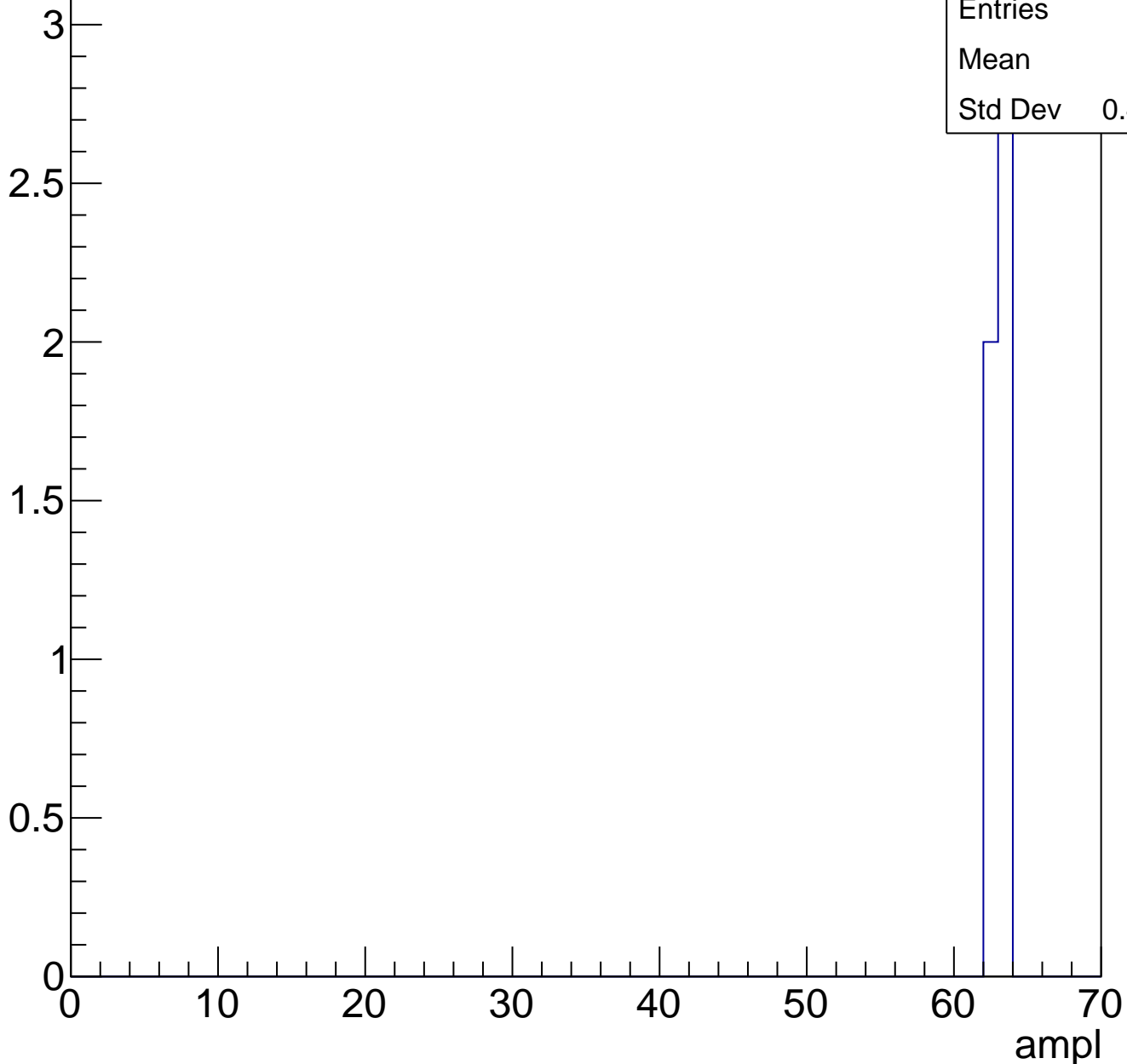
| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.09 |
| Std Dev | 8.165 |



B0L102S, U20-ch0, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch0, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-----|
| Entries | 2 |
| Mean | 9.5 |
| Std Dev | 9.5 |

B0L102S, U20-ch1, adc0

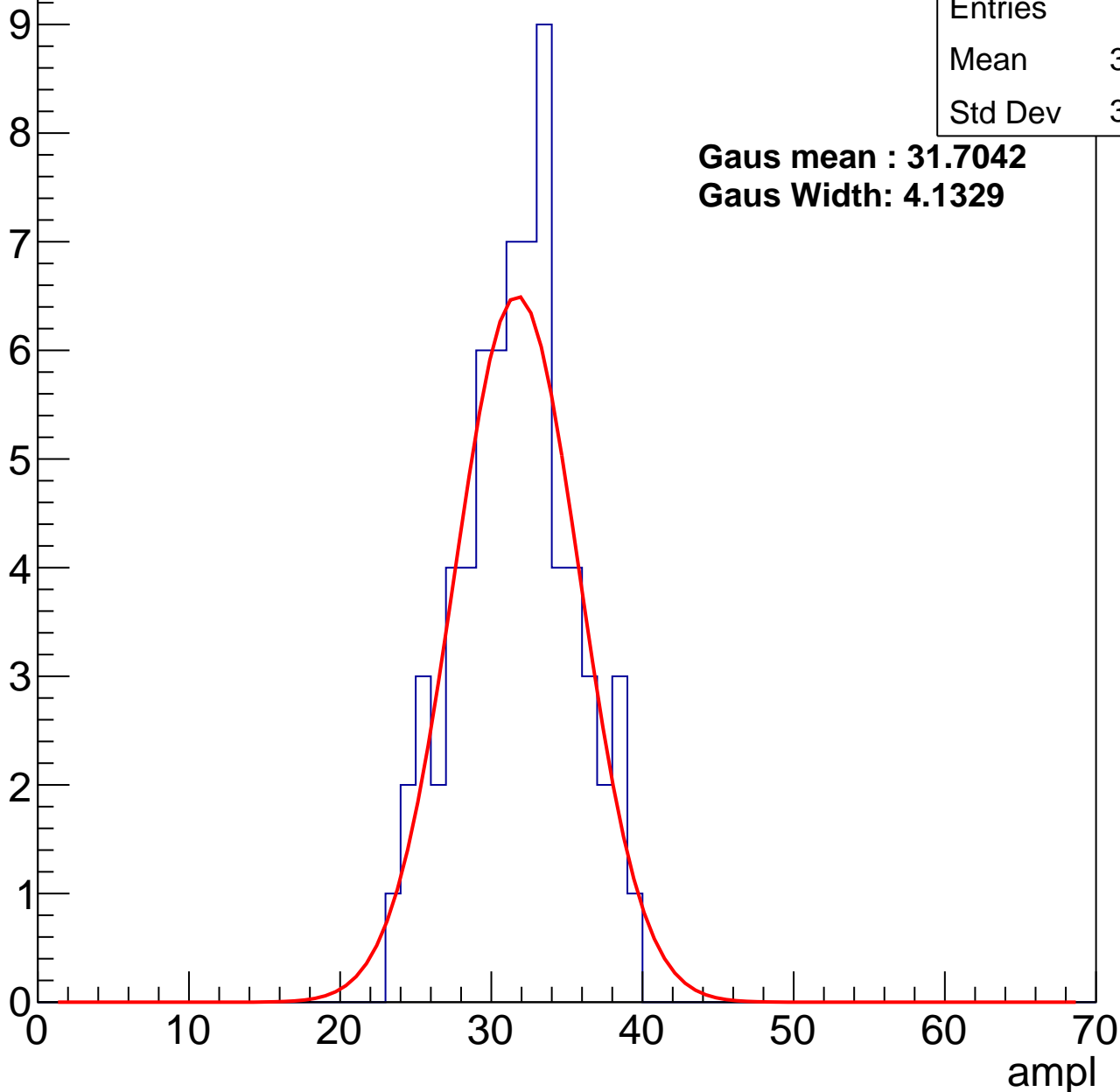
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 31.19 |
| Std Dev | 3.735 |

Gaus mean : 31.7042

Gaus Width: 4.1329



B0L102S, U20-ch1, adc1

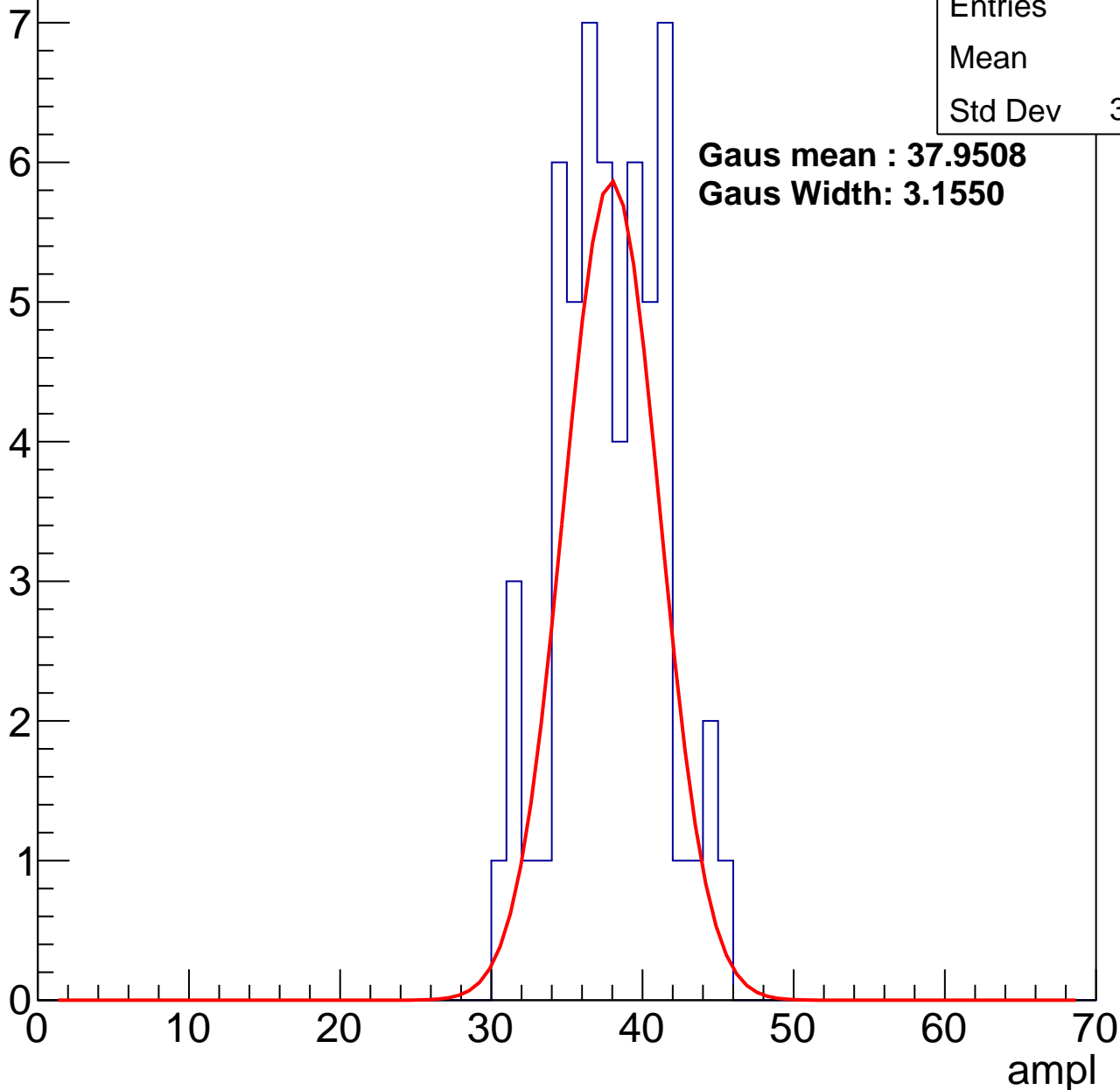
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 37.4 |
| Std Dev | 3.448 |

Gaus mean : 37.9508

Gaus Width: 3.1550



B0L102S, U20-ch1, adc2

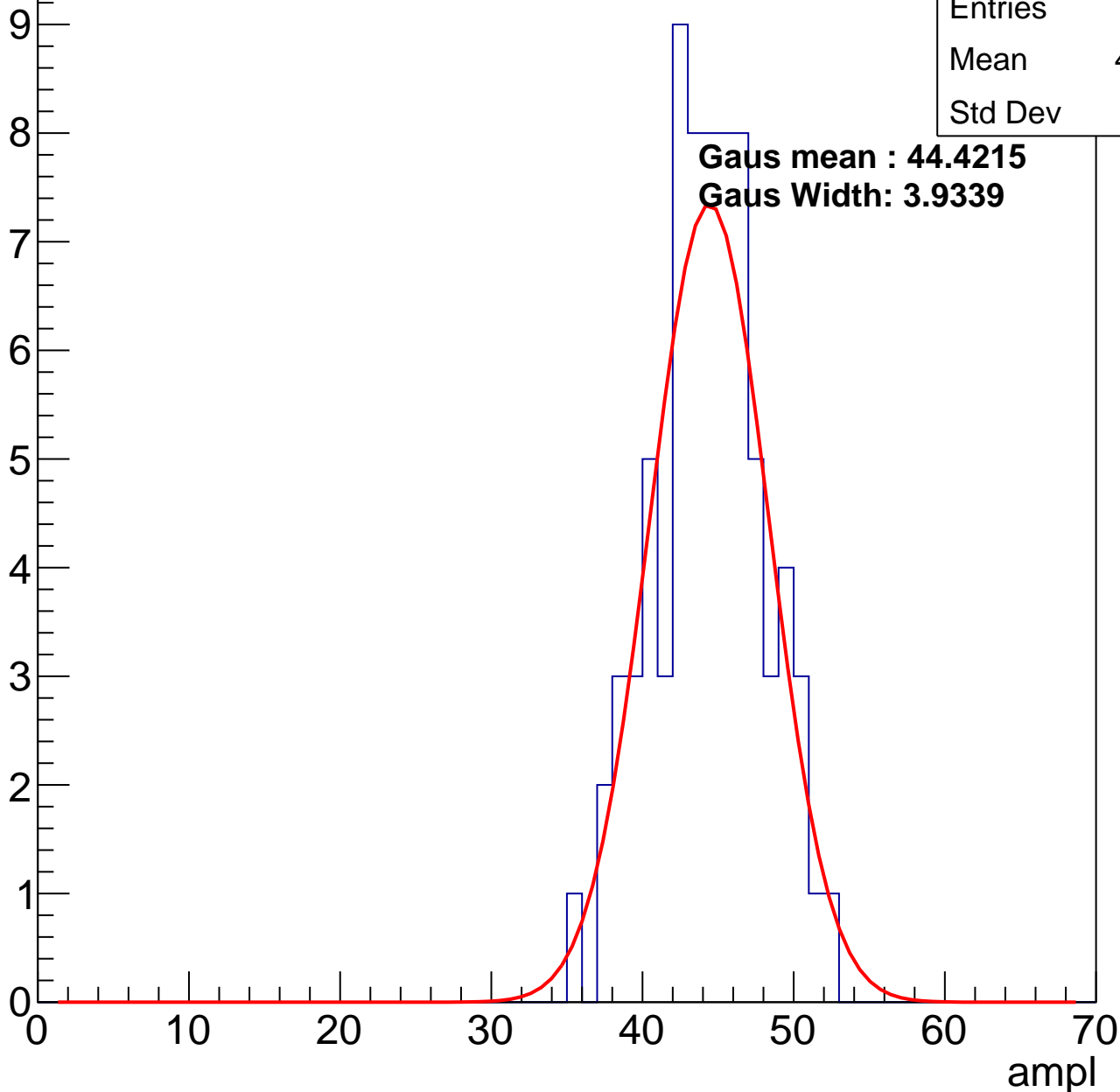
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 43.91 |
| Std Dev | 3.59 |

Gaus mean : 44.4215

Gaus Width: 3.9339

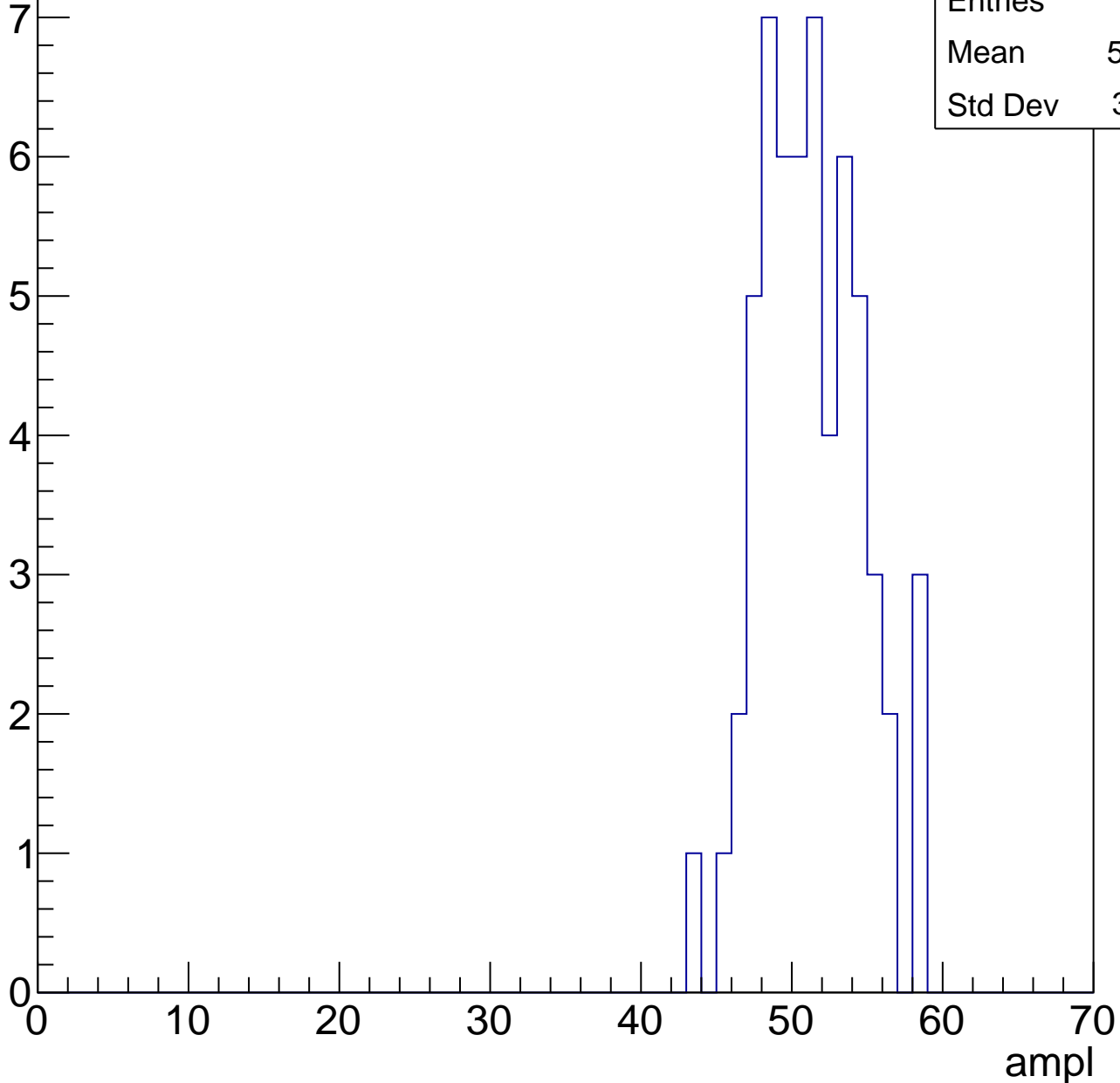


B0L102S, U20-ch1, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 50.84 |
| Std Dev | 3.321 |

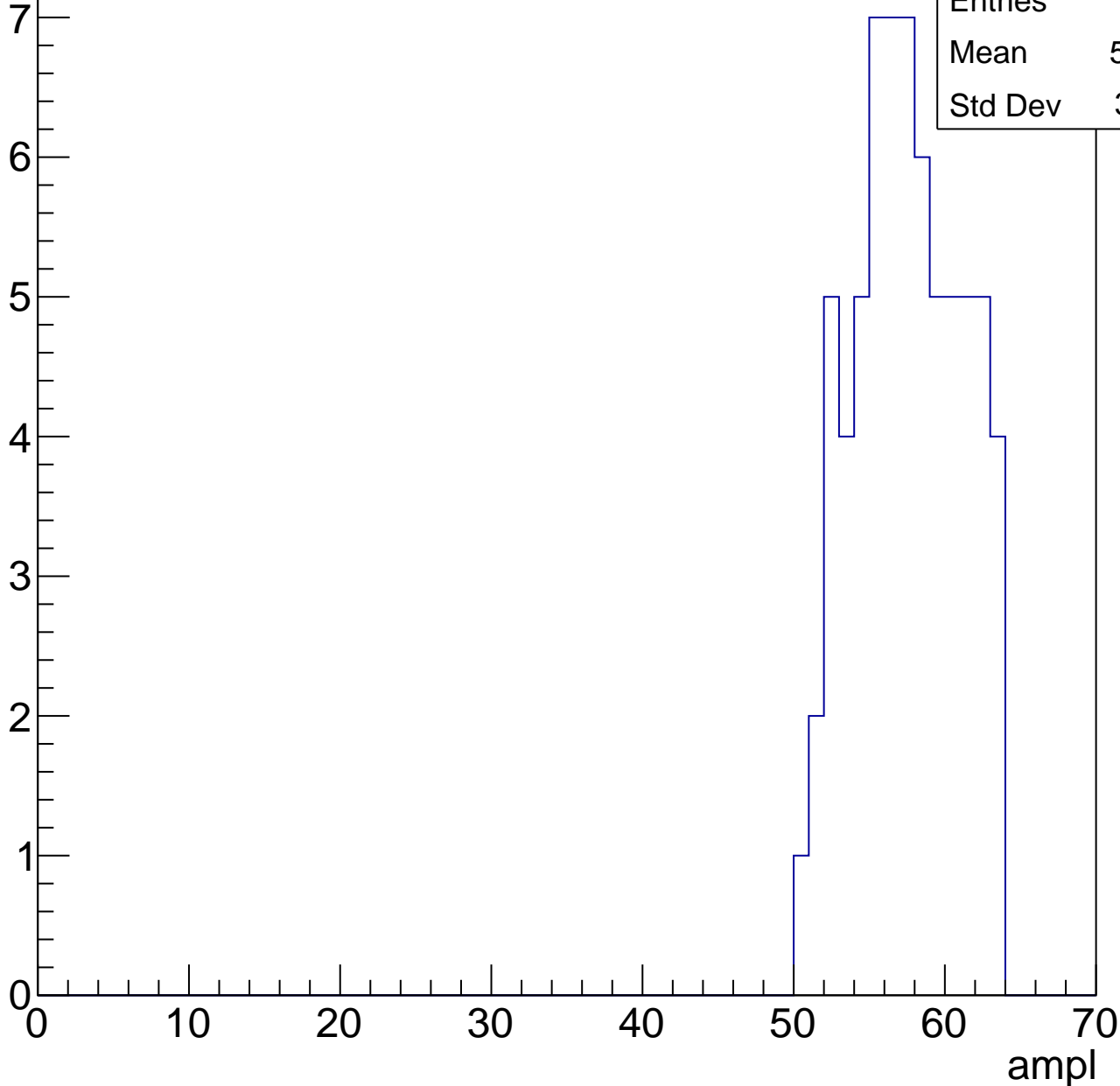


B0L102S, U20-ch1, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 57.06 |
| Std Dev | 3.451 |

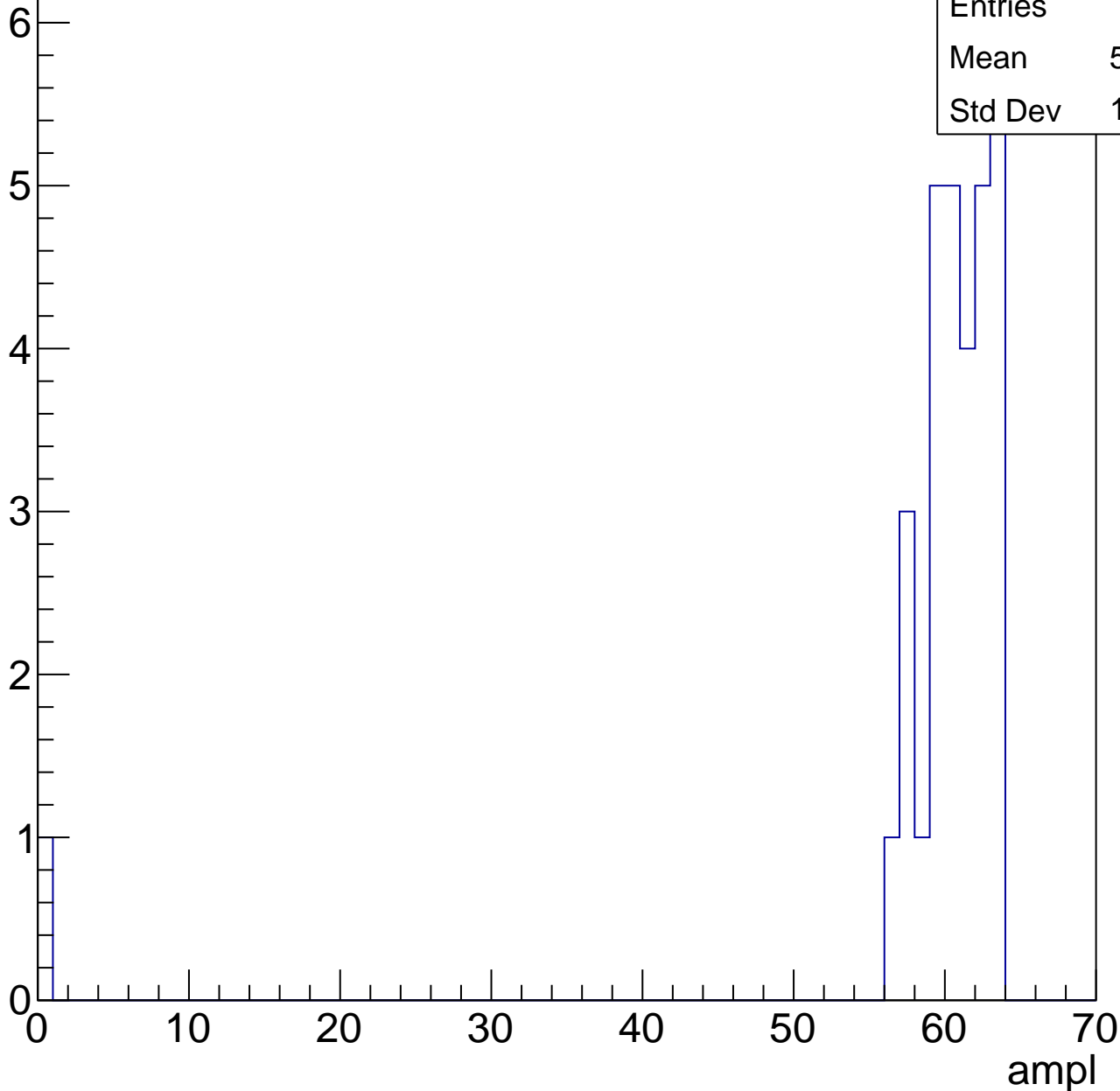


B0L102S, U20-ch1, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

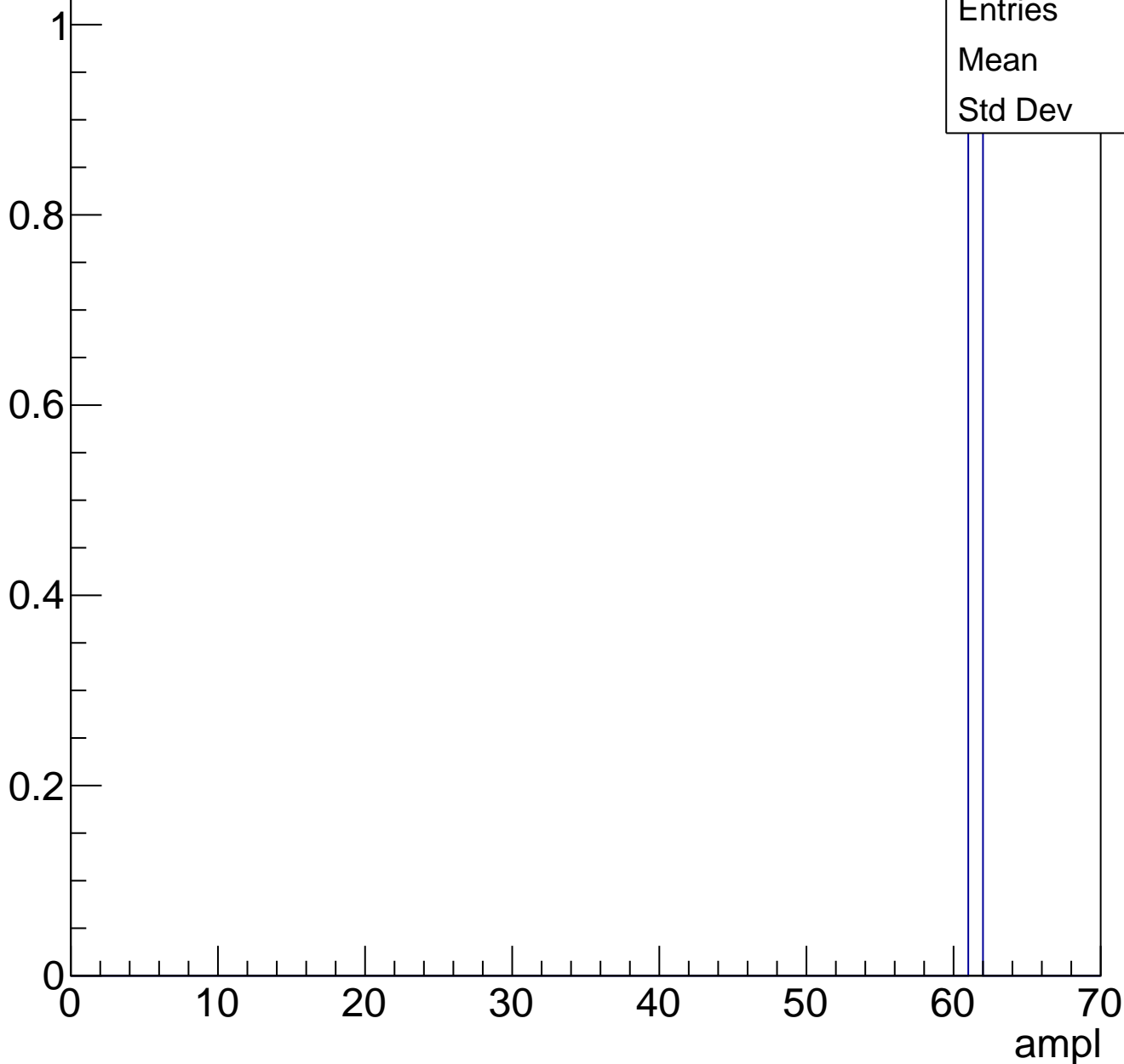
| | |
|---------|-------|
| Entries | 31 |
| Mean | 58.45 |
| Std Dev | 10.86 |



B0L102S, U20-ch1, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch1, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch2, adc0

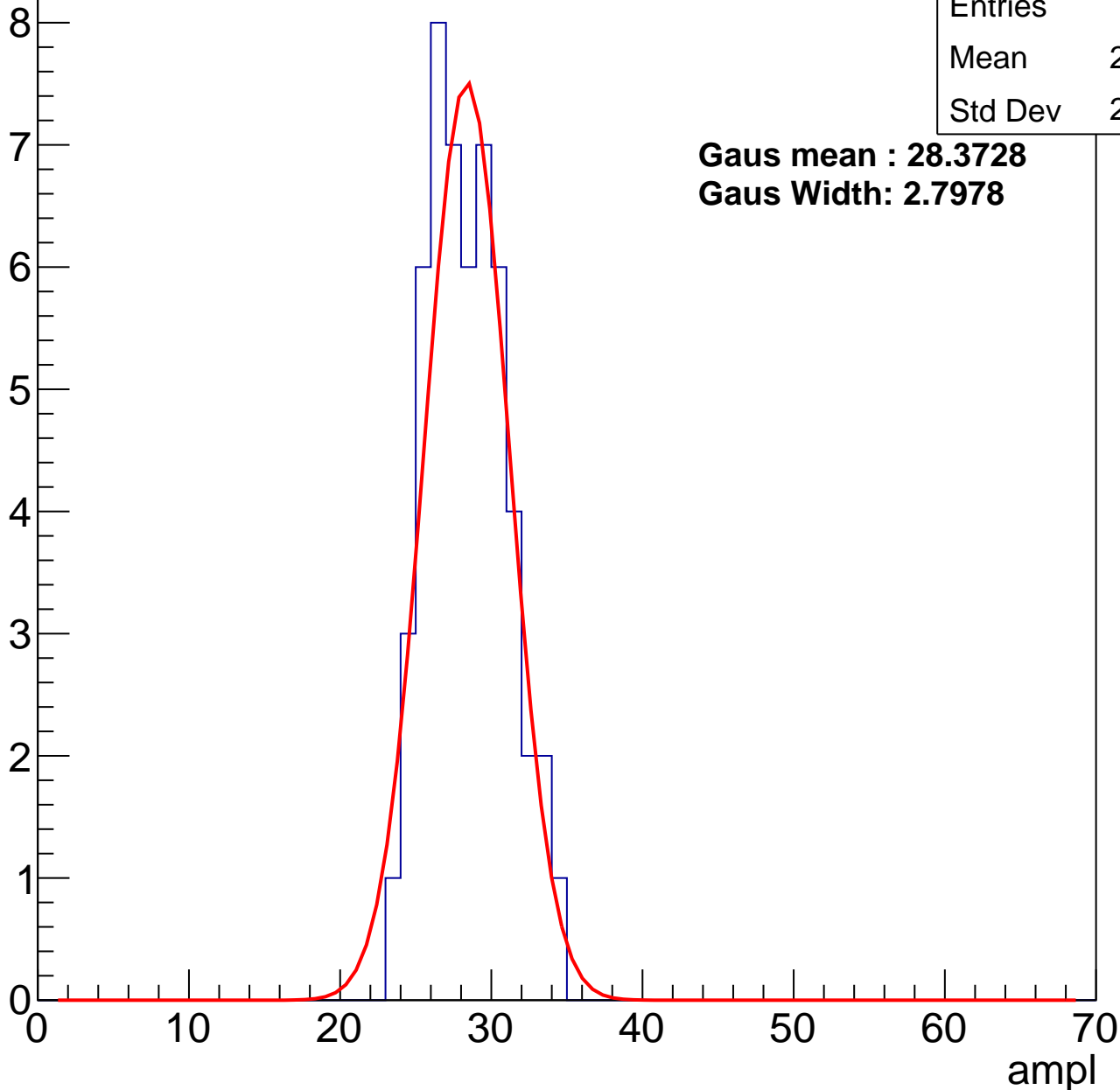
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 27.94 |
| Std Dev | 2.573 |

Gaus mean : 28.3728

Gaus Width: 2.7978



B0L102S, U20-ch2, adc1

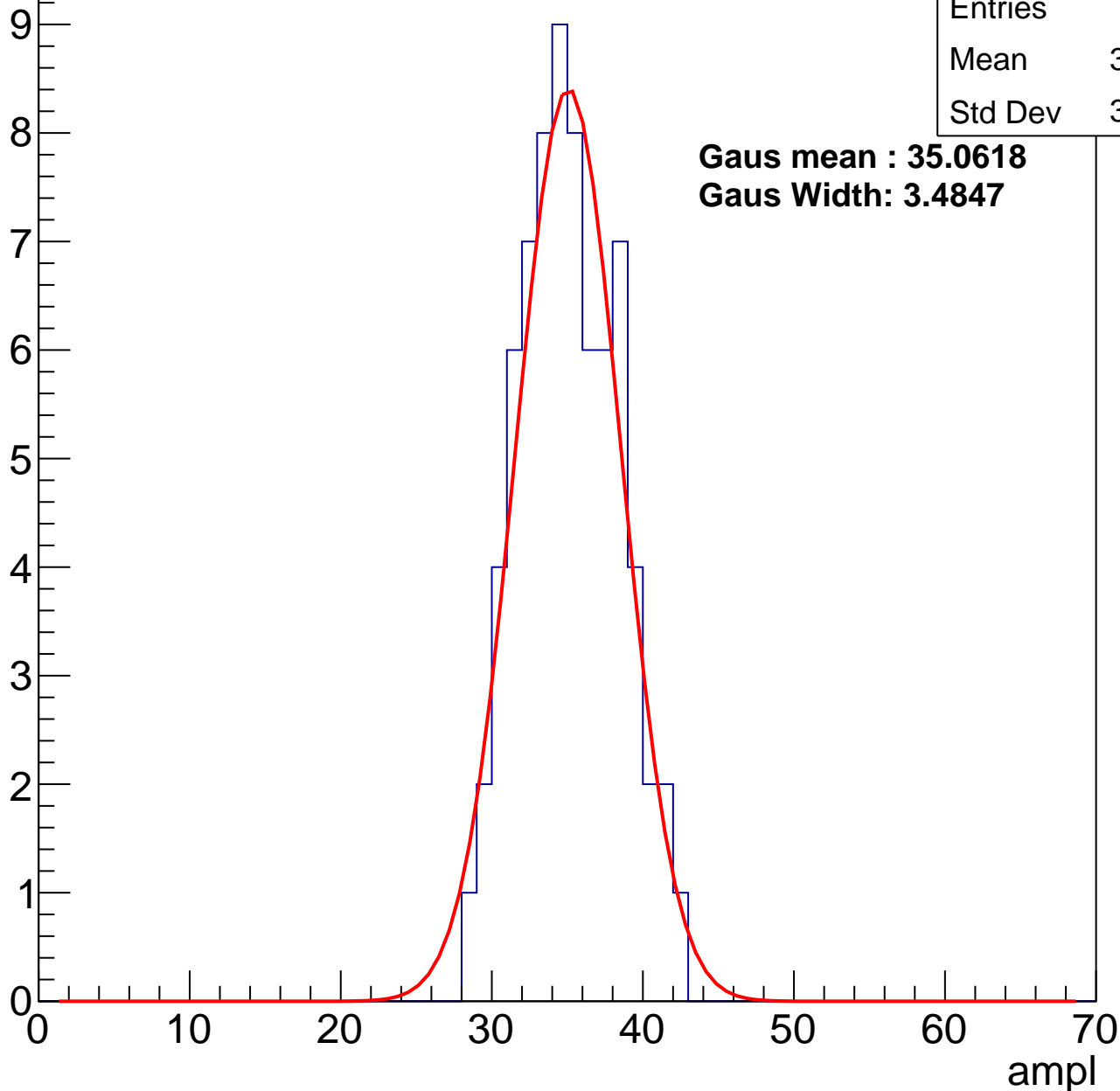
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 34.66 |
| Std Dev | 3.185 |

Gaus mean : 35.0618

Gaus Width: 3.4847



B0L102S, U20-ch2, adc2

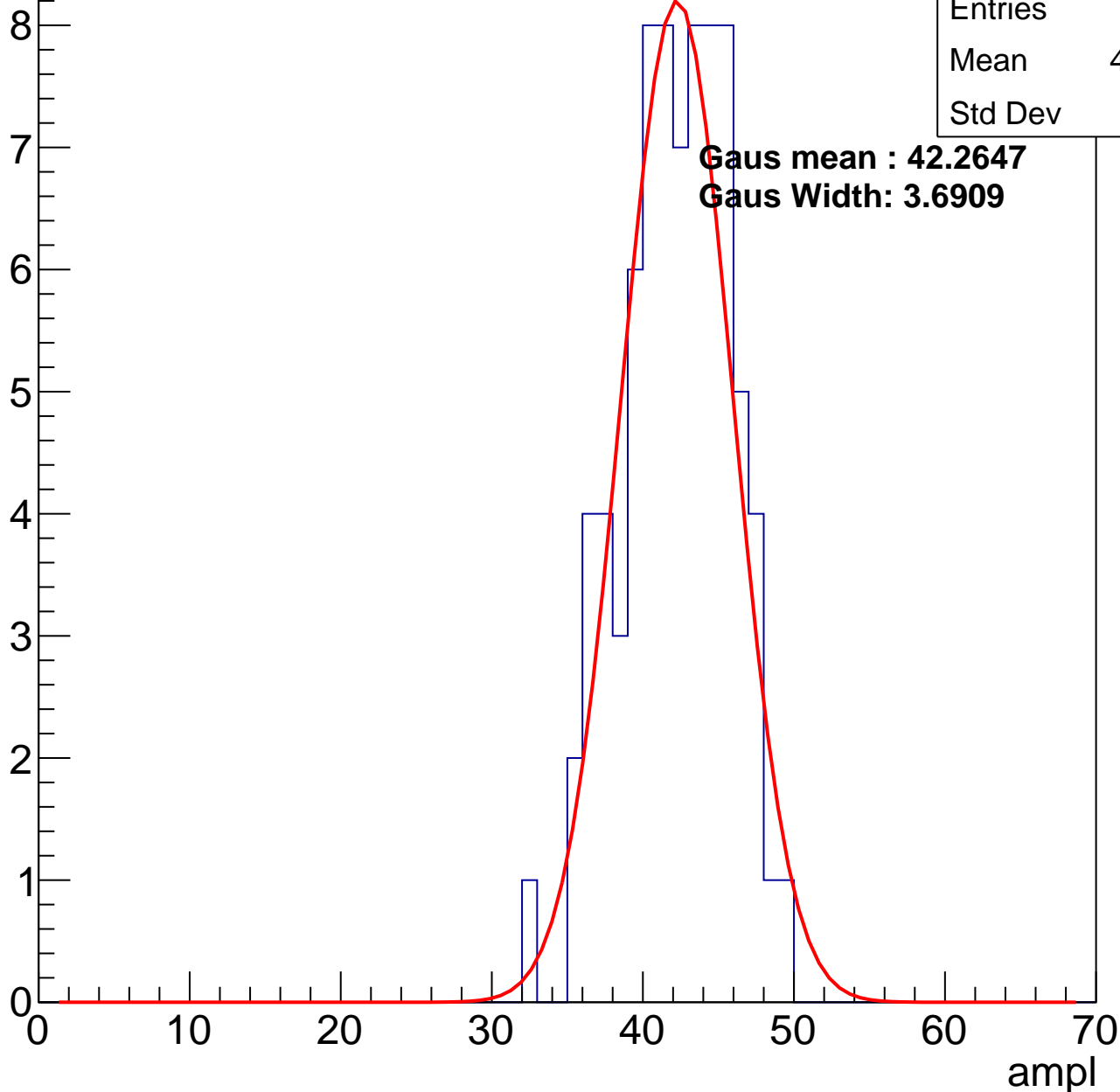
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 41.73 |
| Std Dev | 3.5 |

Gaus mean : 42.2647

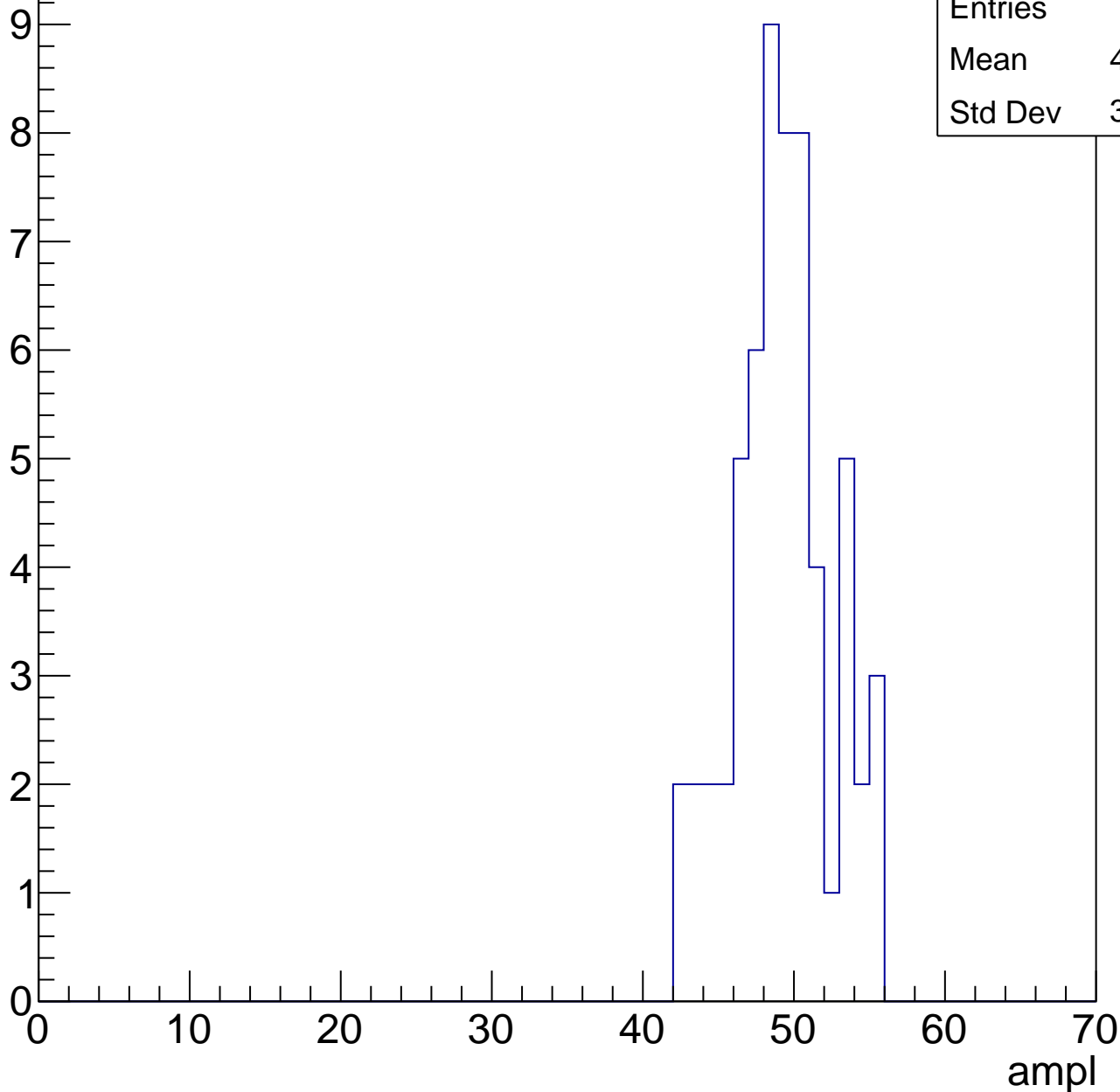
Gaus Width: 3.6909



B0L102S, U20-ch2, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



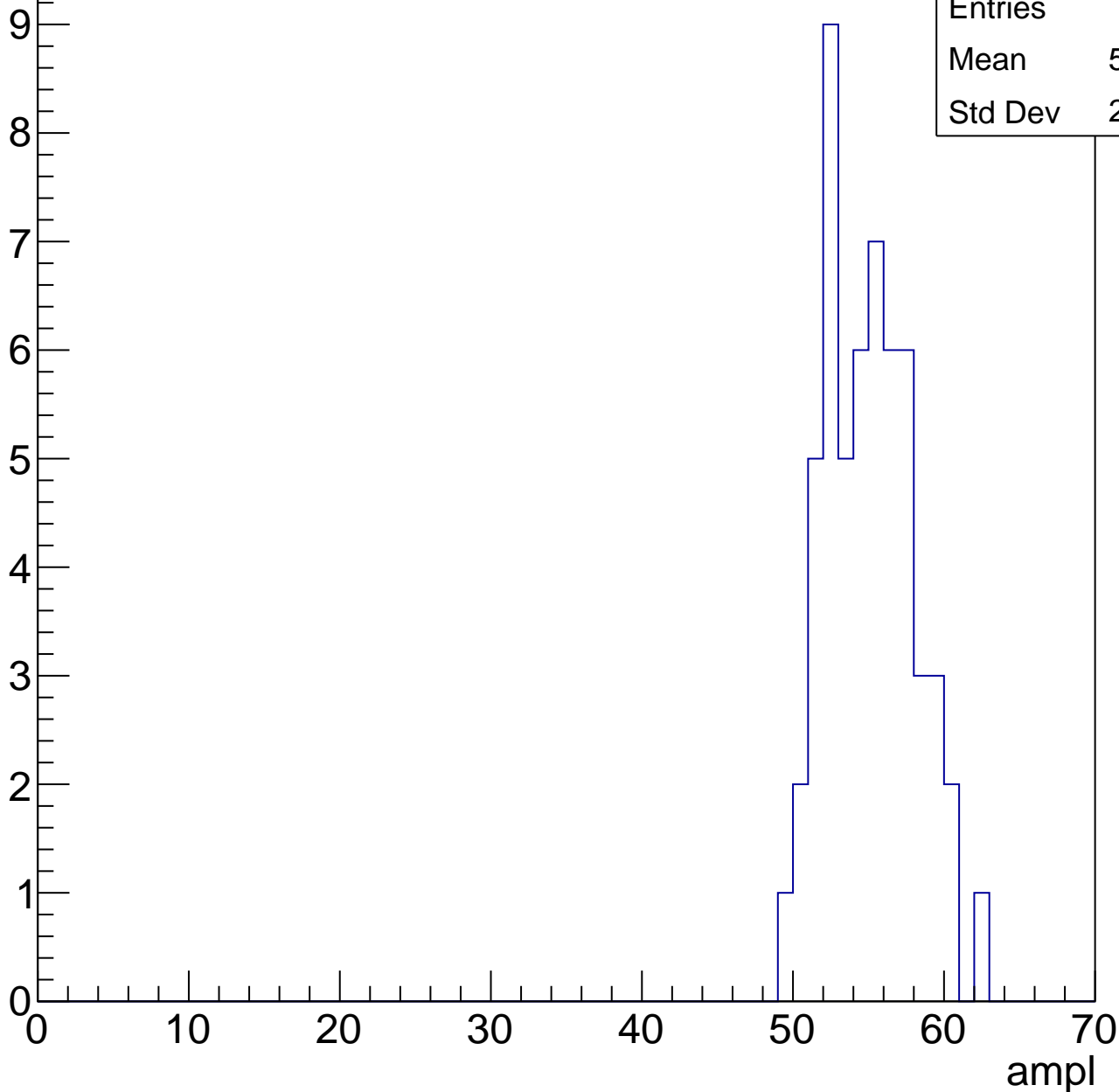
| | |
|---------|-------|
| Entries | 59 |
| Mean | 48.78 |
| Std Dev | 3.184 |

B0L102S, U20-ch2, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 54.59 |
| Std Dev | 2.889 |

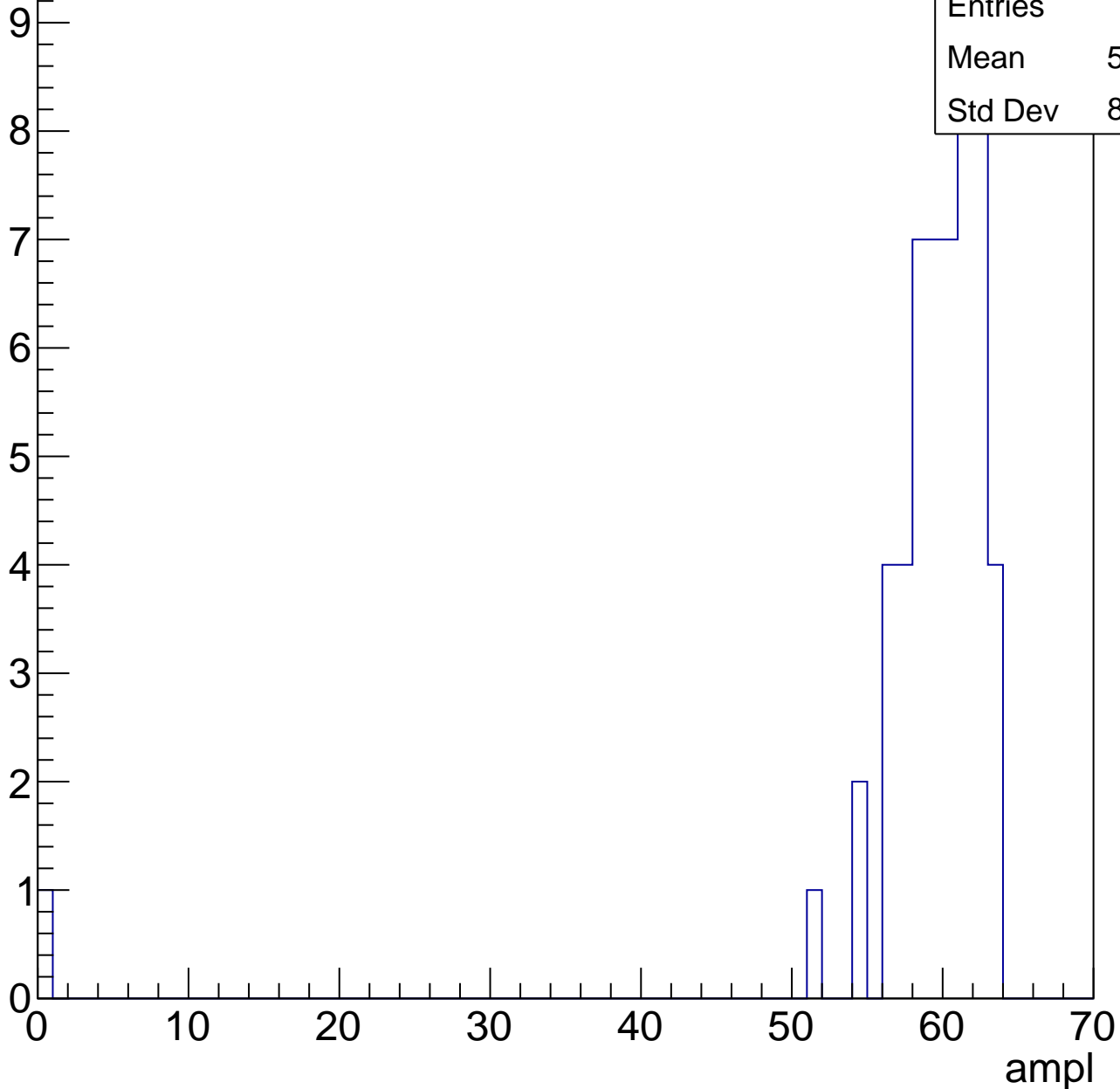


B0L102S, U20-ch2, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

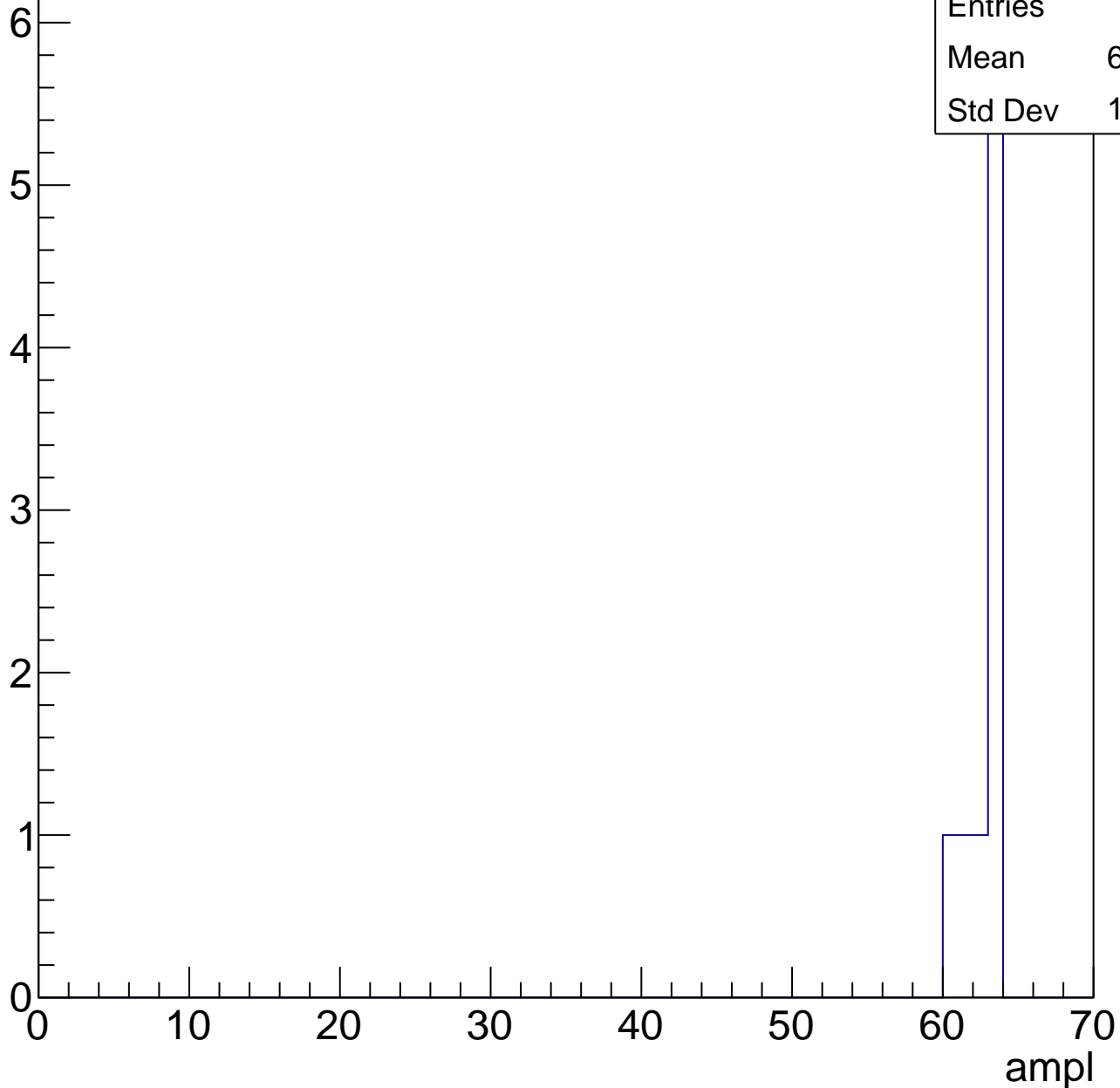
| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.28 |
| Std Dev | 8.394 |



B0L102S, U20-ch2, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 9 |
| Mean | 62.33 |
| Std Dev | 1.054 |

B0L102S, U20-ch2, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch3, adc0

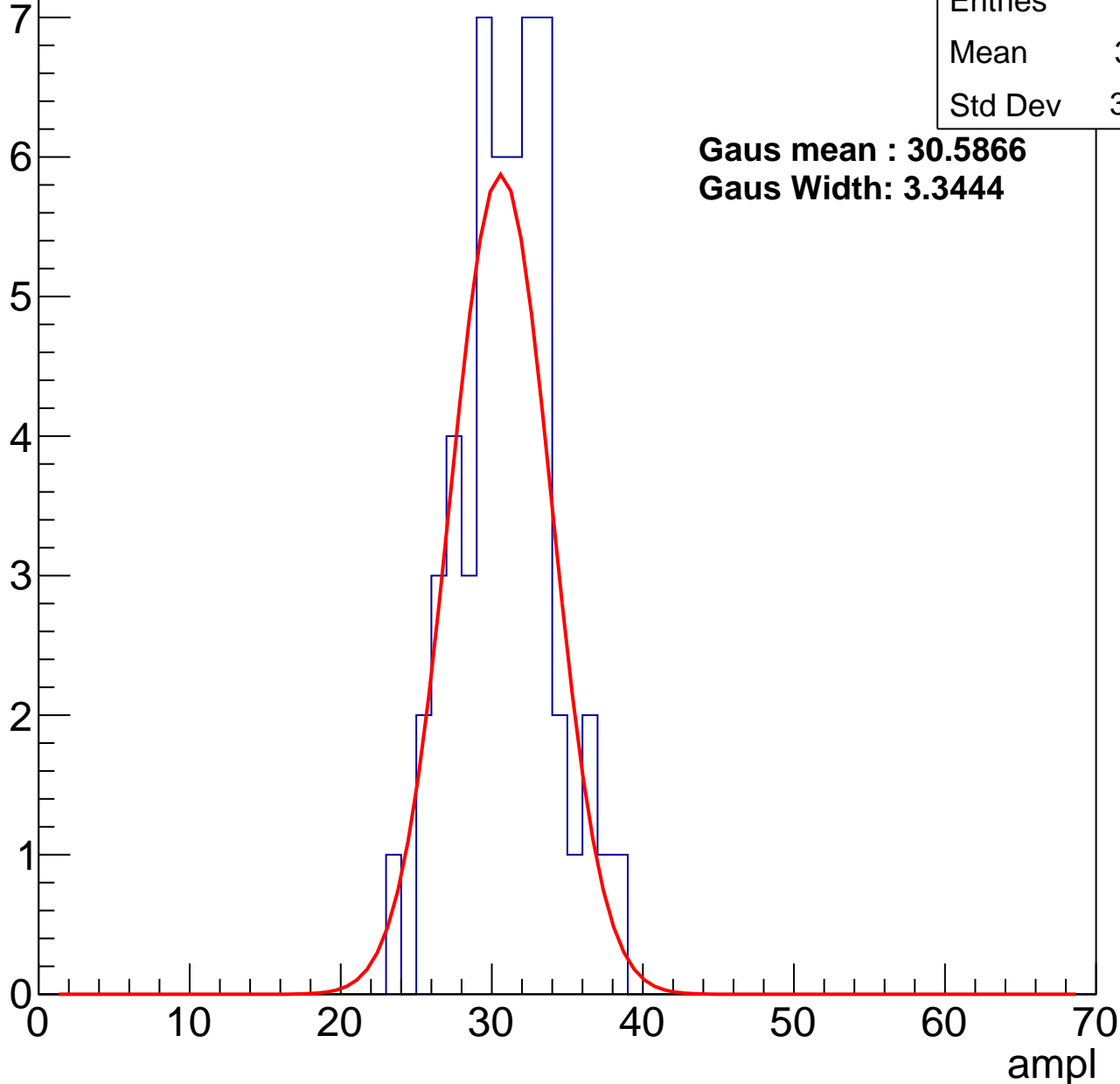
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 30.51 |
| Std Dev | 3.154 |

Gaus mean : 30.5866

Gaus Width: 3.3444



B0L102S, U20-ch3, adc1

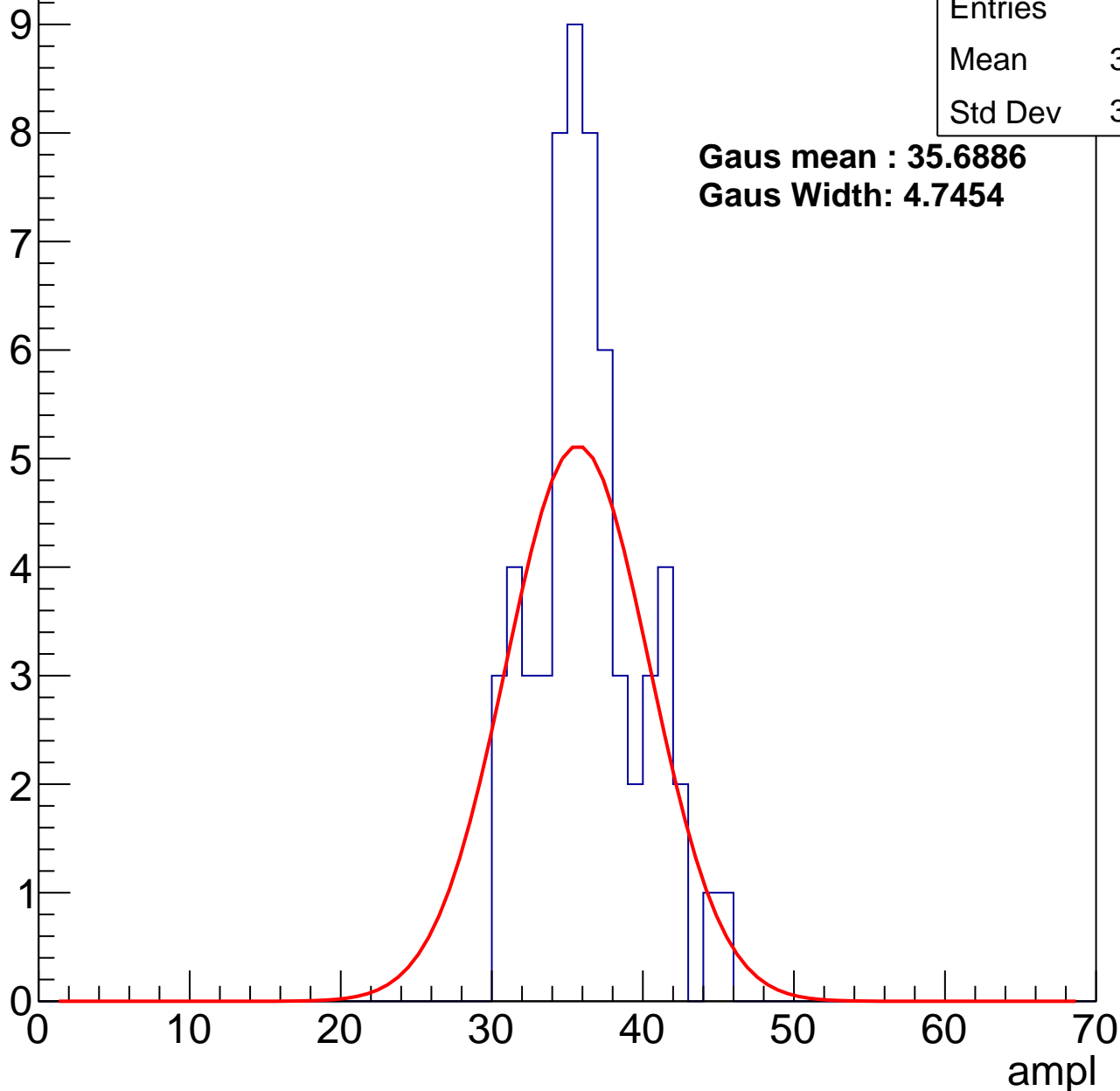
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 35.92 |
| Std Dev | 3.475 |

Gaus mean : 35.6886

Gaus Width: 4.7454



B0L102S, U20-ch3, adc2

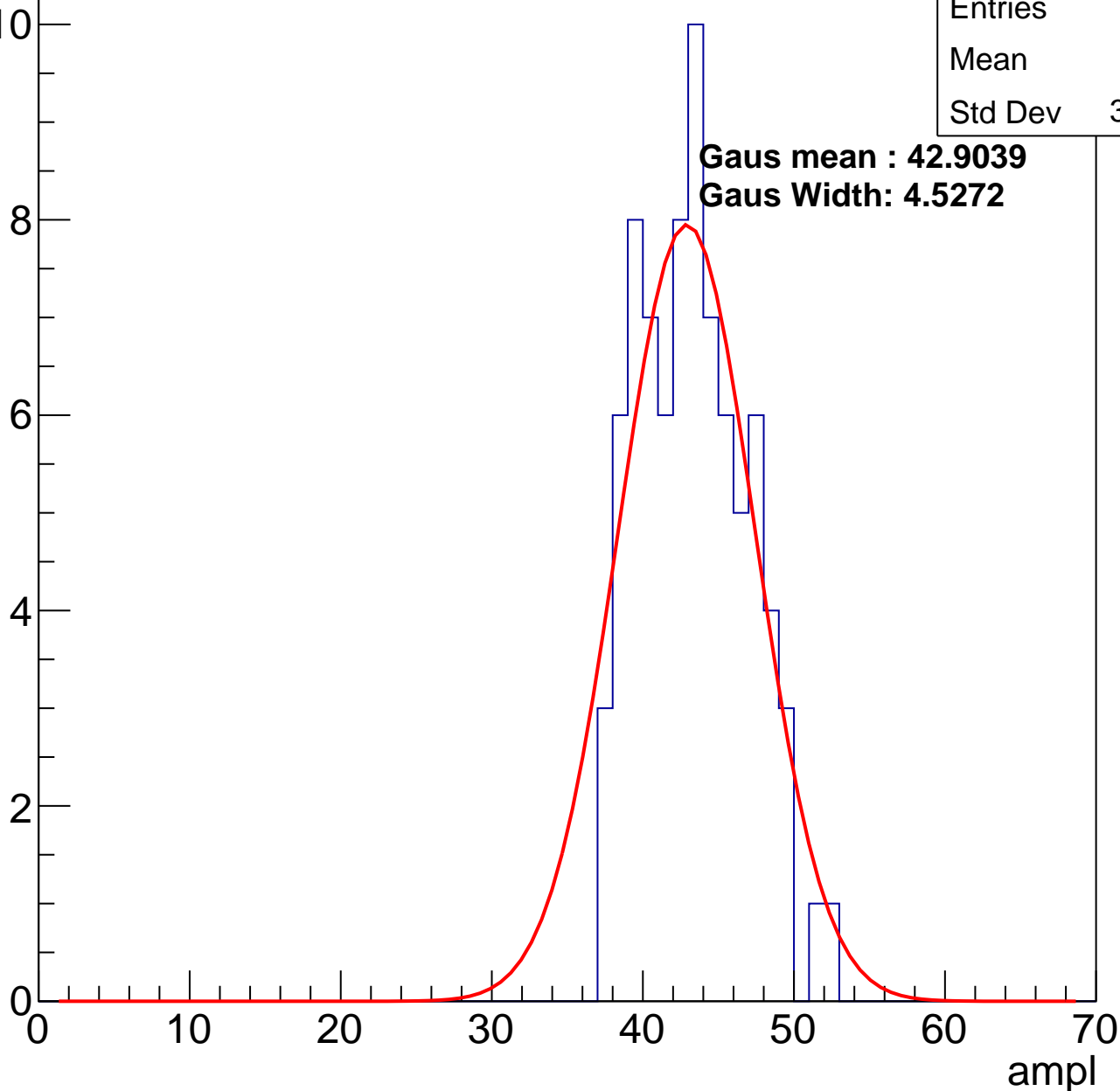
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 42.9 |
| Std Dev | 3.523 |

Gaus mean : 42.9039

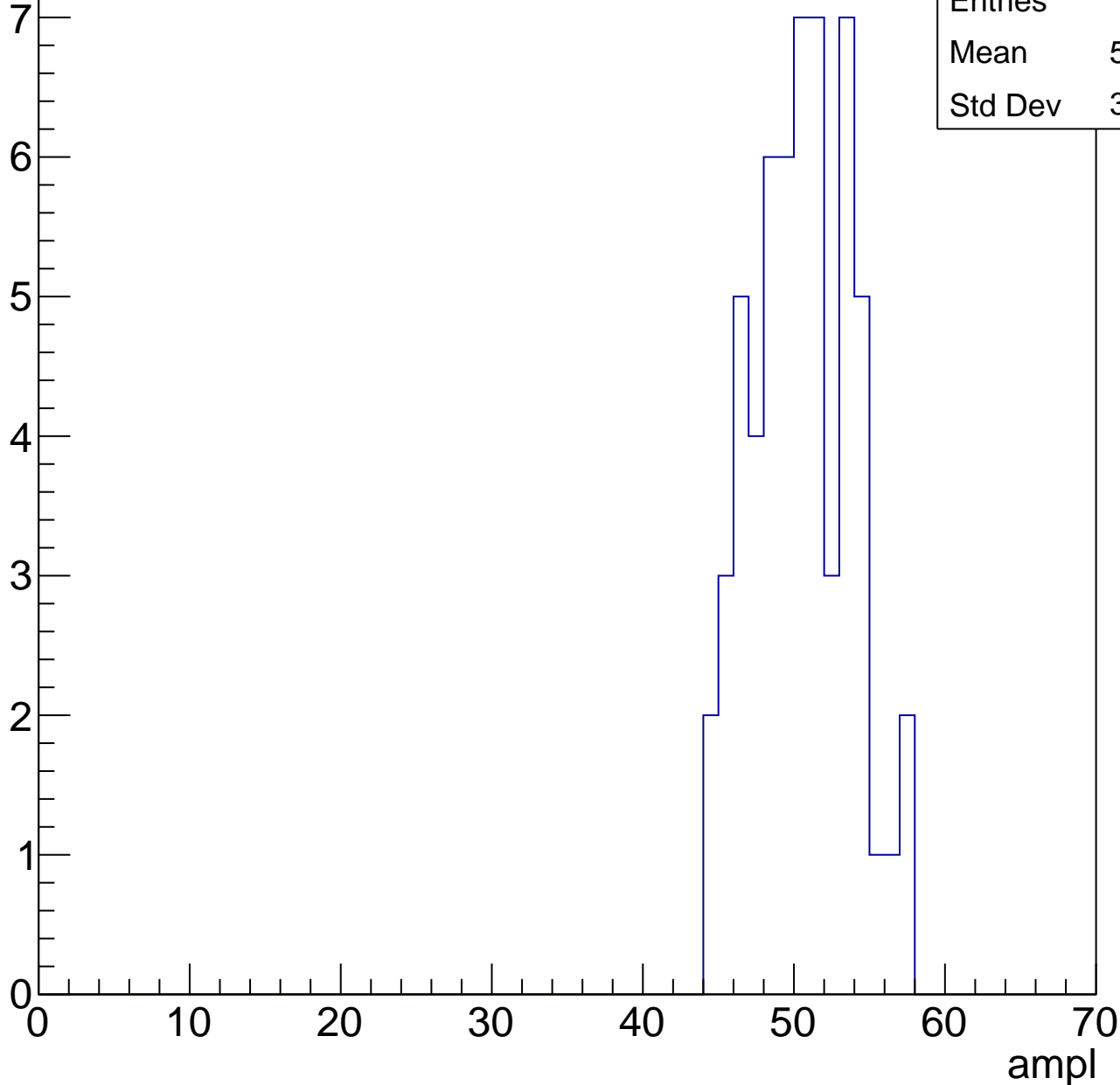
Gaus Width: 4.5272



B0L102S, U20-ch3, adc3

calib_packv5_042523_0143.root, FC#12, port B1

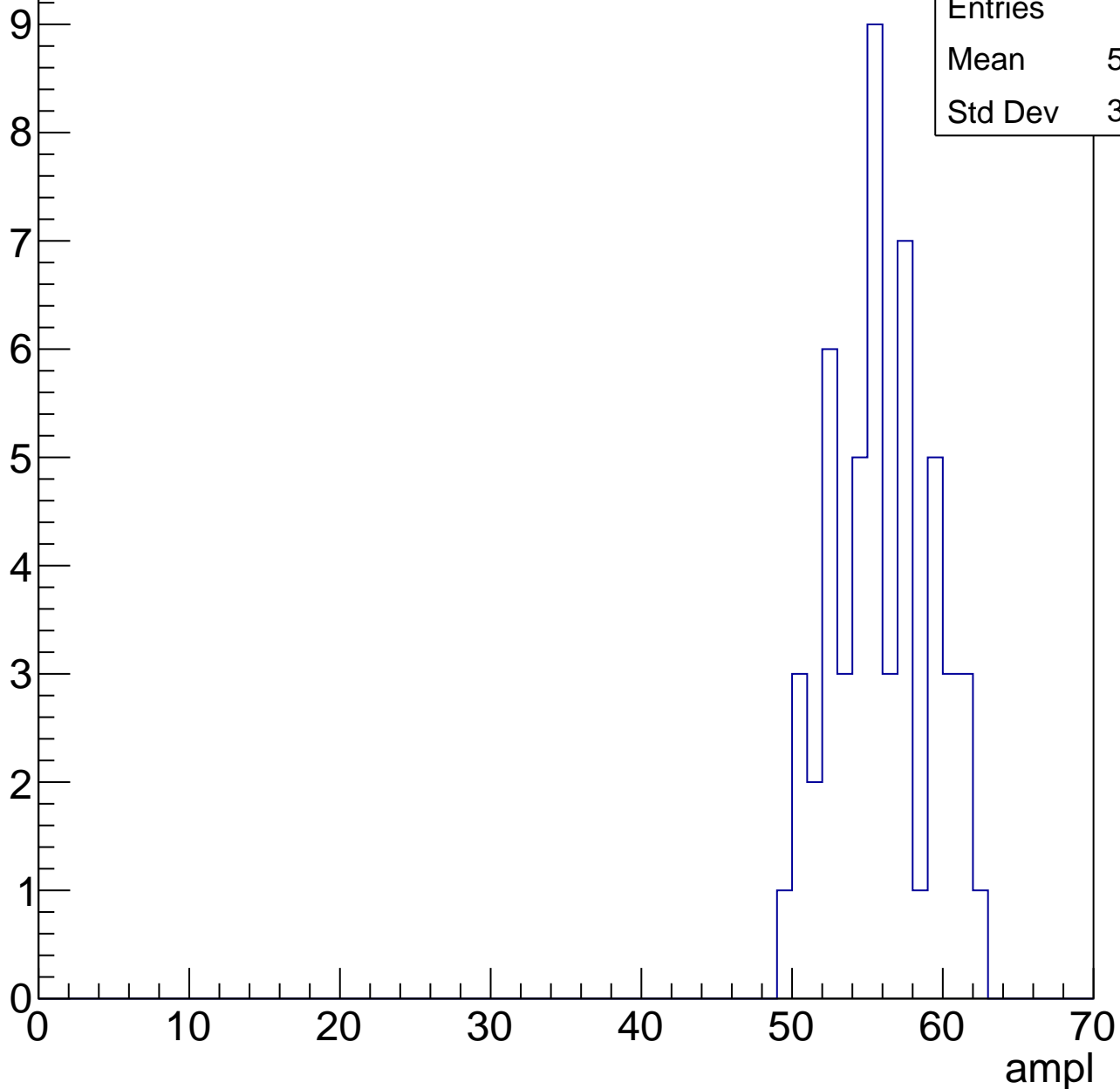
Entry



B0L102S, U20-ch3, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

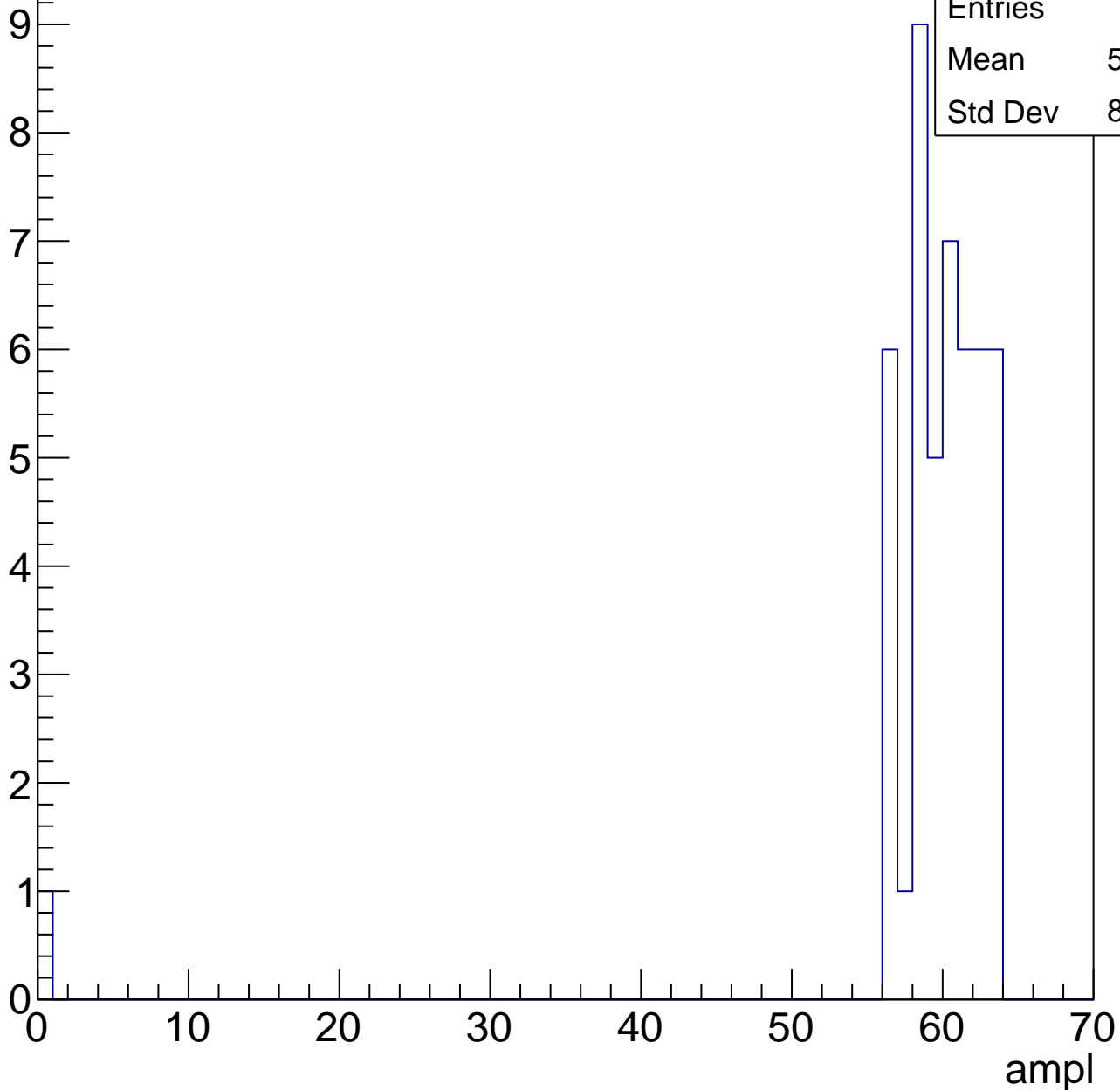


B0L102S, U20-ch3, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

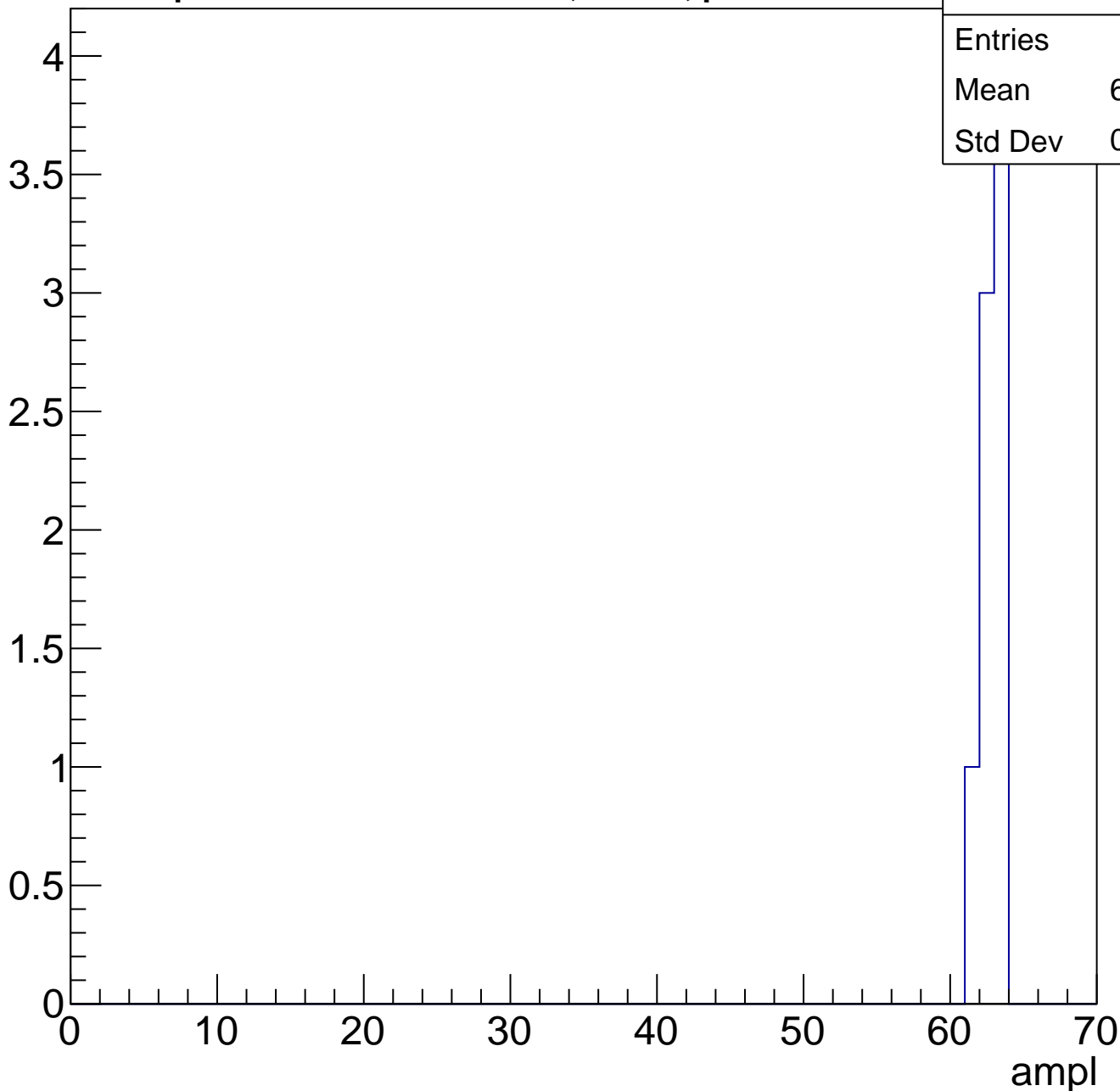
| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.43 |
| Std Dev | 8.889 |



B0L102S, U20-ch3, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch3, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch4, adc0

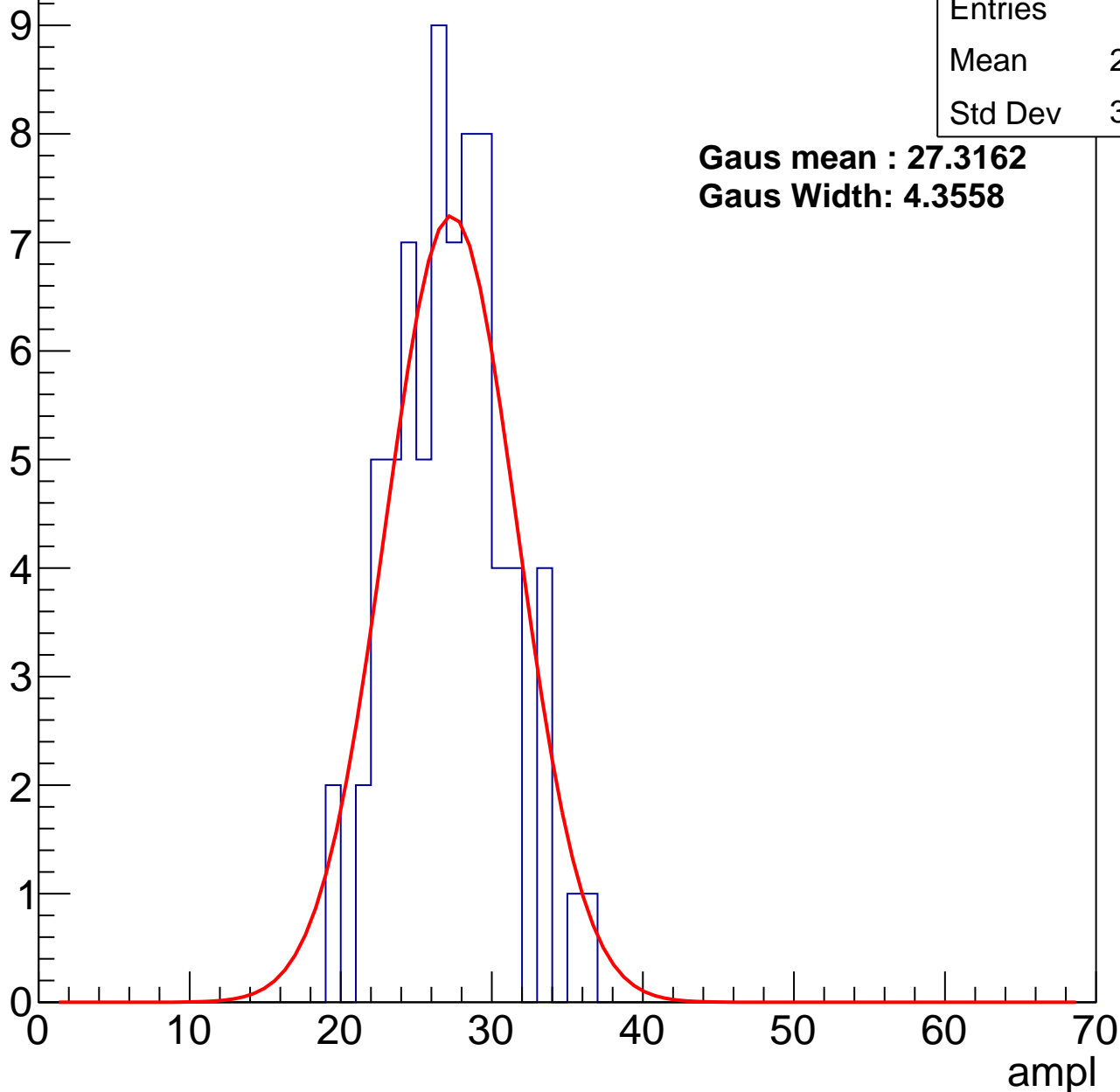
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 26.72 |
| Std Dev | 3.575 |

Gaus mean : 27.3162

Gaus Width: 4.3558



B0L102S, U20-ch4, adc1

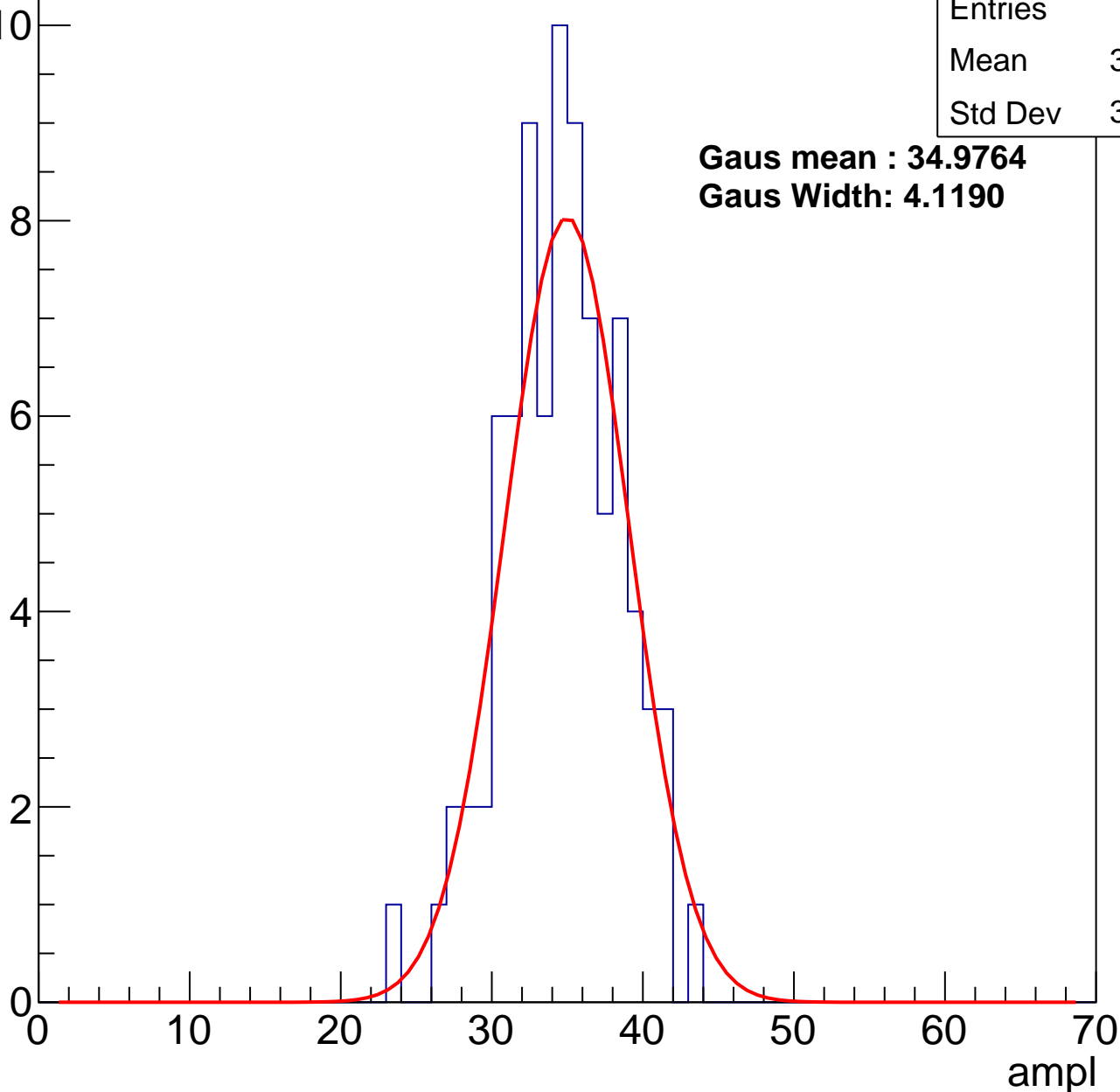
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 34.15 |
| Std Dev | 3.819 |

Gaus mean : 34.9764

Gaus Width: 4.1190



B0L102S, U20-ch4, adc2

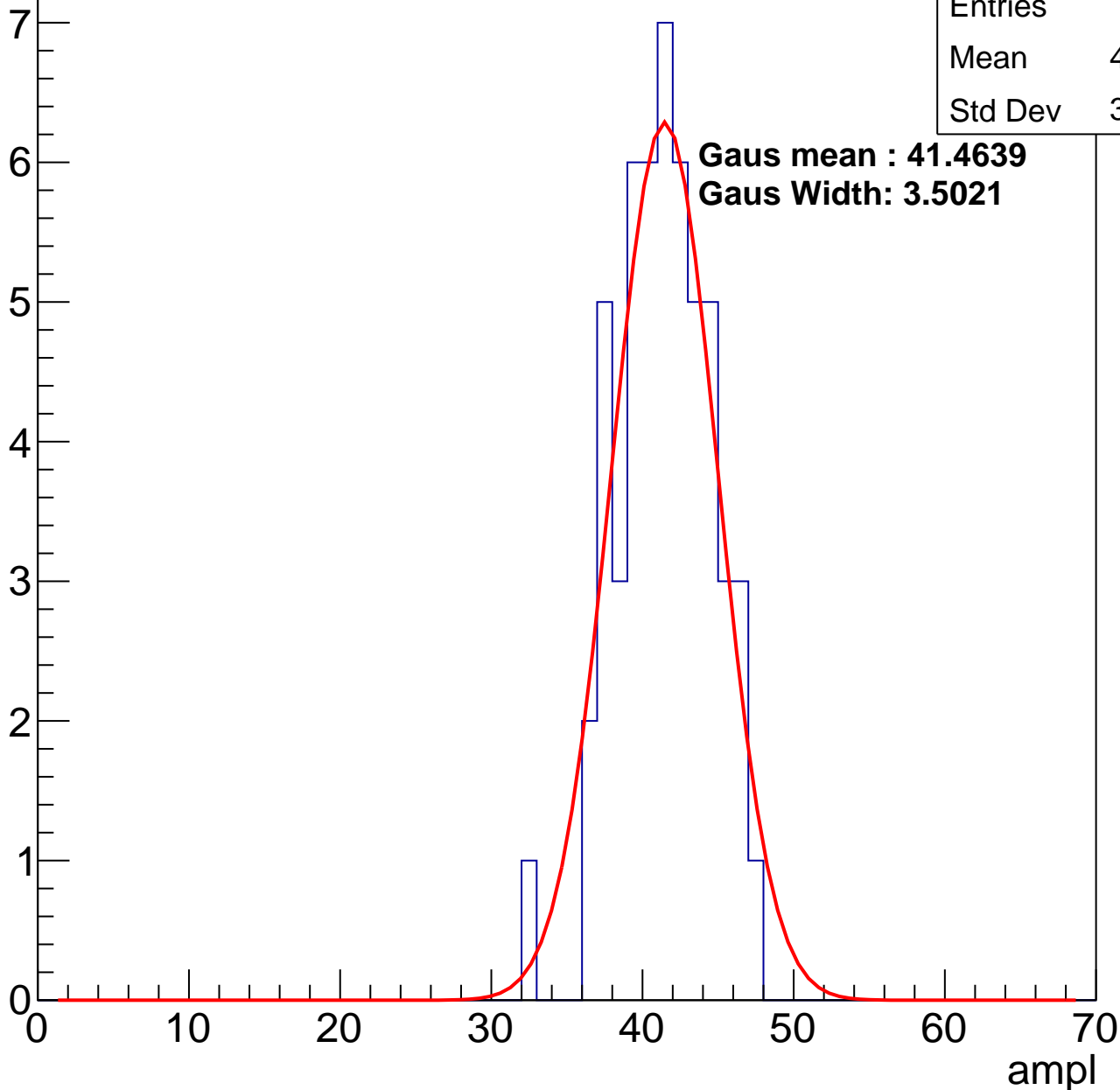
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 40.96 |
| Std Dev | 3.065 |

Gaus mean : 41.4639

Gaus Width: 3.5021

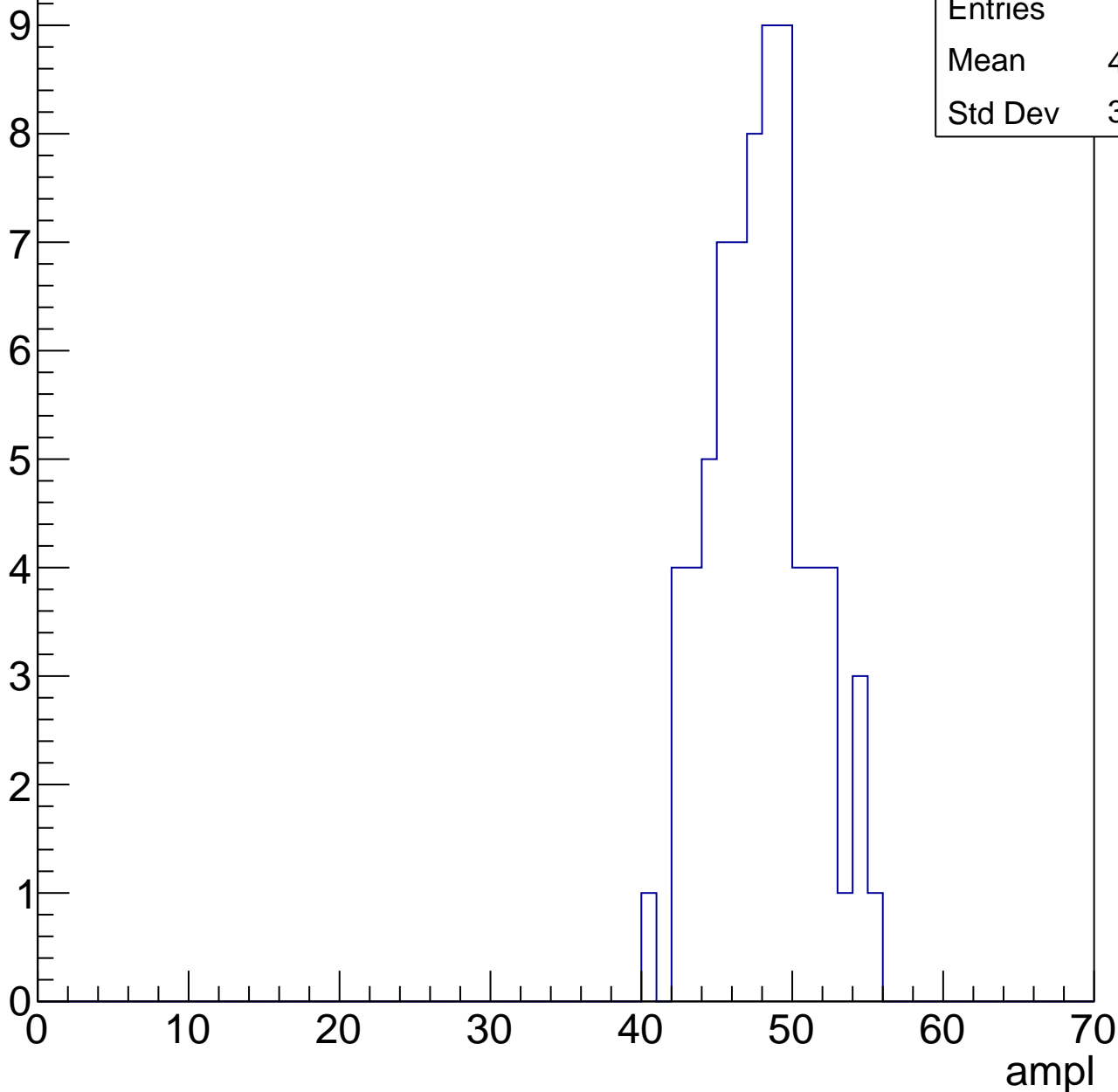


B0L102S, U20-ch4, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 47.44 |
| Std Dev | 3.296 |



B0L102S, U20-ch4, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 57

Mean 53.75

Std Dev 2.891

1

2

3

4

5

6

7

B0L102S, U20-ch4, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

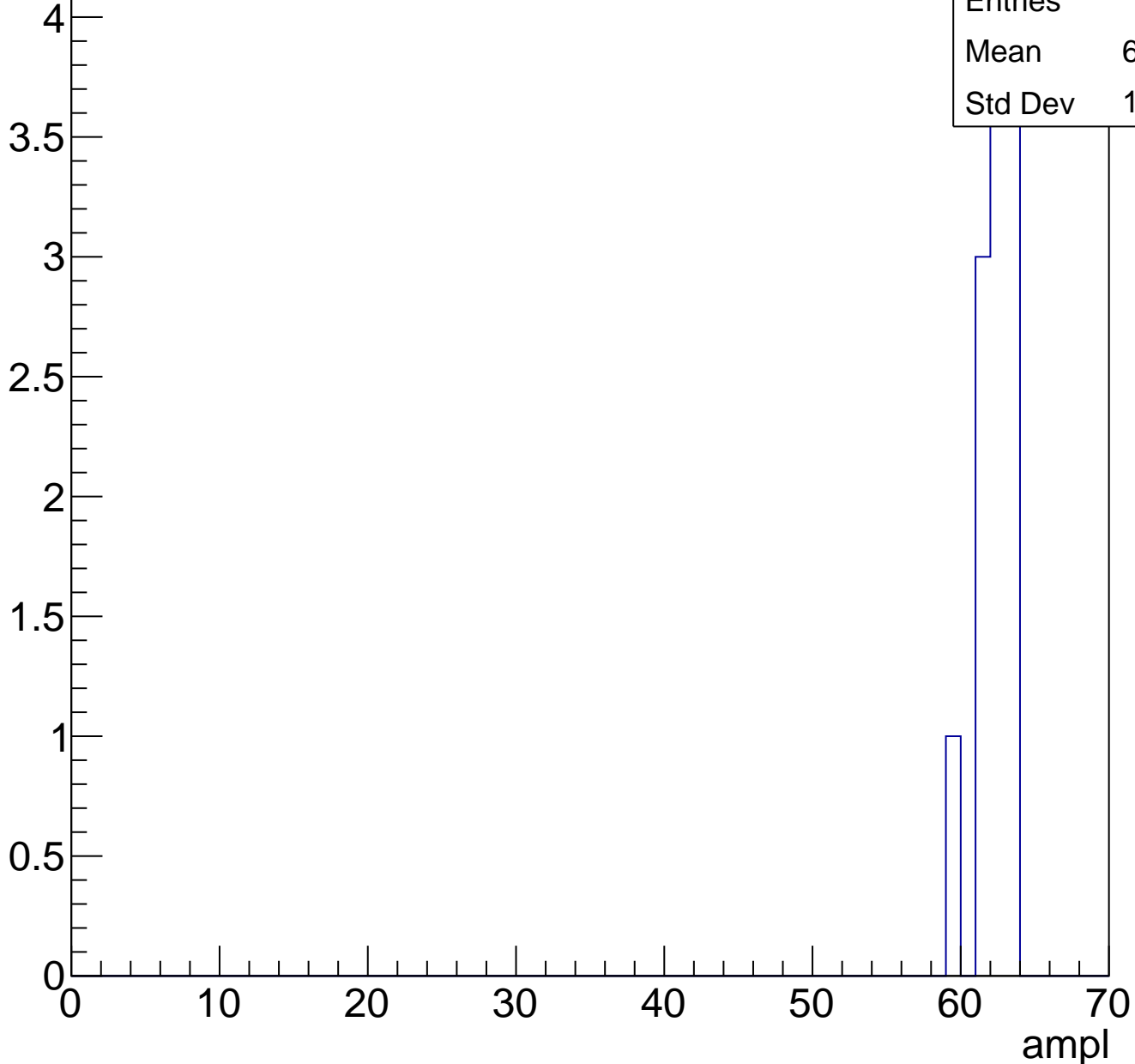
ampl

| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.03 |
| Std Dev | 8.181 |

B0L102S, U20-ch4, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch4, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch5, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 30.64 |
| Std Dev | 3.385 |

Gaus mean : 31.2456

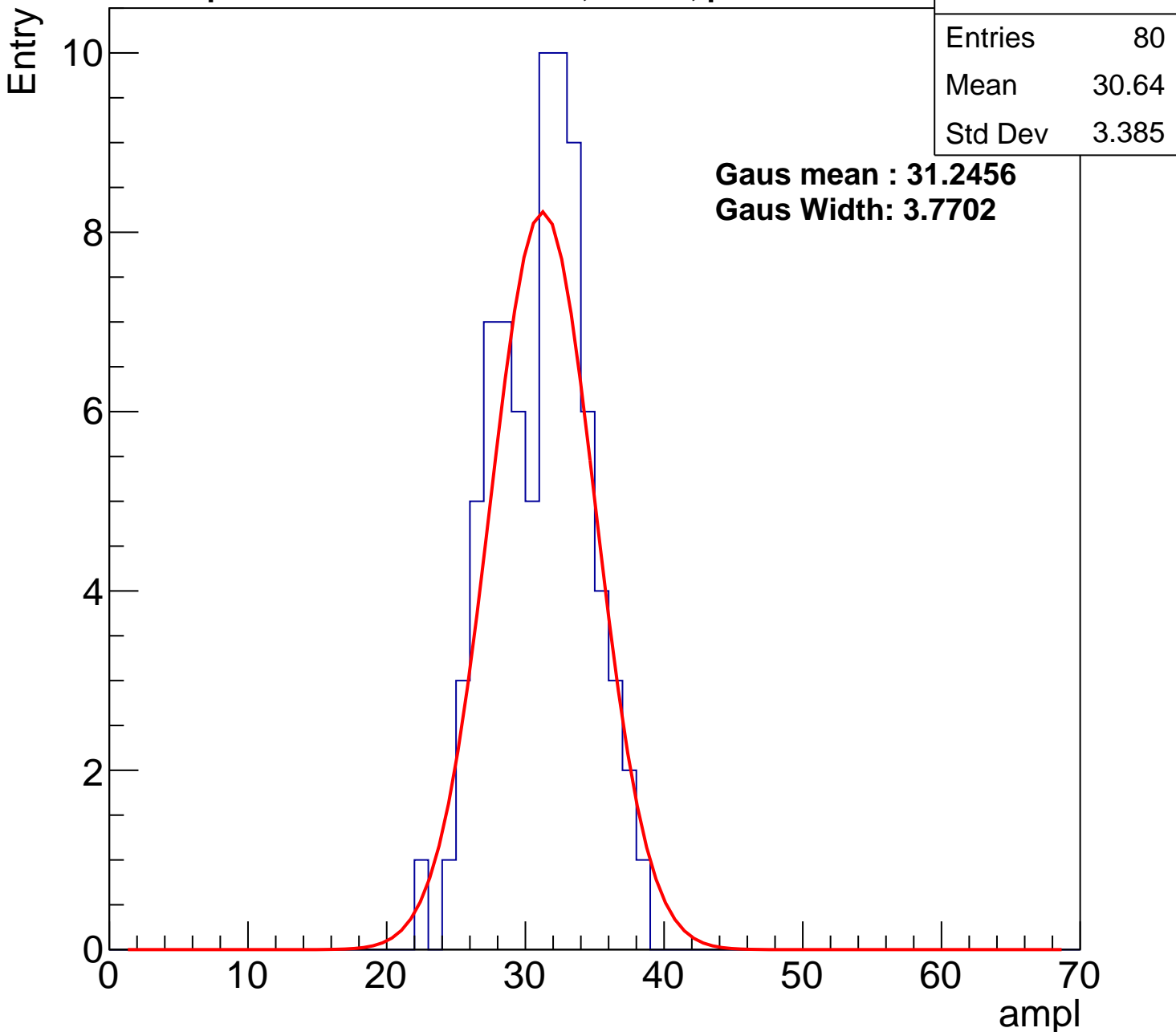
Gaus Width: 3.7702

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch5, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 79 |
| Mean | 39.19 |
| Std Dev | 3.057 |

Gaus mean : 39.5426

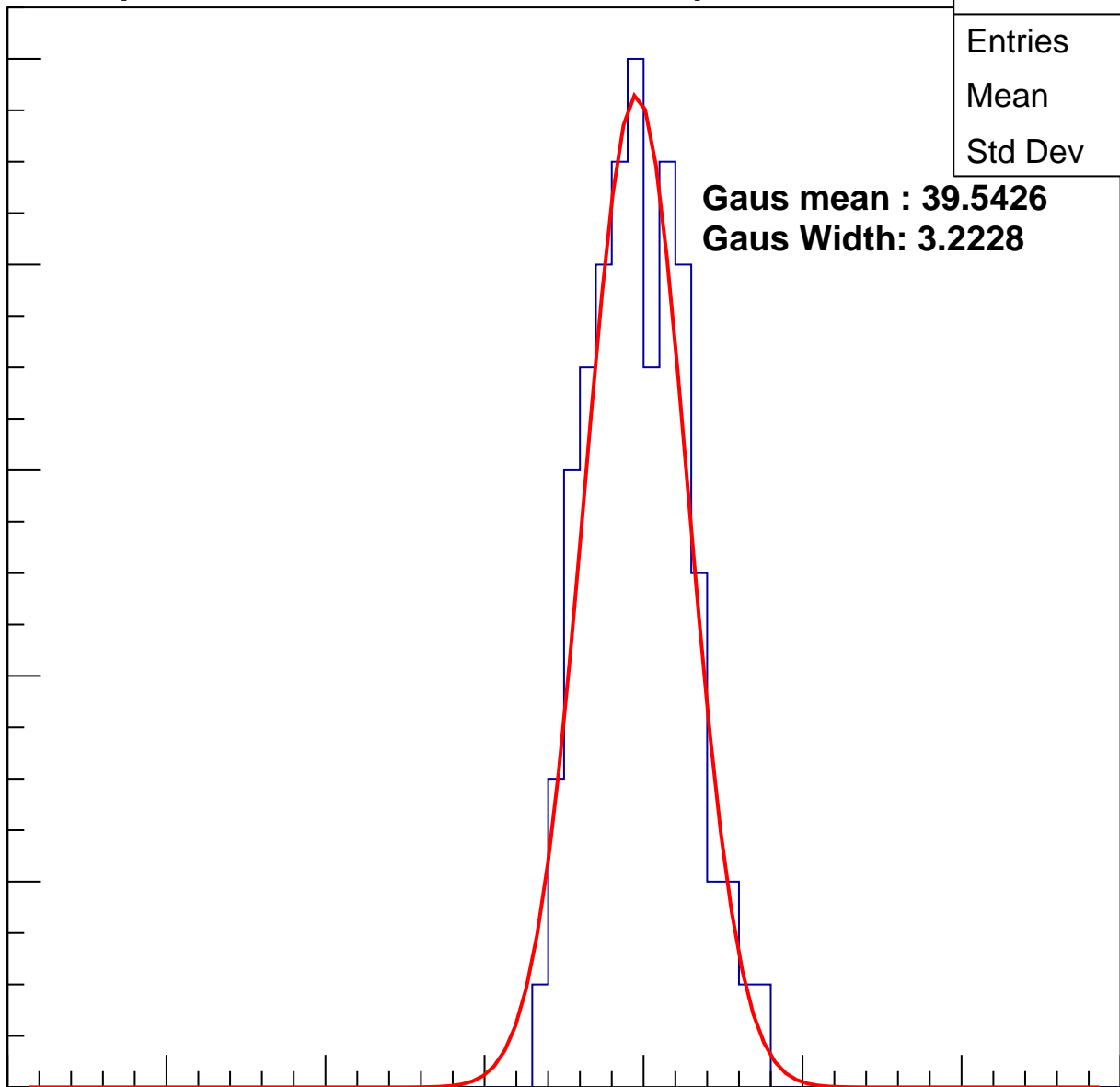
Gaus Width: 3.2228

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U20-ch5, adc2

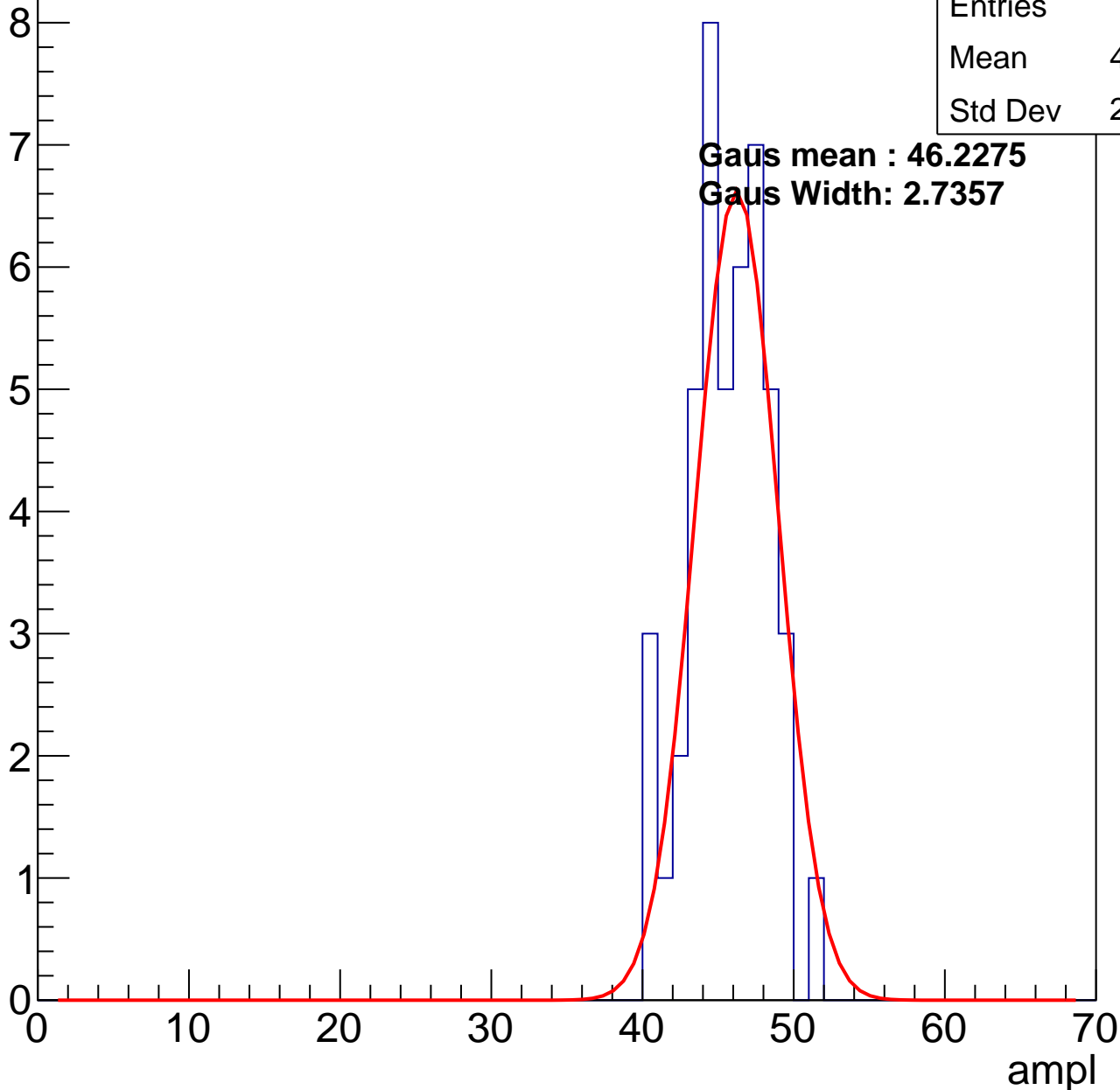
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 45.22 |
| Std Dev | 2.544 |

Gaus mean : 46.2275

Gaus Width: 2.7357

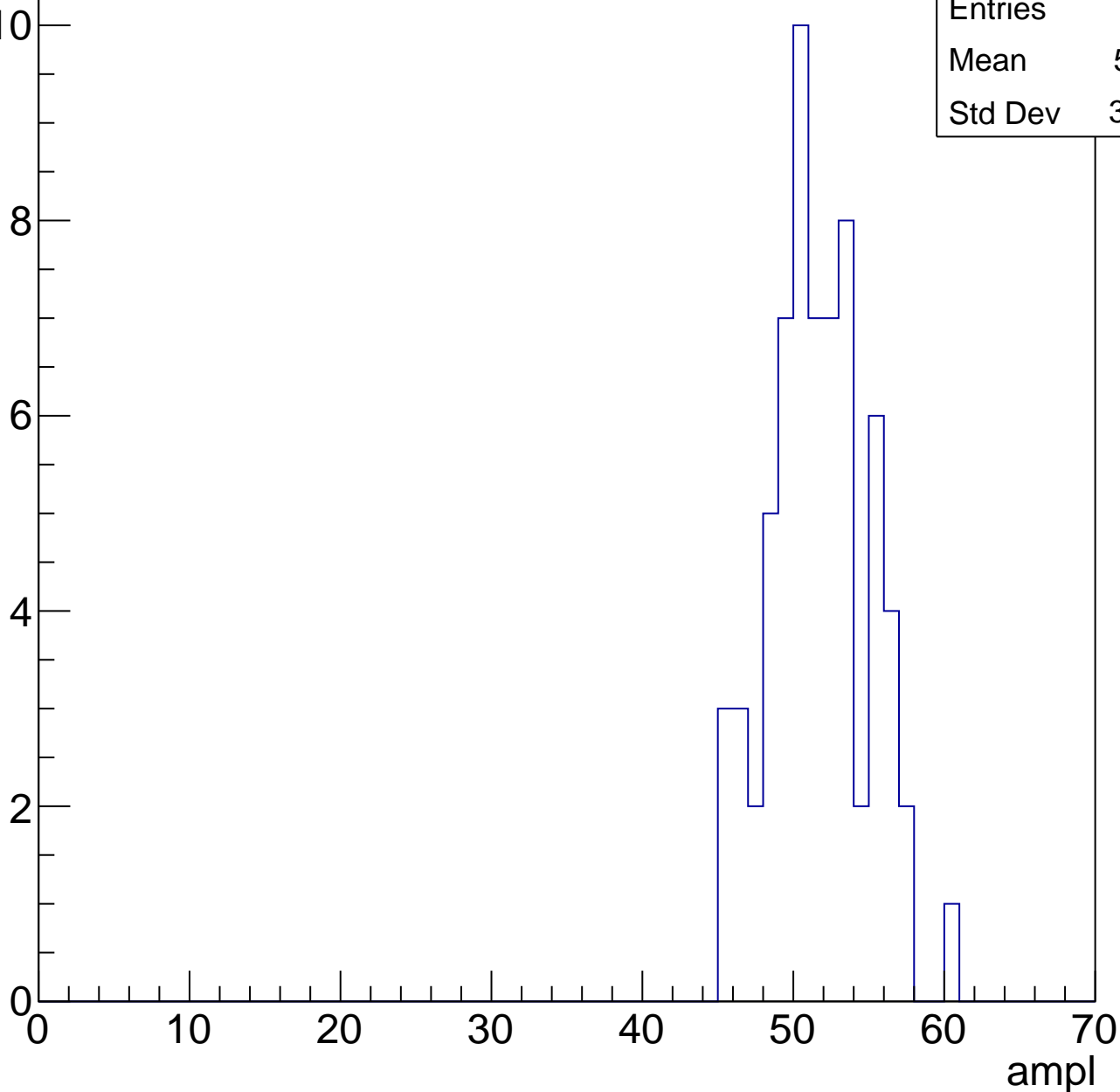


B0L102S, U20-ch5, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 51.21 |
| Std Dev | 3.235 |



B0L102S, U20-ch5, adc4

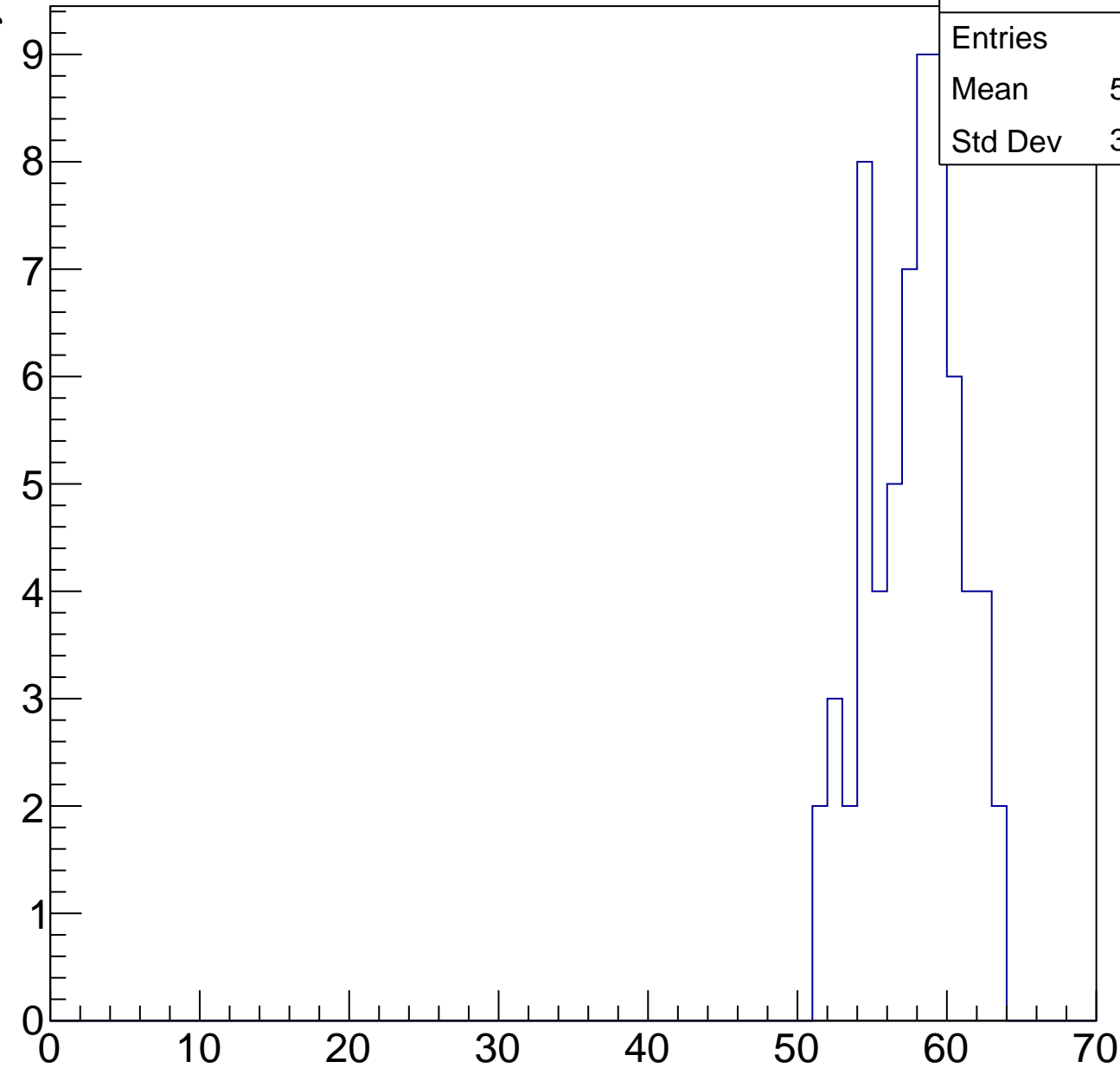
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 65 |
| Mean | 57.32 |
| Std Dev | 3.039 |

ampl

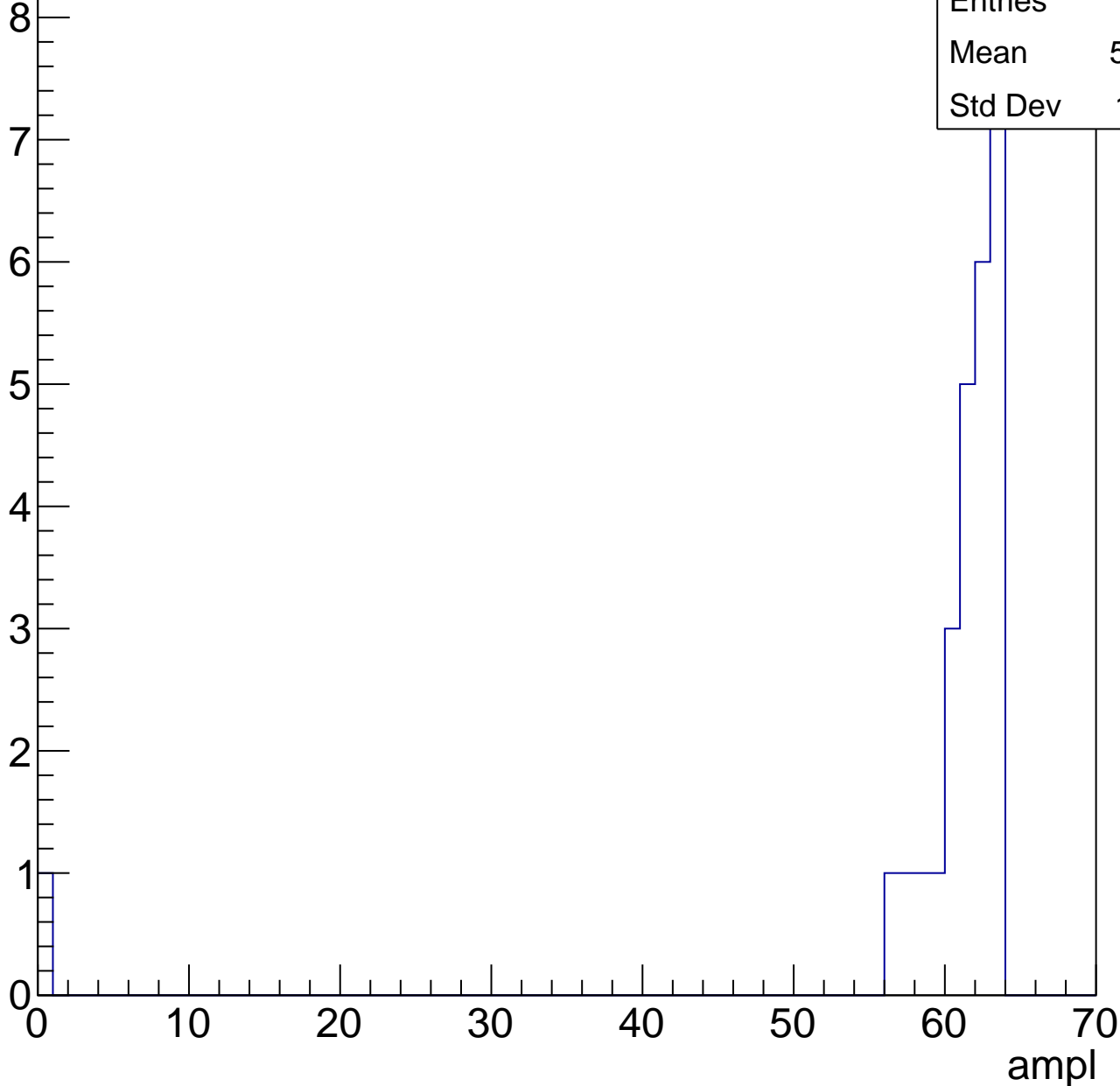


B0L102S, U20-ch5, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

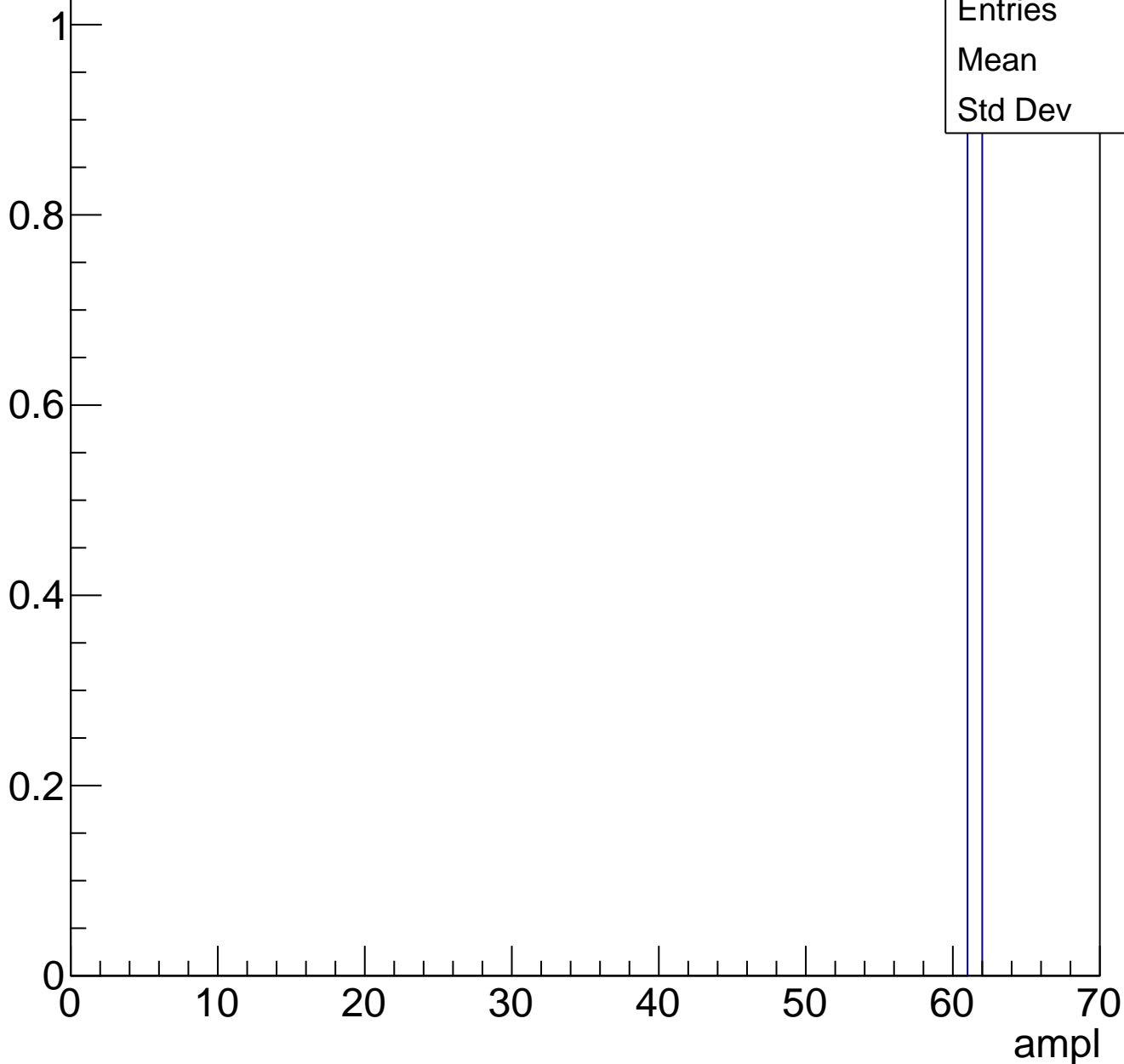
| | |
|---------|-------|
| Entries | 27 |
| Mean | 58.93 |
| Std Dev | 11.71 |



B0L102S, U20-ch5, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch5, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

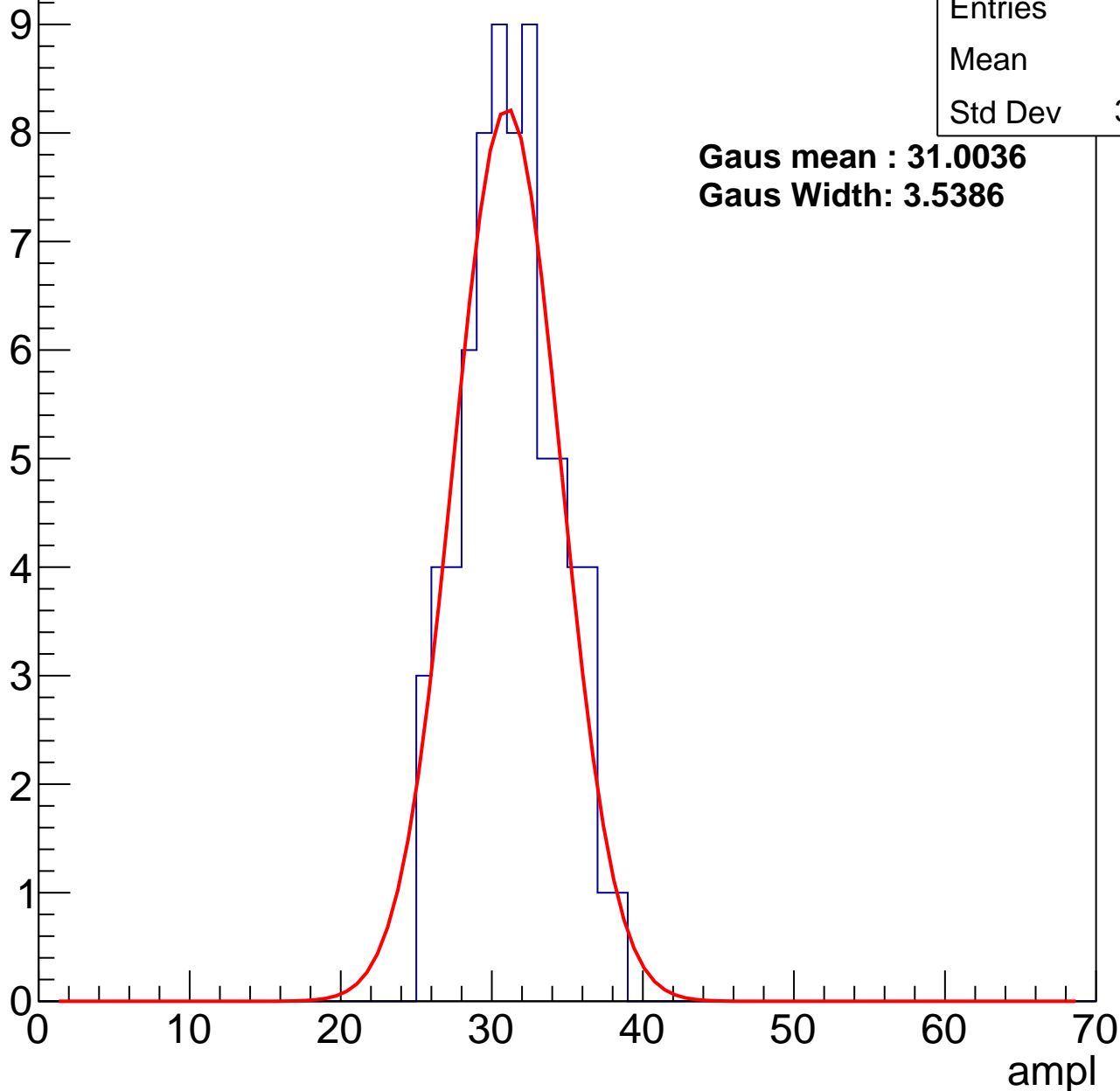


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch6, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch6, adc1

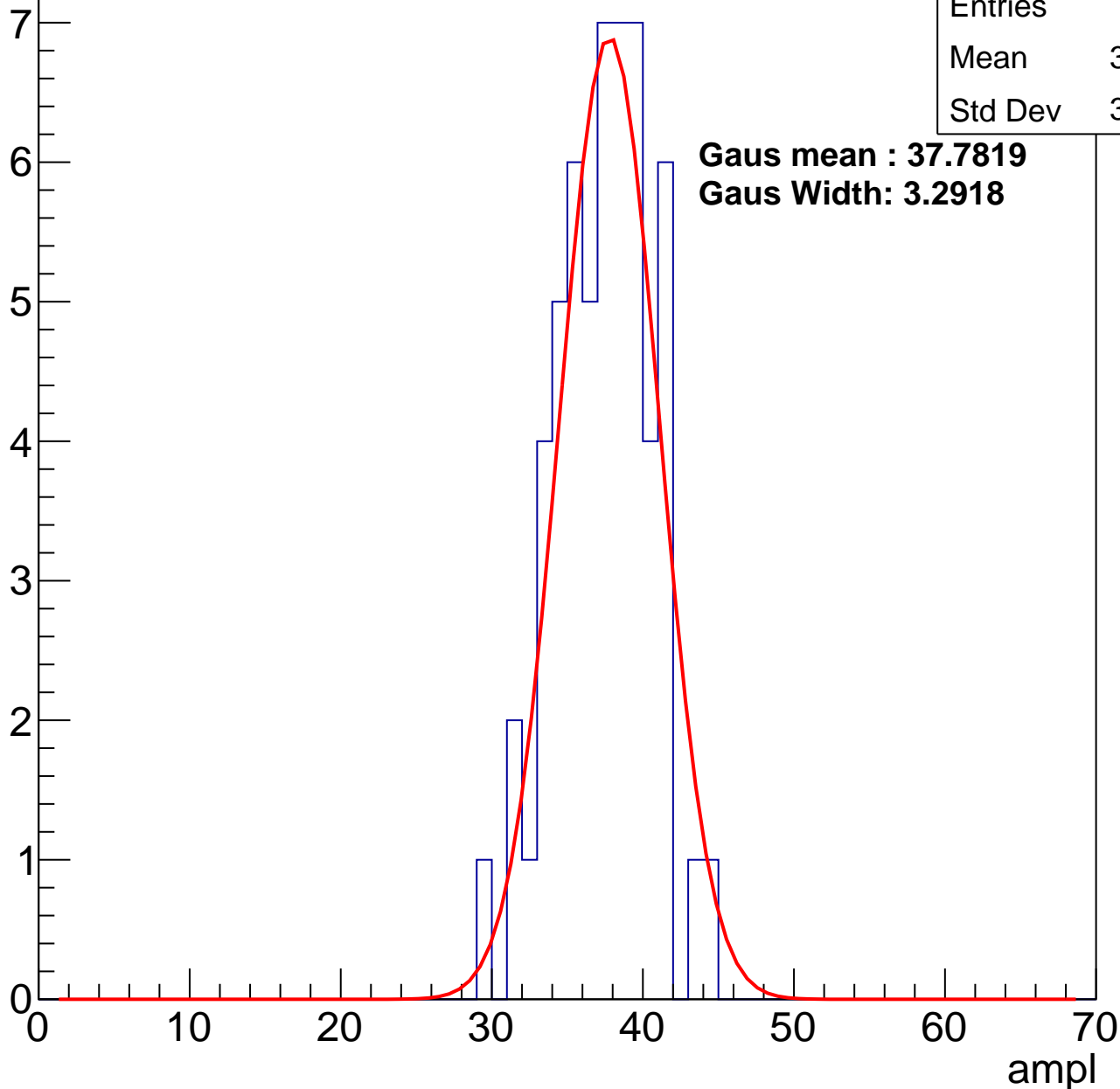
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 36.95 |
| Std Dev | 3.109 |

Gaus mean : 37.7819

Gaus Width: 3.2918



B0L102S, U20-ch6, adc2

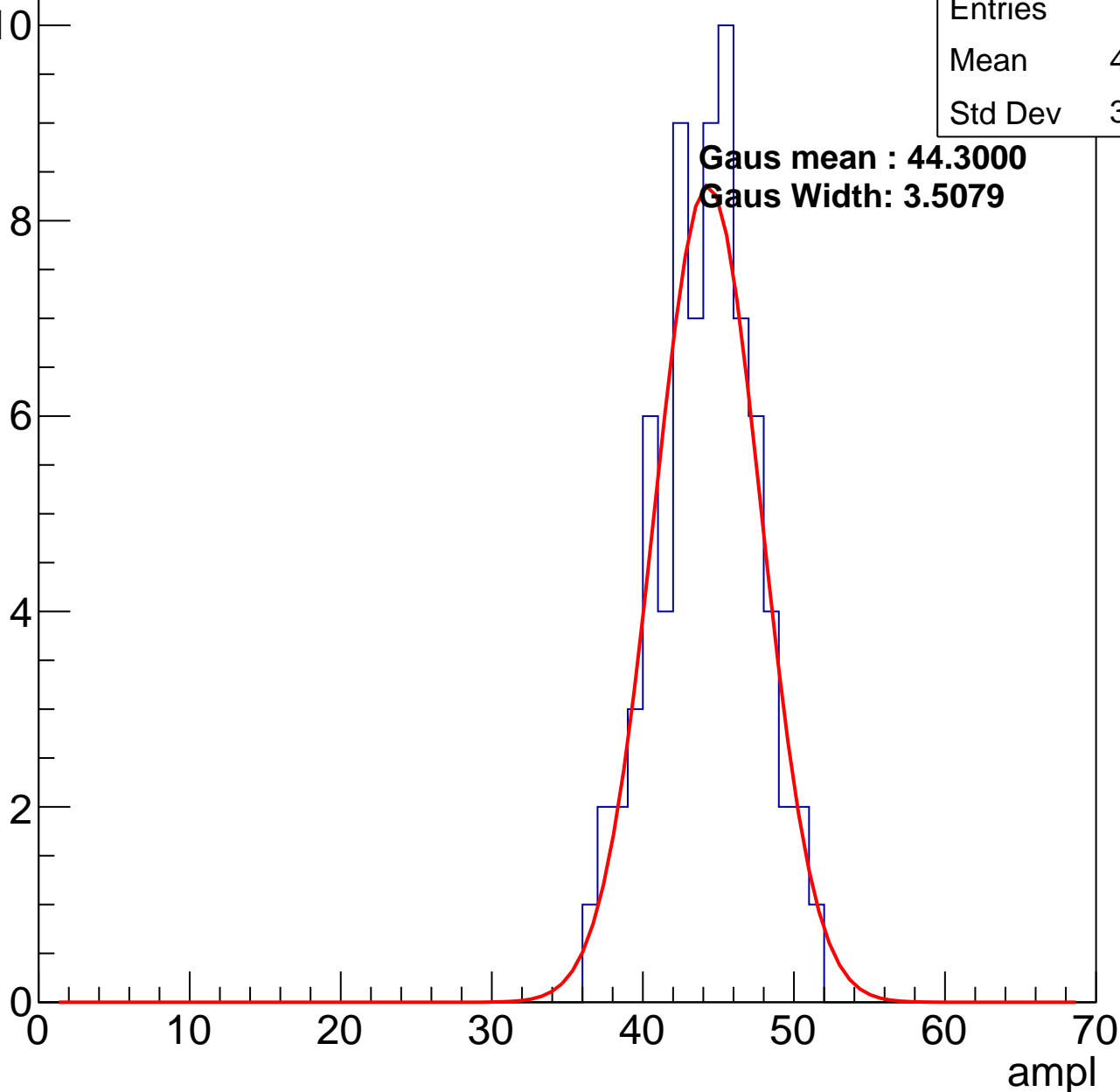
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 43.69 |
| Std Dev | 3.274 |

Gaus mean : 44.3000

Gaus Width: 3.5079

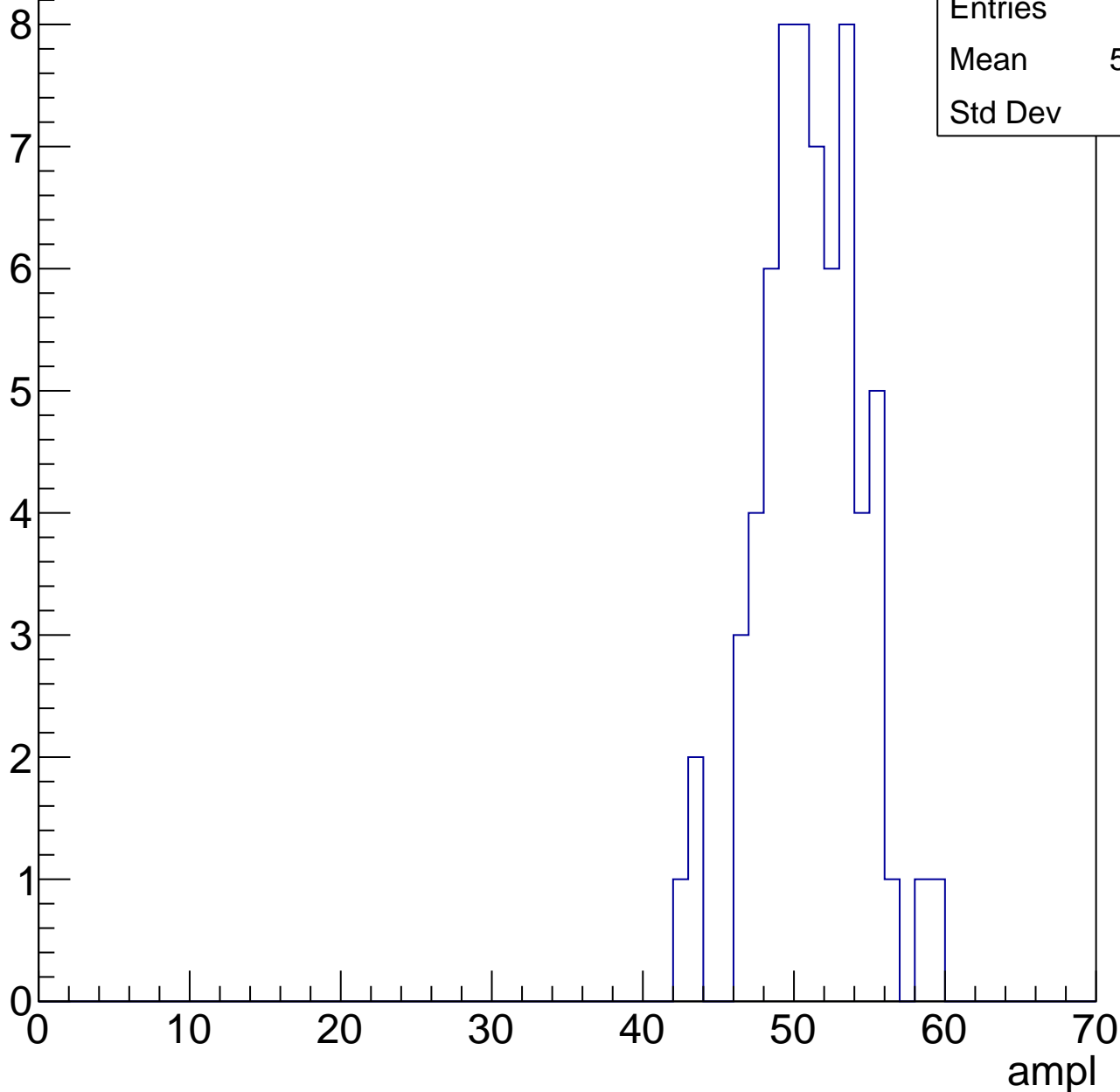


B0L102S, U20-ch6, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.63 |
| Std Dev | 3.34 |

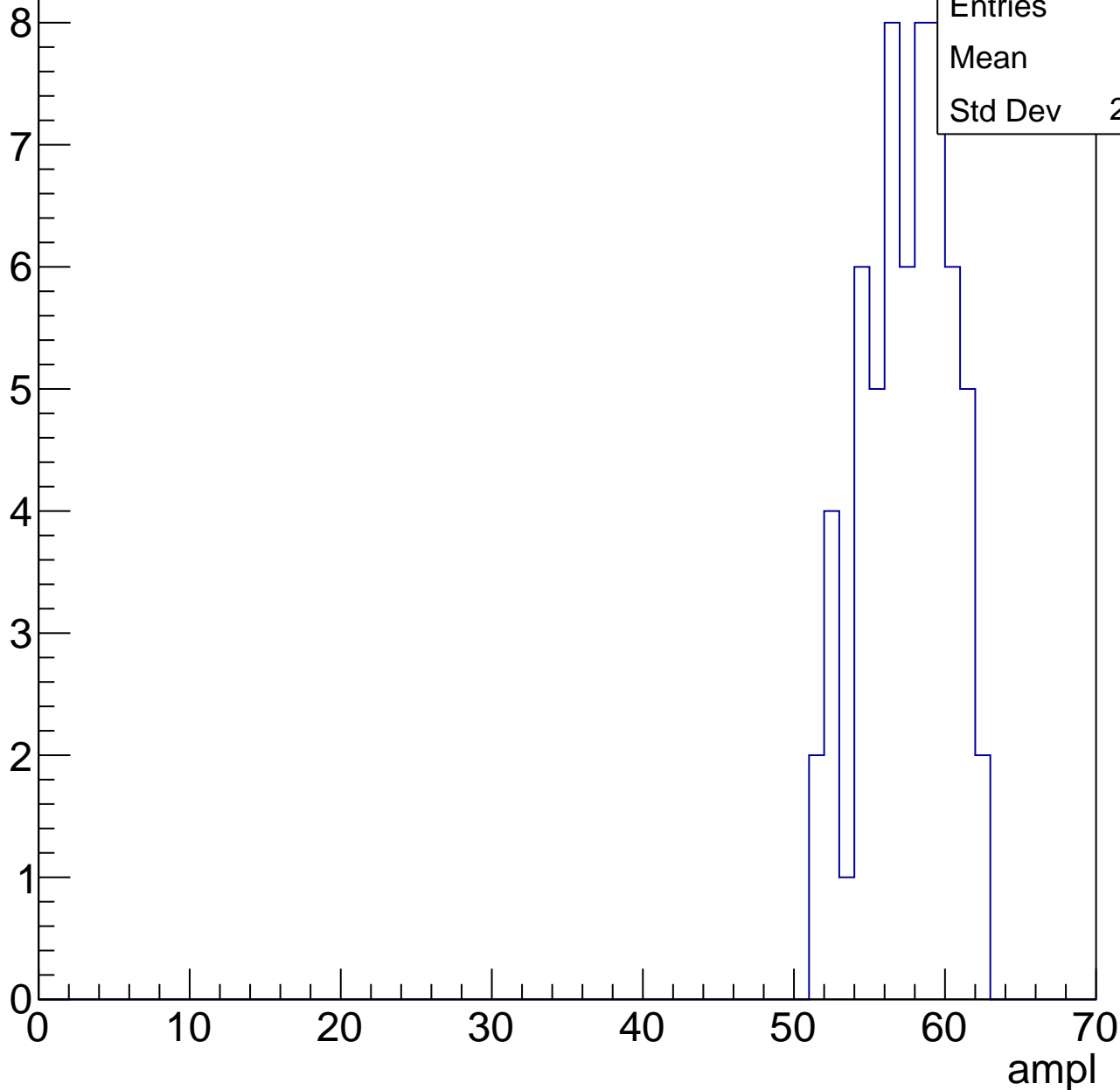


B0L102S, U20-ch6, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 57 |
| Std Dev | 2.846 |

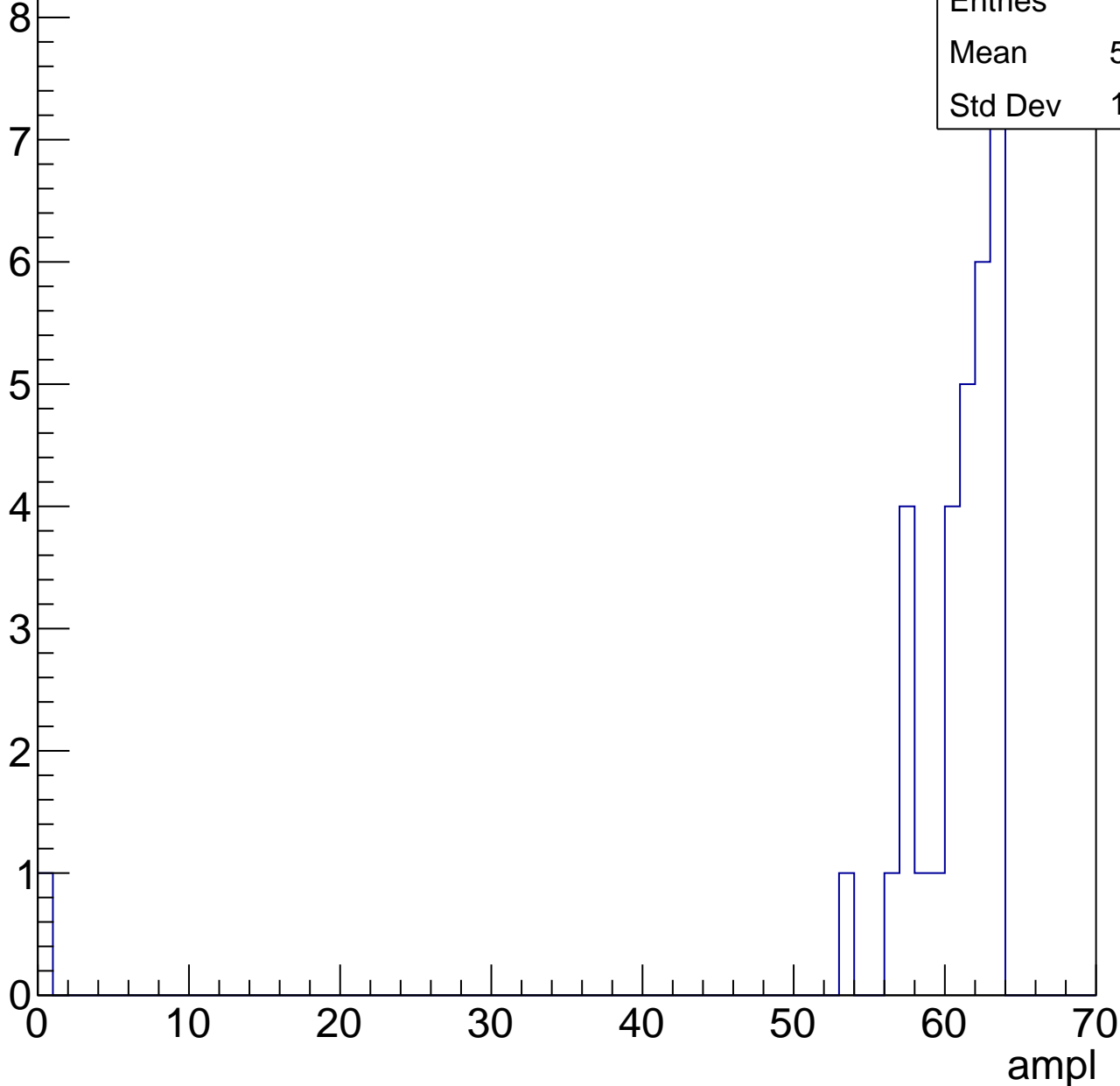


B0L102S, U20-ch6, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.59 |
| Std Dev | 10.82 |



B0L102S, U20-ch6, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 4 |
| Mean | 62.5 |
| Std Dev | 0.5 |

ampl

B0L102S, U20-ch6, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

ampl

B0L102S, U20-ch7, adc0

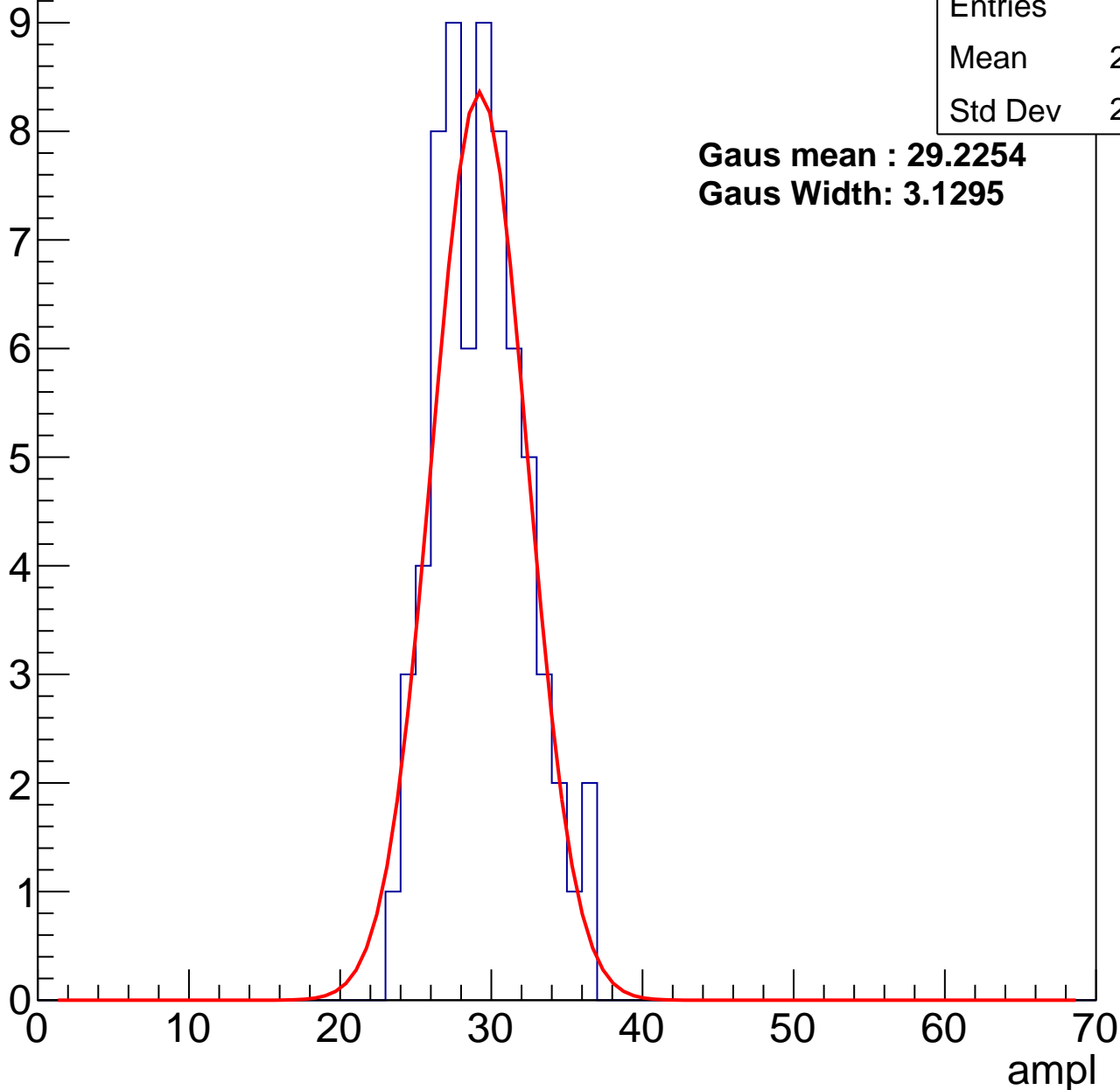
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 28.88 |
| Std Dev | 2.985 |

Gaus mean : 29.2254

Gaus Width: 3.1295



B0L102S, U20-ch7, adc1

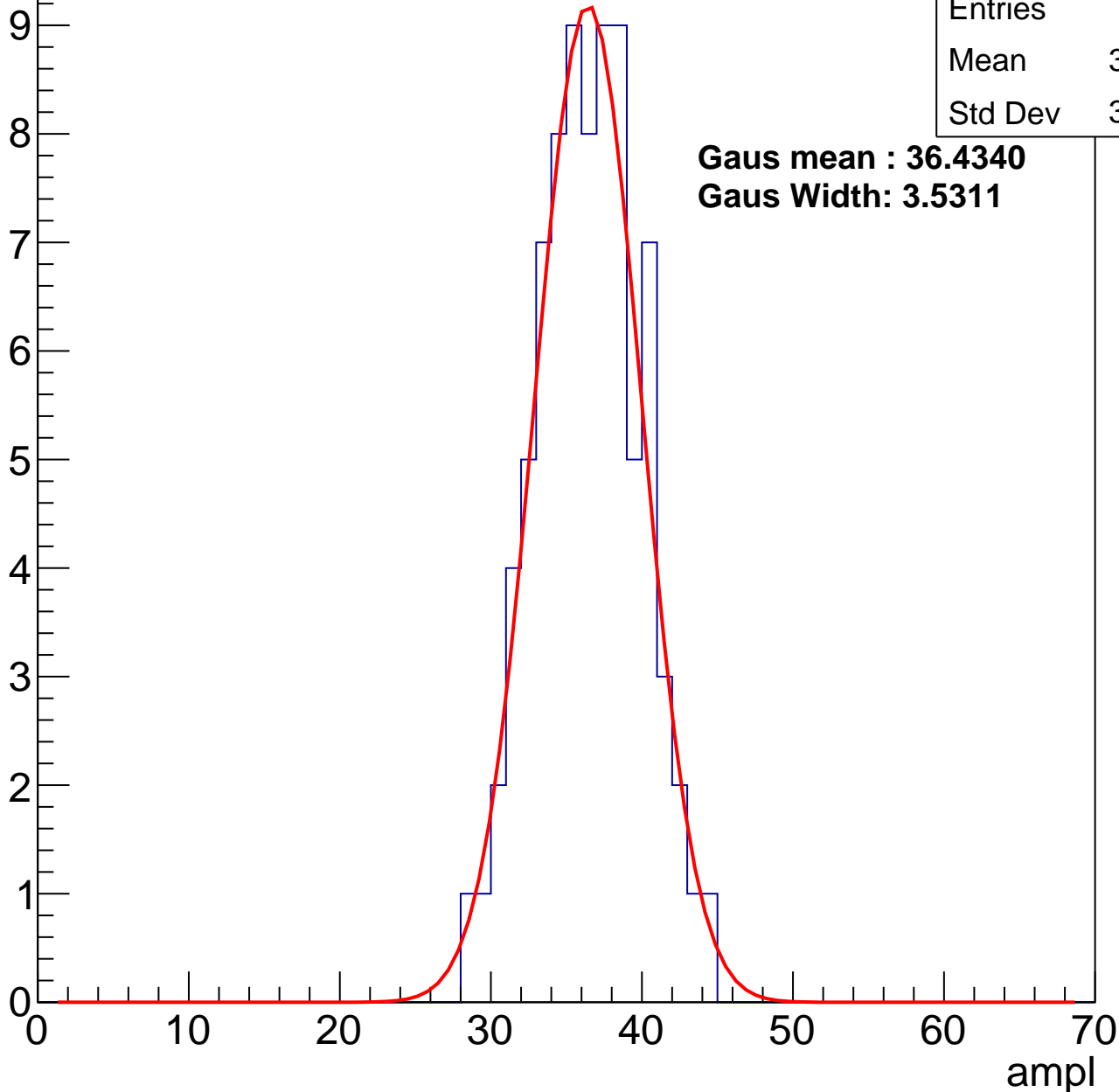
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 35.99 |
| Std Dev | 3.369 |

Gaus mean : 36.4340

Gaus Width: 3.5311

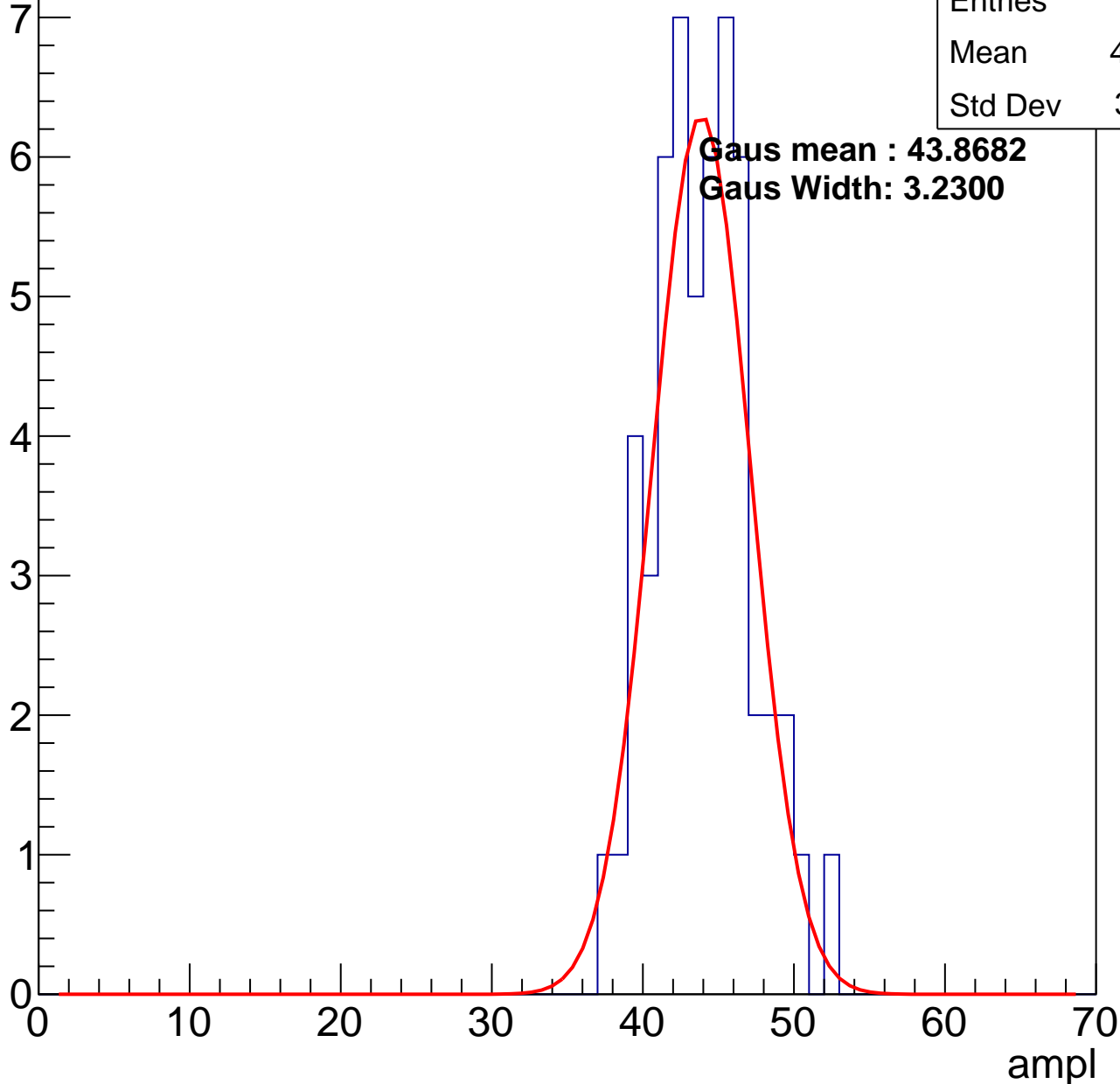


B0L102S, U20-ch7, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.54 |
| Std Dev | 3.161 |

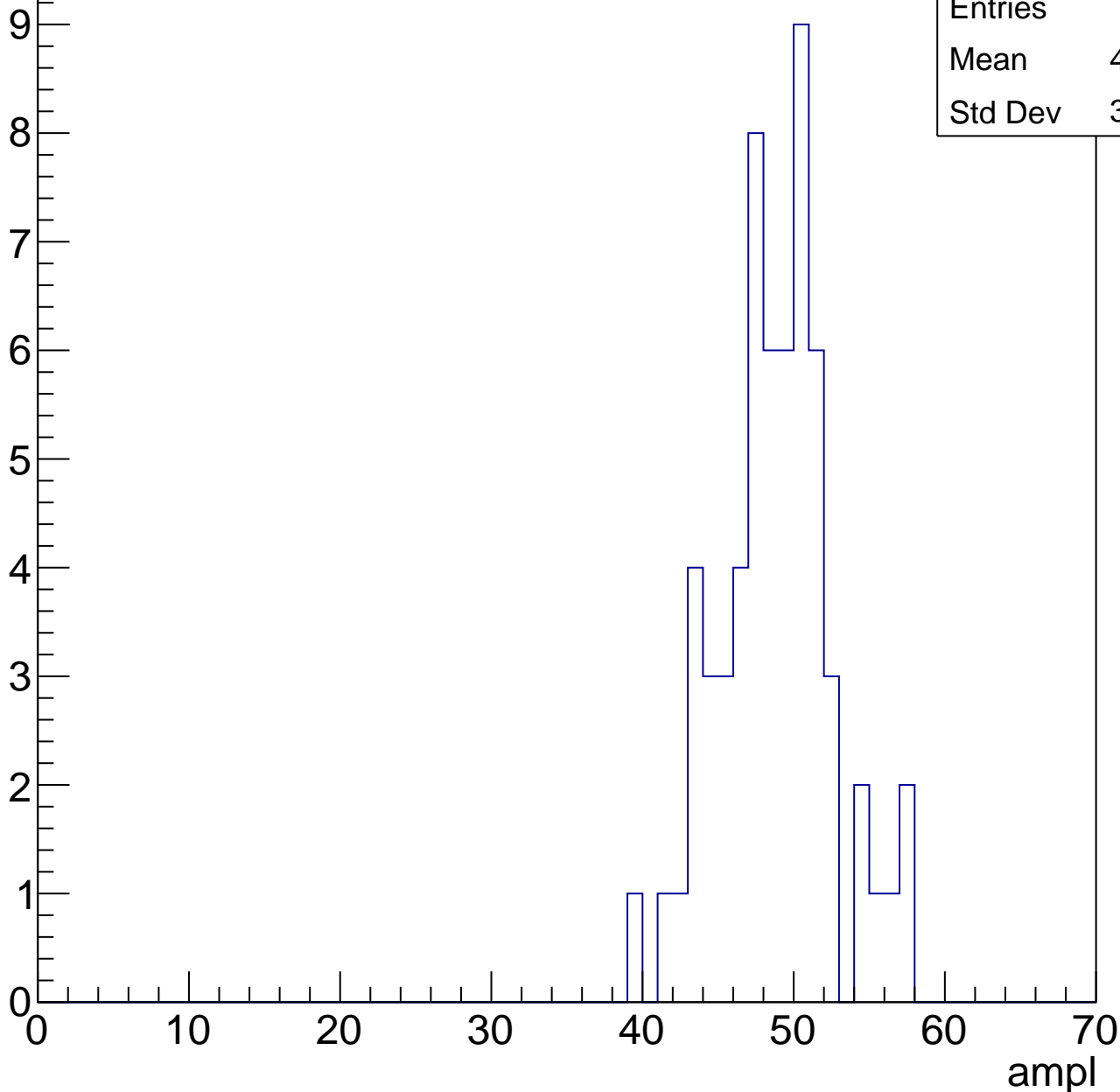


B0L102S, U20-ch7, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

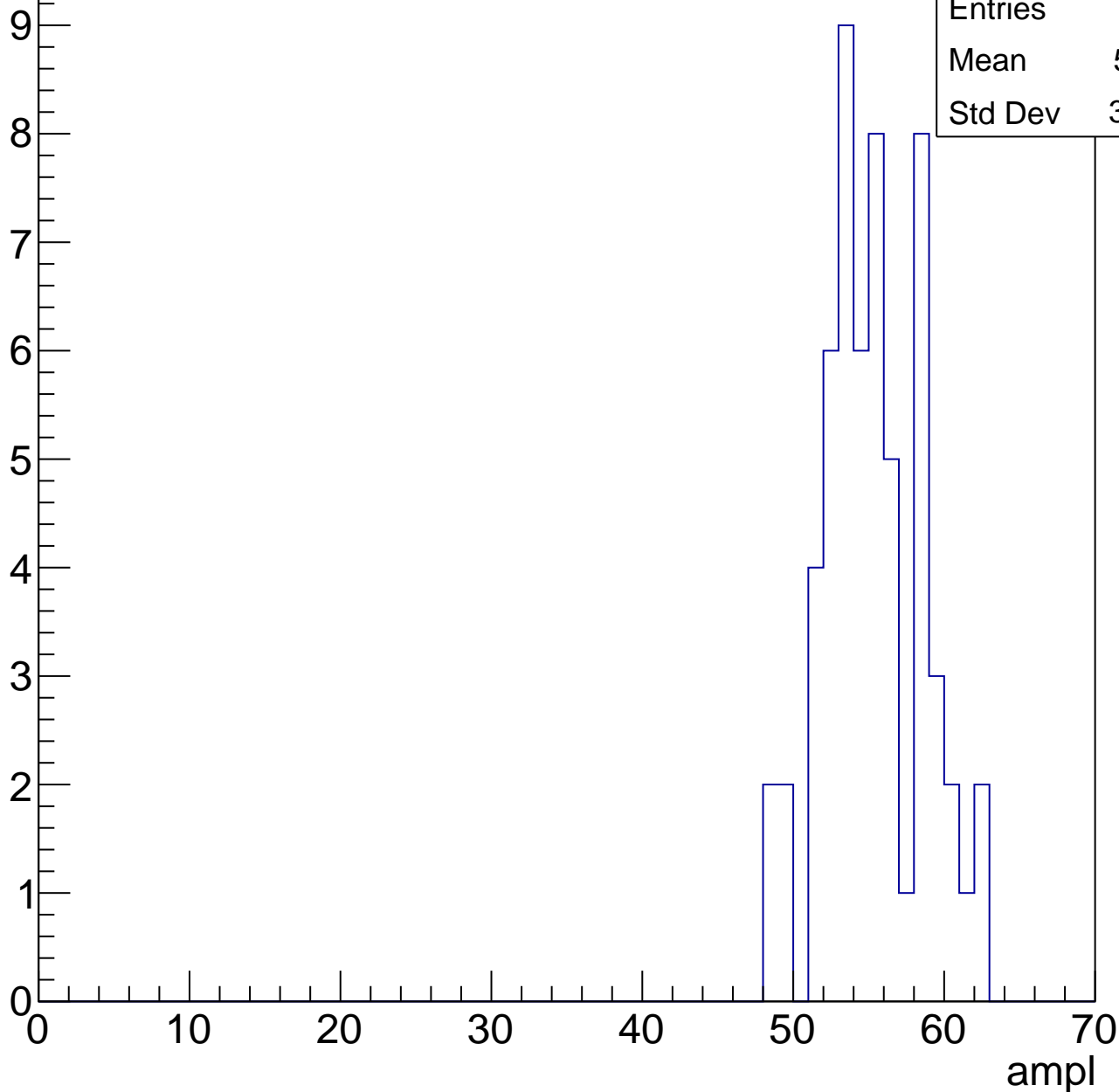
| | |
|---------|-------|
| Entries | 61 |
| Mean | 48.33 |
| Std Dev | 3.732 |



B0L102S, U20-ch7, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch7, adc5

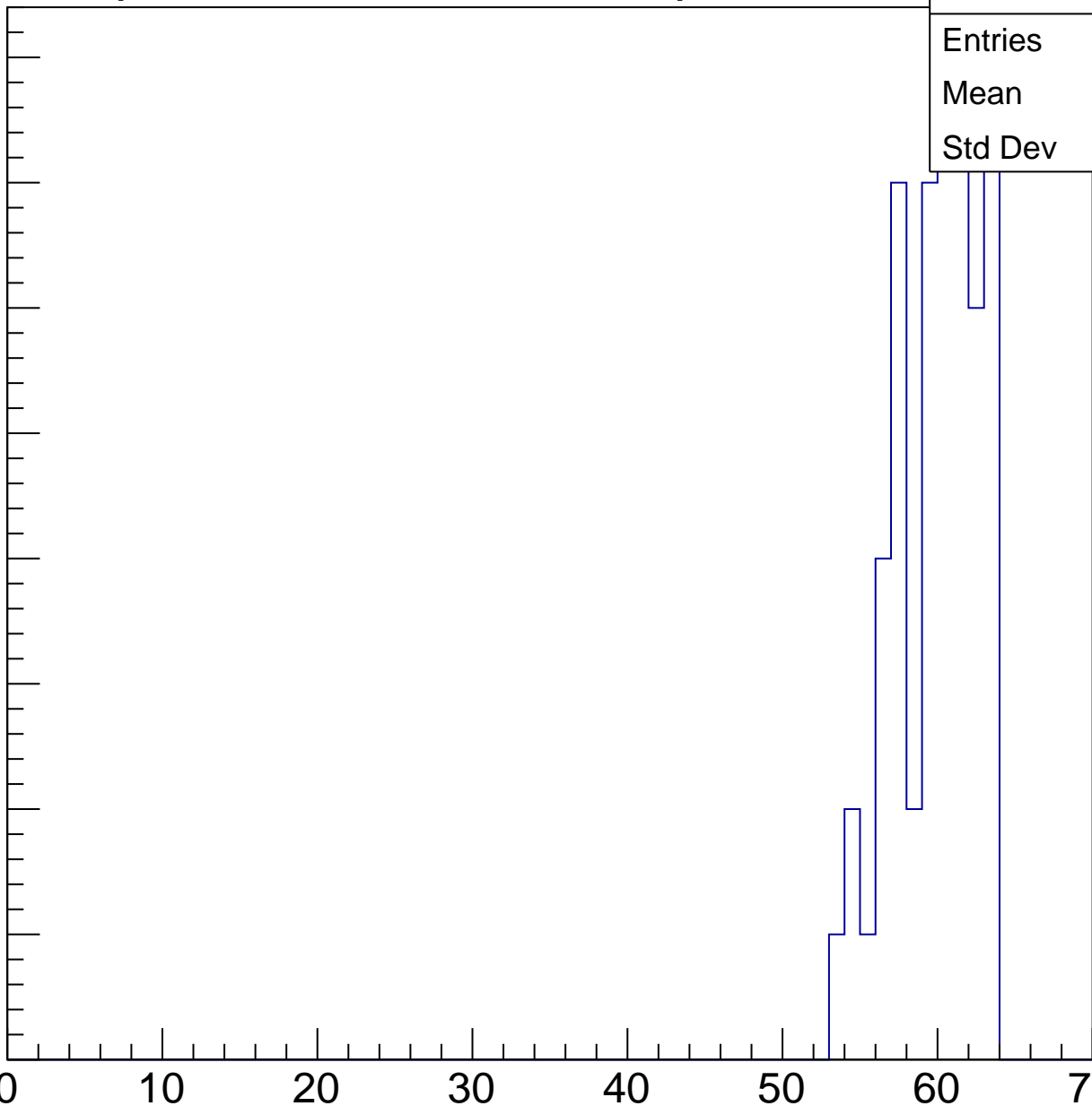
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 54 |
| Mean | 59.48 |
| Std Dev | 2.644 |

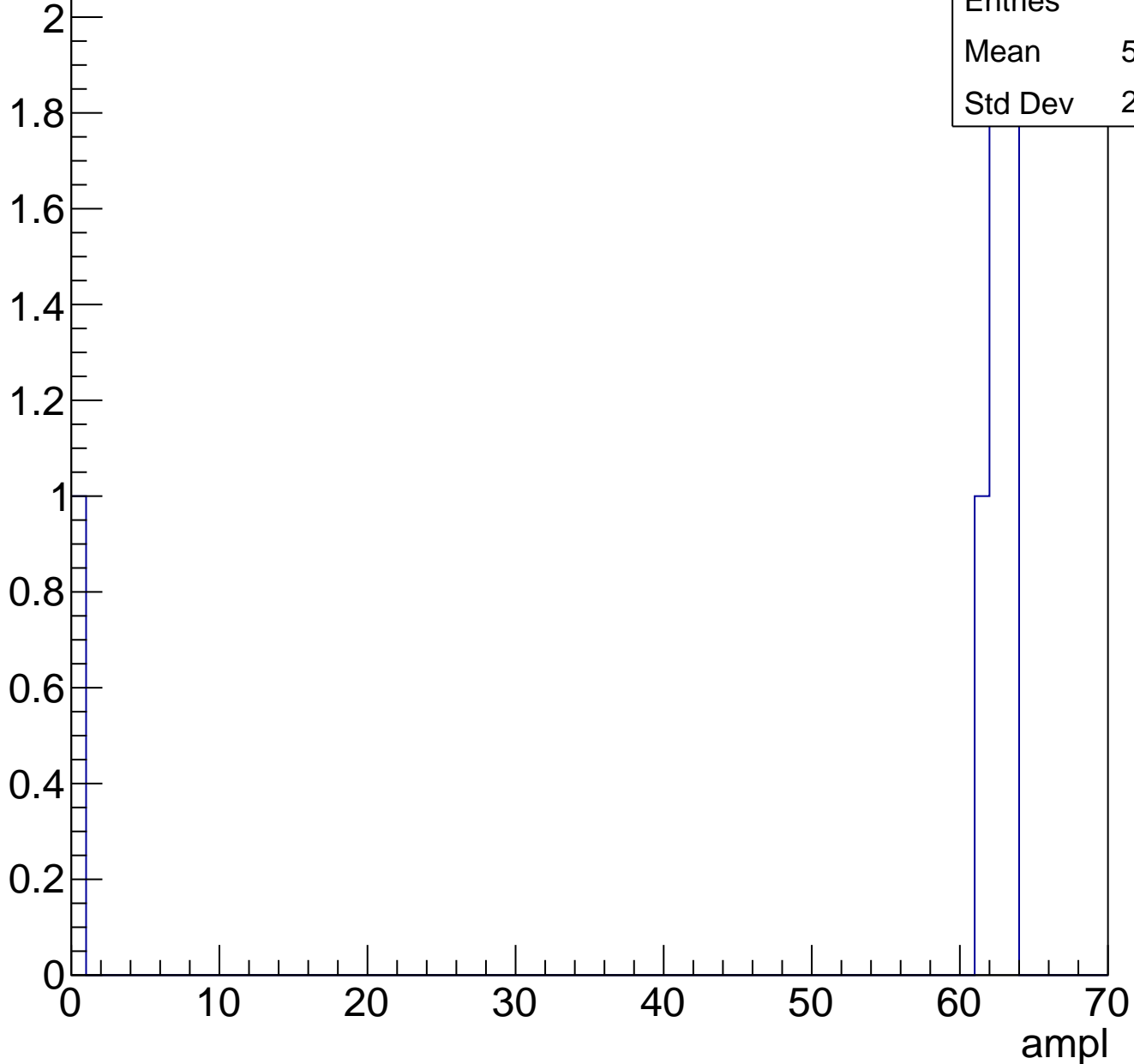
ampl



B0L102S, U20-ch7, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch7, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

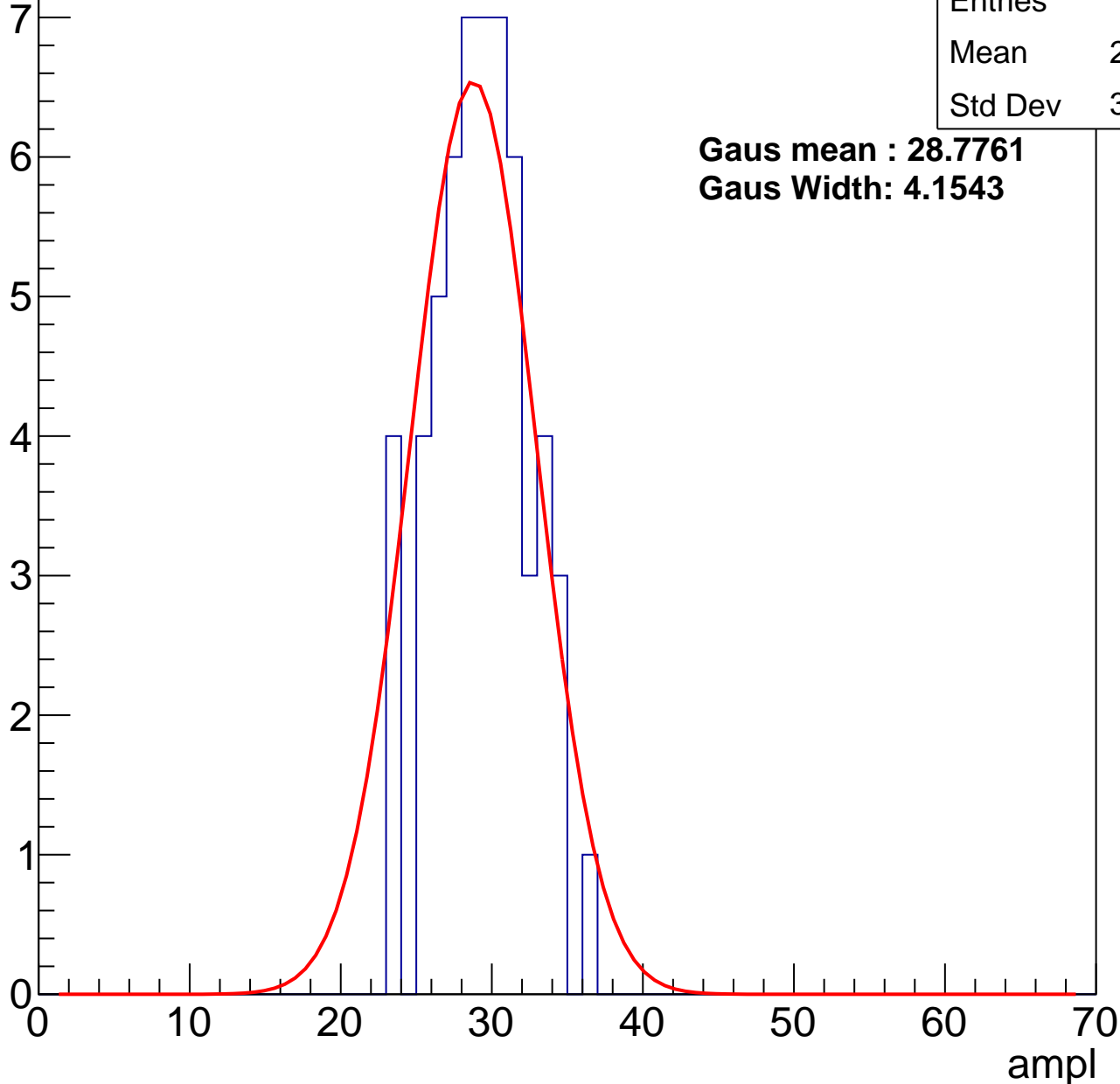
| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl

B0L102S, U20-ch8, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entry

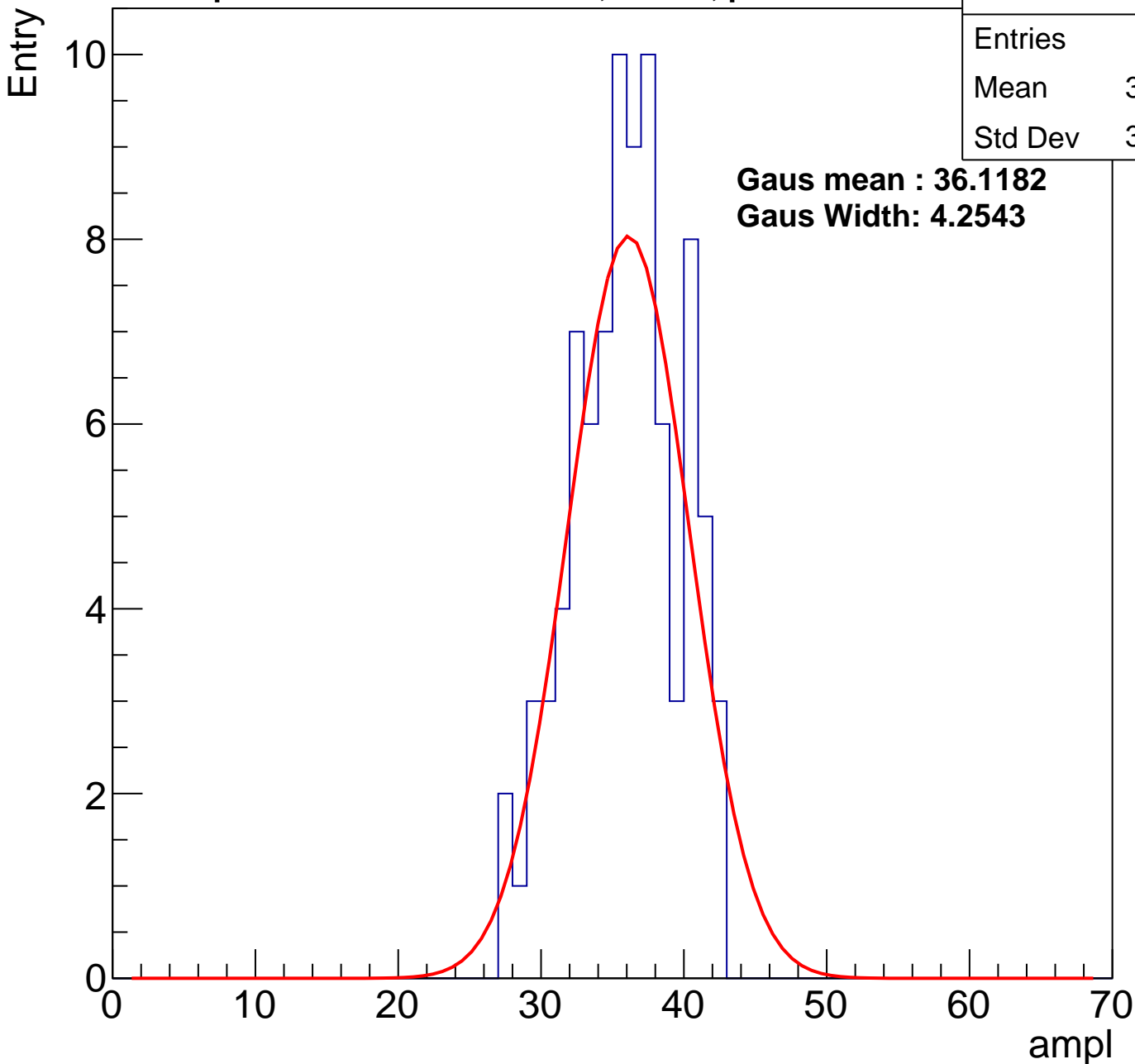


B0L102S, U20-ch8, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 35.44 |
| Std Dev | 3.682 |

Gaus mean : 36.1182
Gaus Width: 4.2543



B0L102S, U20-ch8, adc2

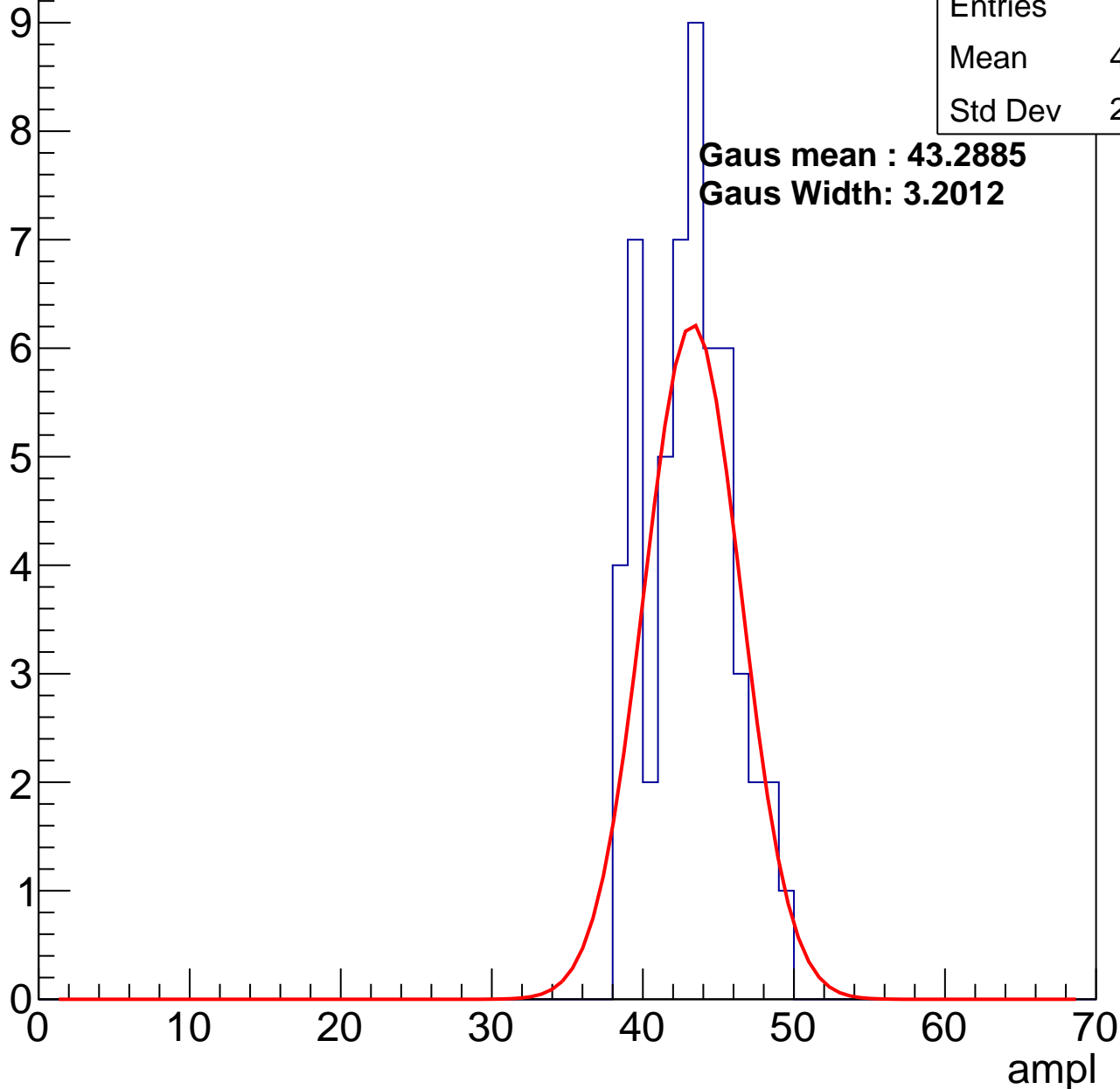
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 42.63 |
| Std Dev | 2.804 |

Gaus mean : 43.2885

Gaus Width: 3.2012

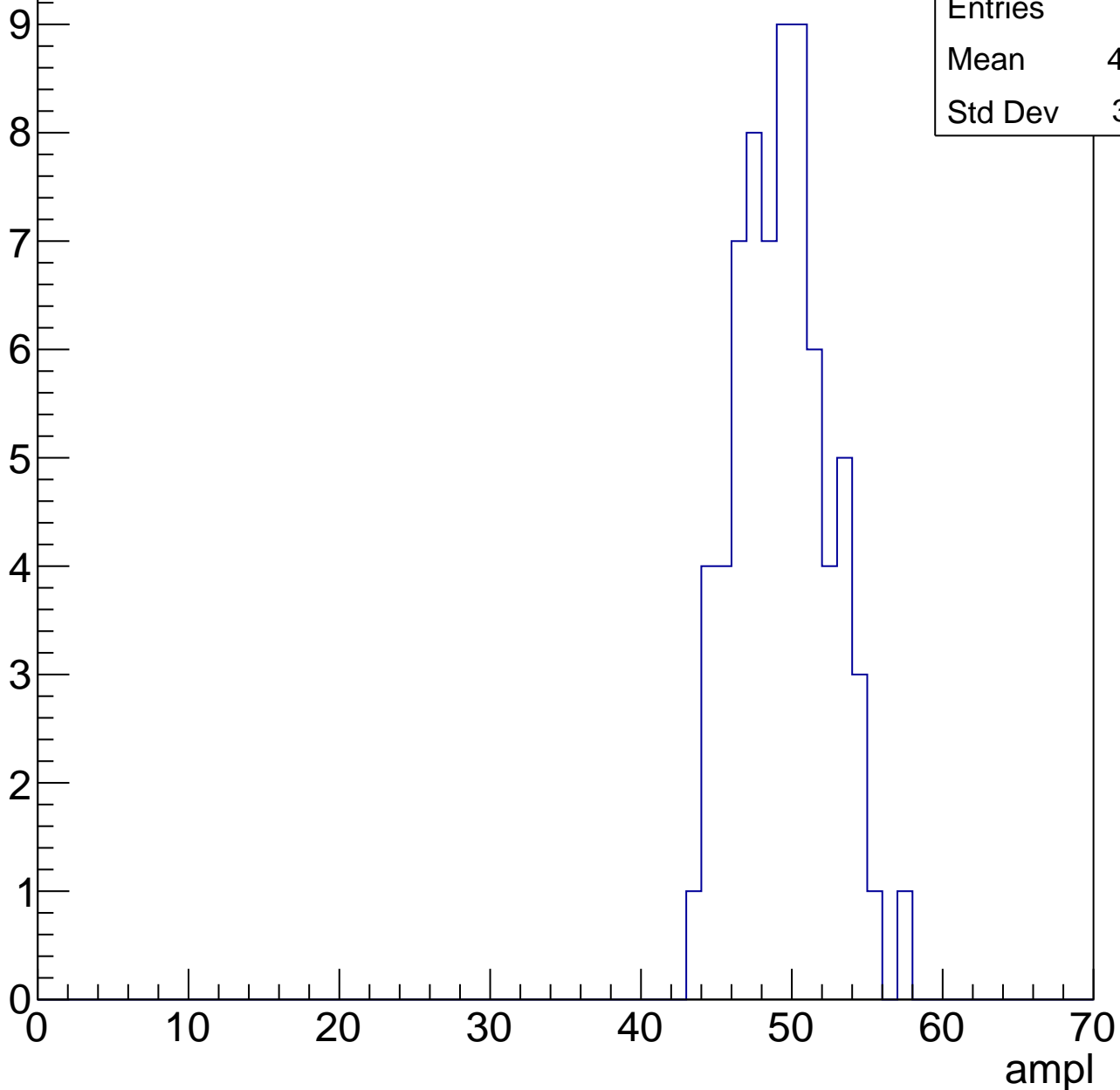


B0L102S, U20-ch8, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.94 |
| Std Dev | 3.011 |

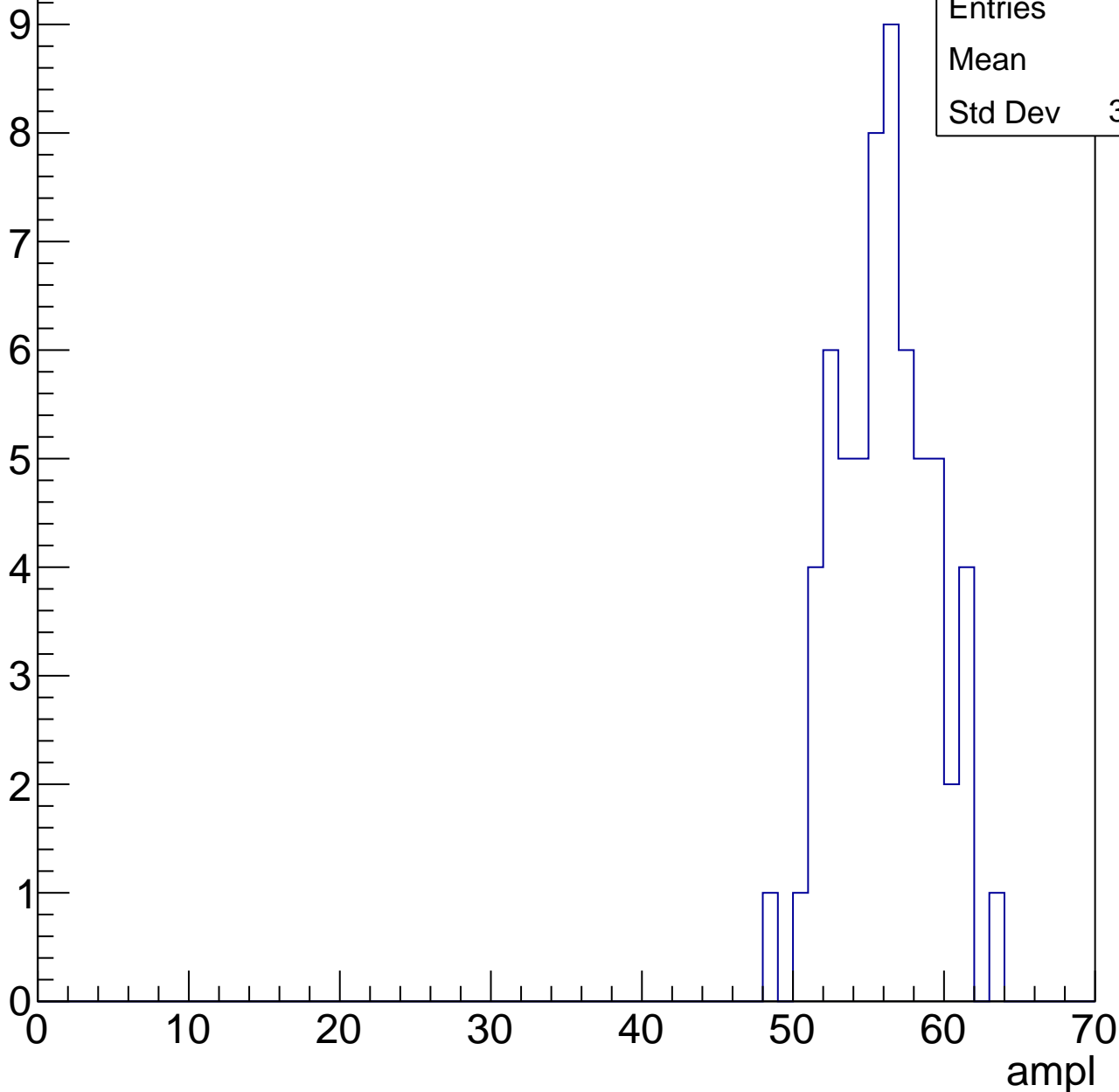


B0L102S, U20-ch8, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 55.6 |
| Std Dev | 3.139 |

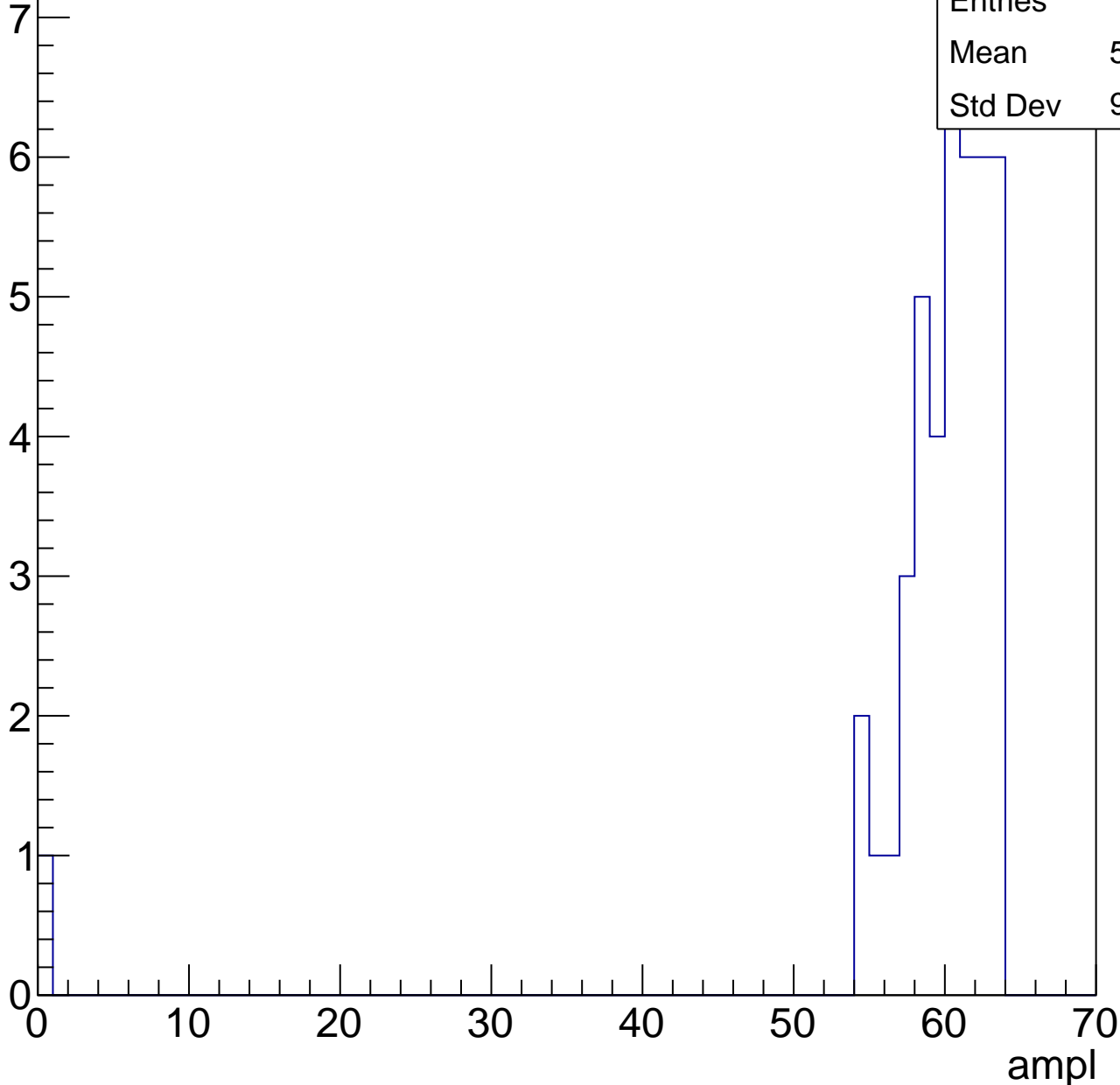


B0L102S, U20-ch8, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

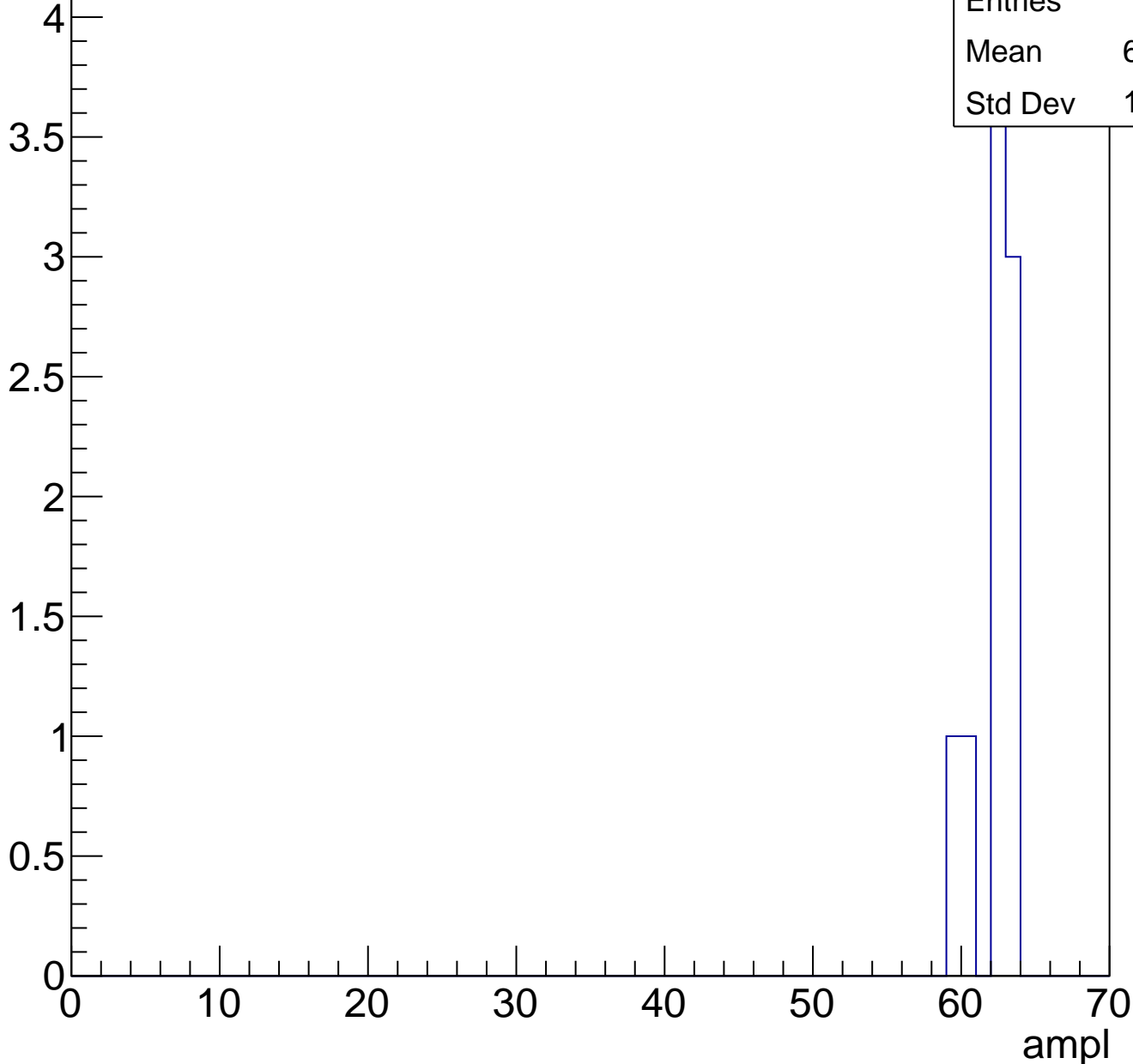
| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.38 |
| Std Dev | 9.434 |



B0L102S, U20-ch8, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch8, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

ampl

B0L102S, U20-ch9, adc0

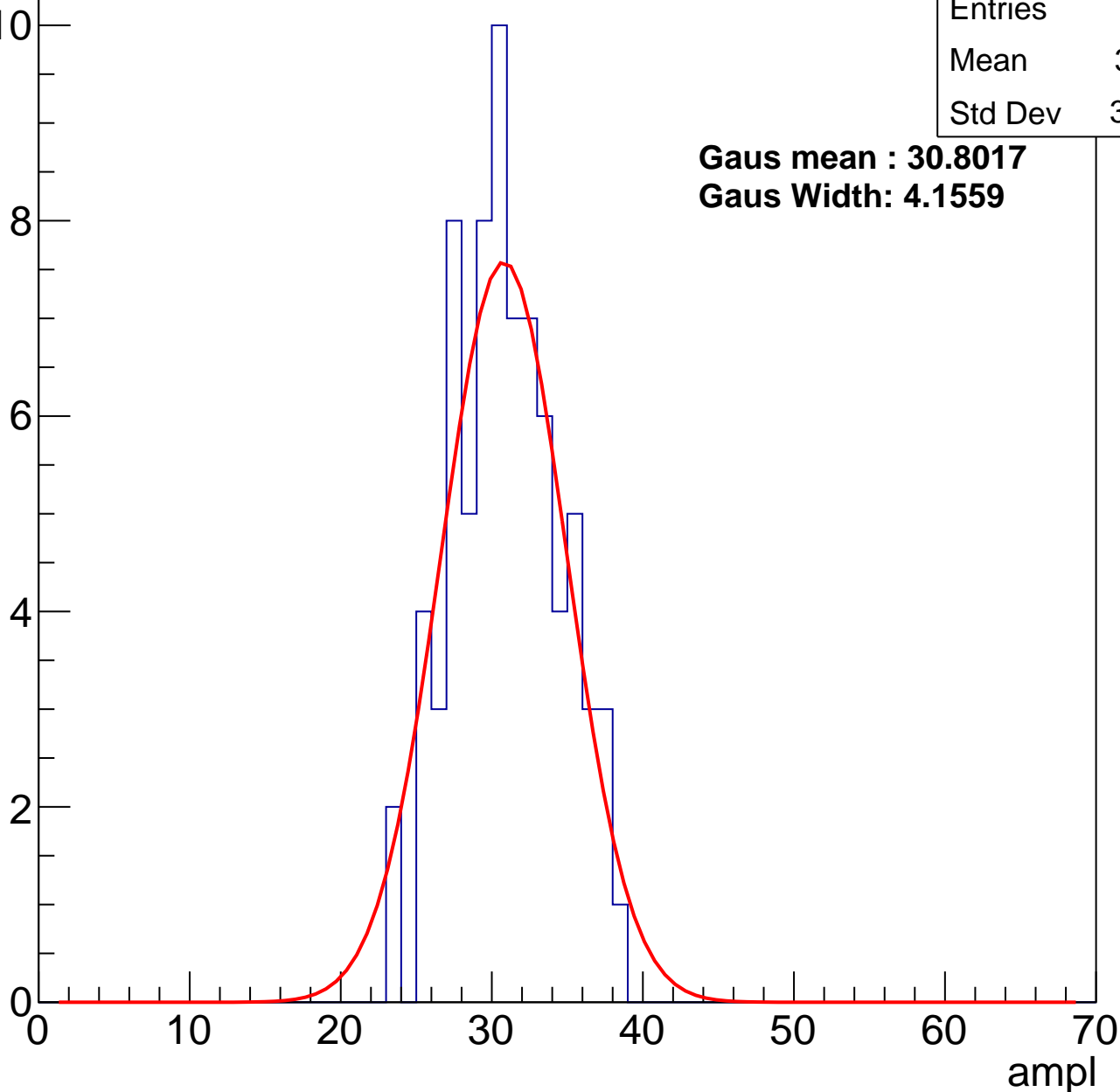
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.51 |
| Std Dev | 3.485 |

Gaus mean : 30.8017

Gaus Width: 4.1559



B0L102S, U20-ch9, adc1

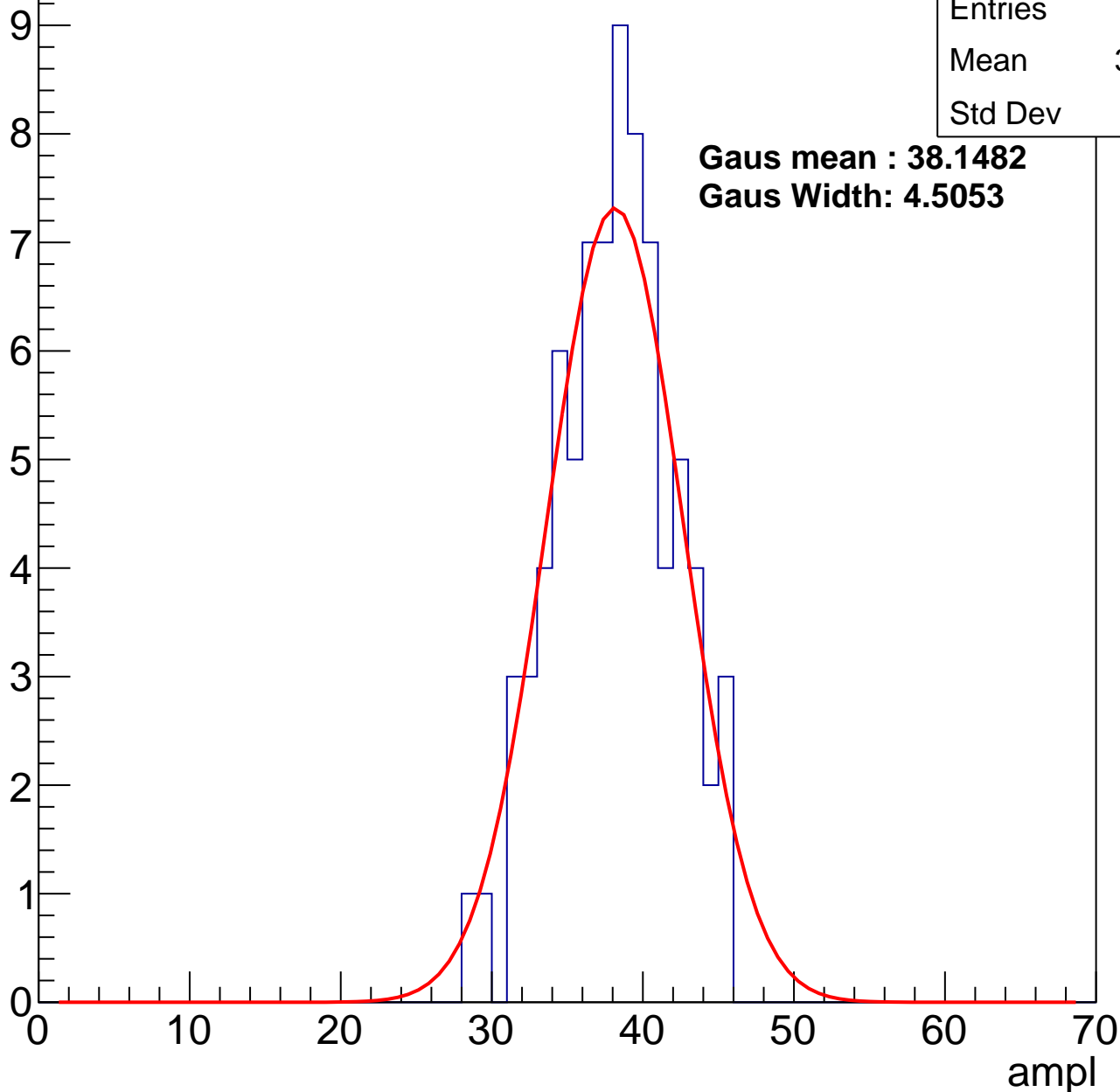
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 37.61 |
| Std Dev | 3.85 |

Gaus mean : 38.1482

Gaus Width: 4.5053



B0L102S, U20-ch9, adc2

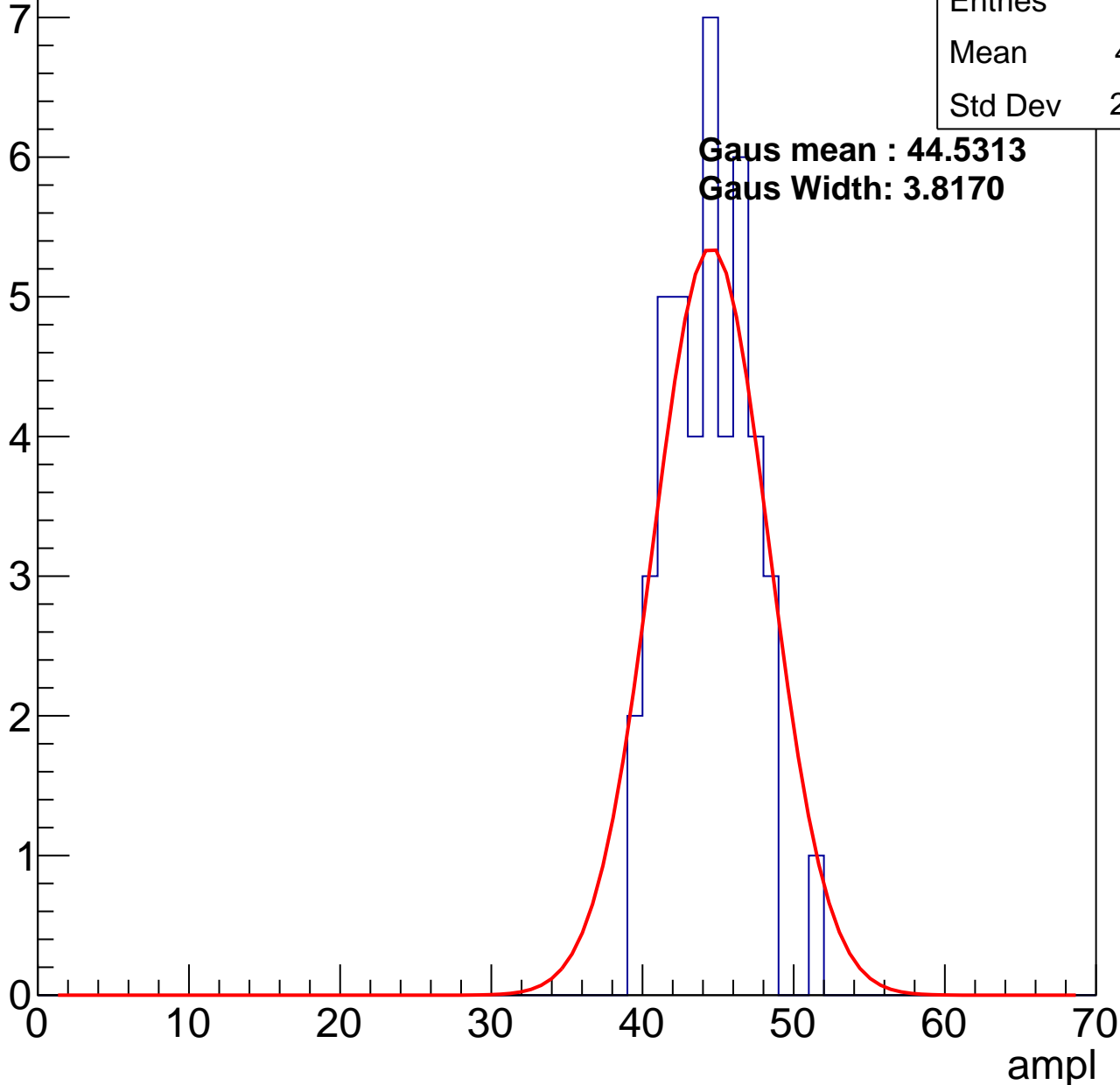
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 43.91 |
| Std Dev | 2.729 |

Gaus mean : 44.5313

Gaus Width: 3.8170

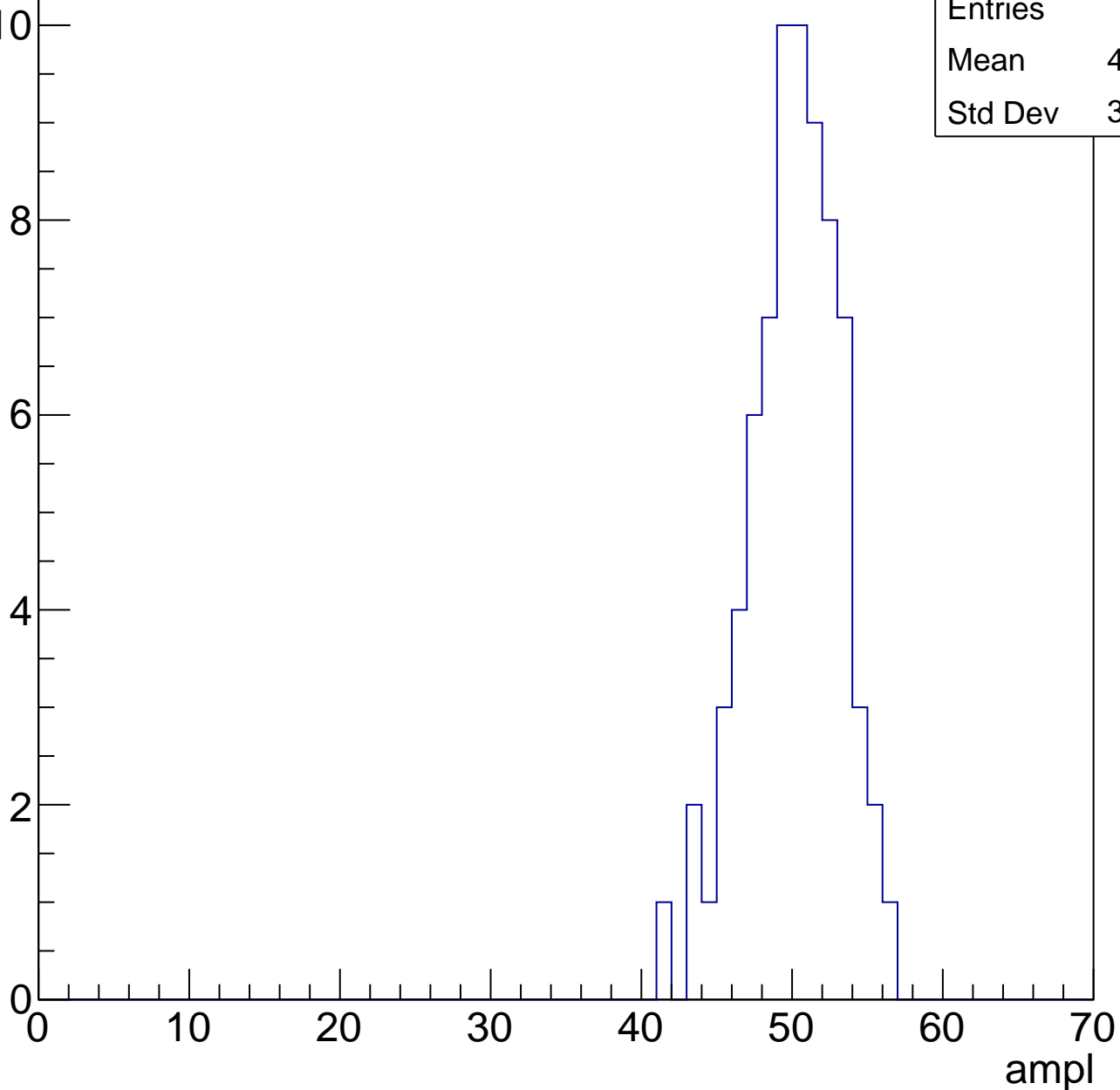


B0L102S, U20-ch9, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 49.62 |
| Std Dev | 3.017 |



B0L102S, U20-ch9, adc4

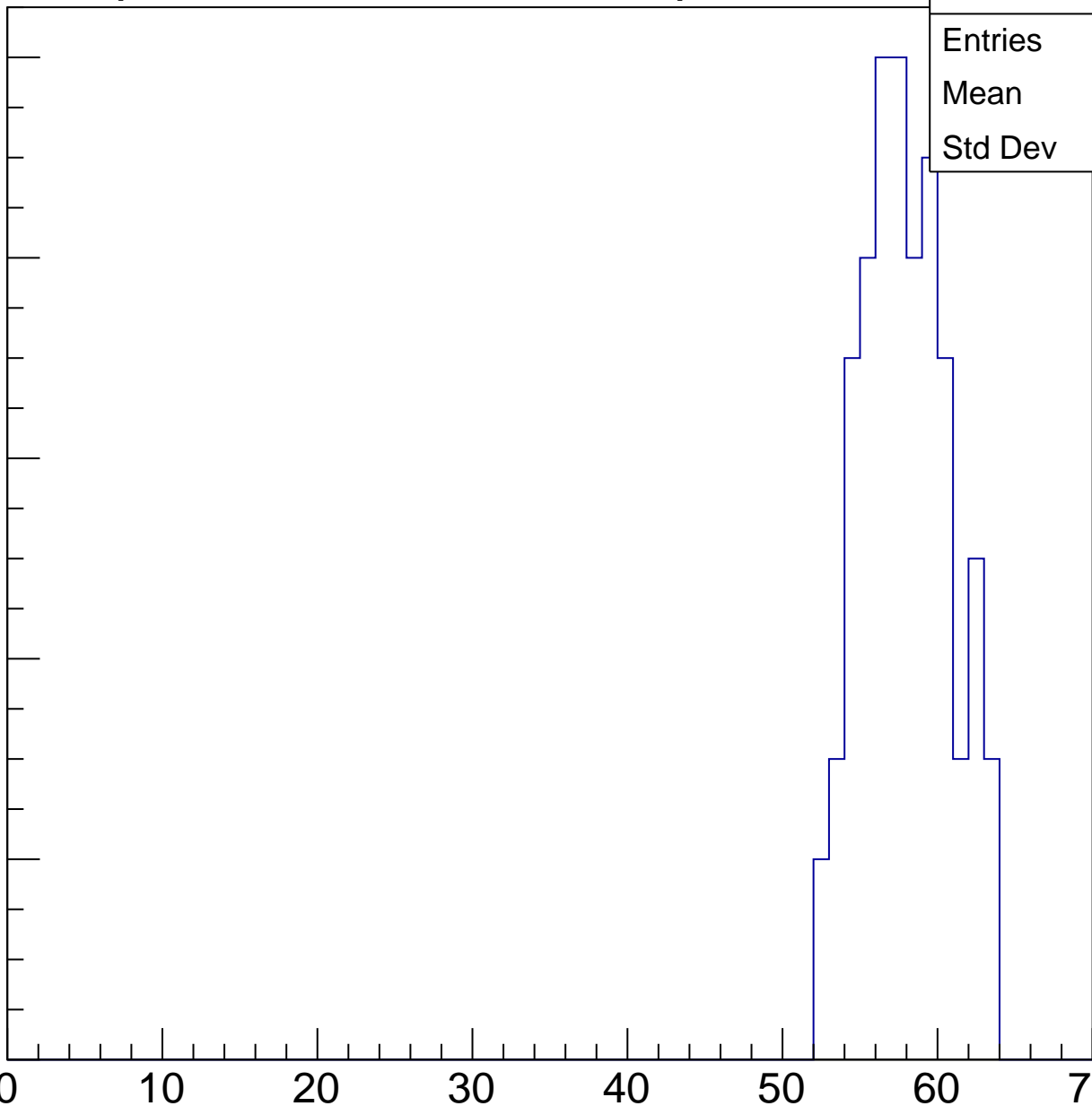
calib_packv5_042523_0143.root, FC#12, port B1

Entry

10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 75 |
| Mean | 57.44 |
| Std Dev | 2.772 |

ampl

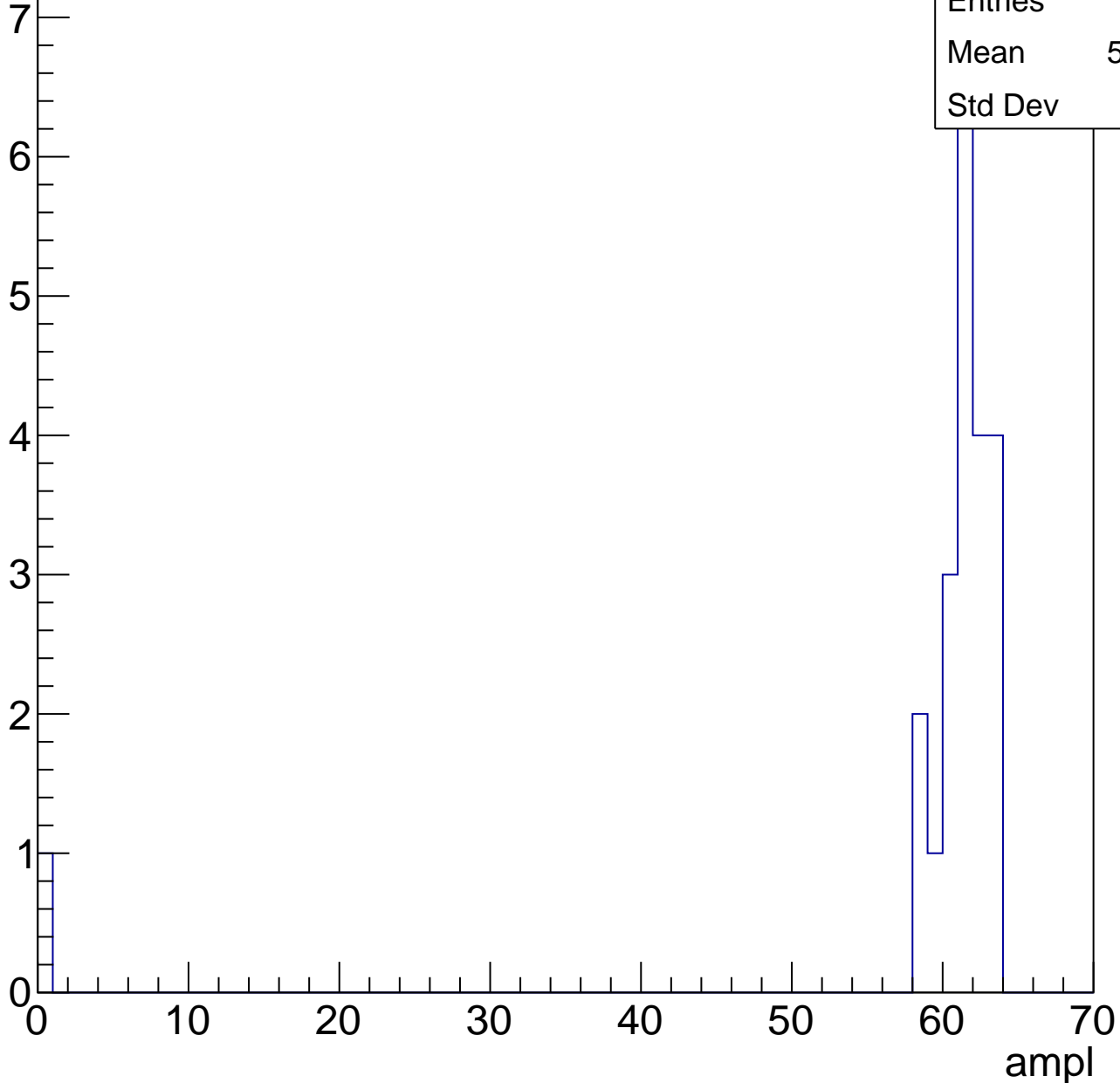


B0L102S, U20-ch9, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 22 |
| Mean | 58.27 |
| Std Dev | 12.8 |



B0L102S, U20-ch9, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch9, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

ampl

B0L102S, U20-ch10, adc0

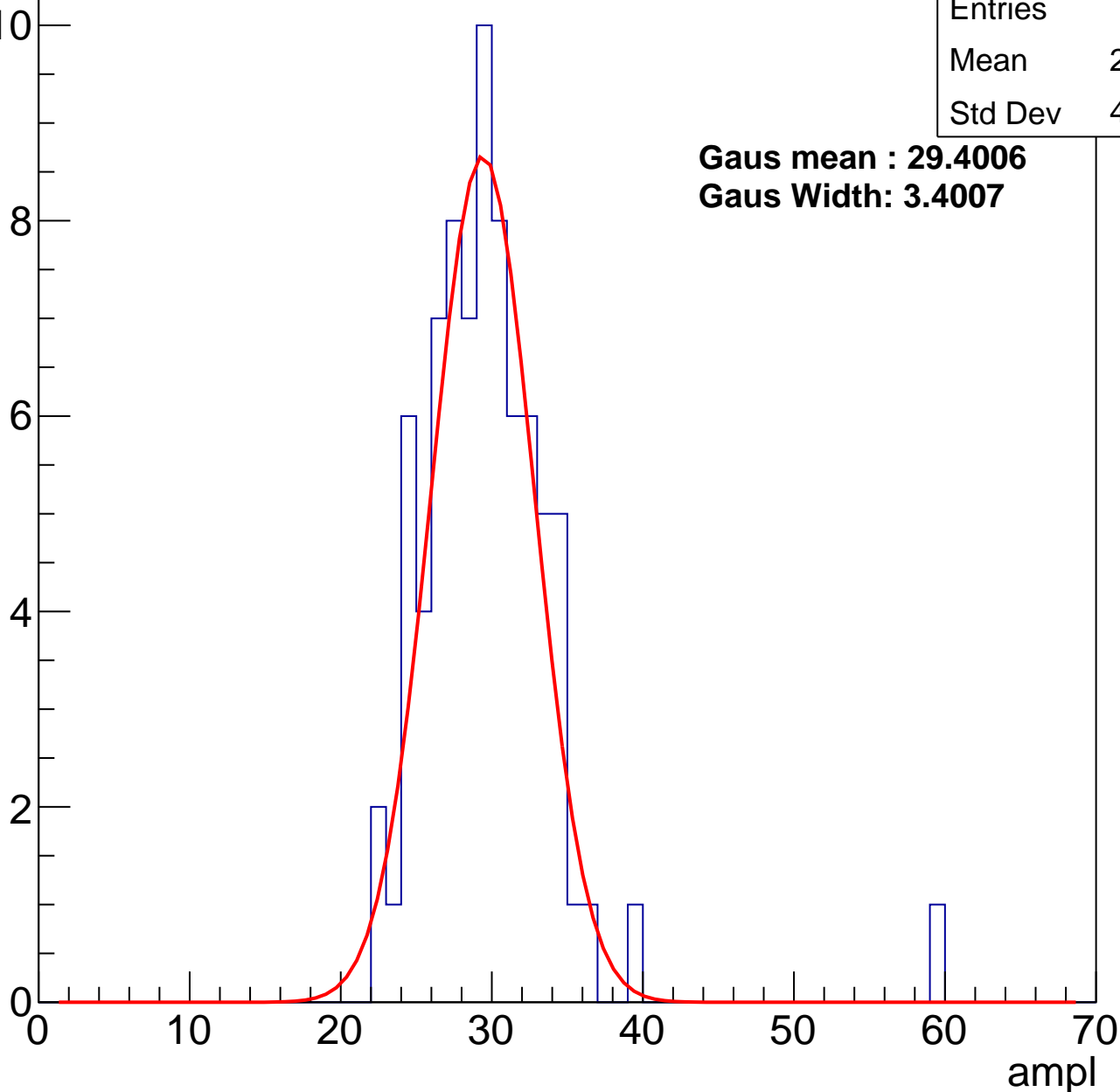
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.33 |
| Std Dev | 4.796 |

Gaus mean : 29.4006

Gaus Width: 3.4007



B0L102S, U20-ch10, adc1

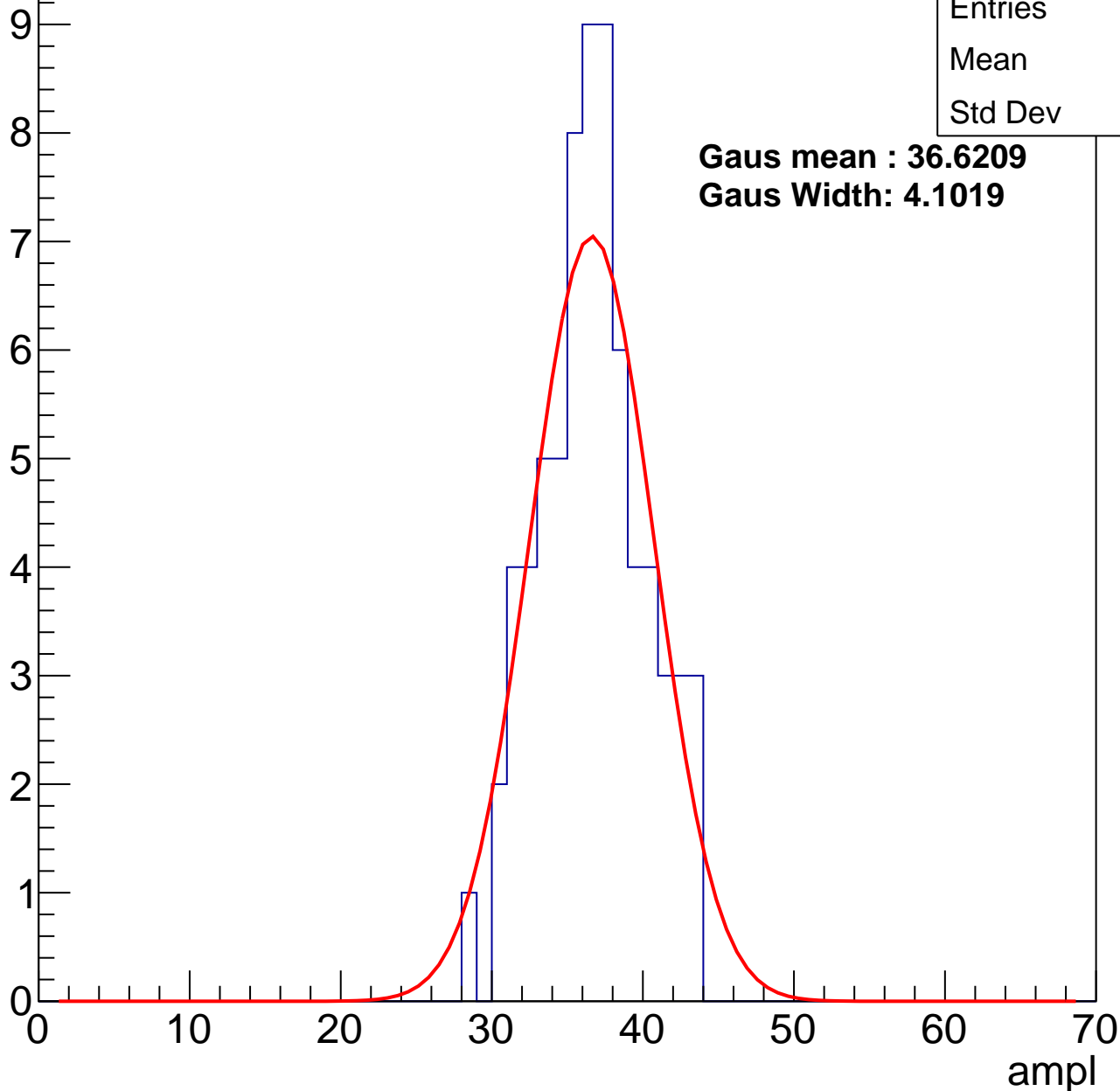
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 70 |
| Mean | 36.2 |
| Std Dev | 3.45 |

Gaus mean : 36.6209

Gaus Width: 4.1019



B0L102S, U20-ch10, adc2

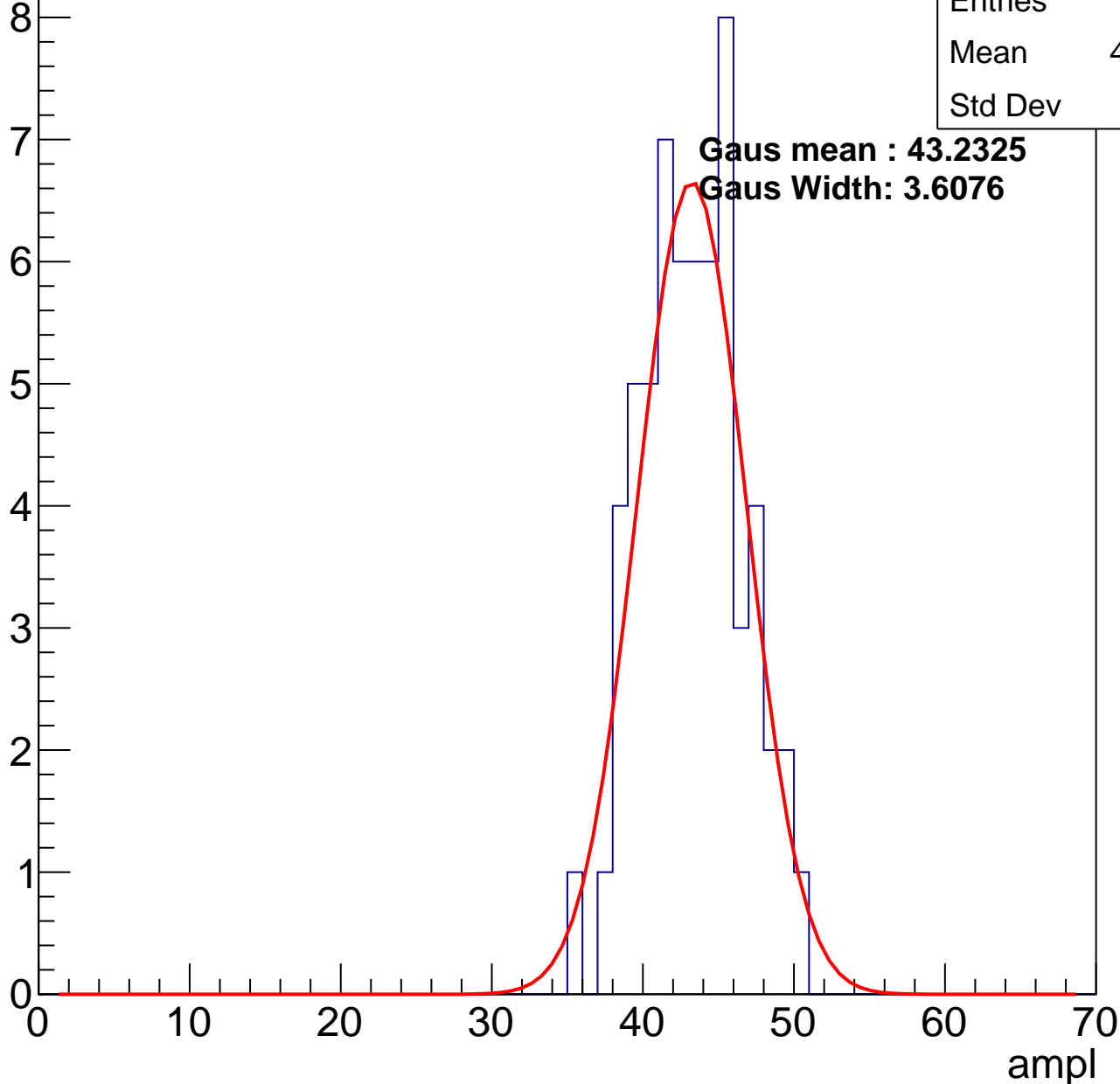
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.79 |
| Std Dev | 3.28 |

Gaus mean : 43.2325

Gaus Width: 3.6076

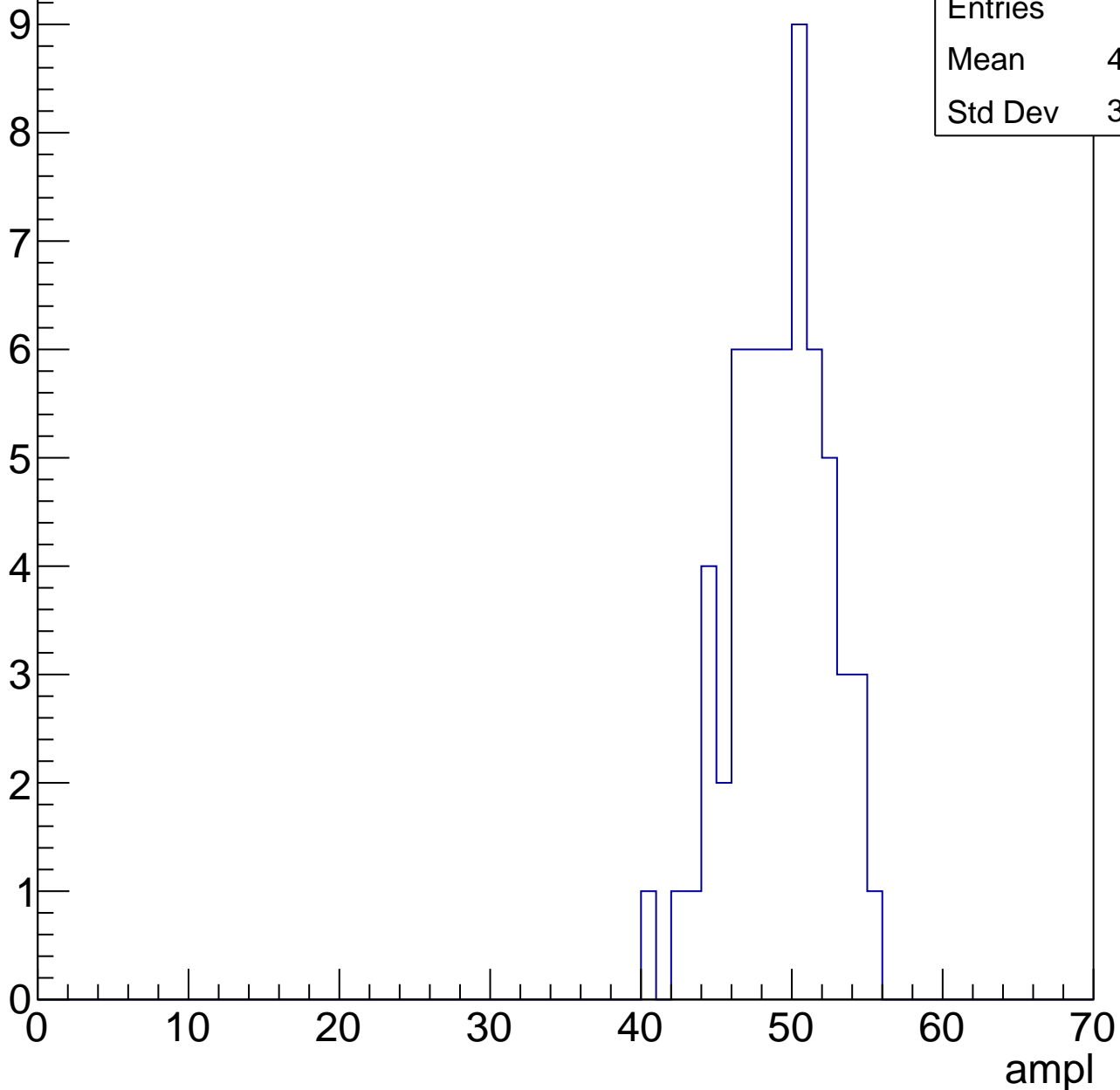


B0L102S, U20-ch10, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.72 |
| Std Dev | 3.199 |

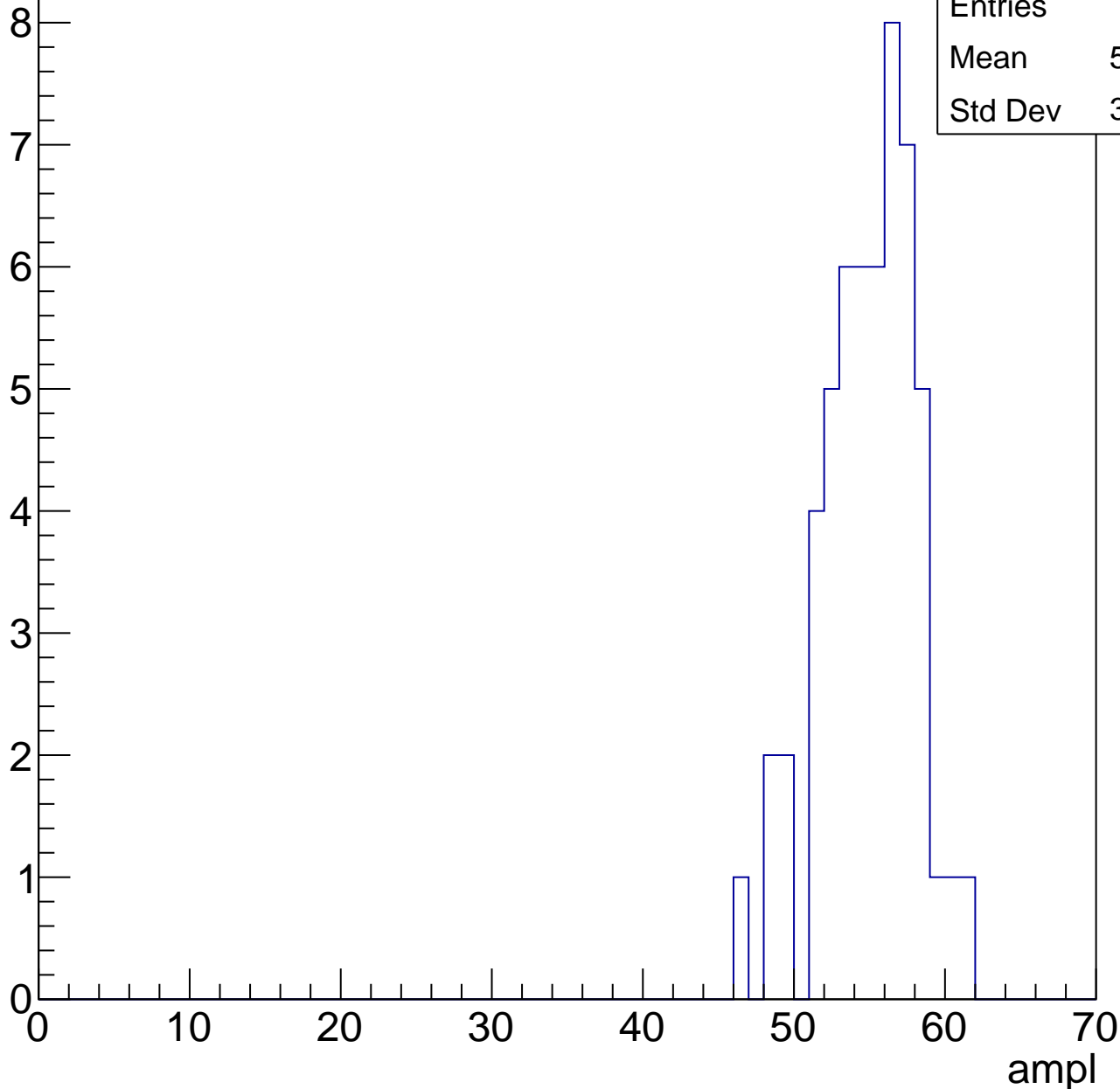


B0L102S, U20-ch10, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.42 |
| Std Dev | 3.102 |

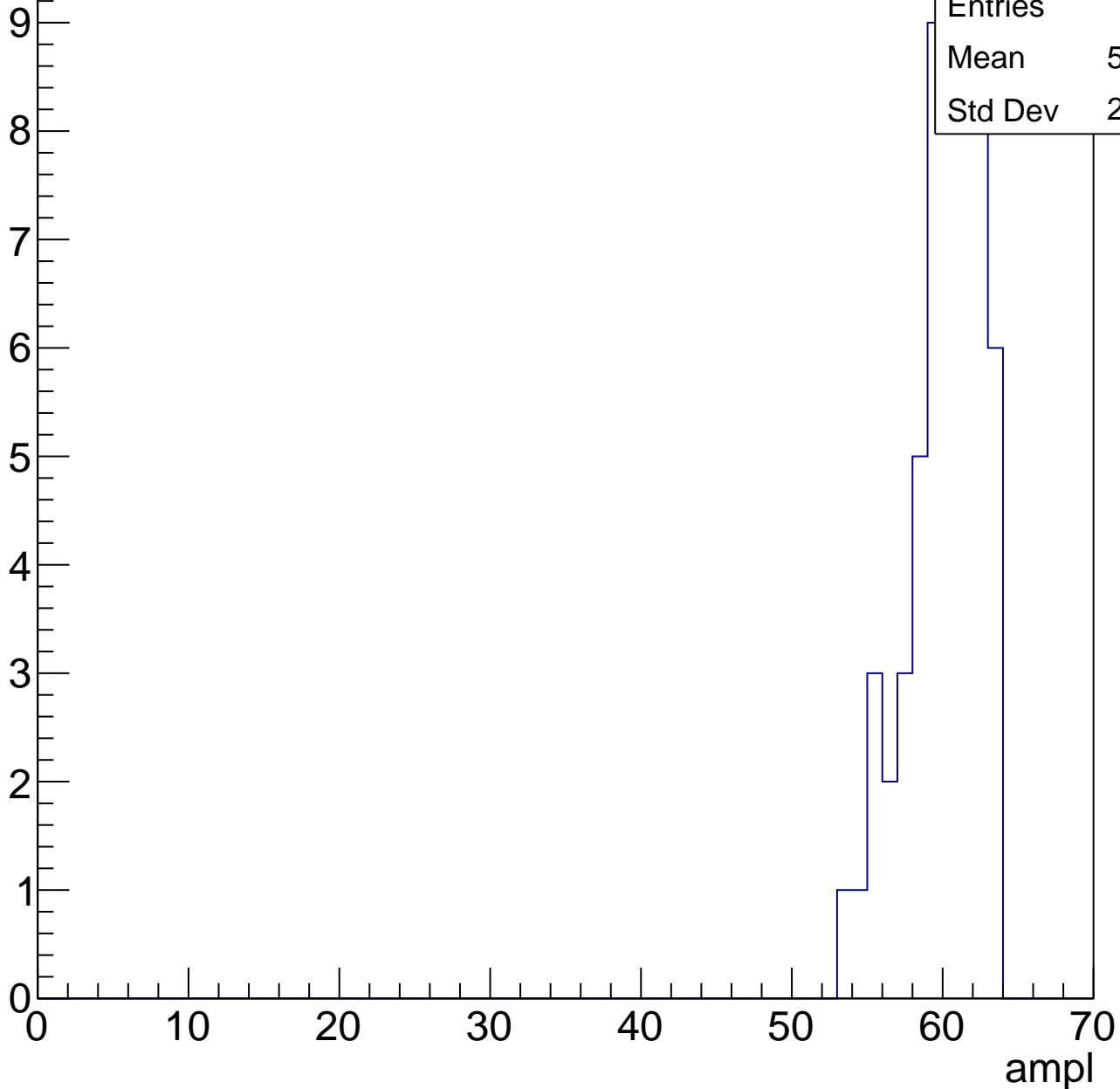


B0L102S, U20-ch10, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

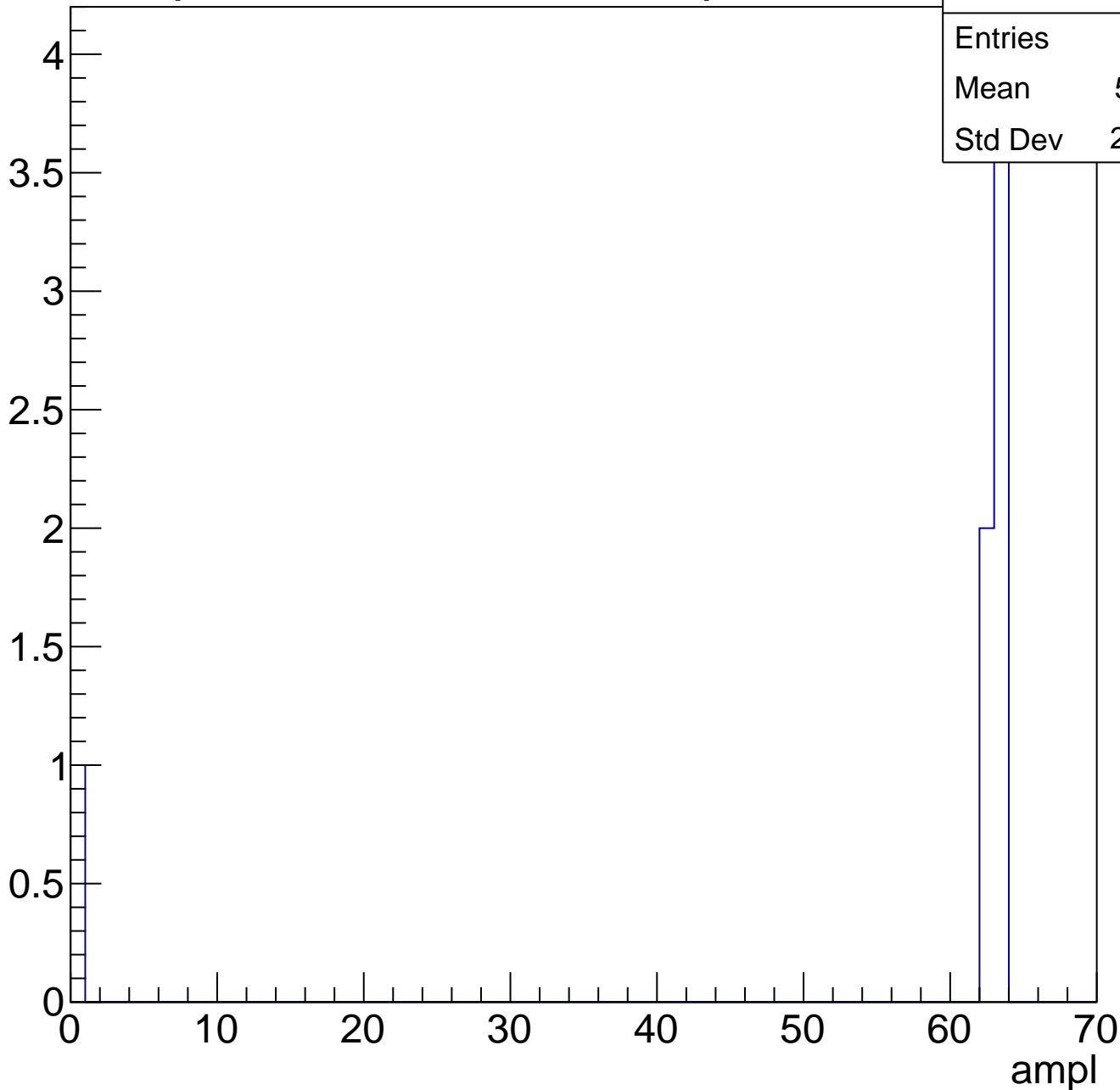
| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.62 |
| Std Dev | 2.446 |



B0L102S, U20-ch10, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch10, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch11, adc0

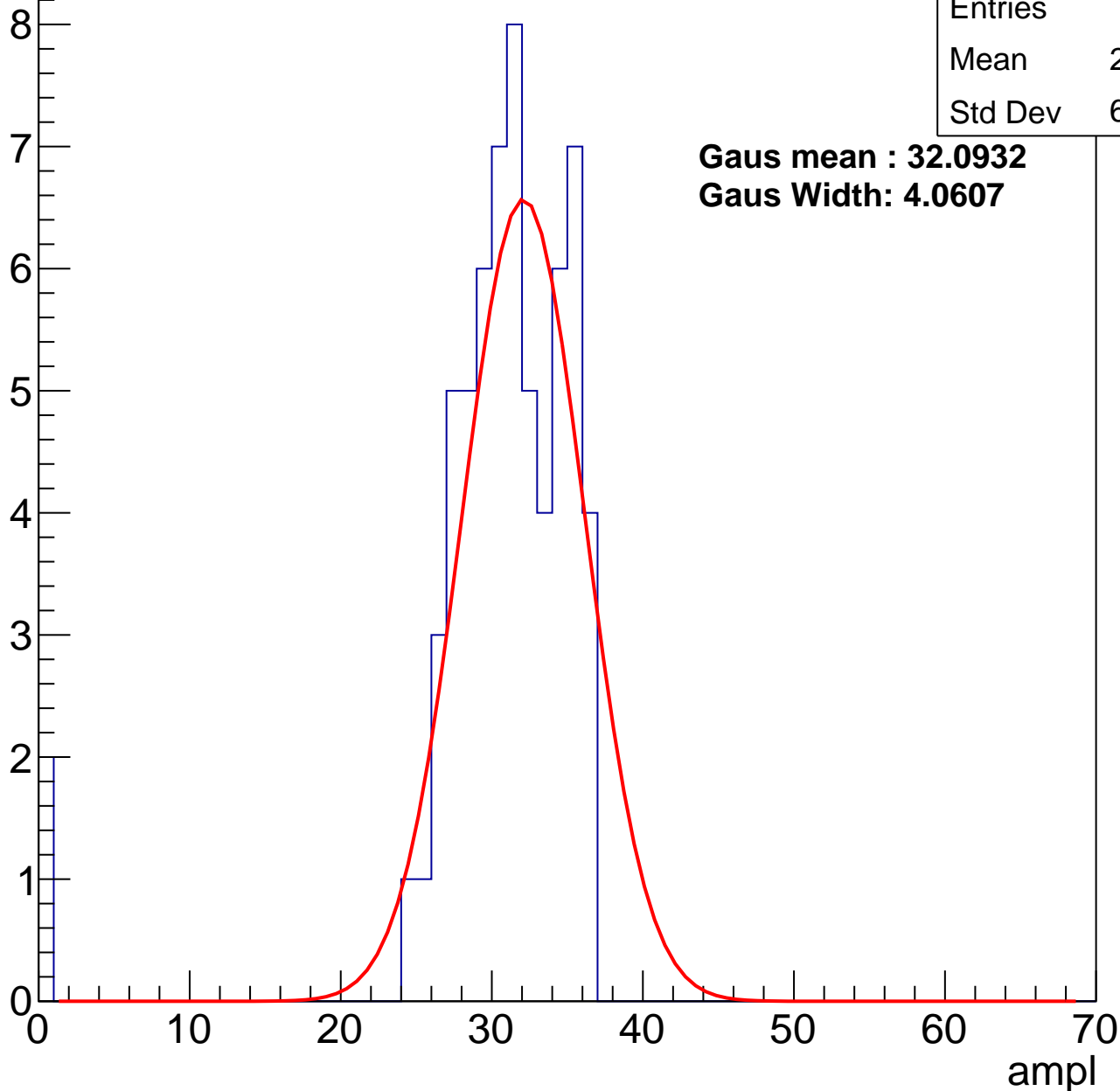
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.98 |
| Std Dev | 6.199 |

Gaus mean : 32.0932

Gaus Width: 4.0607



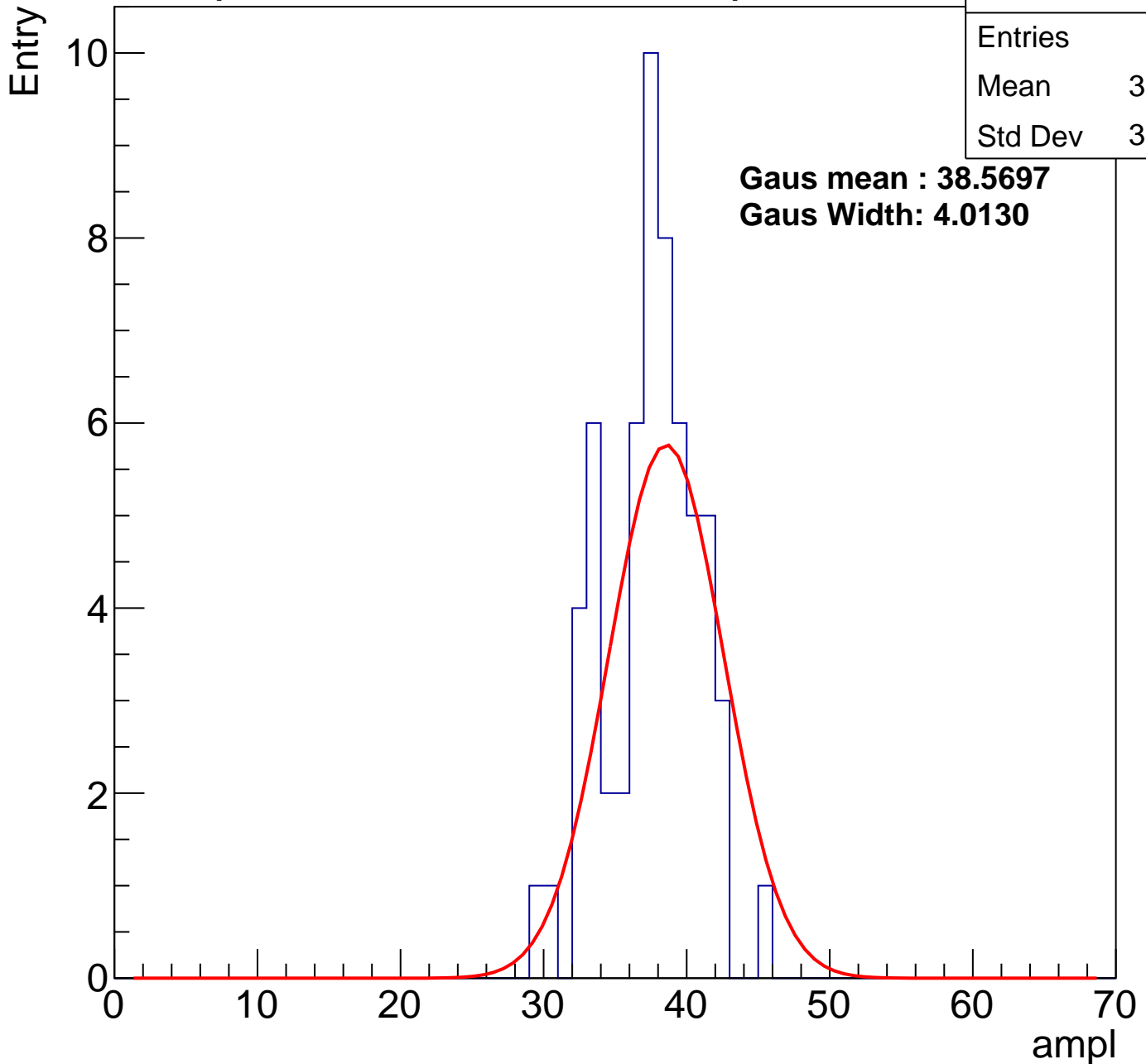
B0L102S, U20-ch11, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 60 |
| Mean | 37.05 |
| Std Dev | 3.258 |

Gaus mean : 38.5697

Gaus Width: 4.0130



B0L102S, U20-ch11, adc2

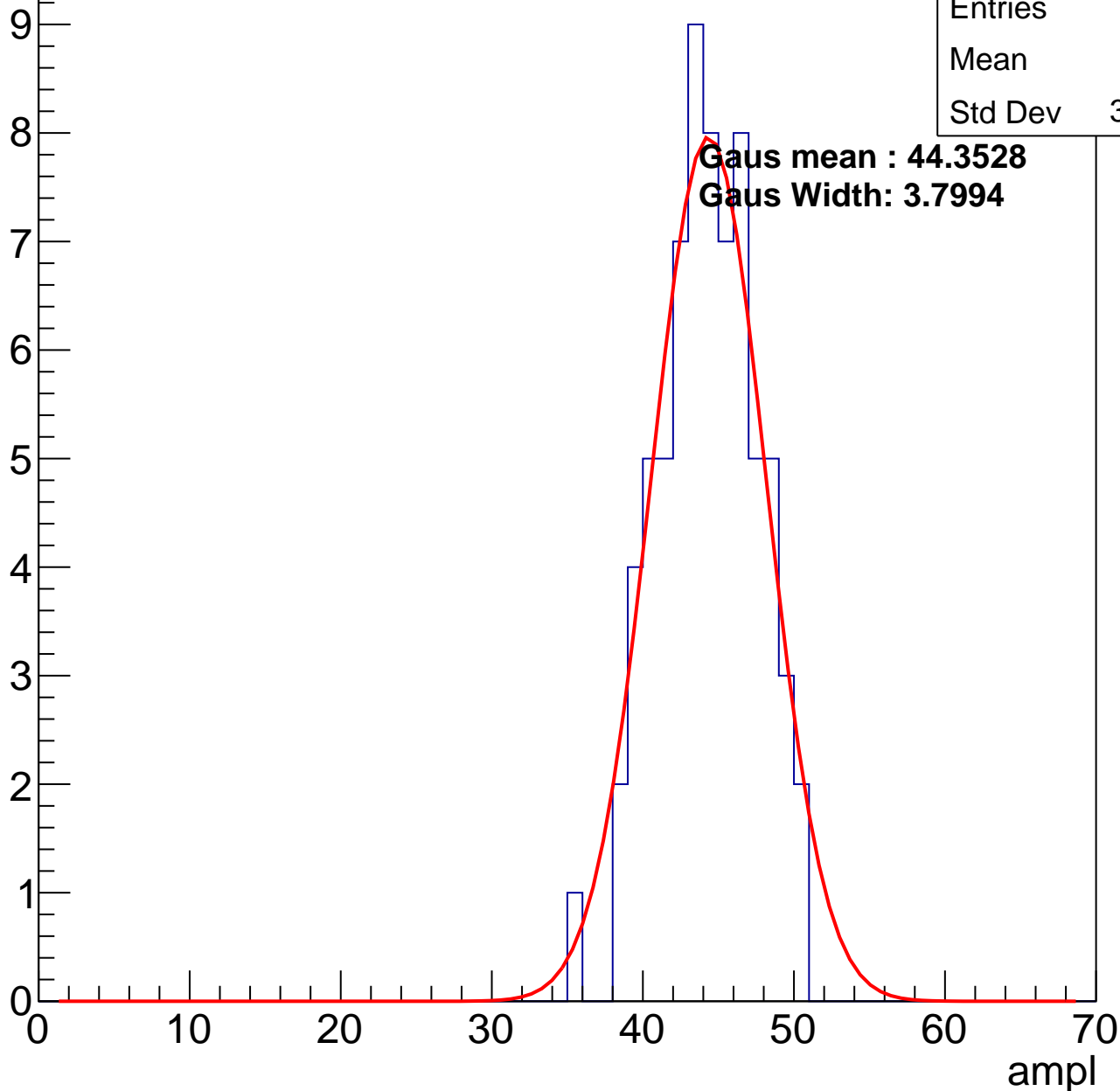
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 43.8 |
| Std Dev | 3.192 |

Gaus mean : 44.3528

Gaus Width: 3.7994

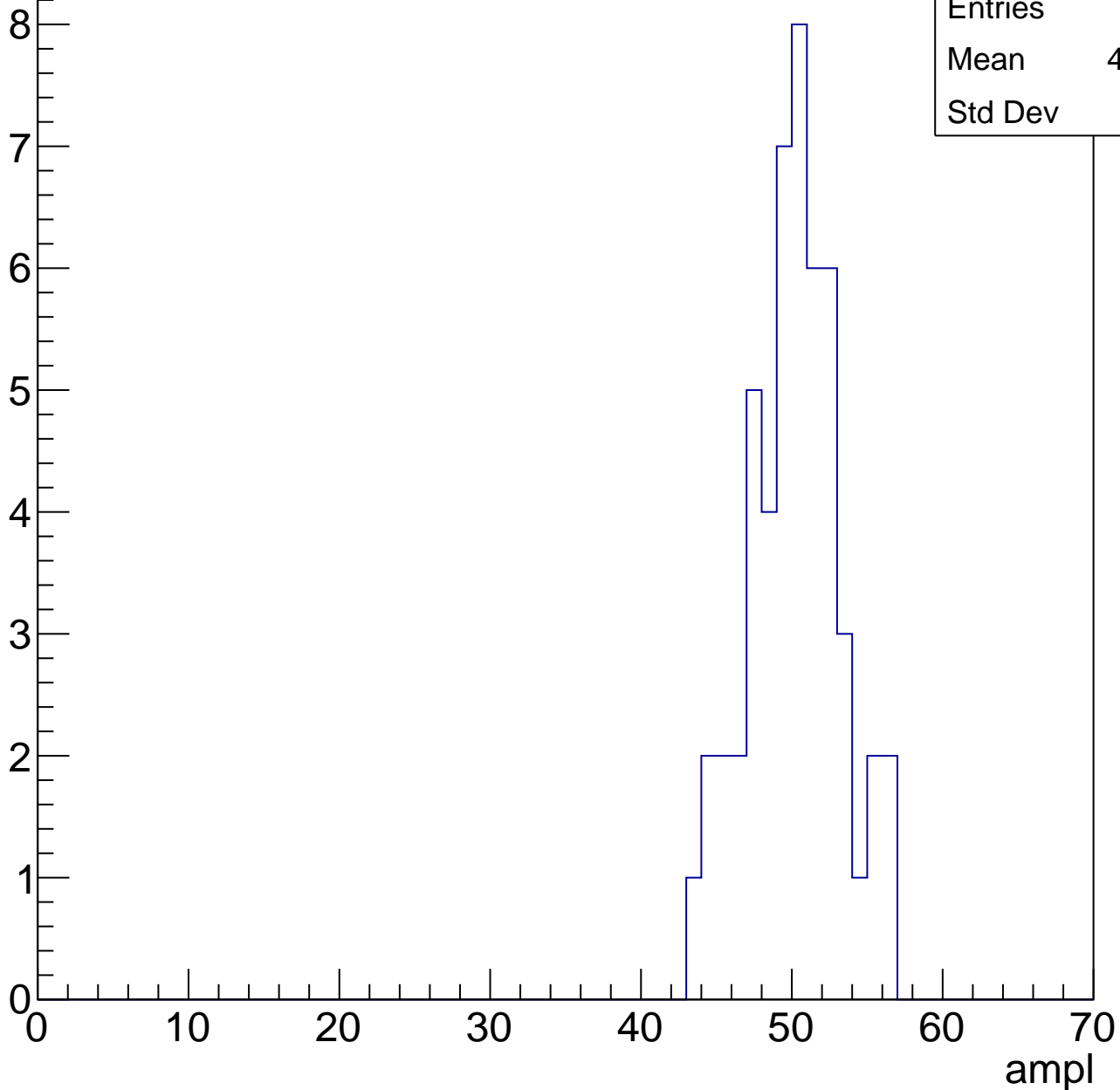


B0L102S, U20-ch11, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 49.73 |
| Std Dev | 3.01 |

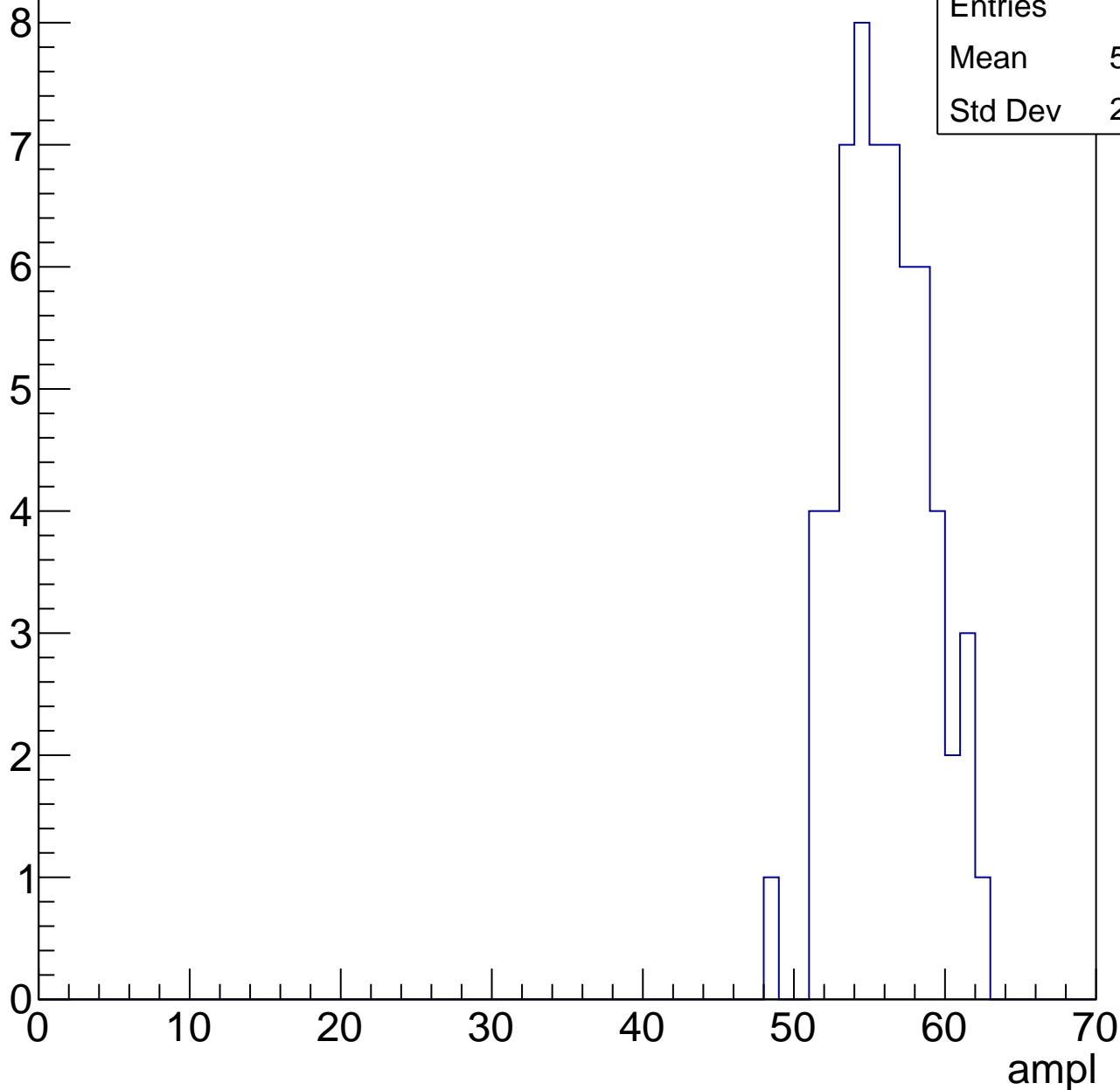


B0L102S, U20-ch11, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 55.52 |
| Std Dev | 2.958 |



B0L102S, U20-ch11, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.9 |
| Std Dev | 8.791 |

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

B0L102S, U20-ch11, adc6

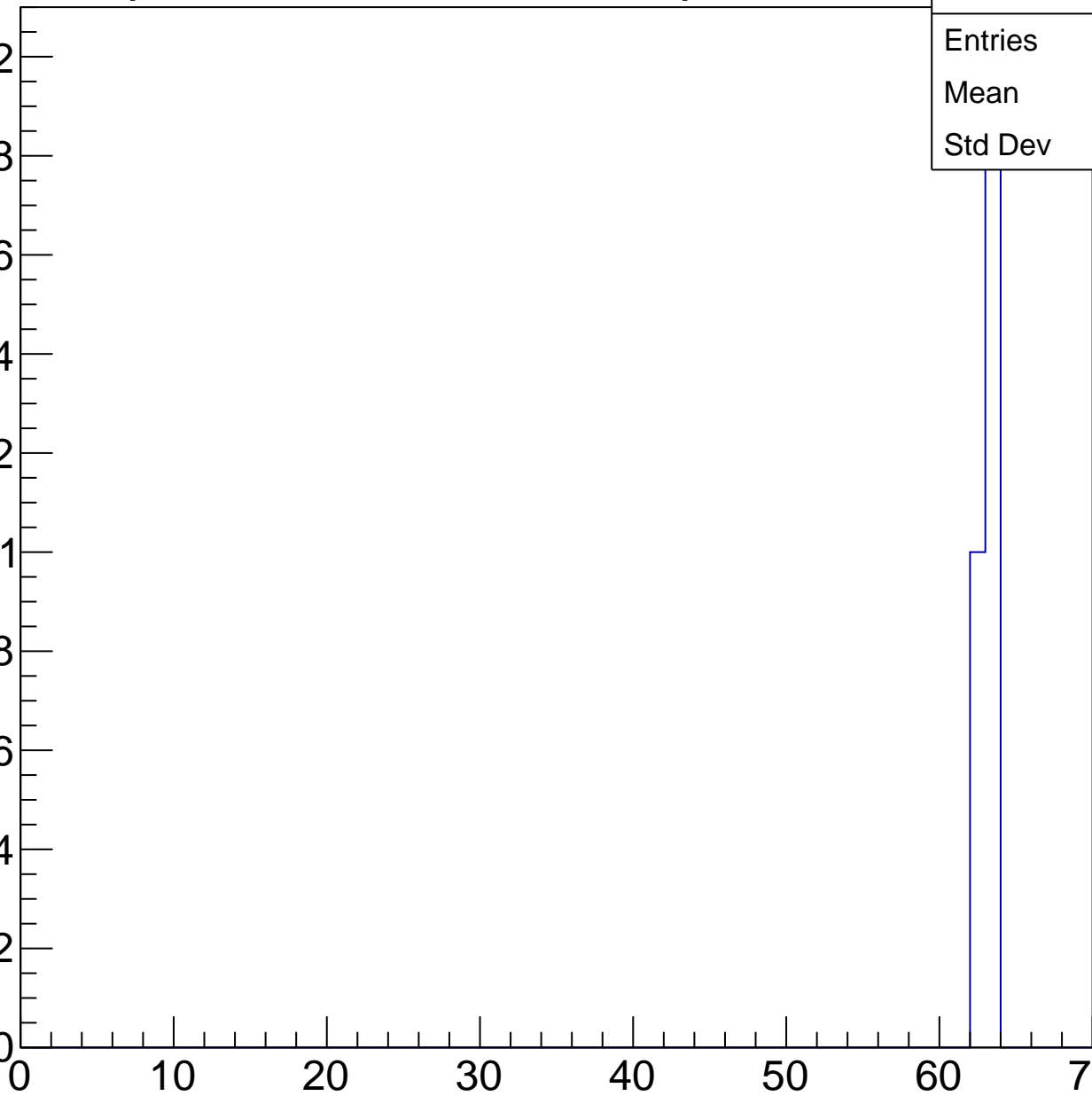
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl



B0L102S, U20-ch11, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch12, adc0

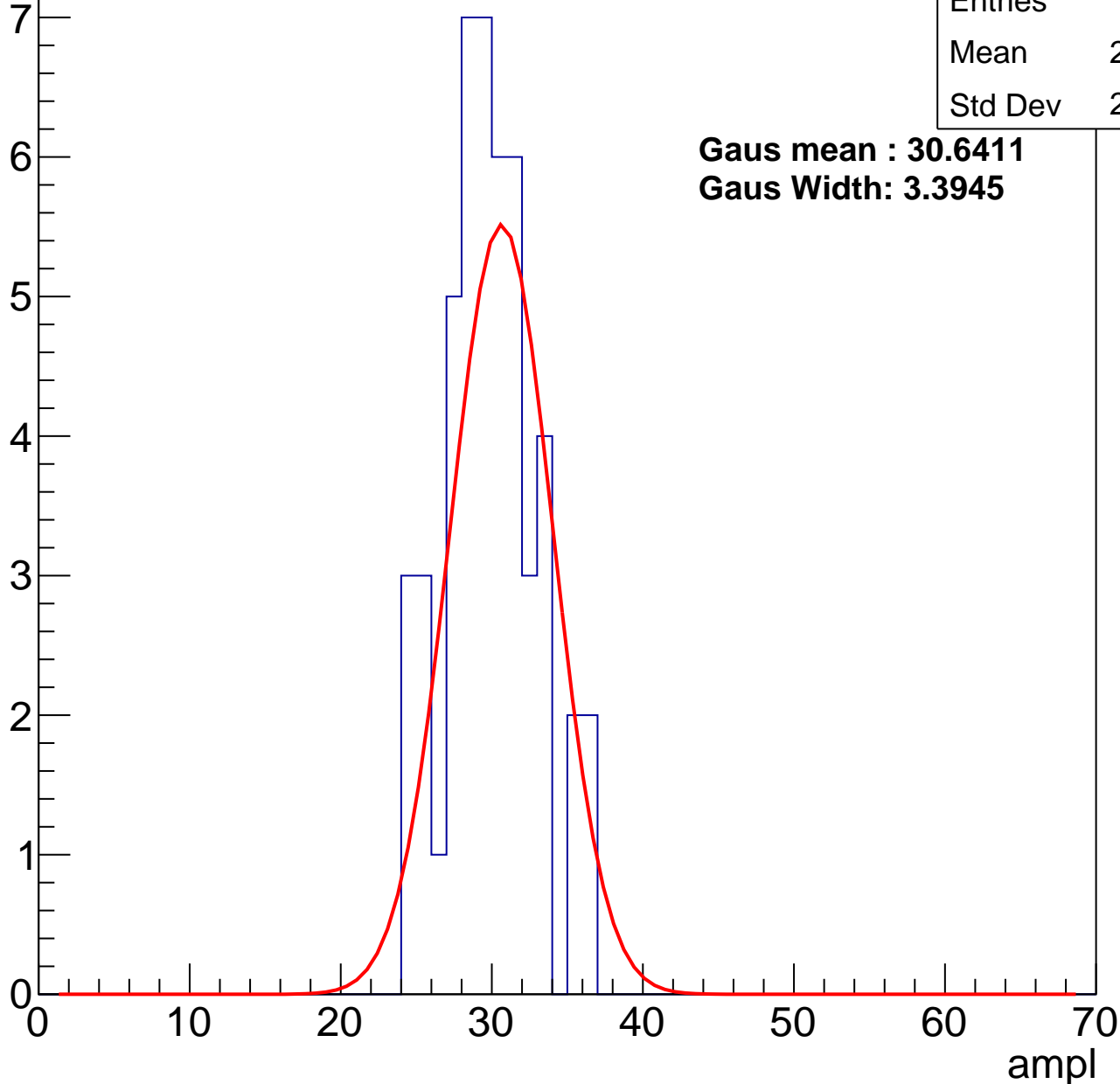
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 29.45 |
| Std Dev | 2.997 |

Gaus mean : 30.6411

Gaus Width: 3.3945



B0L102S, U20-ch12, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.54 |
| Std Dev | 3.481 |

Gaus mean : 35.7180

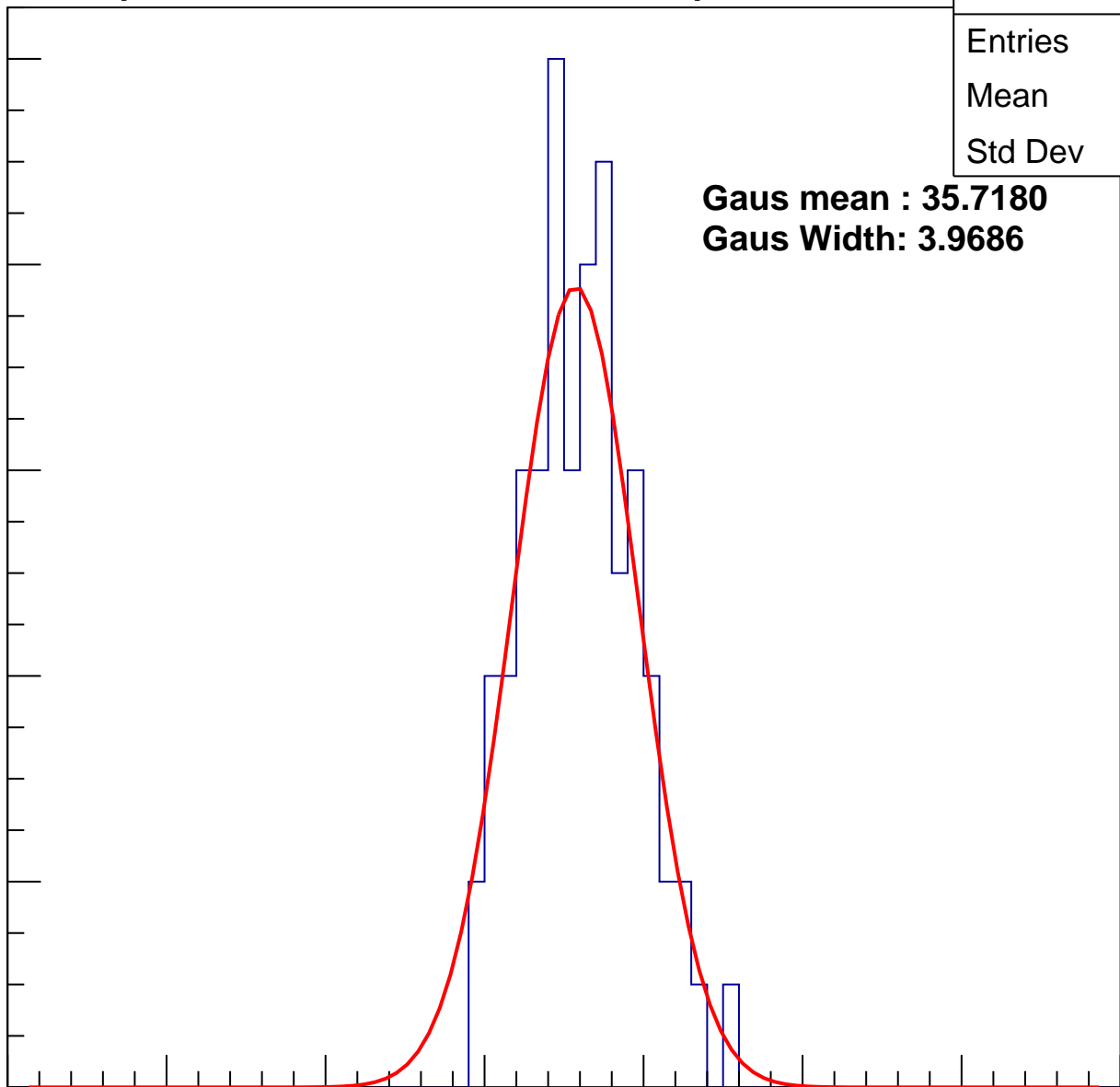
Gaus Width: 3.9686

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch12, adc2

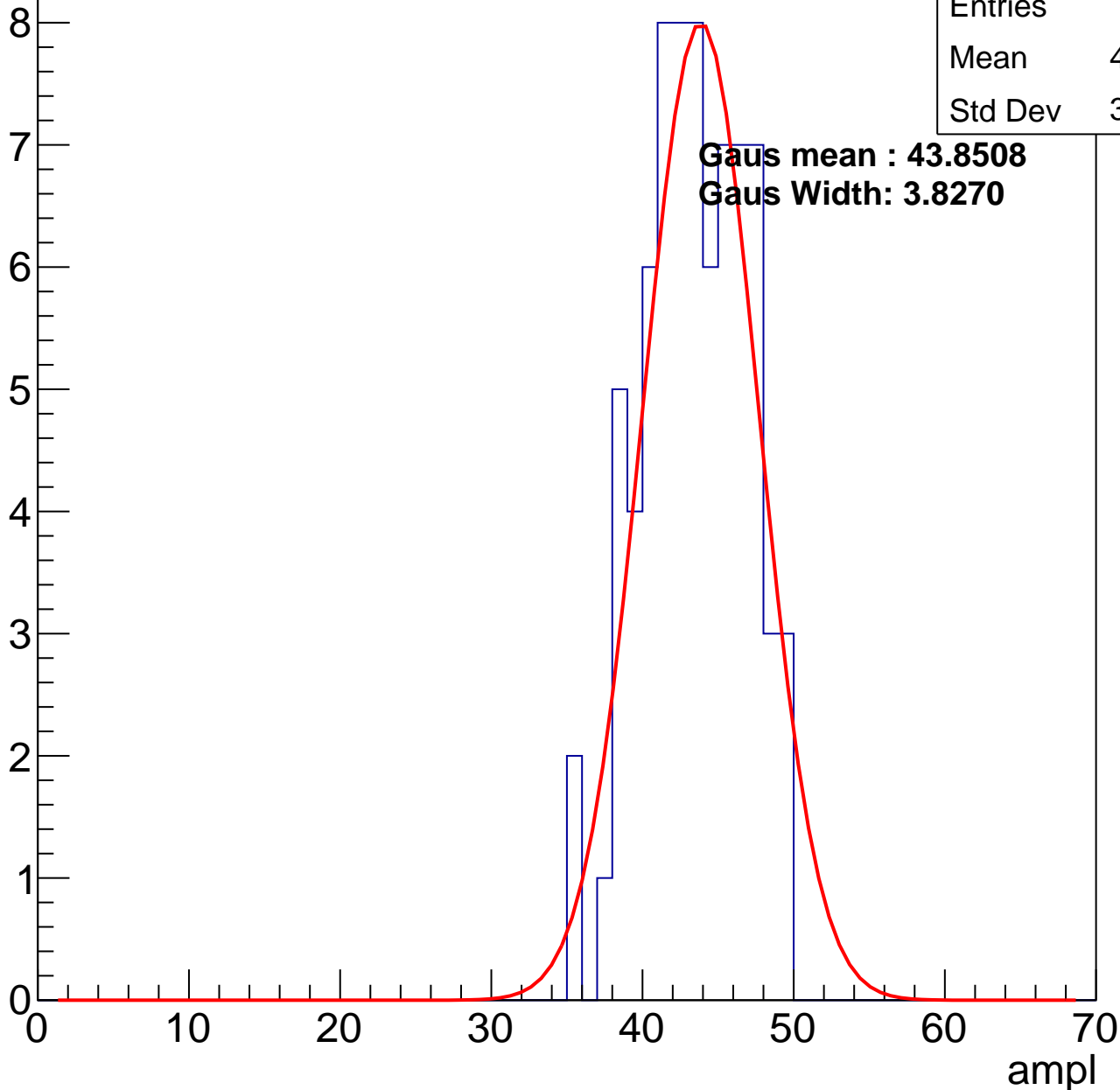
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 42.96 |
| Std Dev | 3.344 |

Gaus mean : 43.8508

Gaus Width: 3.8270

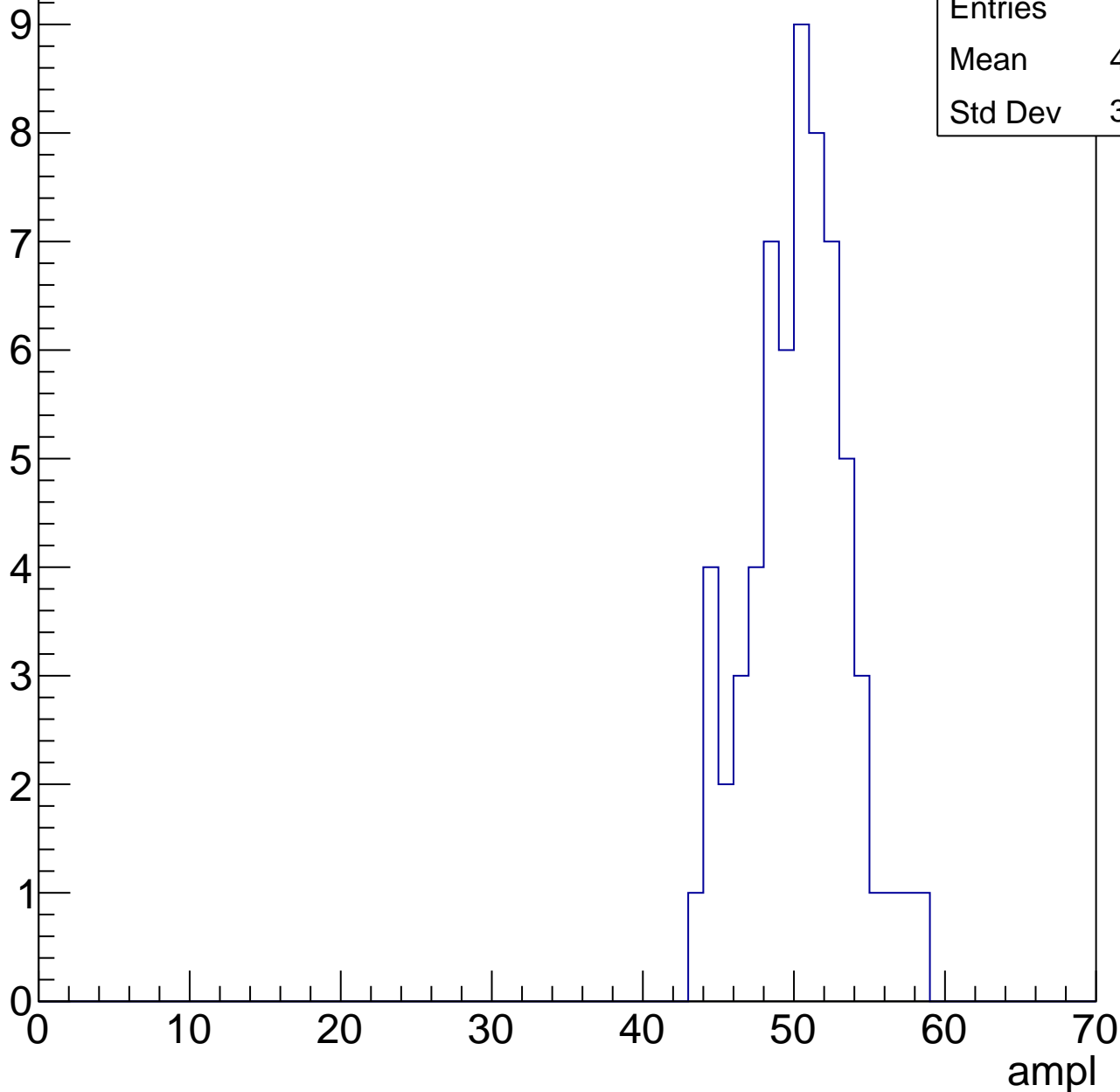


B0L102S, U20-ch12, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 49.84 |
| Std Dev | 3.243 |

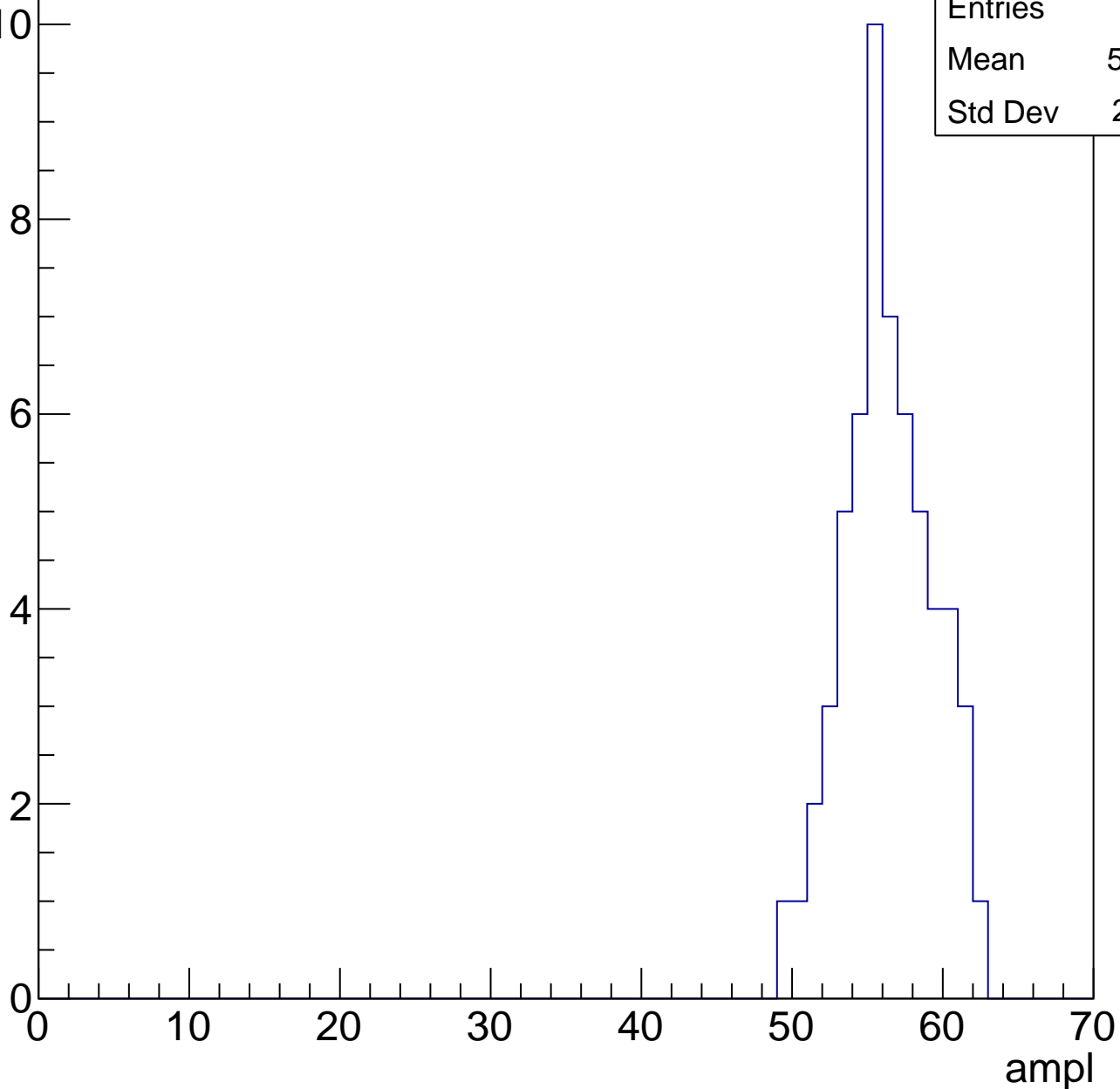


B0L102S, U20-ch12, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.88 |
| Std Dev | 2.931 |

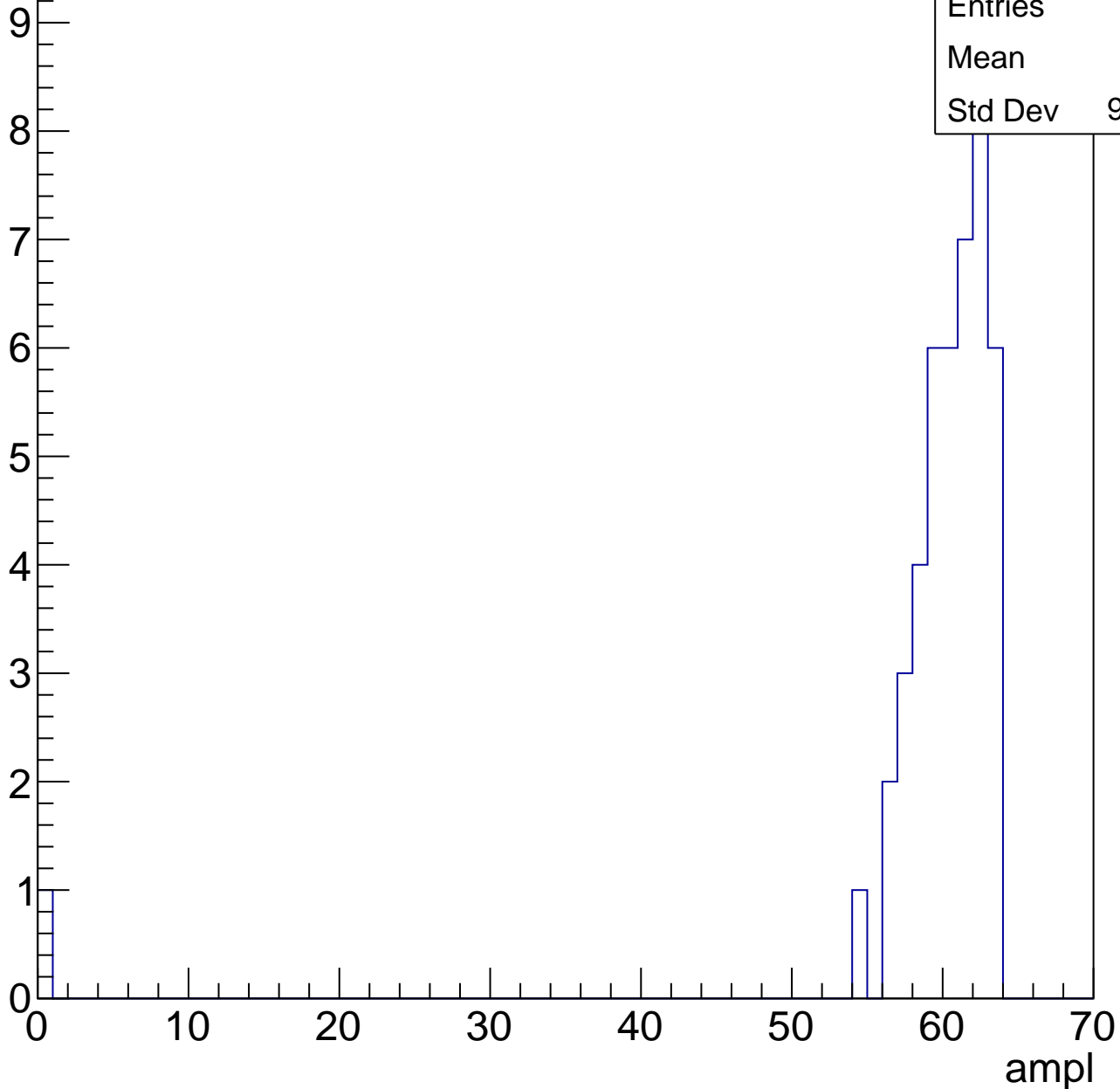


B0L102S, U20-ch12, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.8 |
| Std Dev | 9.128 |



B0L102S, U20-ch12, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch12, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L102S, U20-ch13, adc0

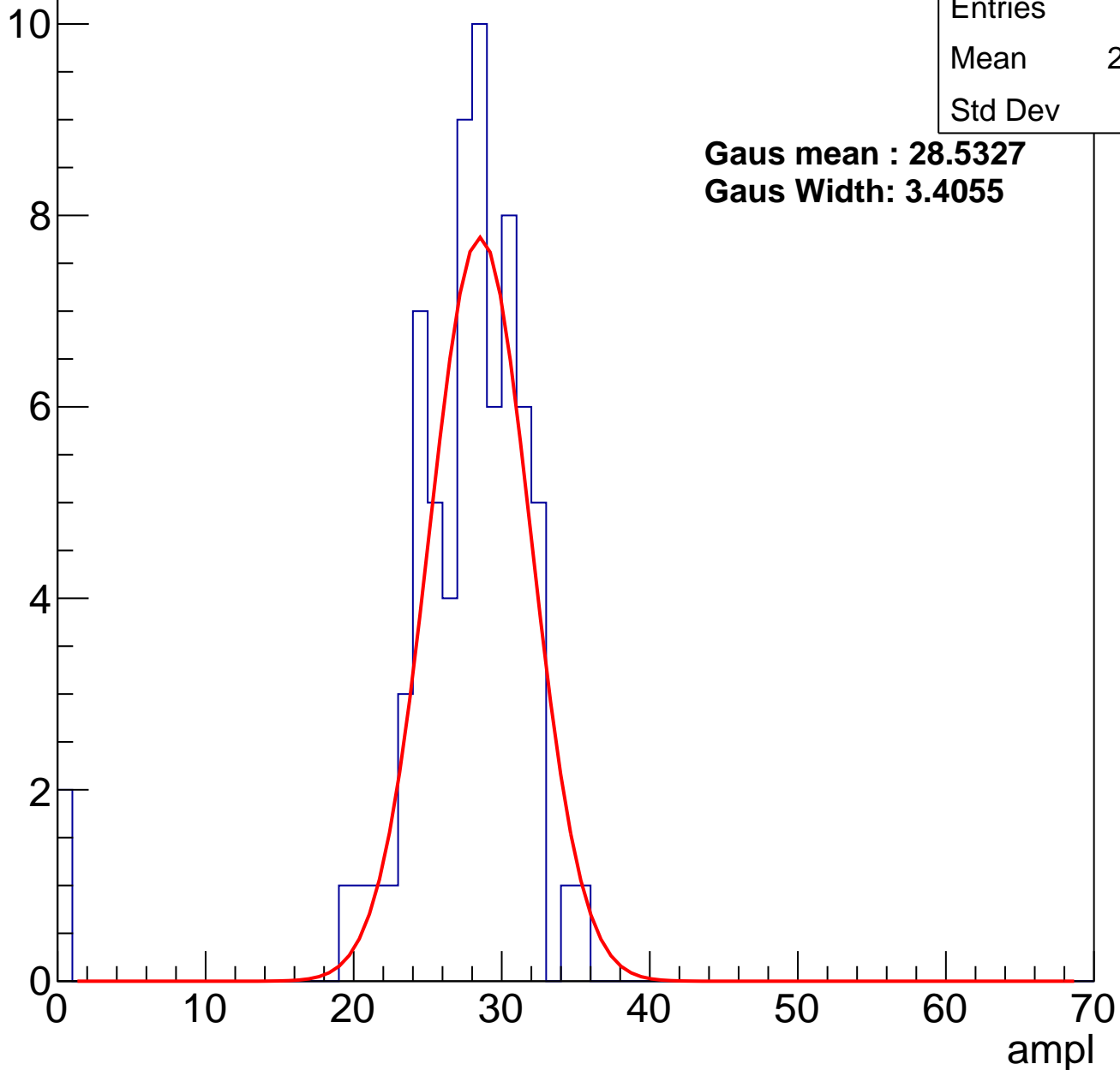
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 26.76 |
| Std Dev | 5.57 |

Gaus mean : 28.5327

Gaus Width: 3.4055

Entry



B0L102S, U20-ch13, adc1

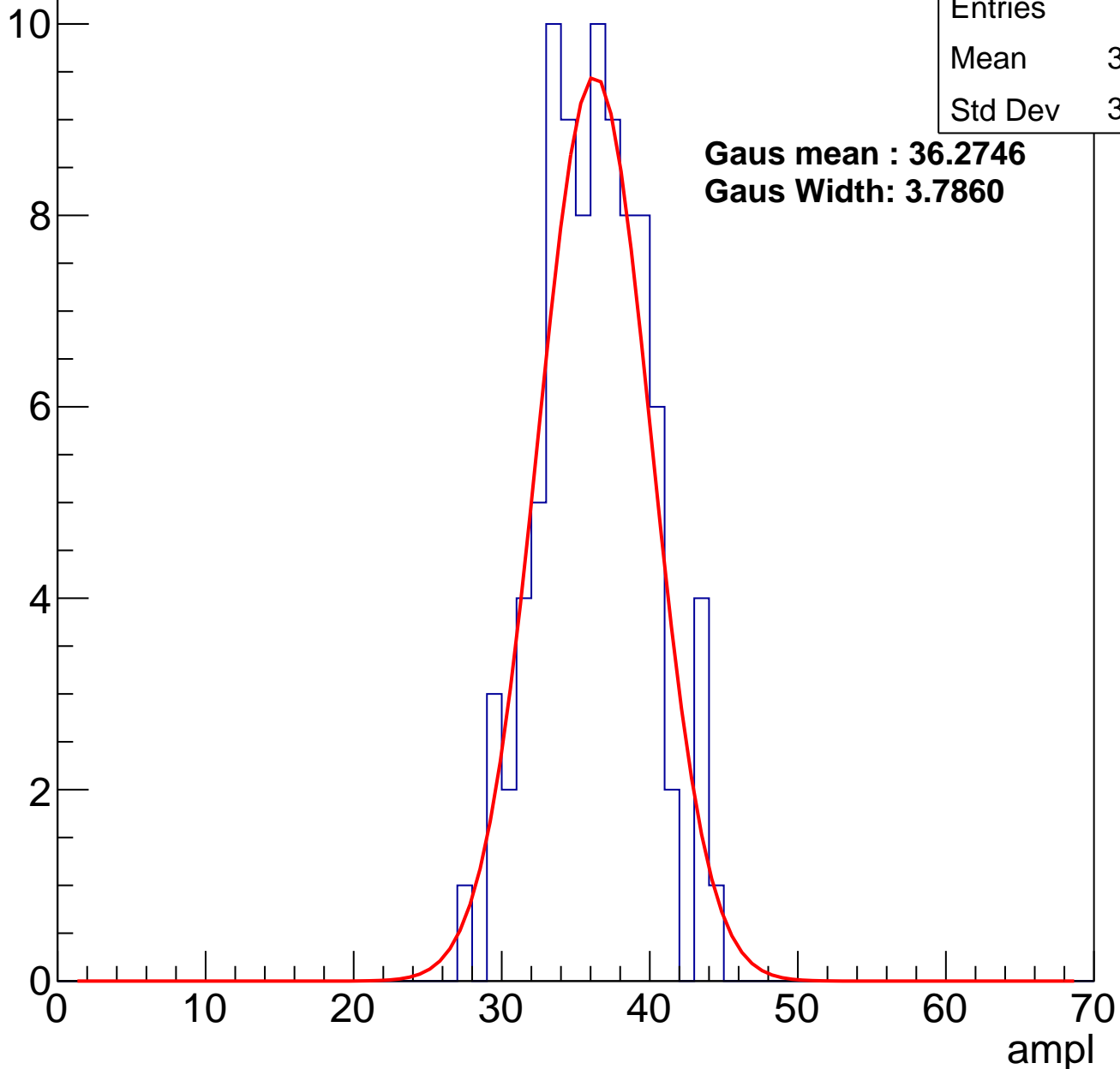
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 90 |
| Mean | 35.79 |
| Std Dev | 3.542 |

Gaus mean : 36.2746

Gaus Width: 3.7860

Entry



B0L102S, U20-ch13, adc2

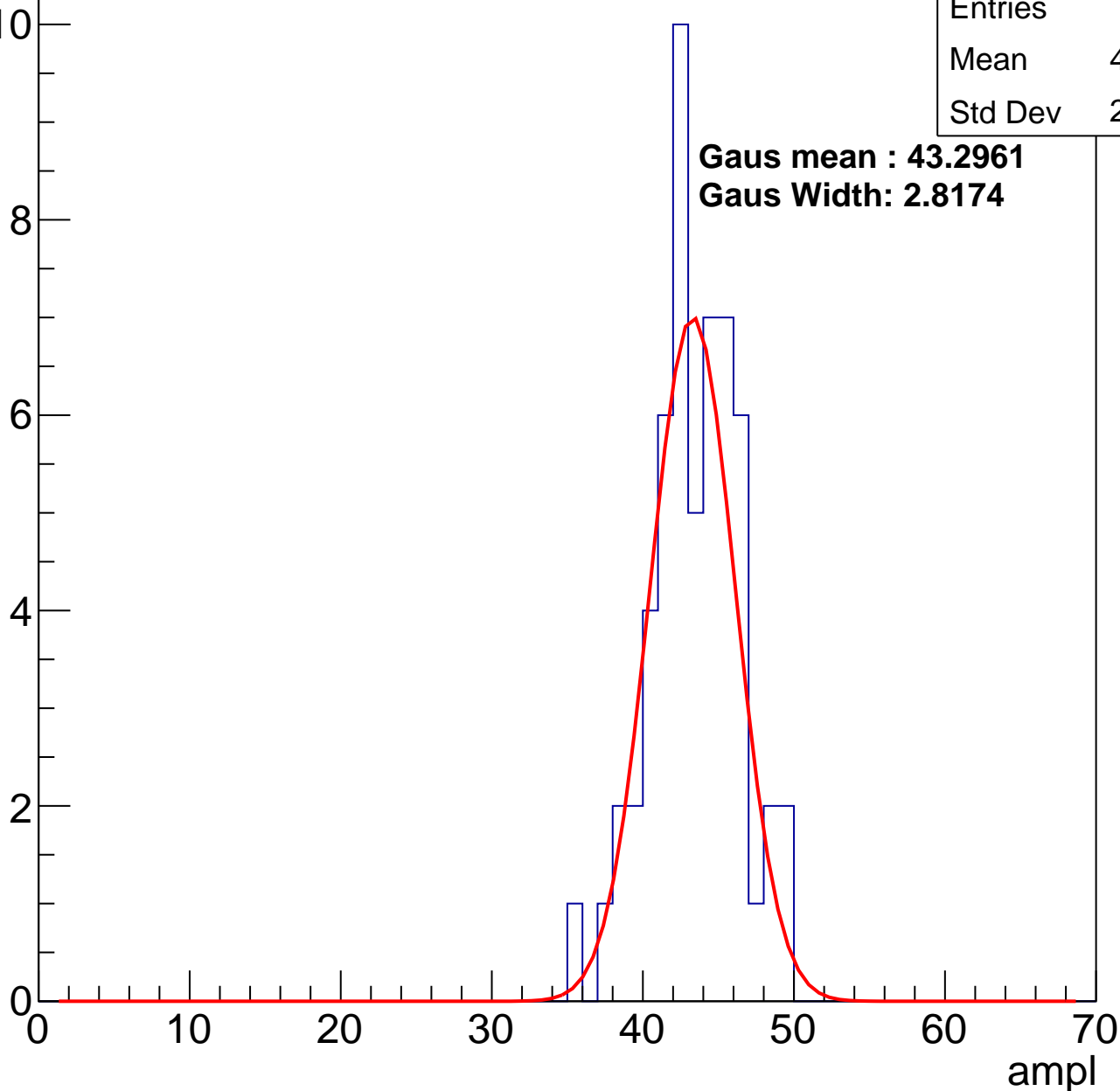
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 42.98 |
| Std Dev | 2.925 |

Gaus mean : 43.2961

Gaus Width: 2.8174

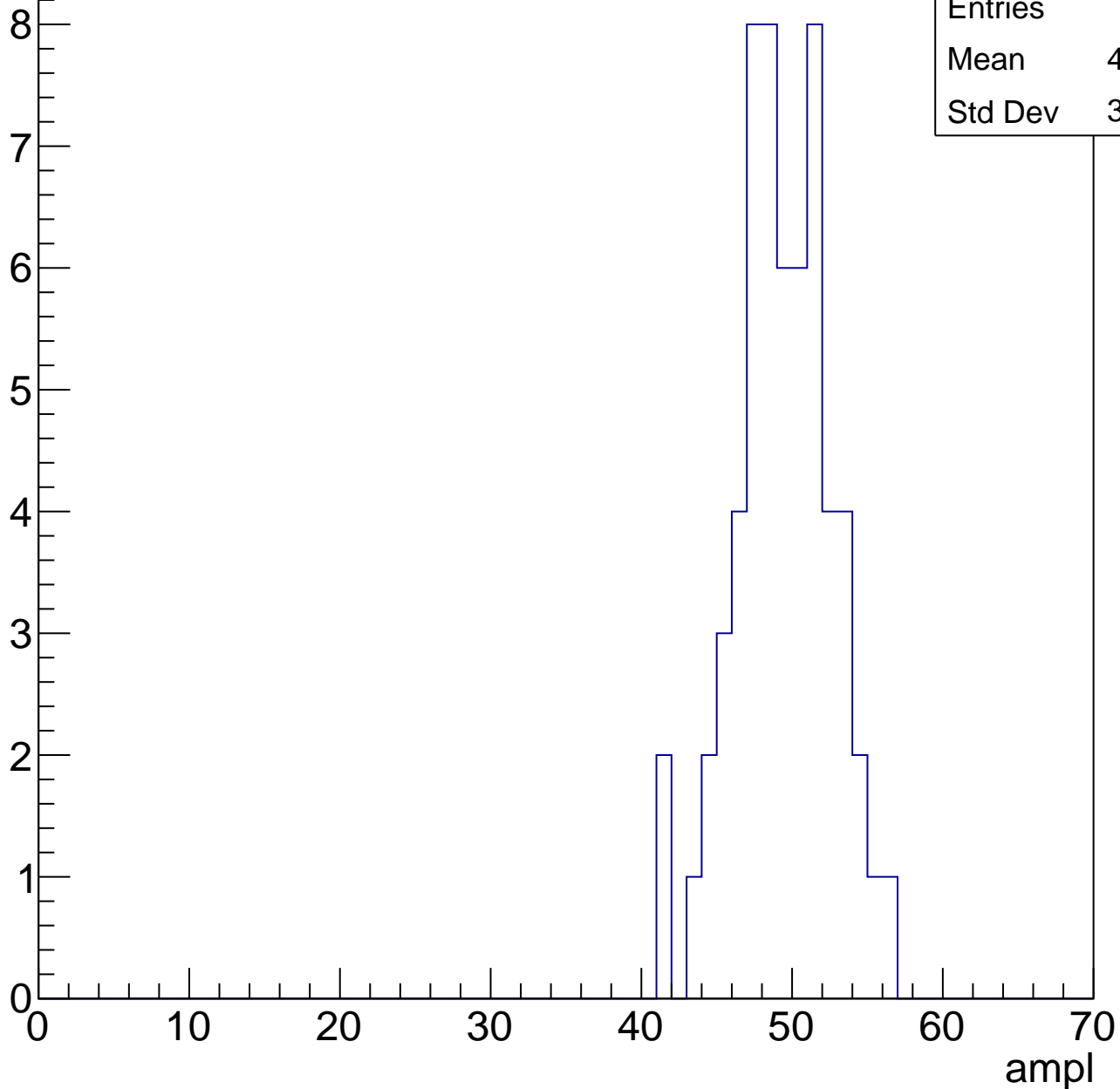


B0L102S, U20-ch13, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.88 |
| Std Dev | 3.189 |

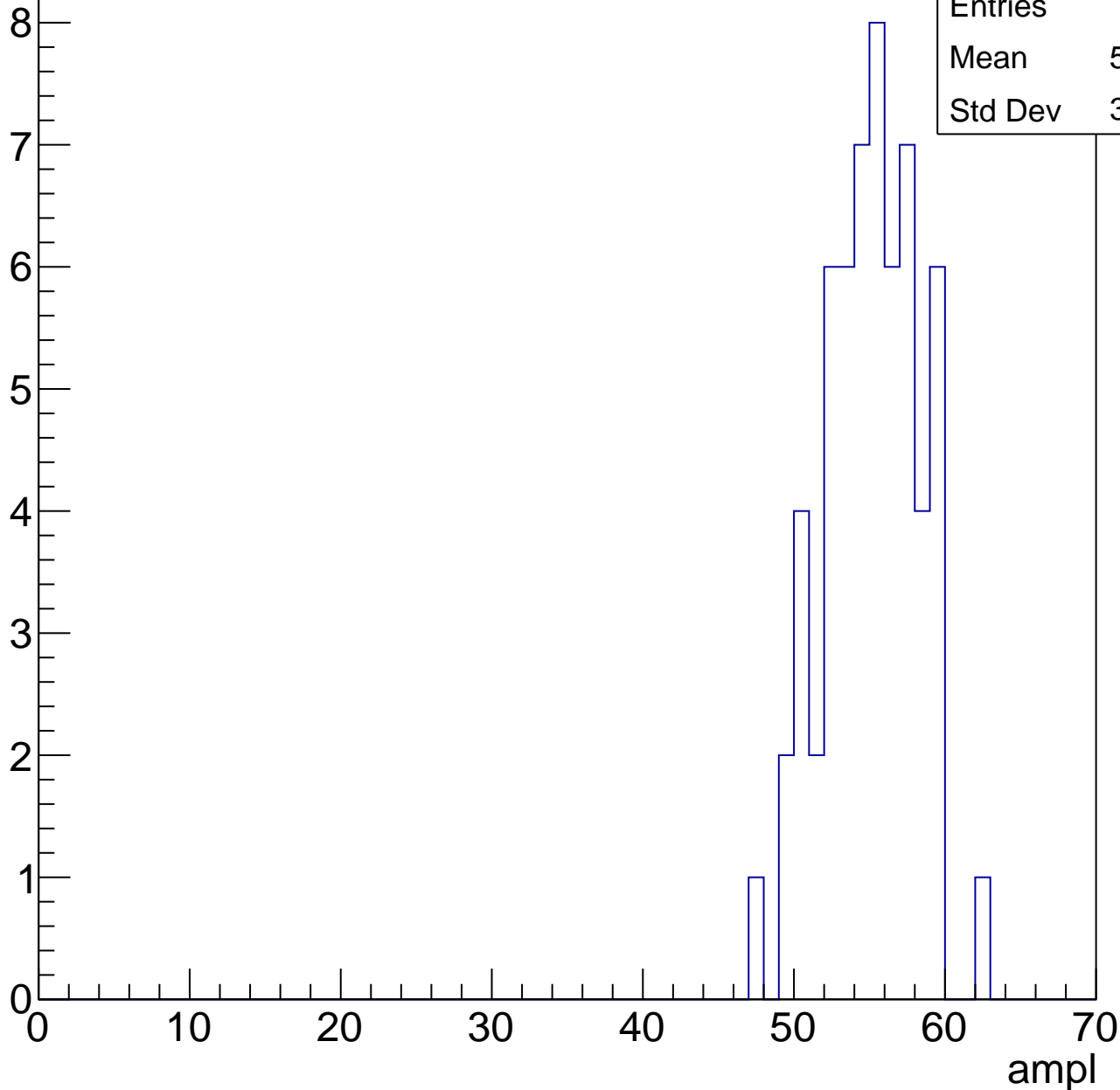


B0L102S, U20-ch13, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 54.63 |
| Std Dev | 3.055 |

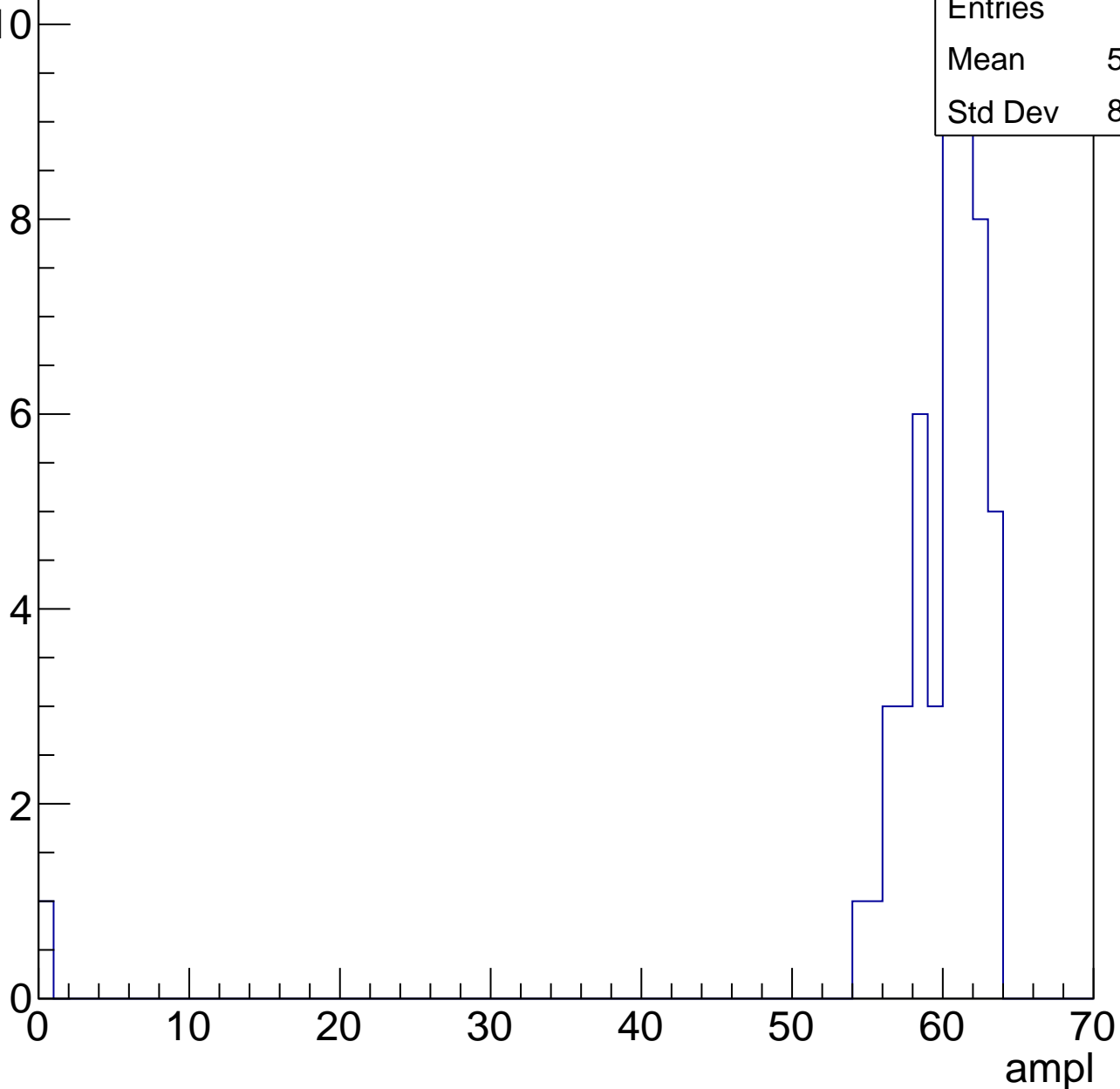


B0L102S, U20-ch13, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.68 |
| Std Dev | 8.675 |

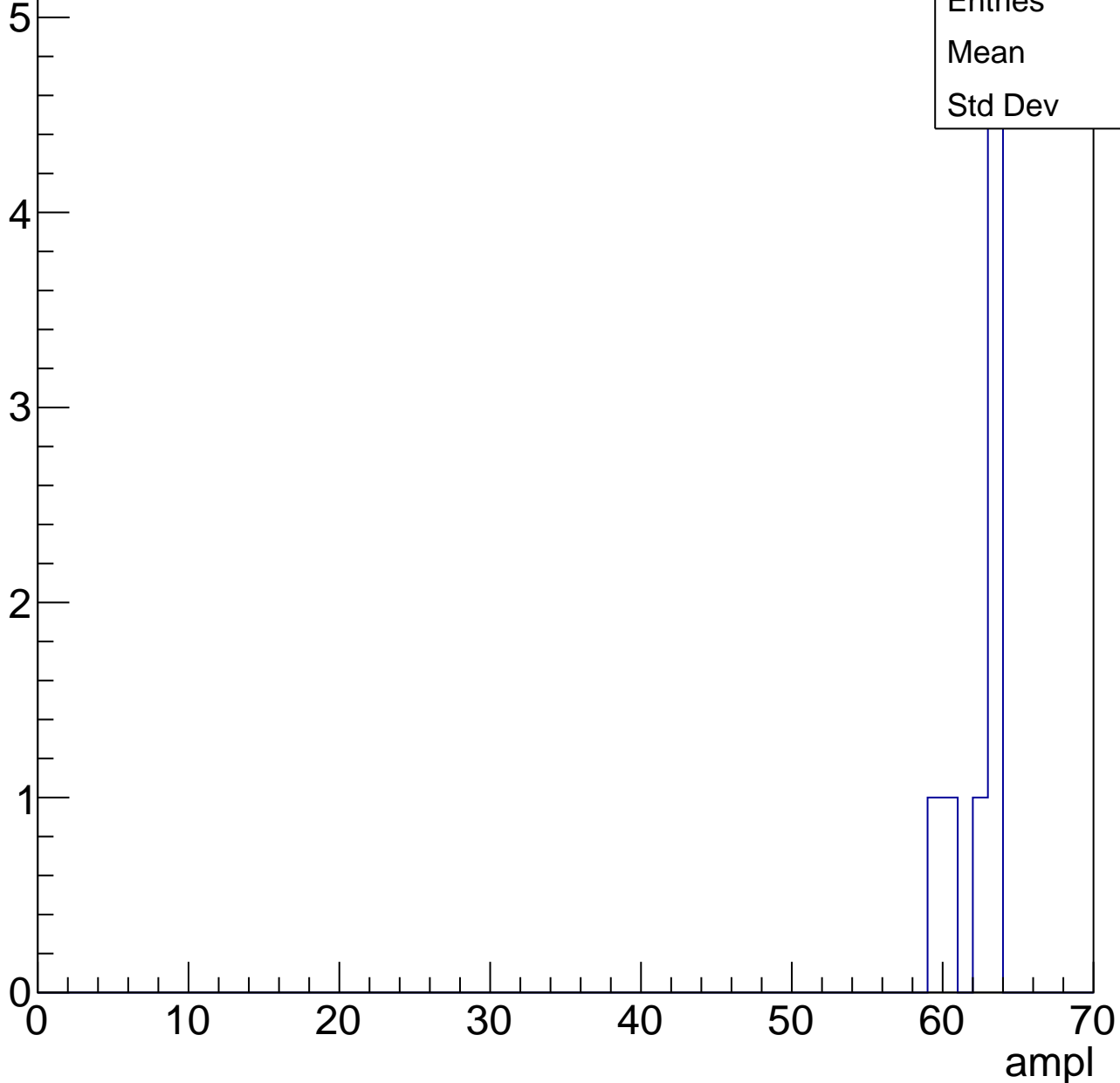


B0L102S, U20-ch13, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-----|
| Entries | 8 |
| Mean | 62 |
| Std Dev | 1.5 |



B0L102S, U20-ch13, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch14, adc0

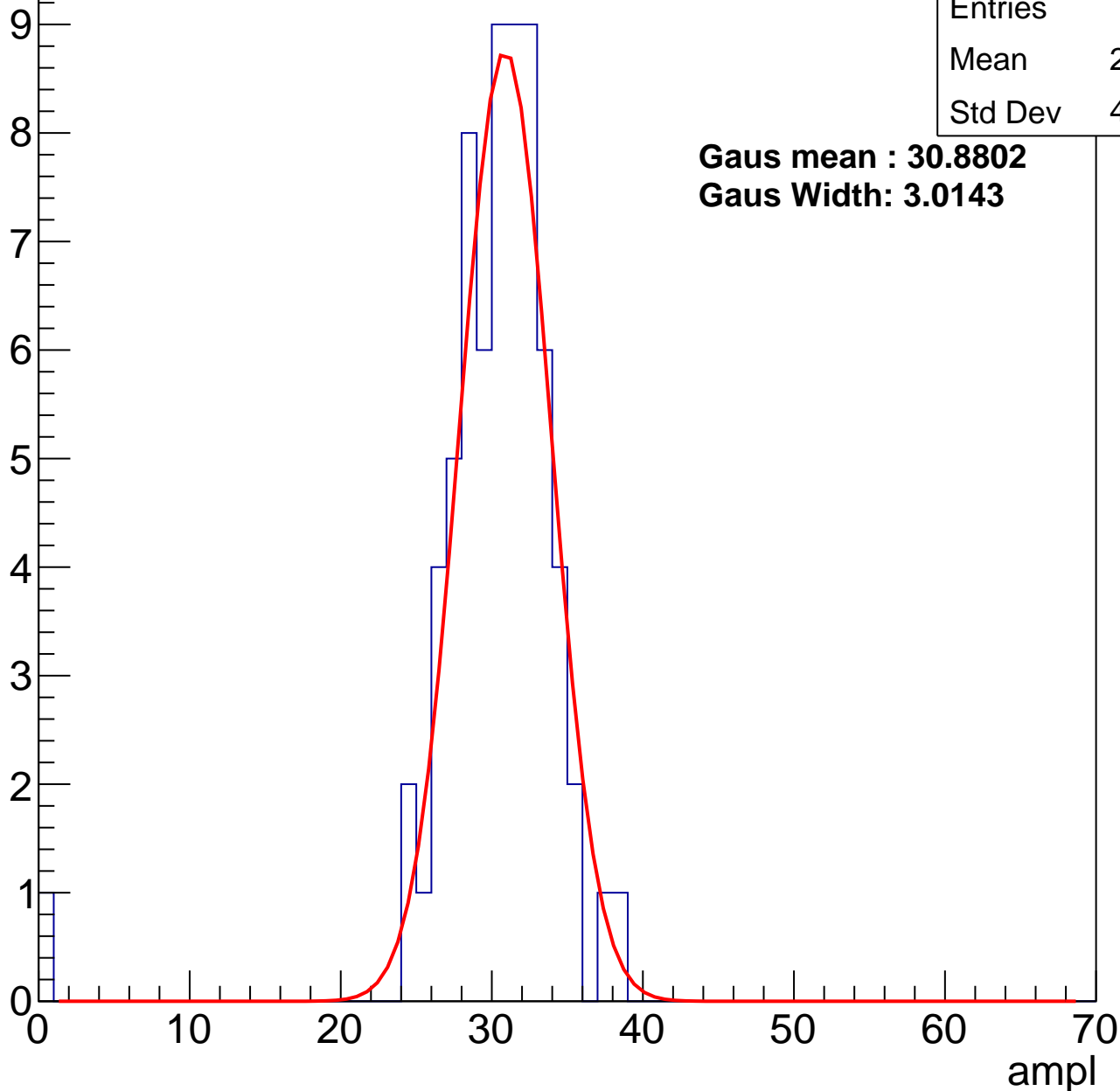
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.79 |
| Std Dev | 4.645 |

Gaus mean : 30.8802

Gaus Width: 3.0143

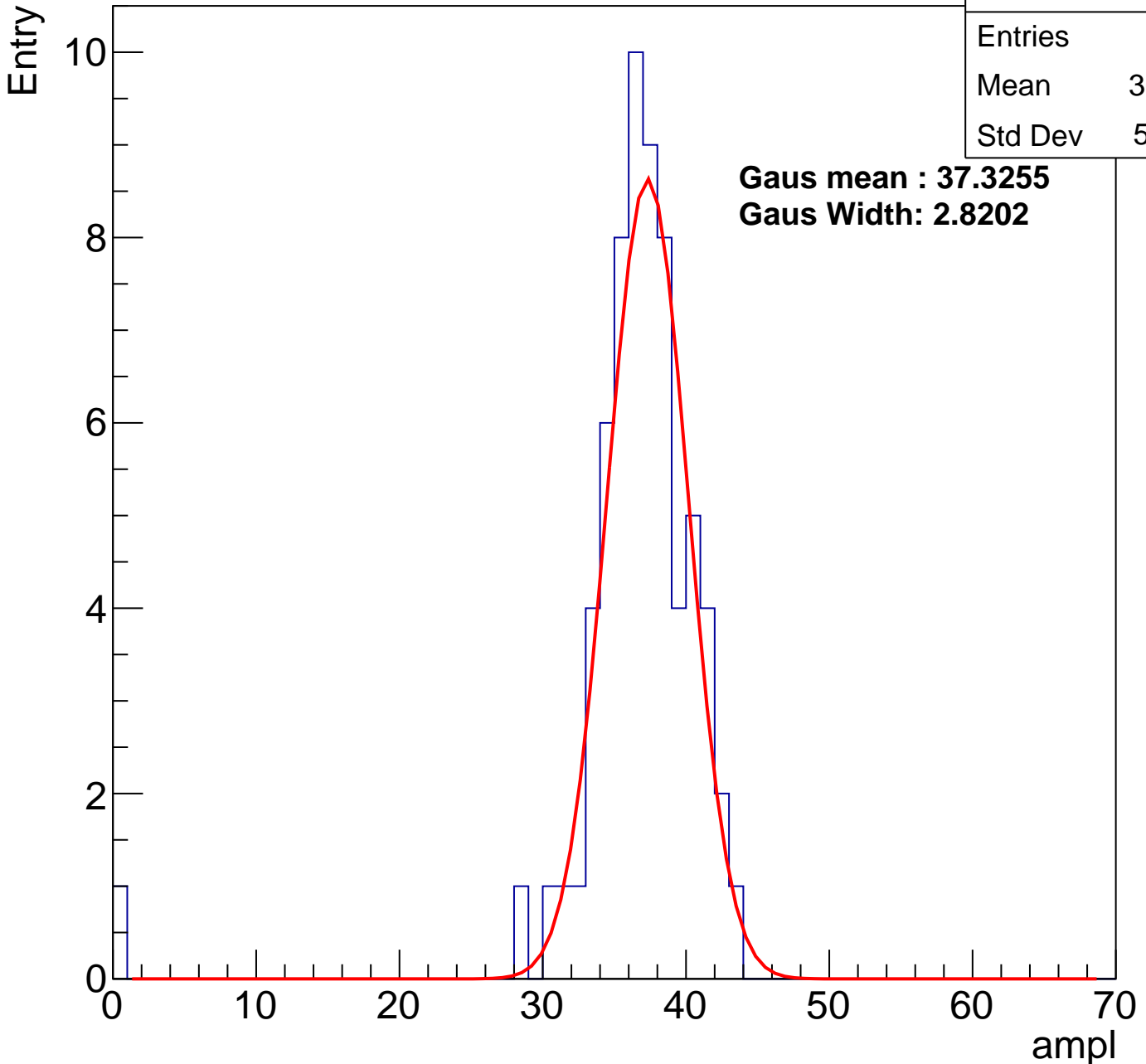


B0L102S, U20-ch14, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.08 |
| Std Dev | 5.341 |

Gaus mean : 37.3255
Gaus Width: 2.8202



B0L102S, U20-ch14, adc2

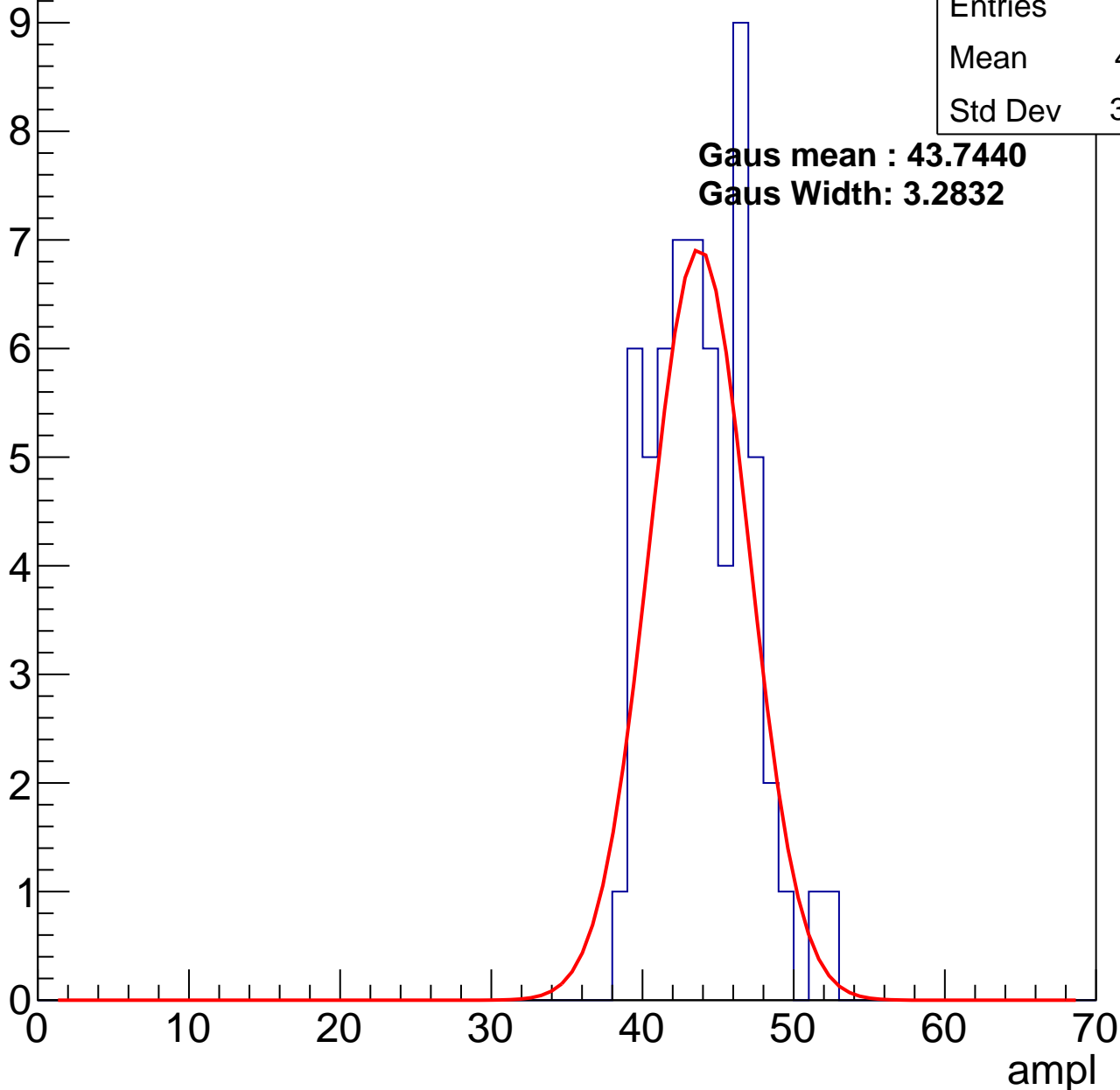
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 43.51 |
| Std Dev | 3.124 |

Gaus mean : 43.7440

Gaus Width: 3.2832

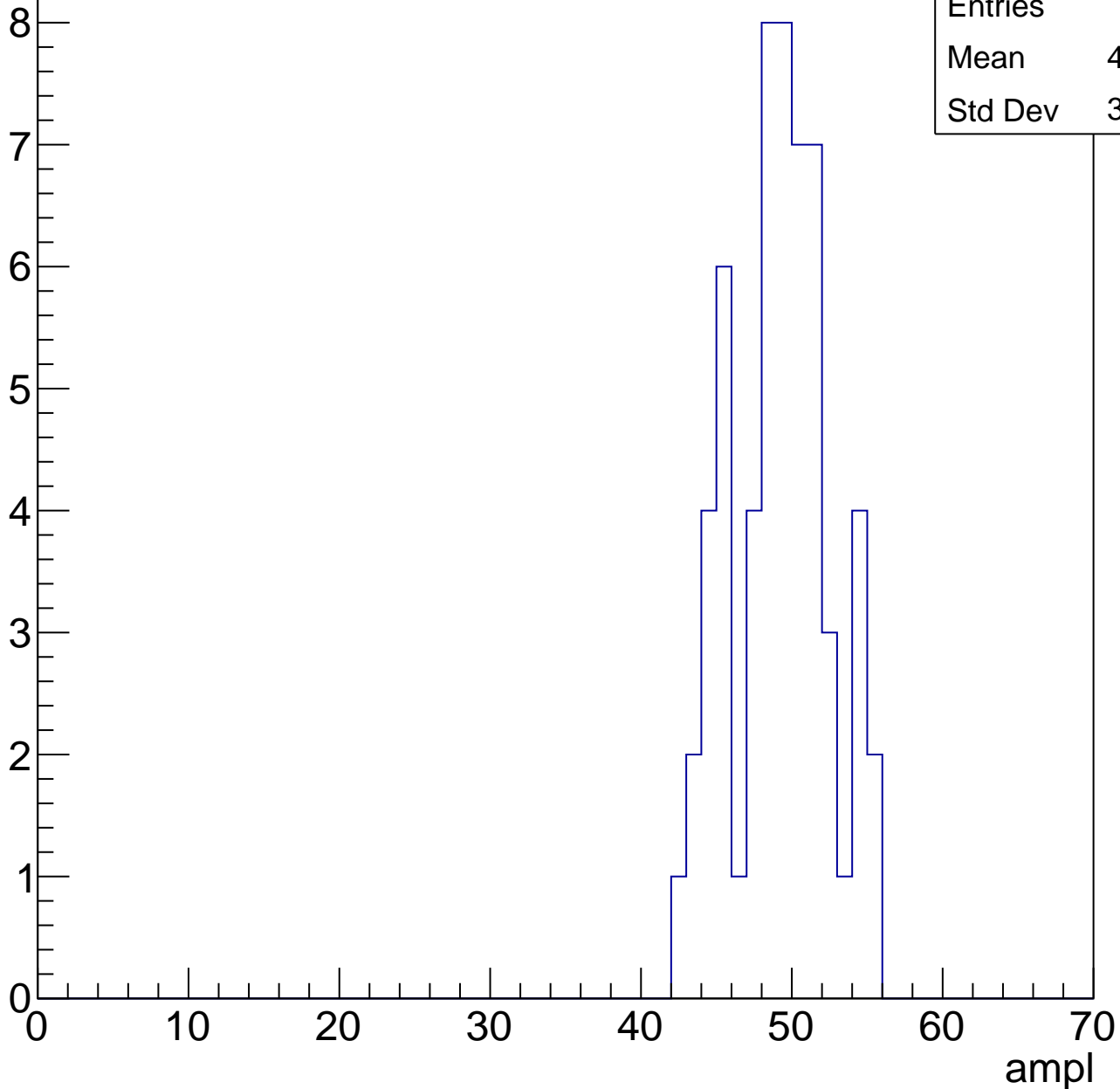


B0L102S, U20-ch14, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 48.72 |
| Std Dev | 3.204 |

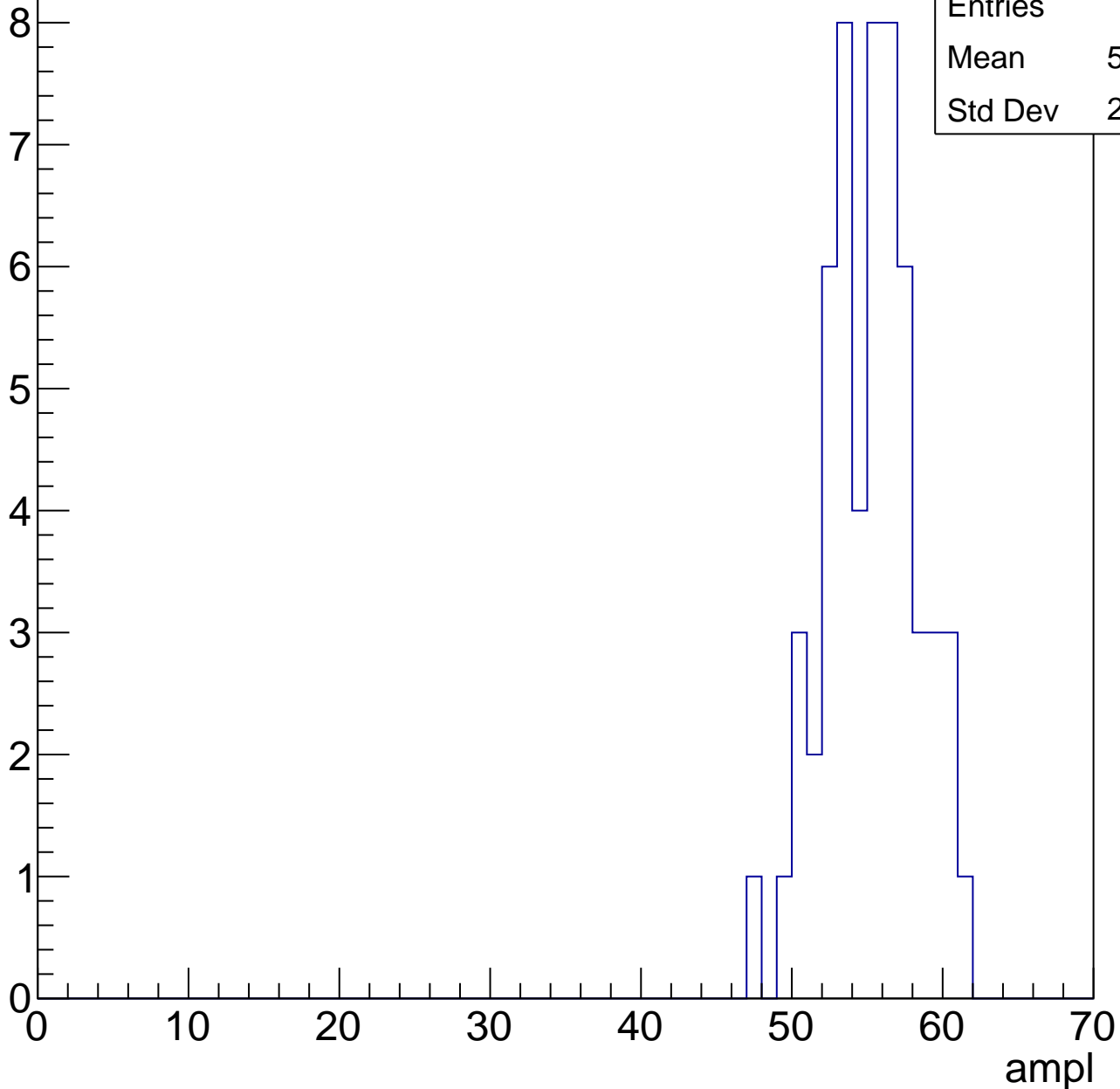


B0L102S, U20-ch14, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.77 |
| Std Dev | 2.997 |

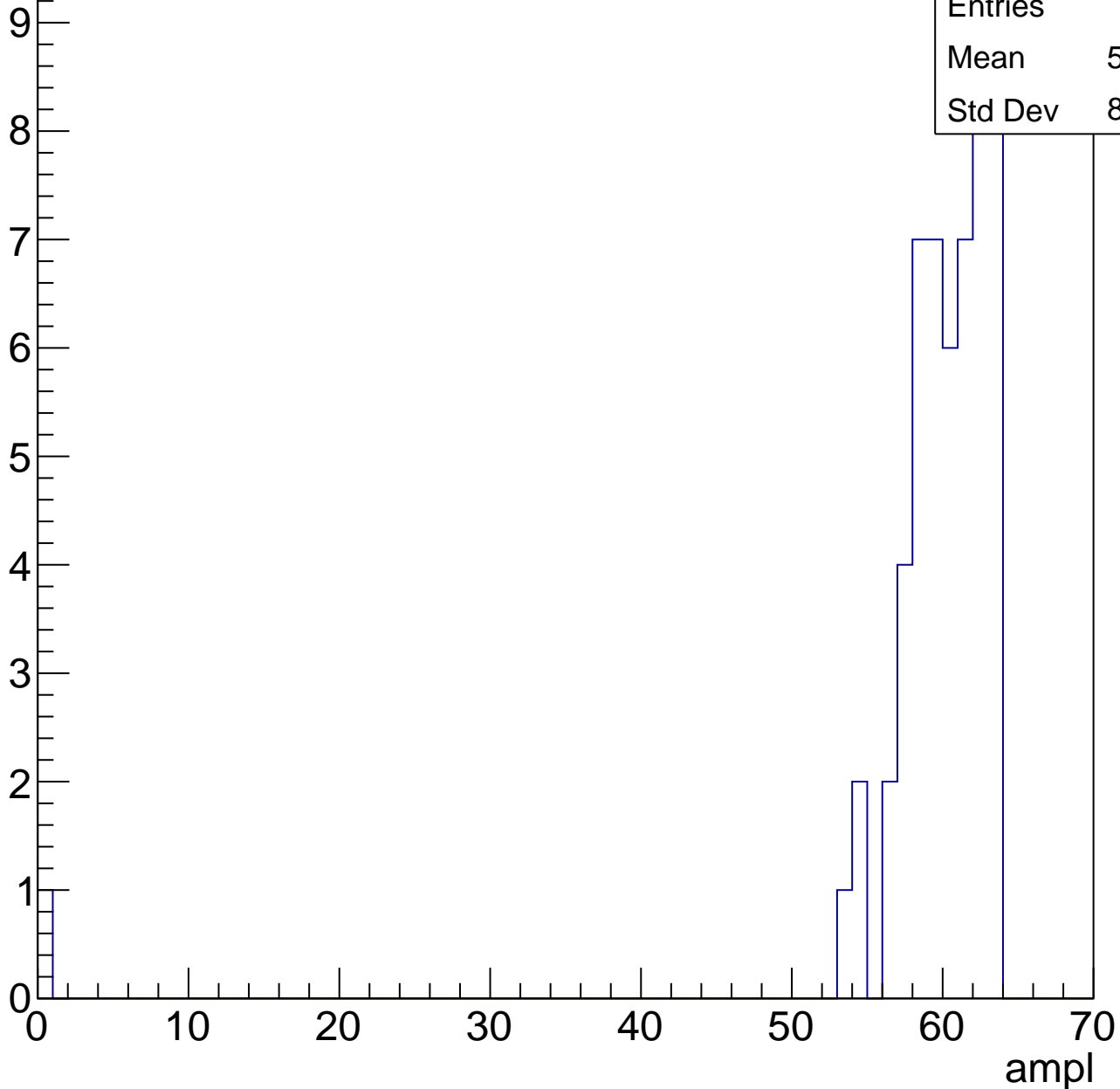


B0L102S, U20-ch14, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.76 |
| Std Dev | 8.382 |



B0L102S, U20-ch14, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch14, adc7

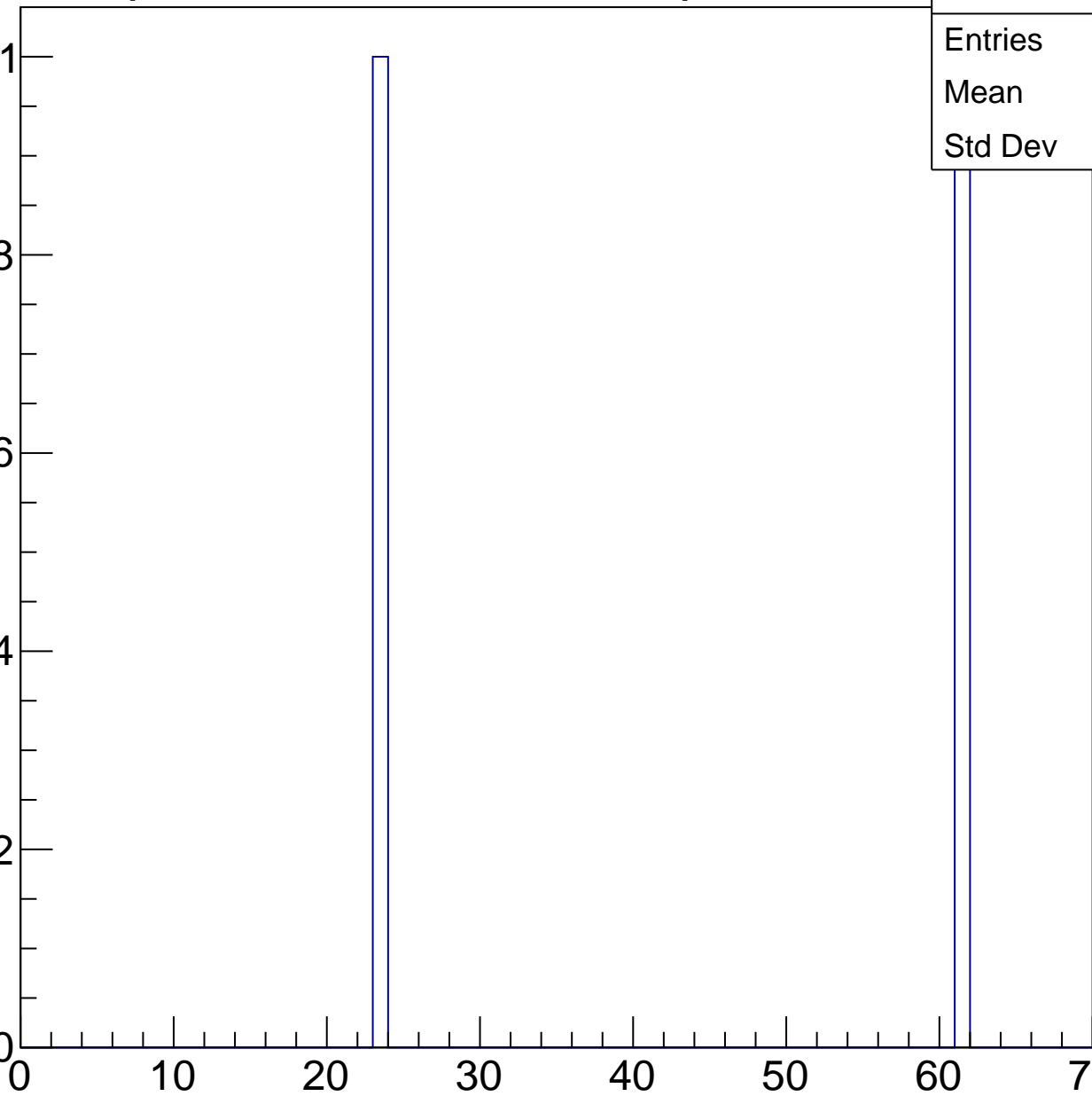
calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 42 |
| Std Dev | 19 |

ampl



B0L102S, U20-ch15, adc0

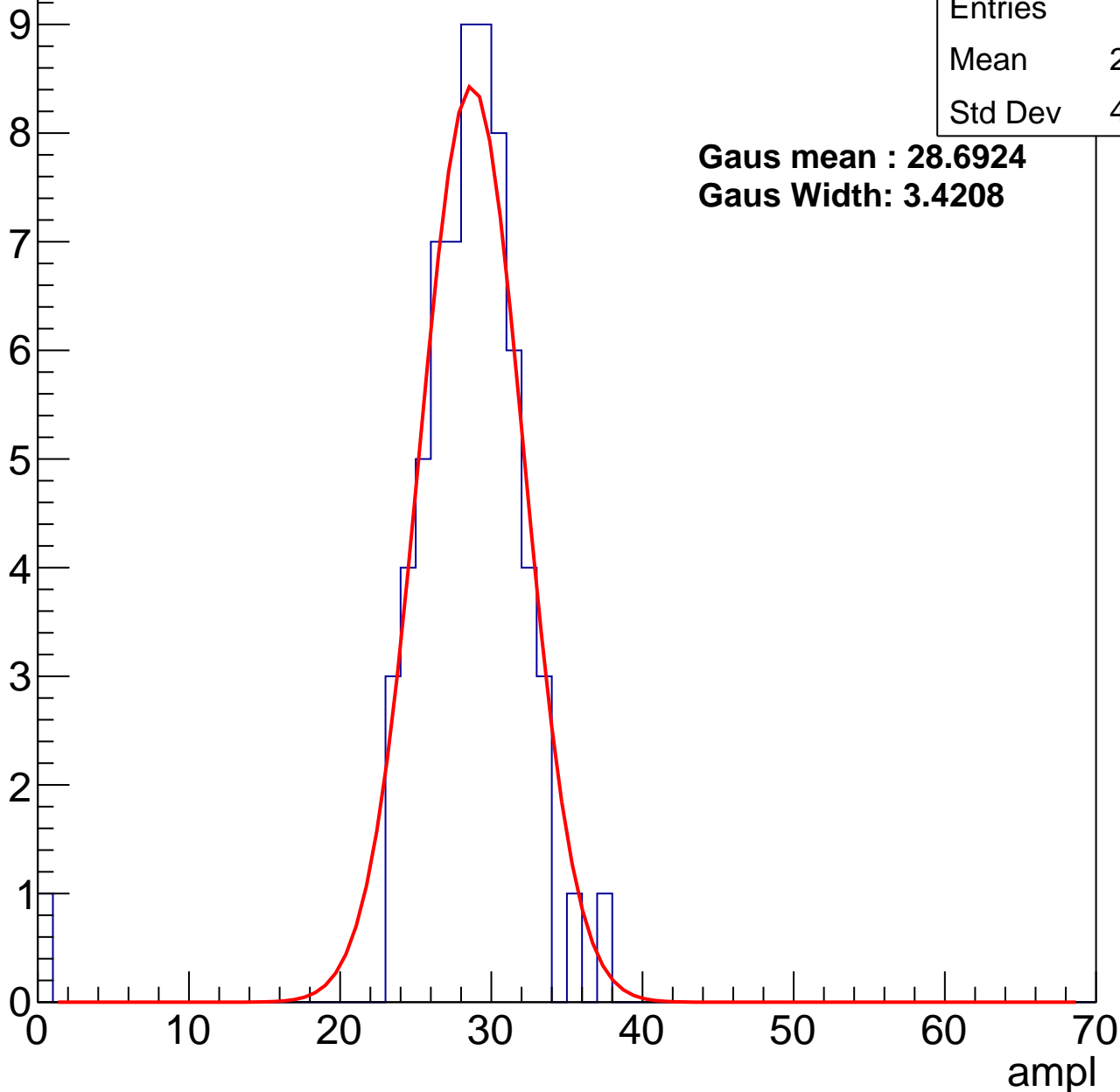
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 27.93 |
| Std Dev | 4.483 |

Gaus mean : 28.6924

Gaus Width: 3.4208

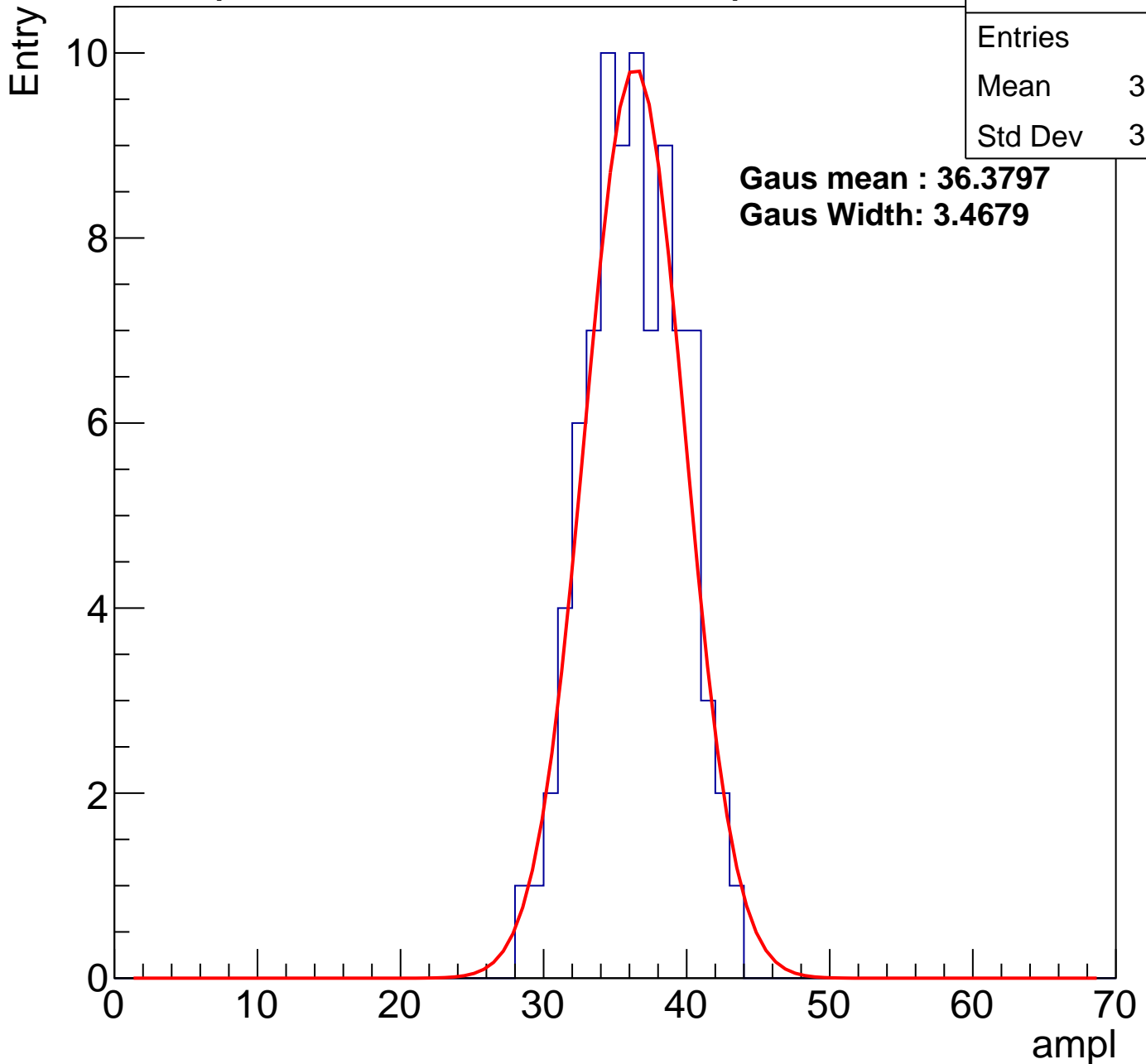


B0L102S, U20-ch15, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 86 |
| Mean | 35.85 |
| Std Dev | 3.244 |

Gaus mean : 36.3797
Gaus Width: 3.4679



B0L102S, U20-ch15, adc2

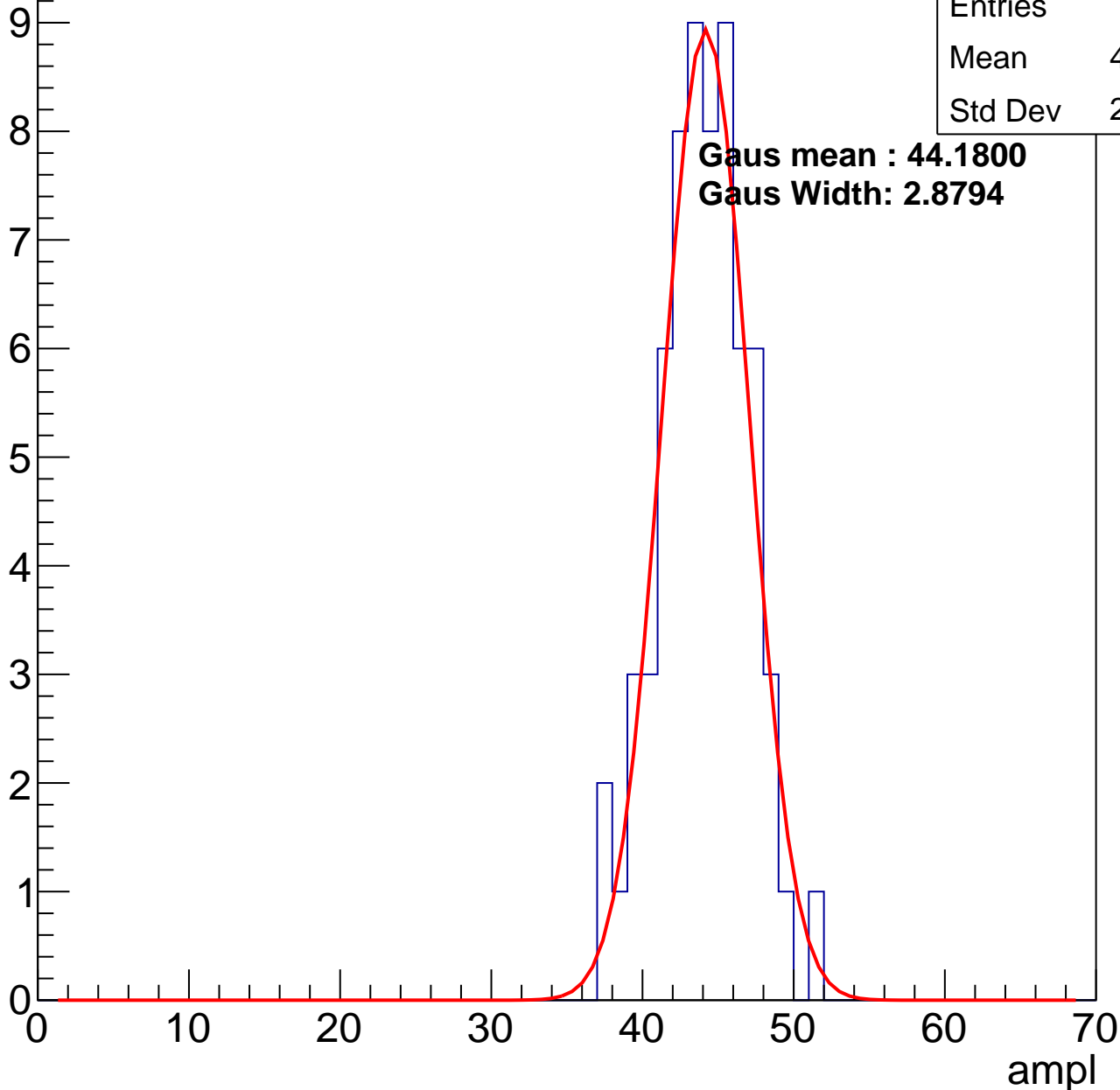
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 43.59 |
| Std Dev | 2.887 |

Gaus mean : 44.1800

Gaus Width: 2.8794



B0L102S, U20-ch15, adc3

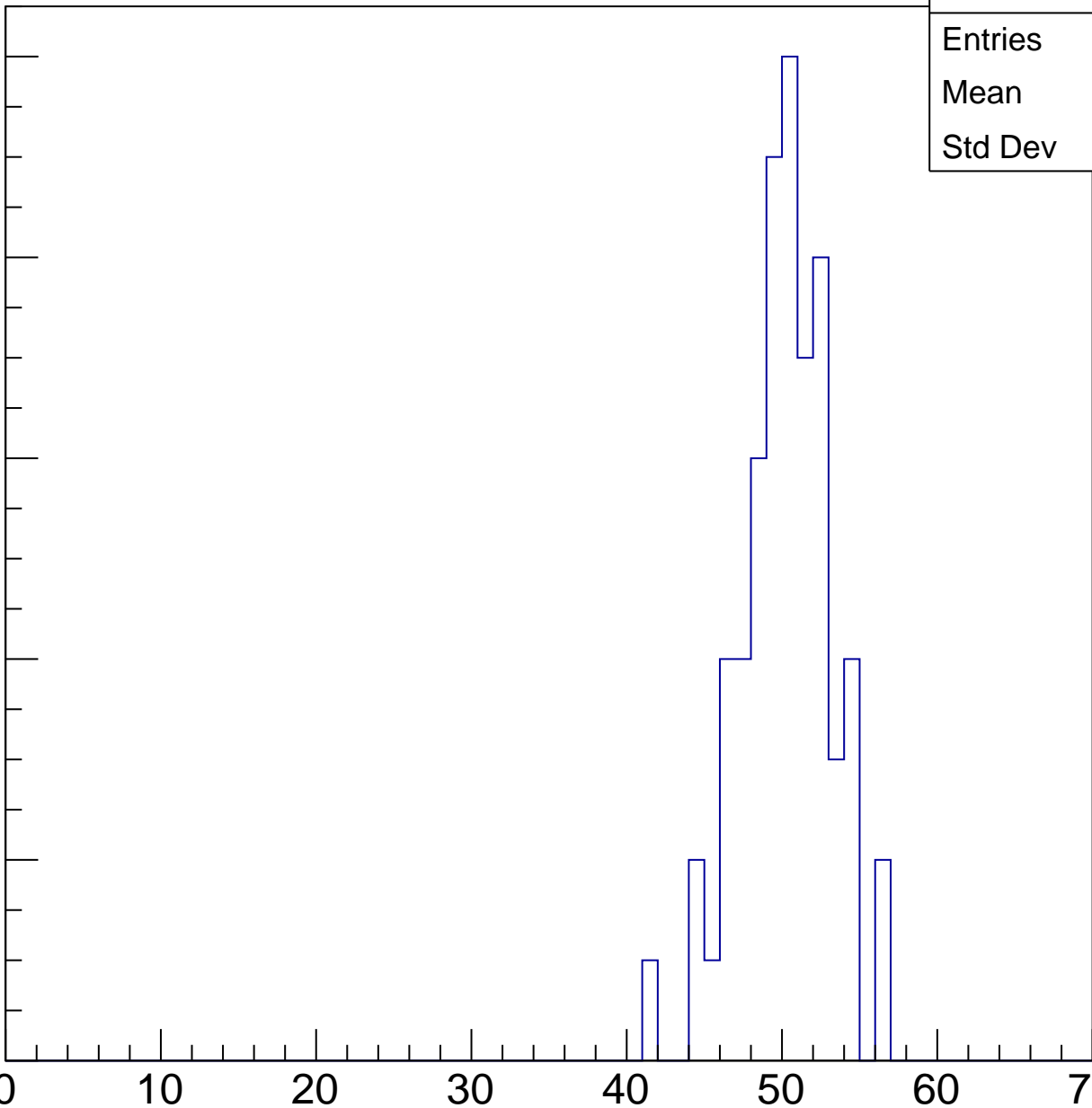
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.75 |
| Std Dev | 2.89 |

Entry

10
8
6
4
2
0

ampl

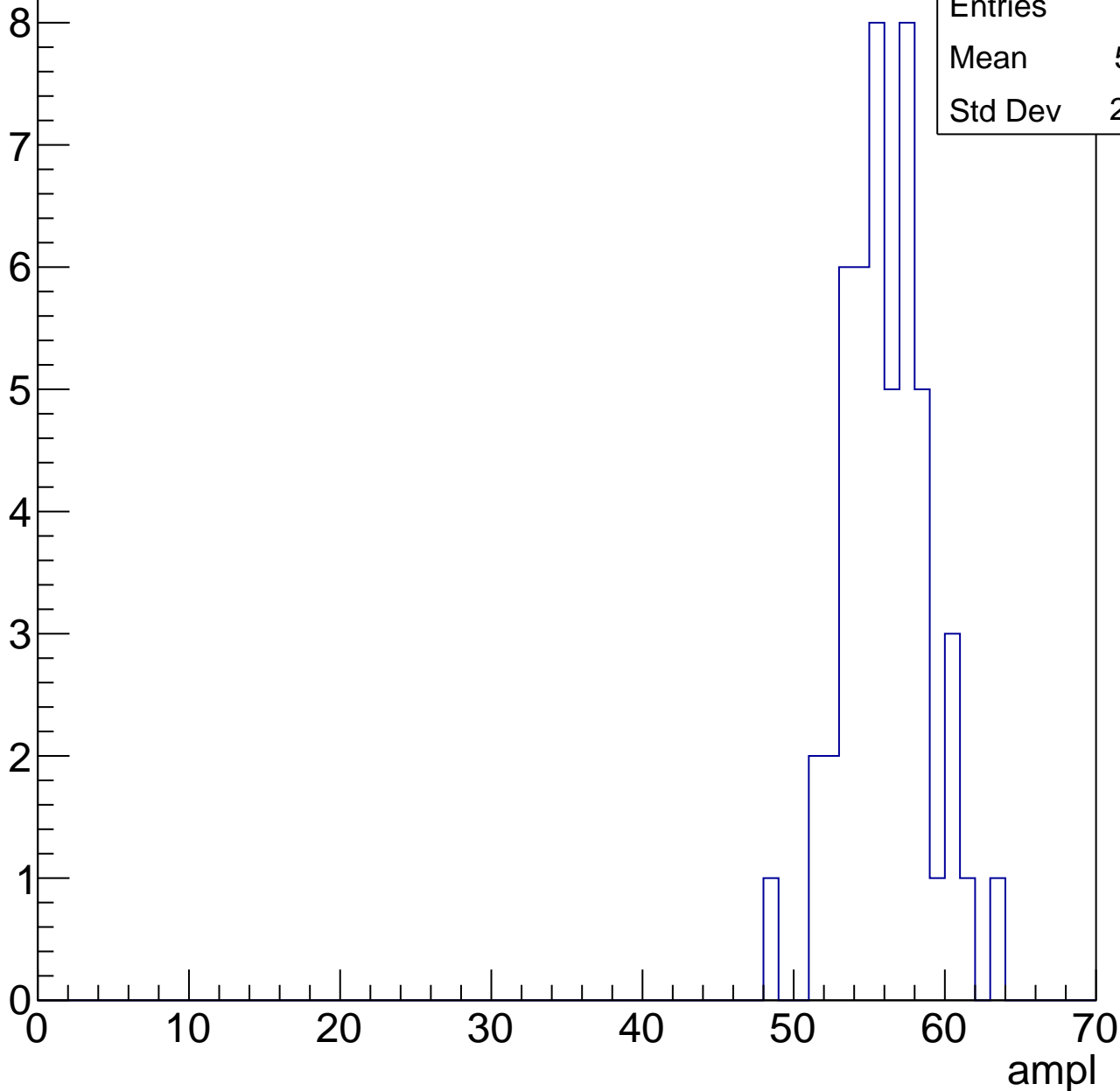


B0L102S, U20-ch15, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

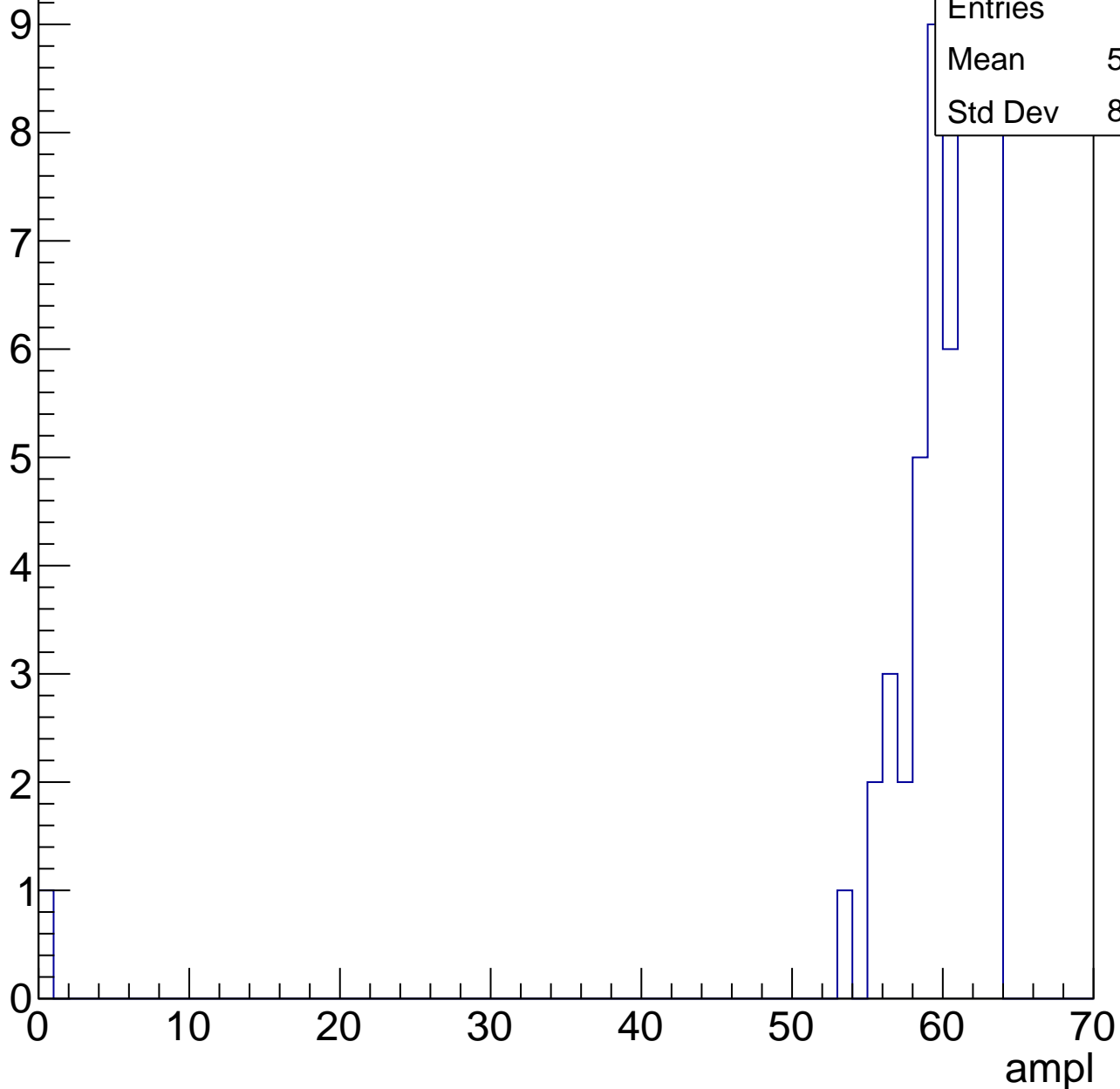
| | |
|---------|-------|
| Entries | 49 |
| Mean | 55.61 |
| Std Dev | 2.798 |



B0L102S, U20-ch15, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch15, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch15, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch16, adc0

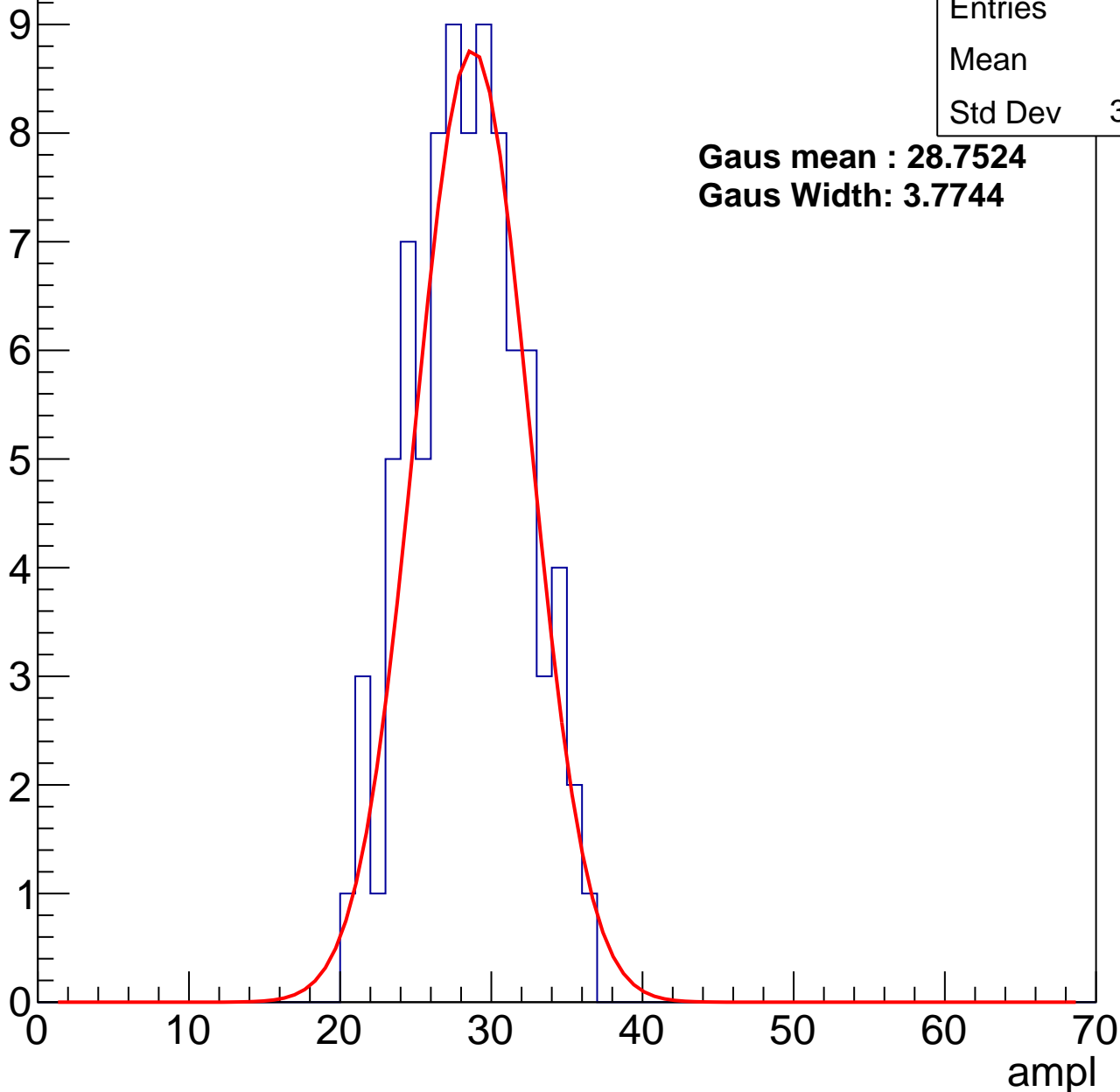
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 28 |
| Std Dev | 3.644 |

Gaus mean : 28.7524

Gaus Width: 3.7744



B0L102S, U20-ch16, adc1

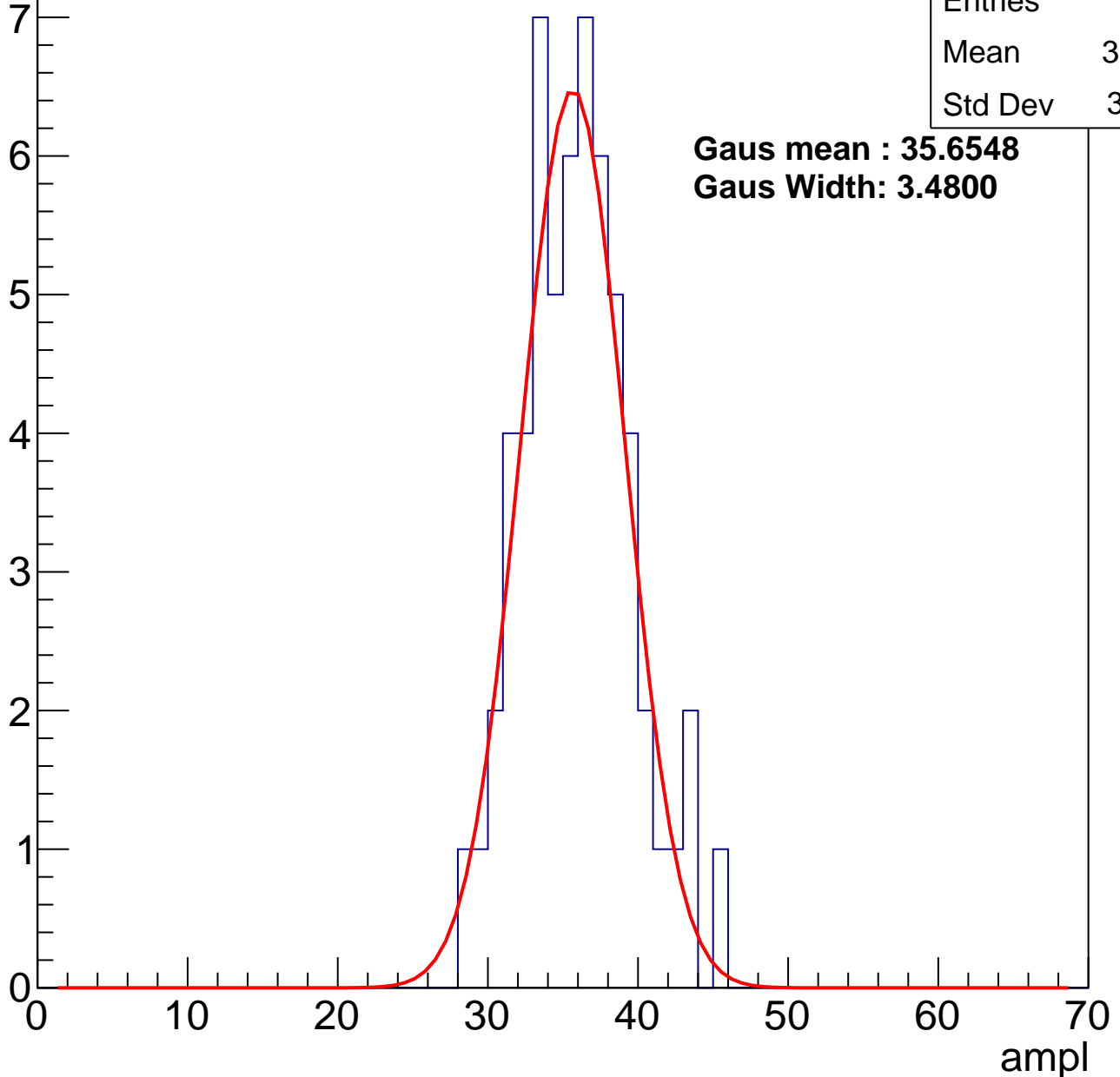
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.49 |
| Std Dev | 3.581 |

Gaus mean : 35.6548

Gaus Width: 3.4800



B0L102S, U20-ch16, adc2

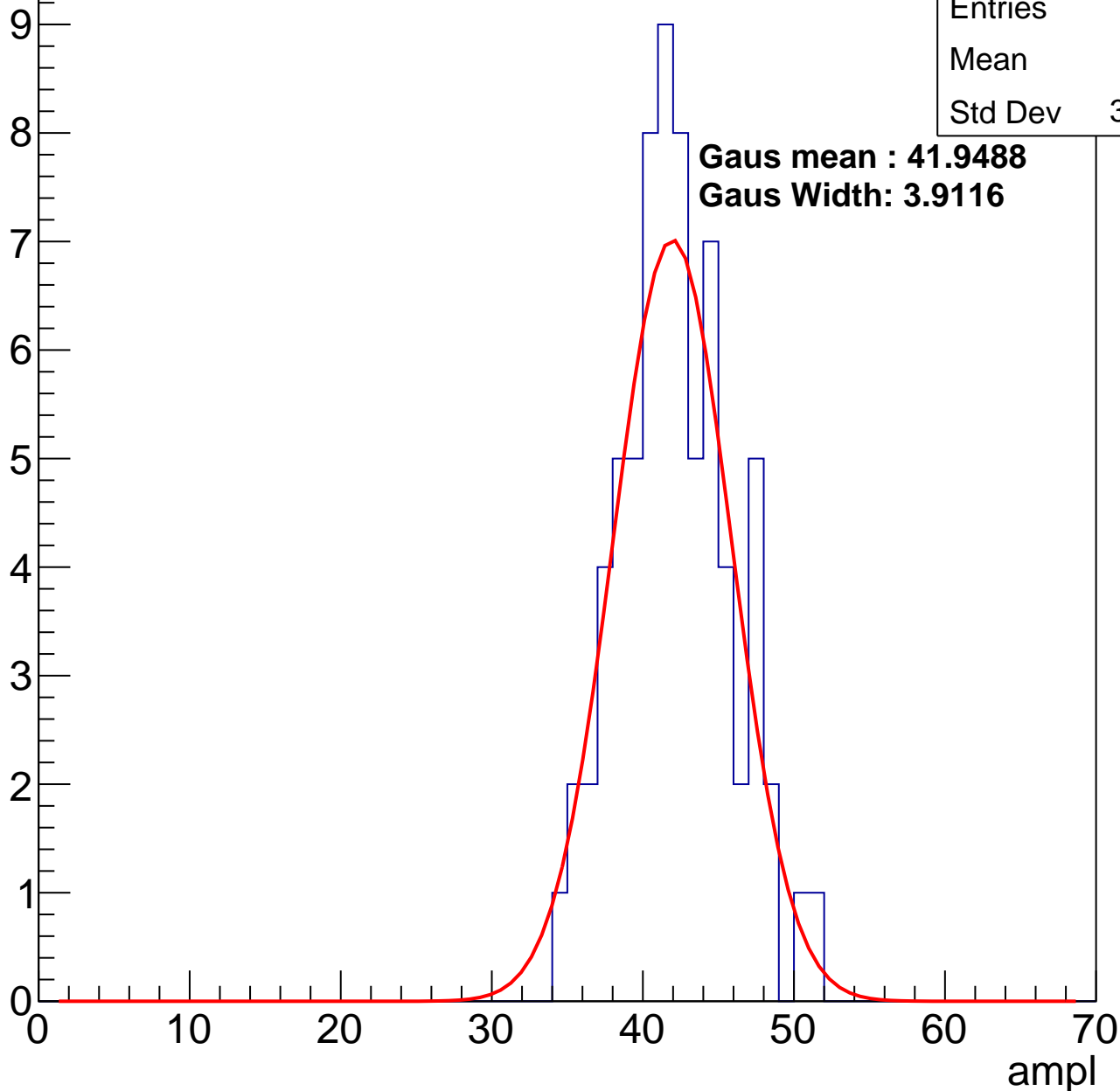
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 41.7 |
| Std Dev | 3.632 |

Gaus mean : 41.9488

Gaus Width: 3.9116

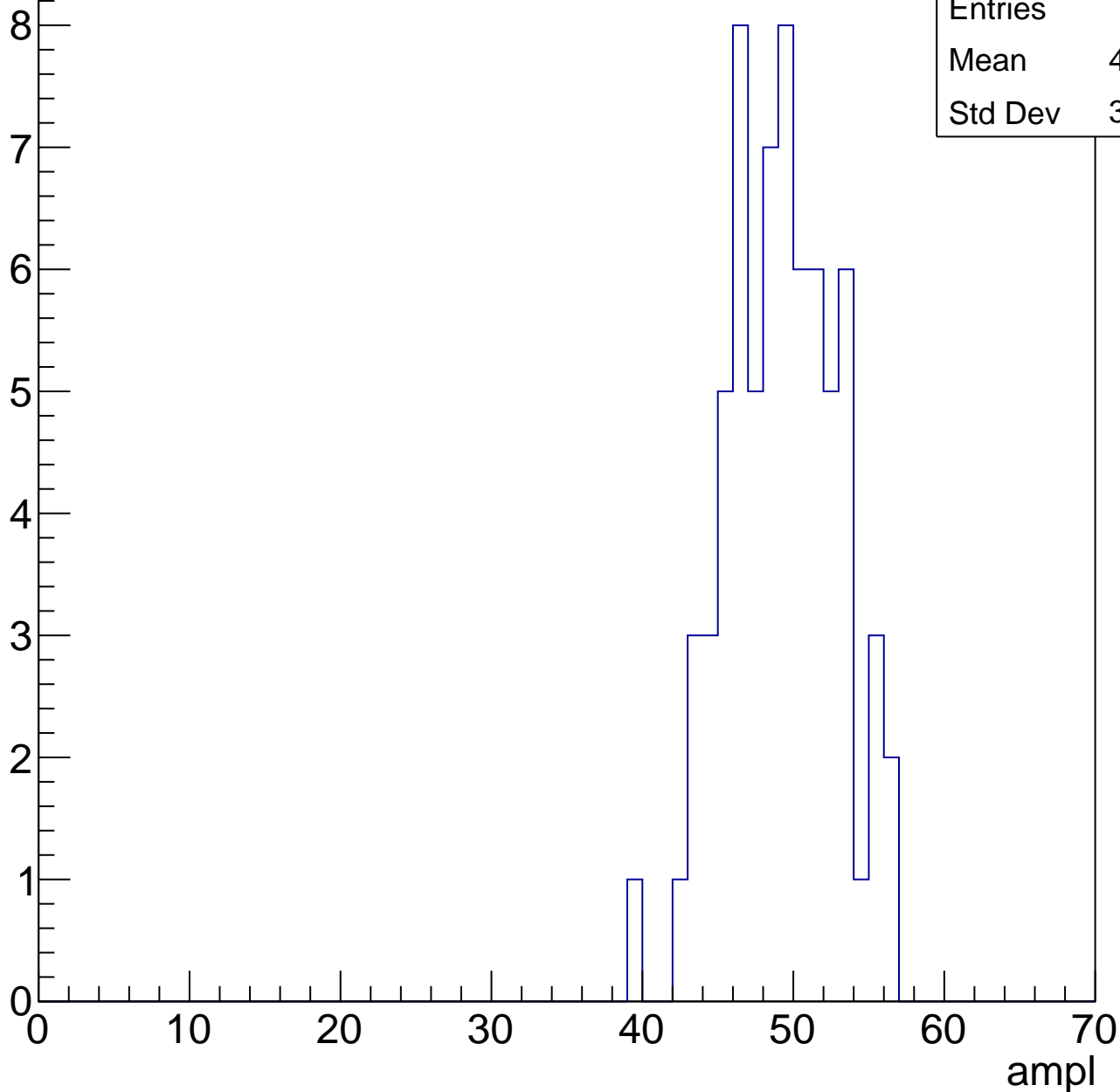


B0L102S, U20-ch16, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 48.76 |
| Std Dev | 3.603 |

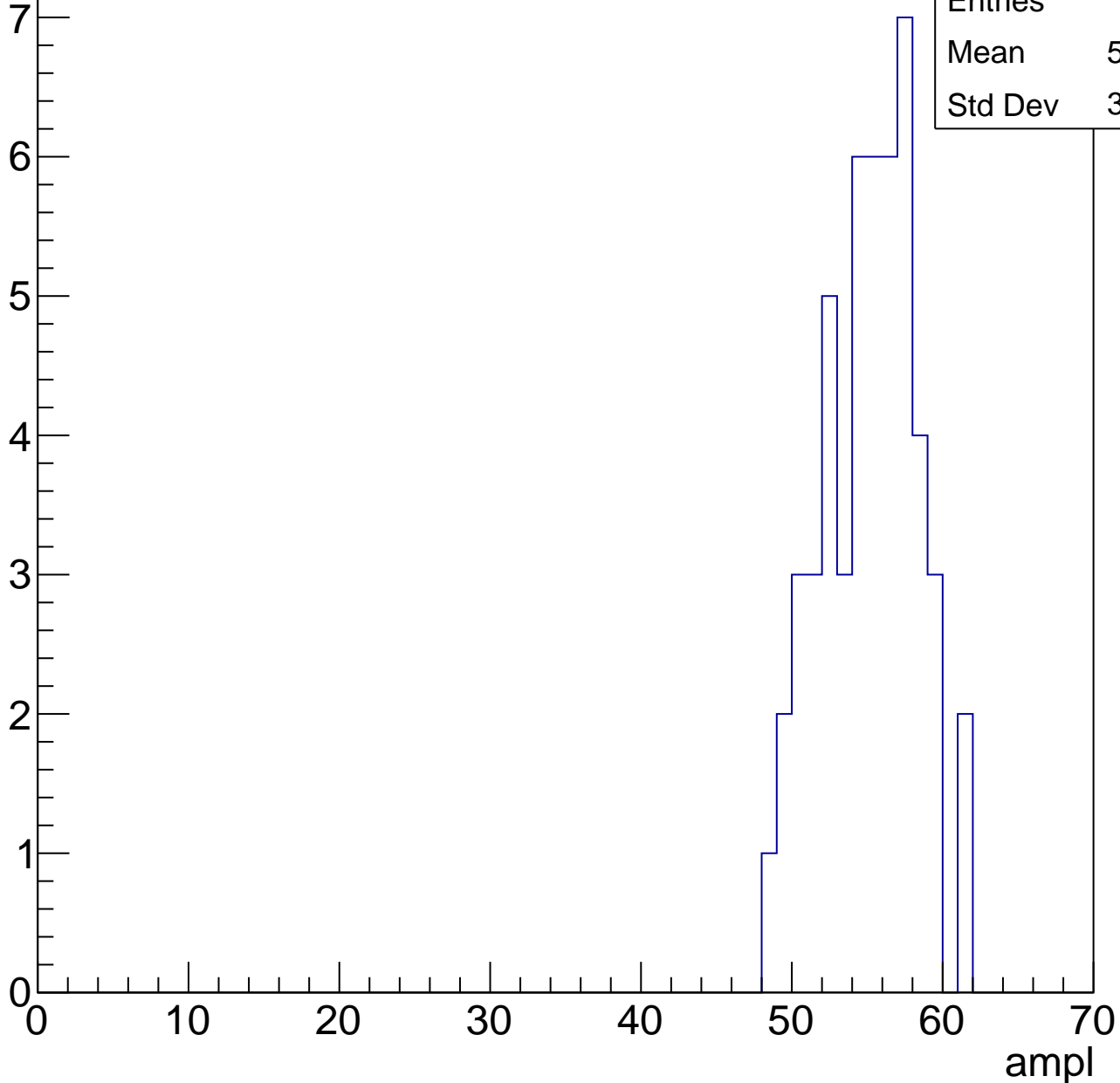


B0L102S, U20-ch16, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

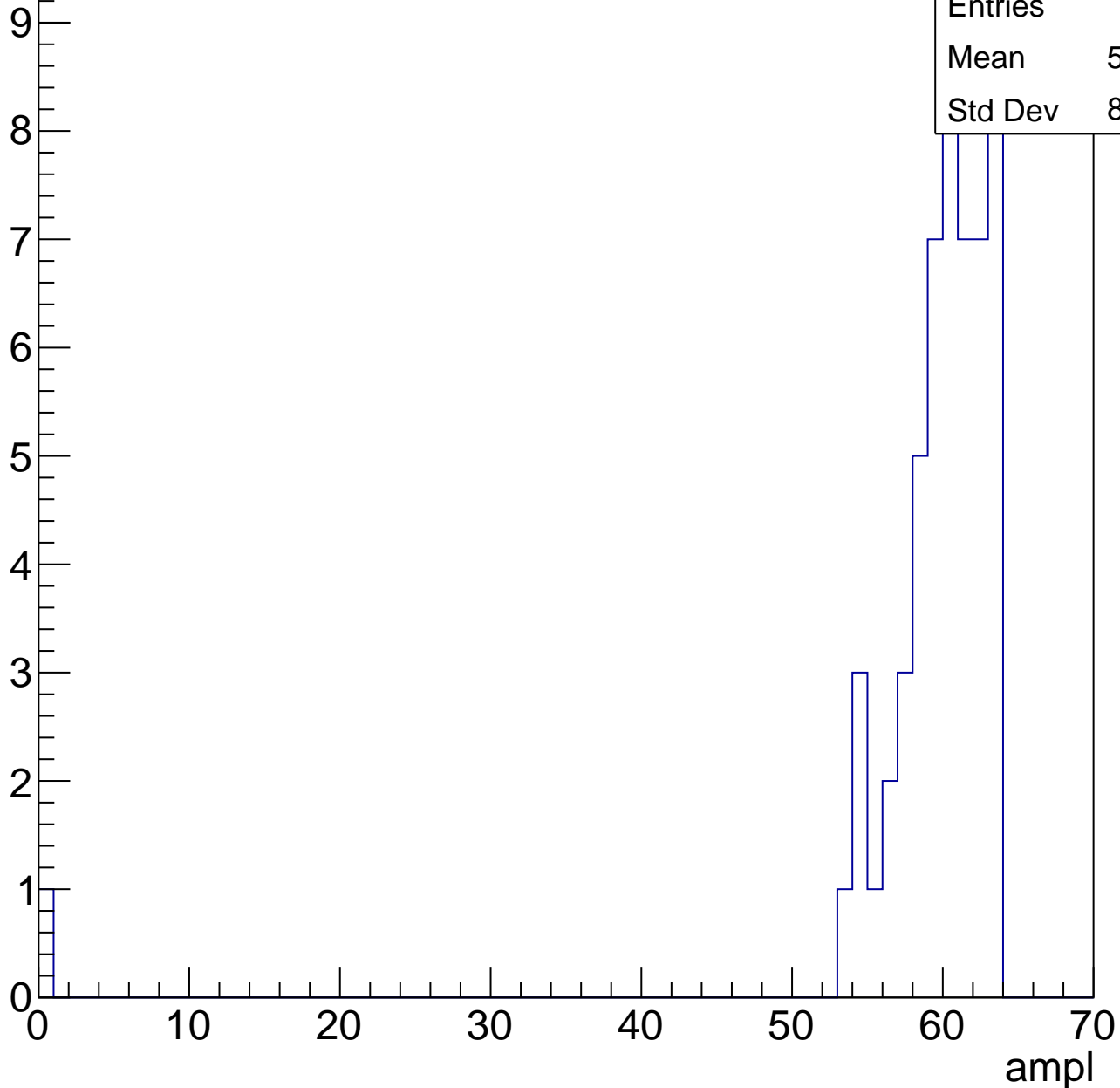
| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.67 |
| Std Dev | 3.104 |



B0L102S, U20-ch16, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch16, adc6

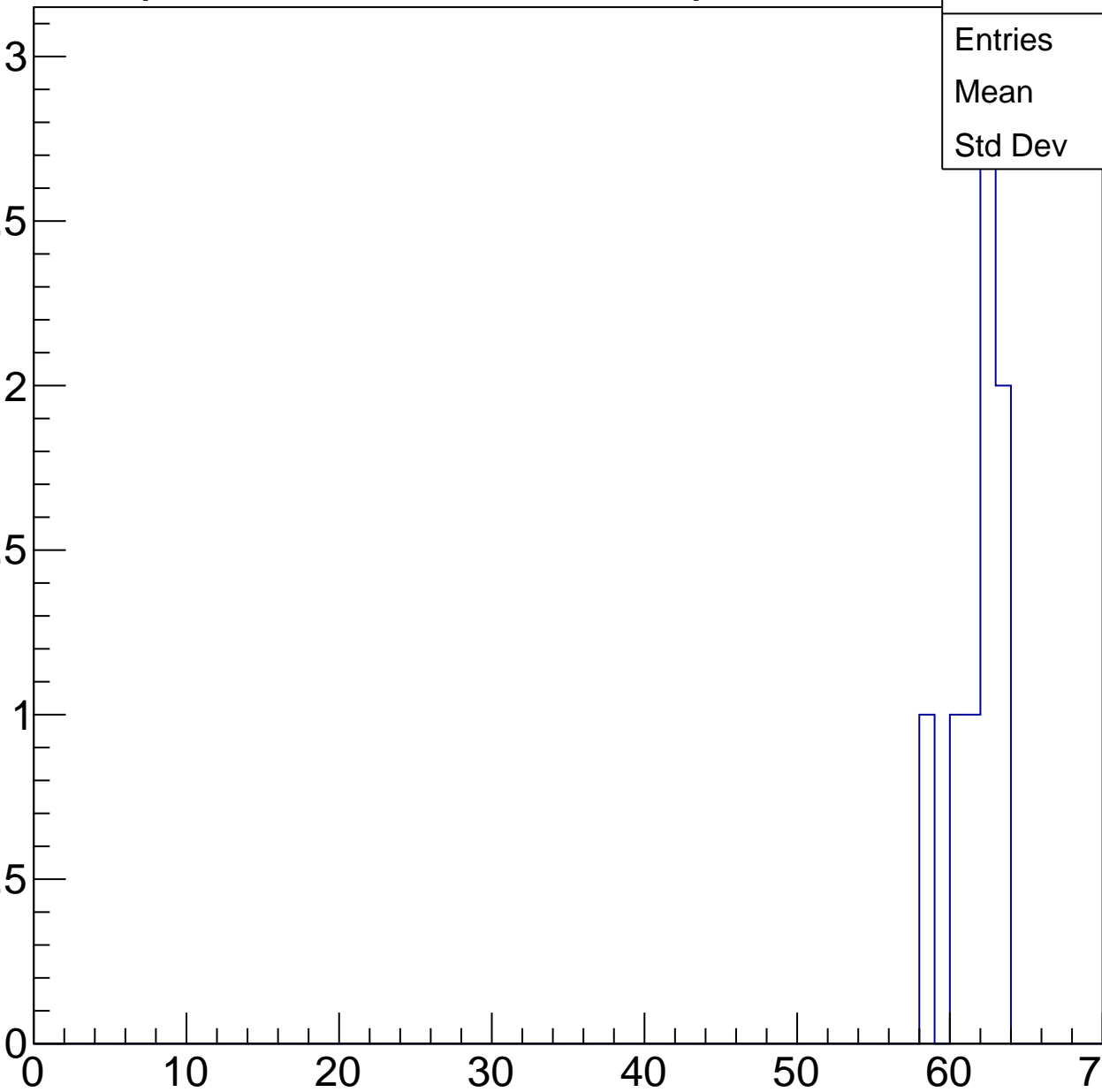
calib_packv5_042523_0143.root, FC#12, port B1

Entry

3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 61.38 |
| Std Dev | 1.576 |

ampl



B0L102S, U20-ch16, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 10 |
| Std Dev | 10 |

B0L102S, U20-ch17, adc0

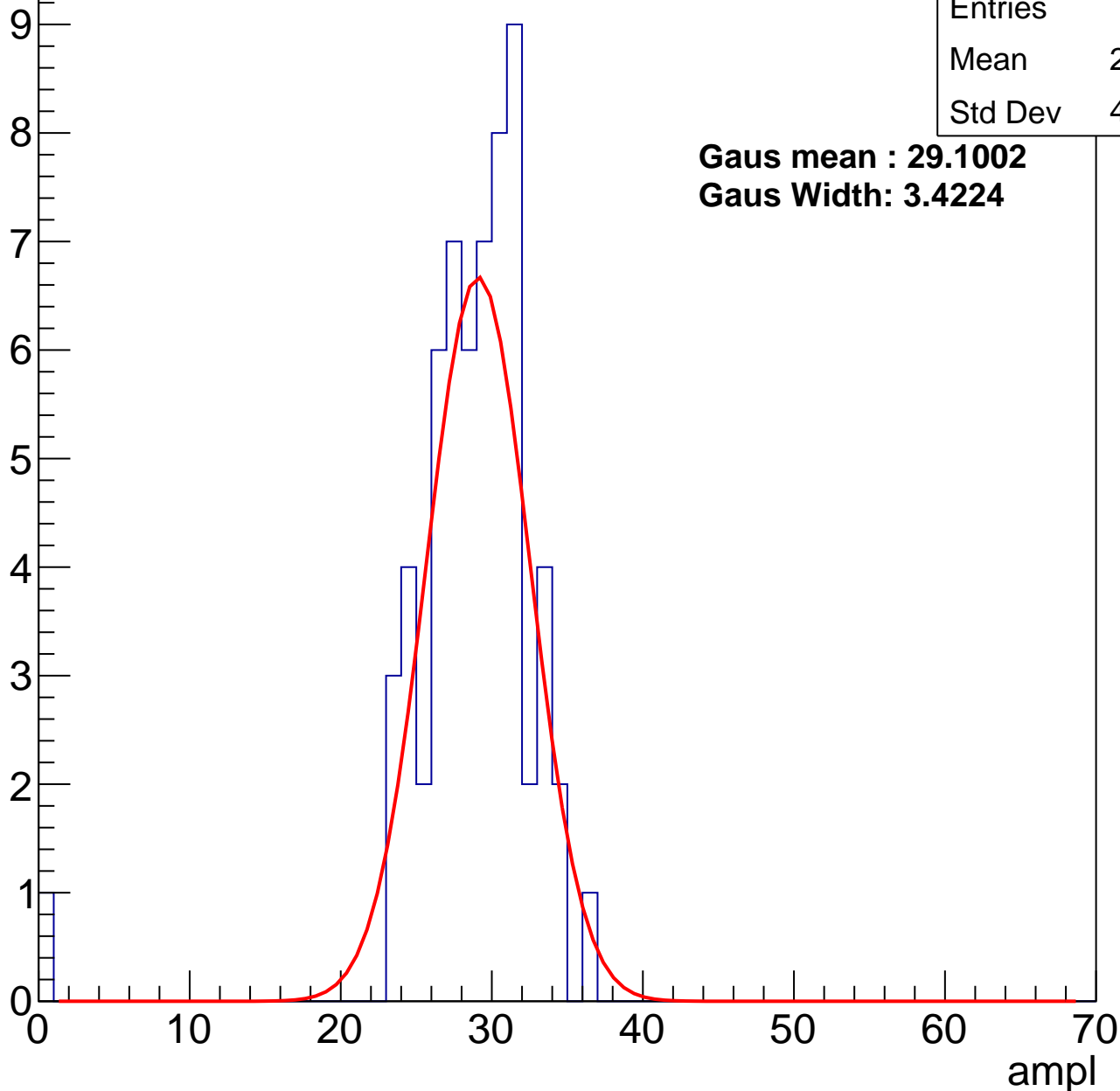
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.23 |
| Std Dev | 4.678 |

Gaus mean : 29.1002

Gaus Width: 3.4224



B0L102S, U20-ch17, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 35.89 |
| Std Dev | 3.7 |

Gaus mean : 37.0029

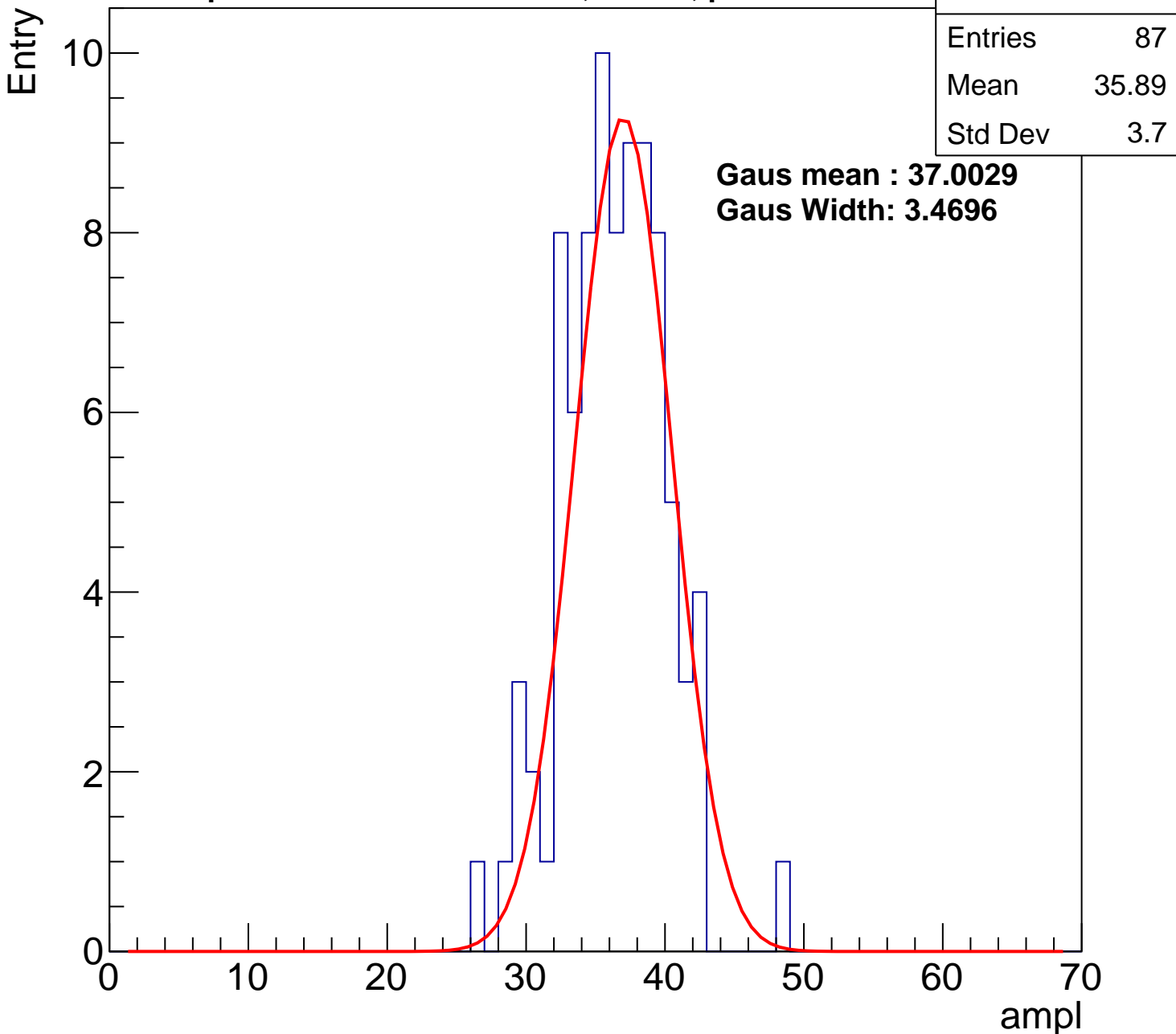
Gaus Width: 3.4696

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U20-ch17, adc2

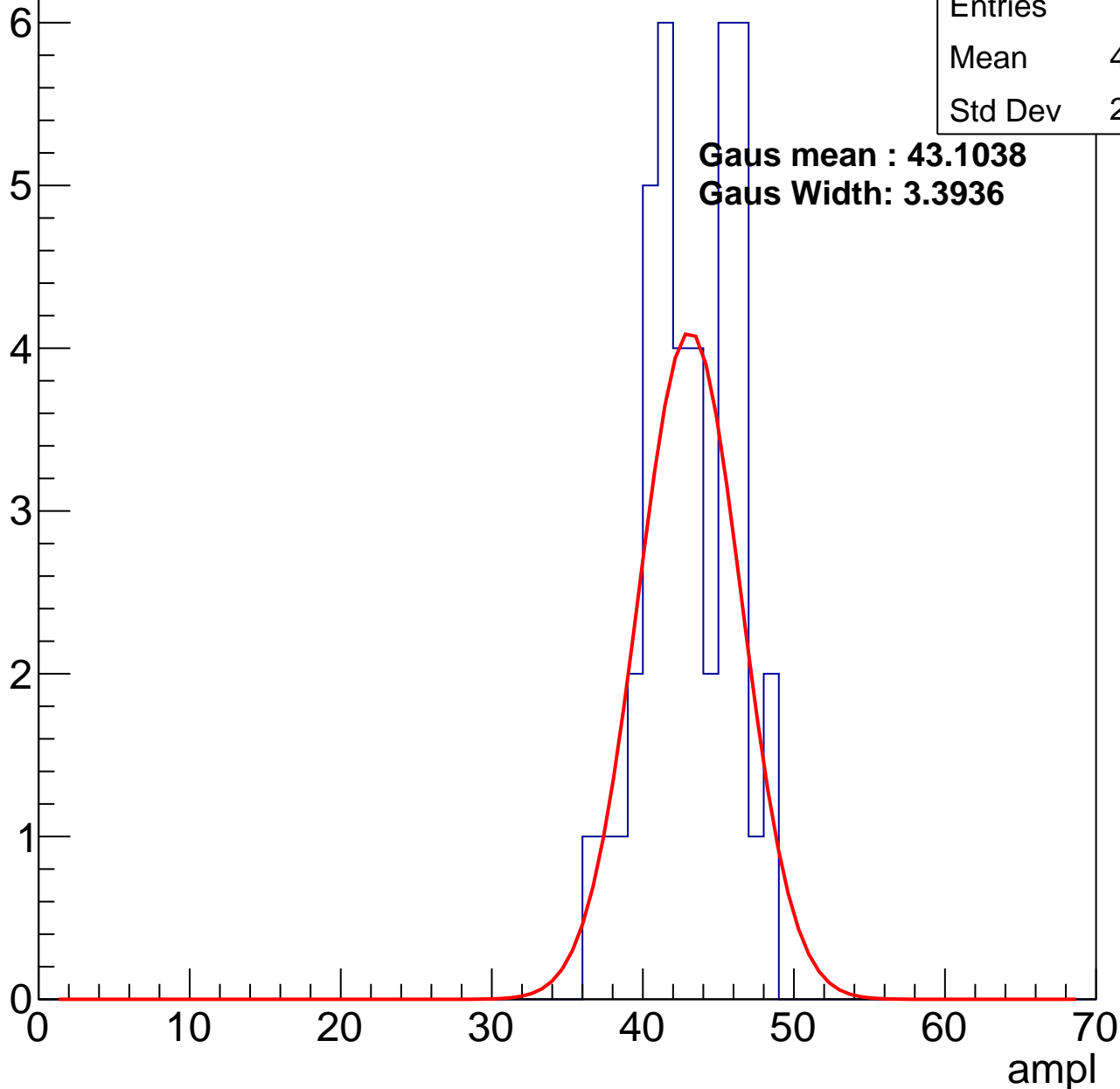
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 42.73 |
| Std Dev | 2.955 |

Gaus mean : 43.1038

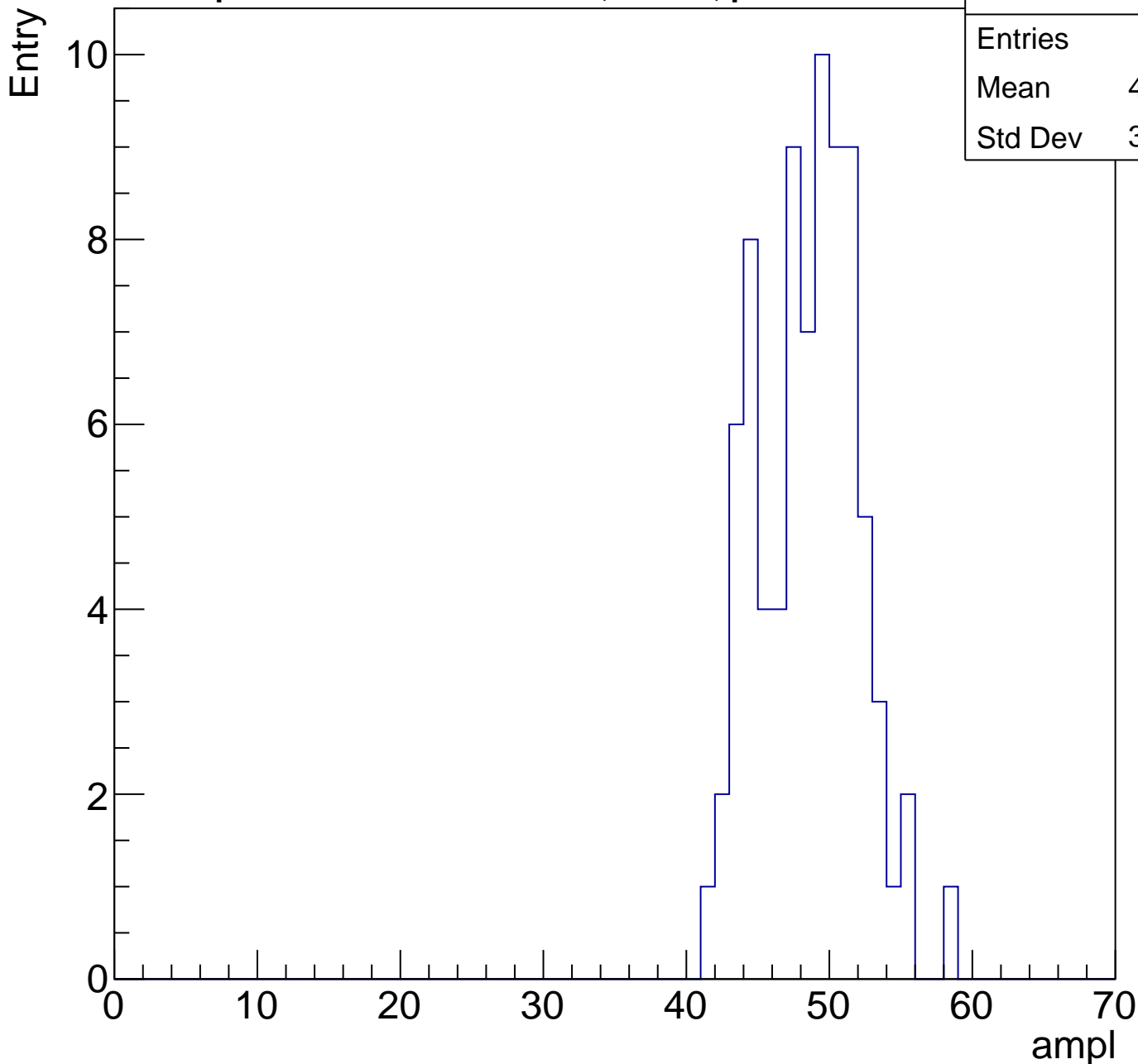
Gaus Width: 3.3936



B0L102S, U20-ch17, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 81 |
| Mean | 48.12 |
| Std Dev | 3.469 |

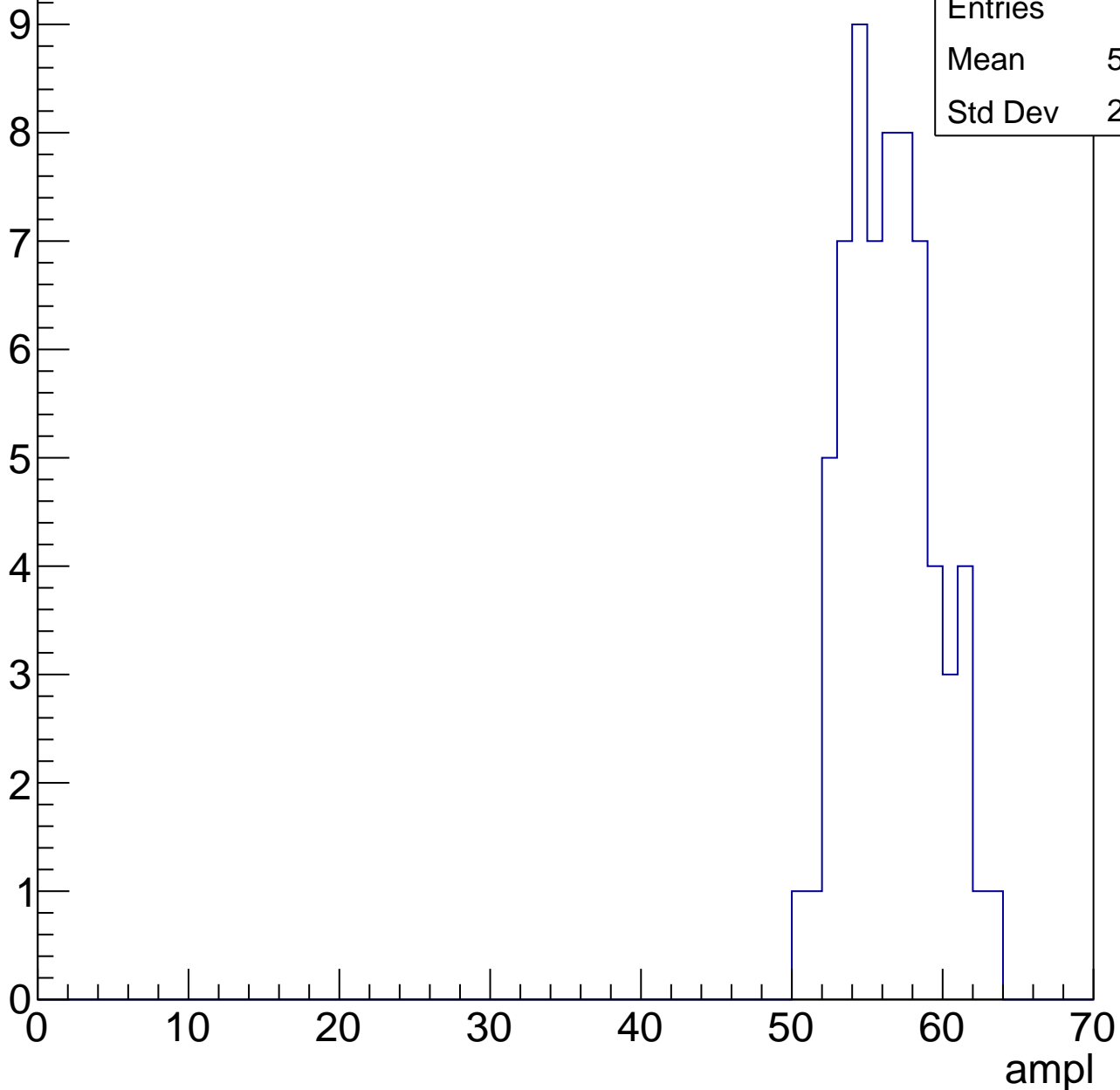


B0L102S, U20-ch17, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 56.03 |
| Std Dev | 2.892 |

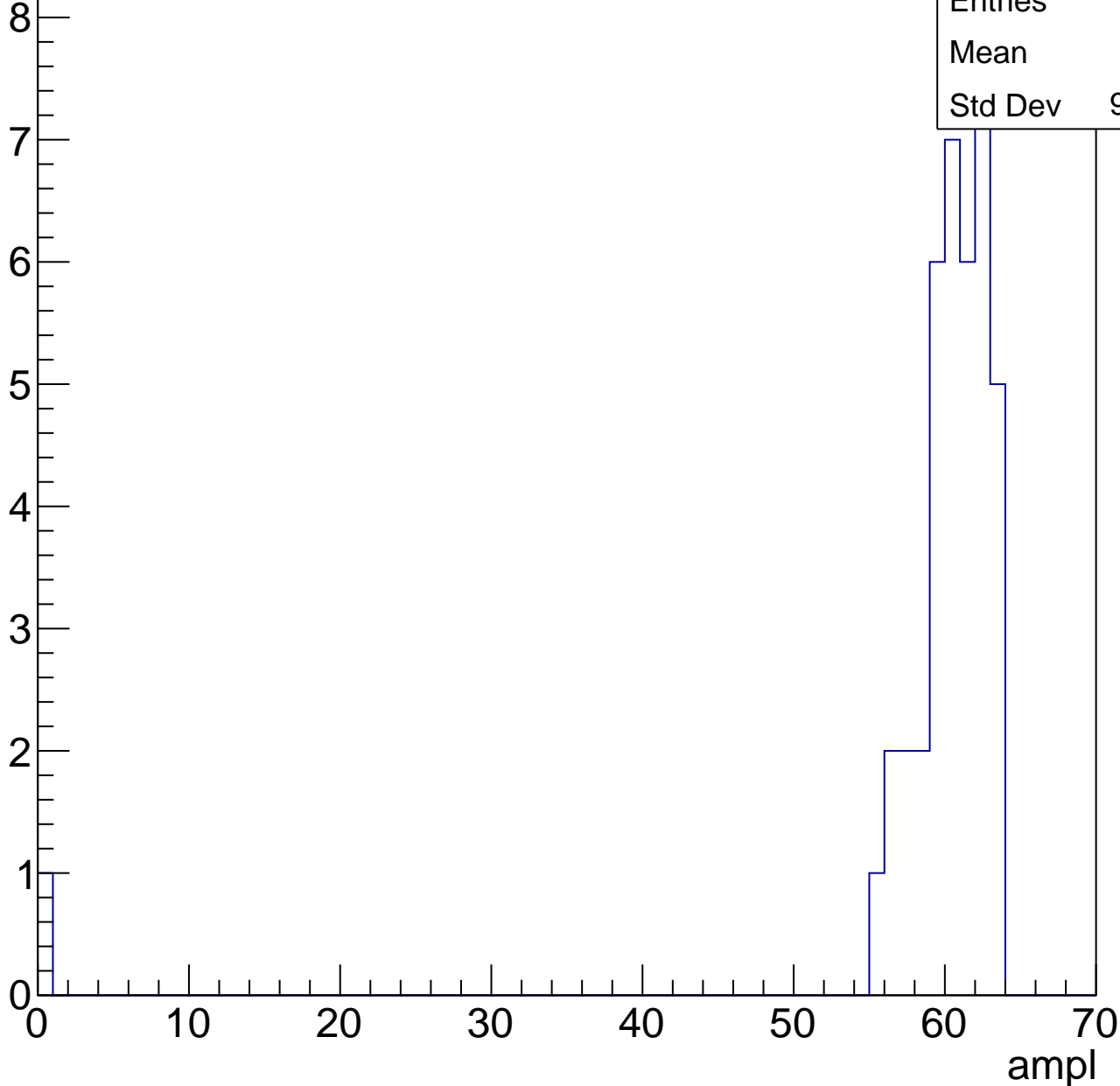


B0L102S, U20-ch17, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.7 |
| Std Dev | 9.623 |



B0L102S, U20-ch17, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch17, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch18, adc0

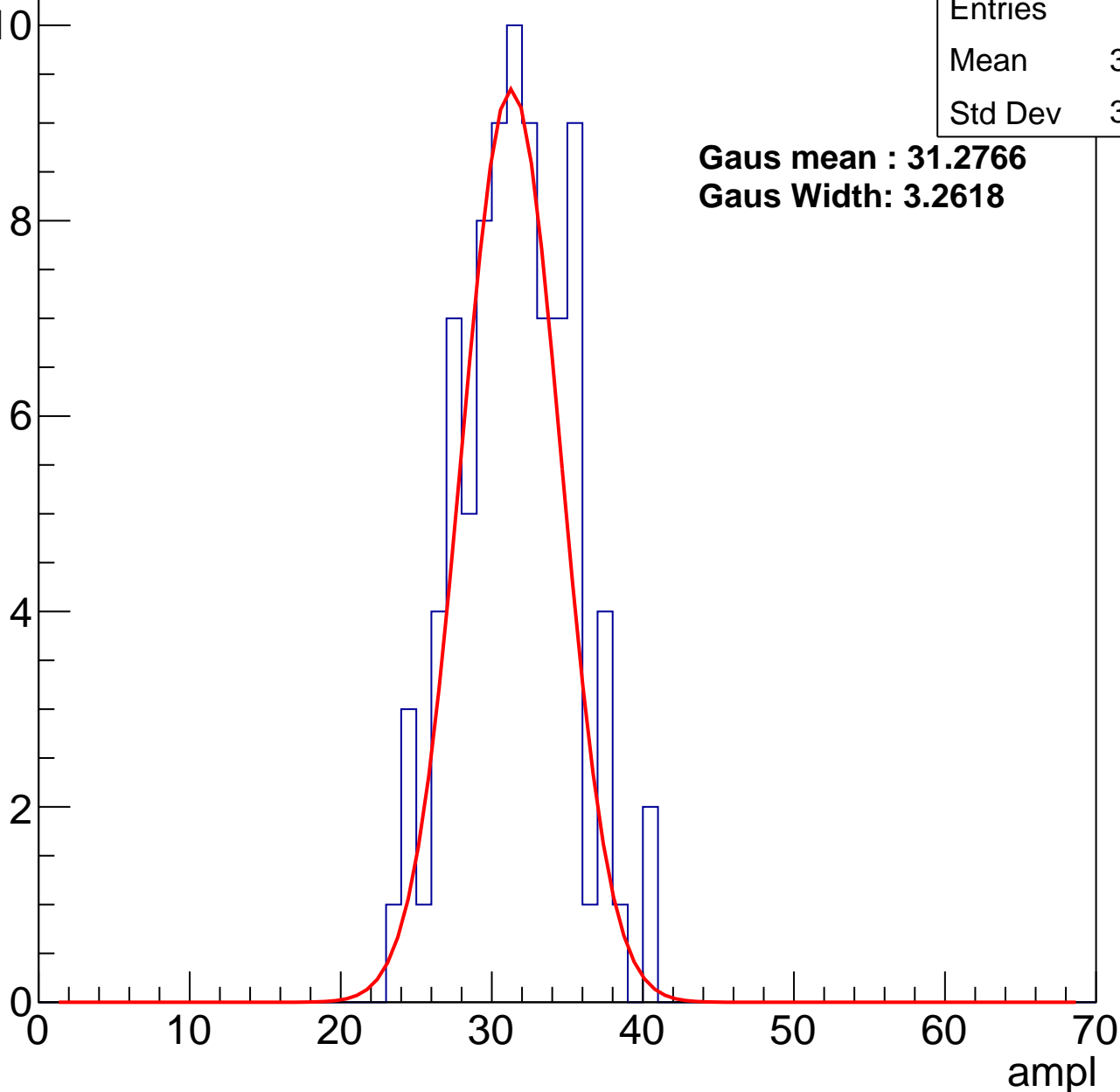
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 31.12 |
| Std Dev | 3.643 |

Gaus mean : 31.2766

Gaus Width: 3.2618



B0L102S, U20-ch18, adc1

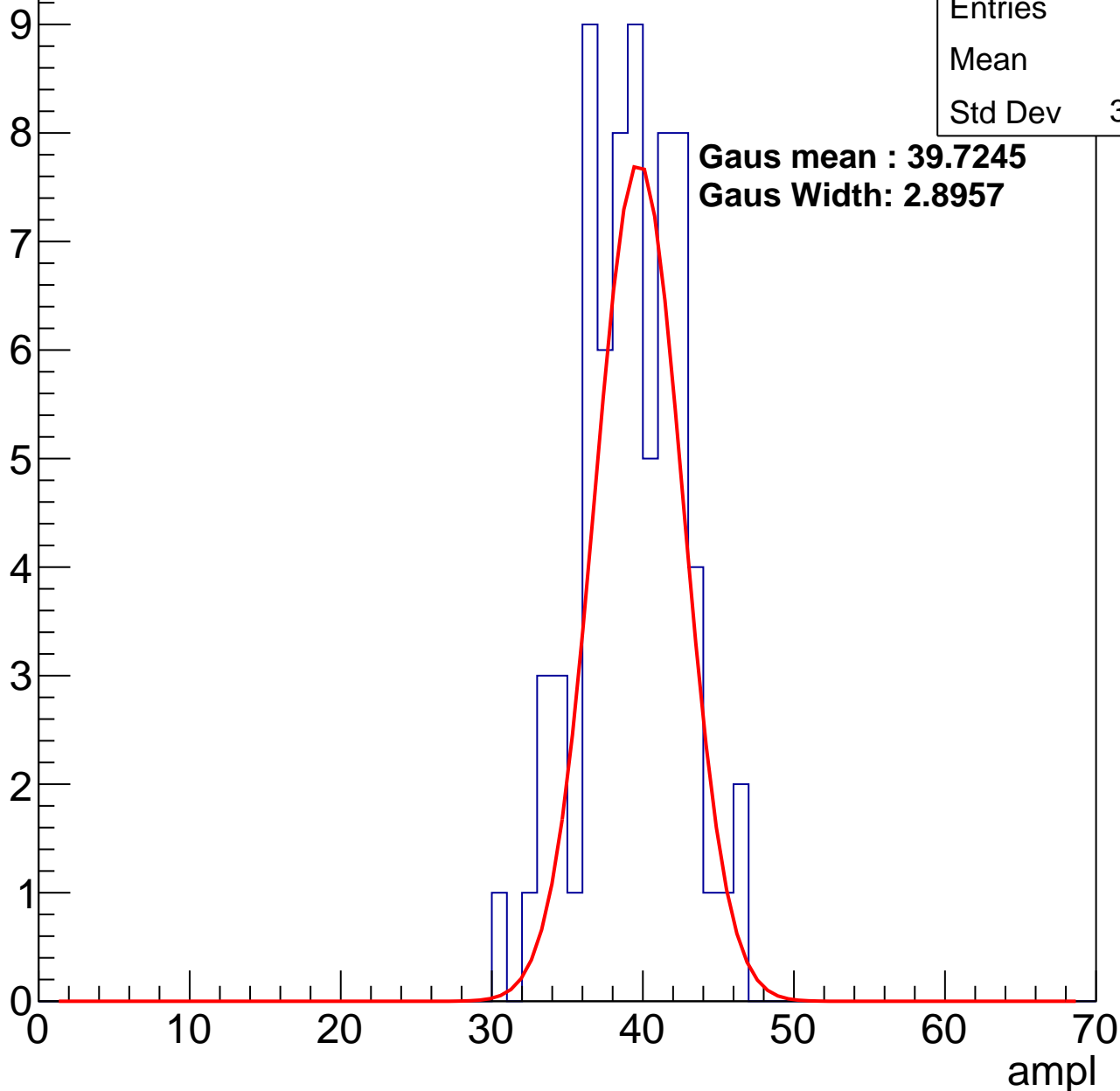
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 38.8 |
| Std Dev | 3.319 |

Gaus mean : 39.7245

Gaus Width: 2.8957



B0L102S, U20-ch18, adc2

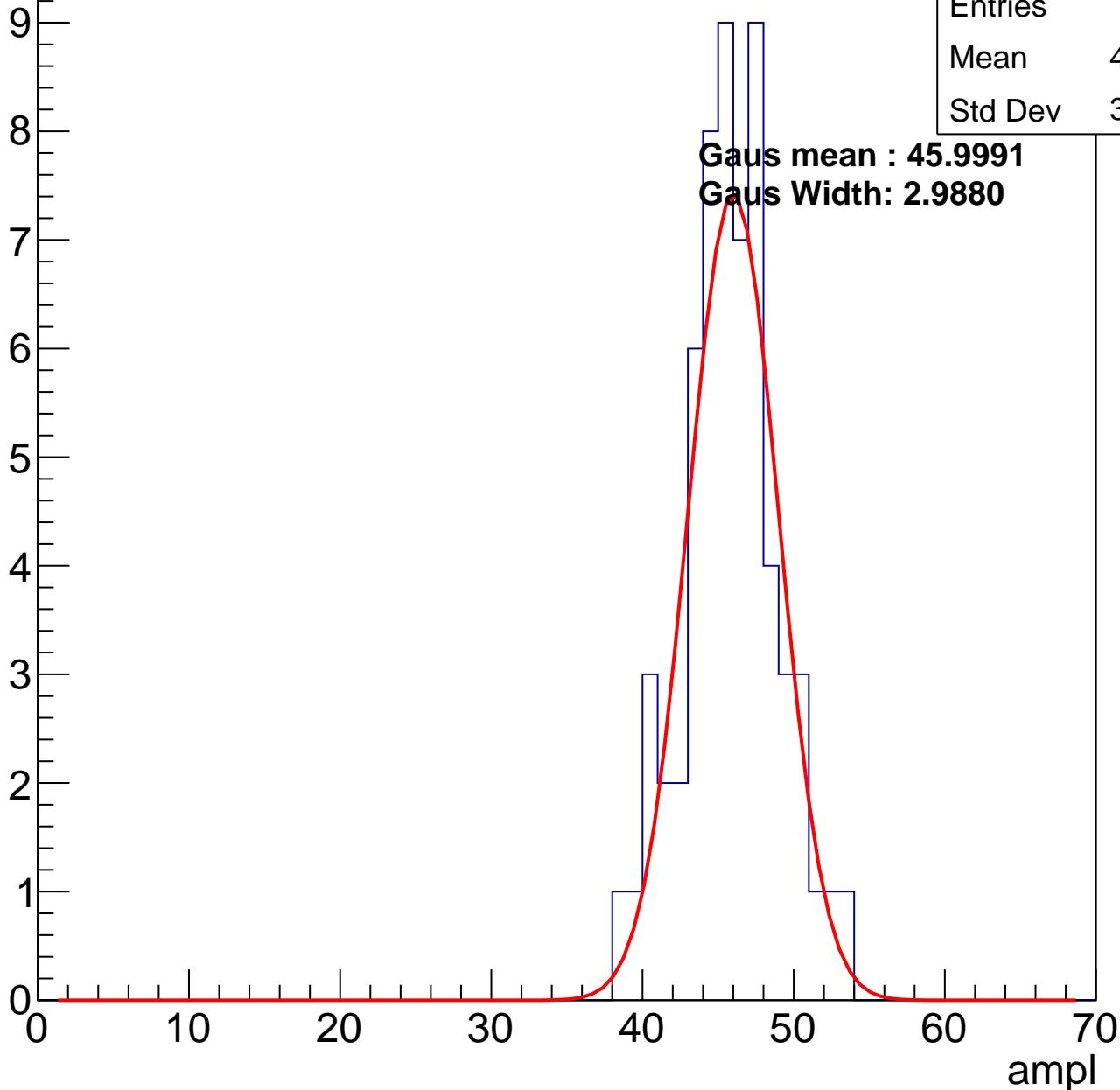
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 45.38 |
| Std Dev | 3.095 |

Gaus mean : 45.9991

Gaus Width: 2.9880

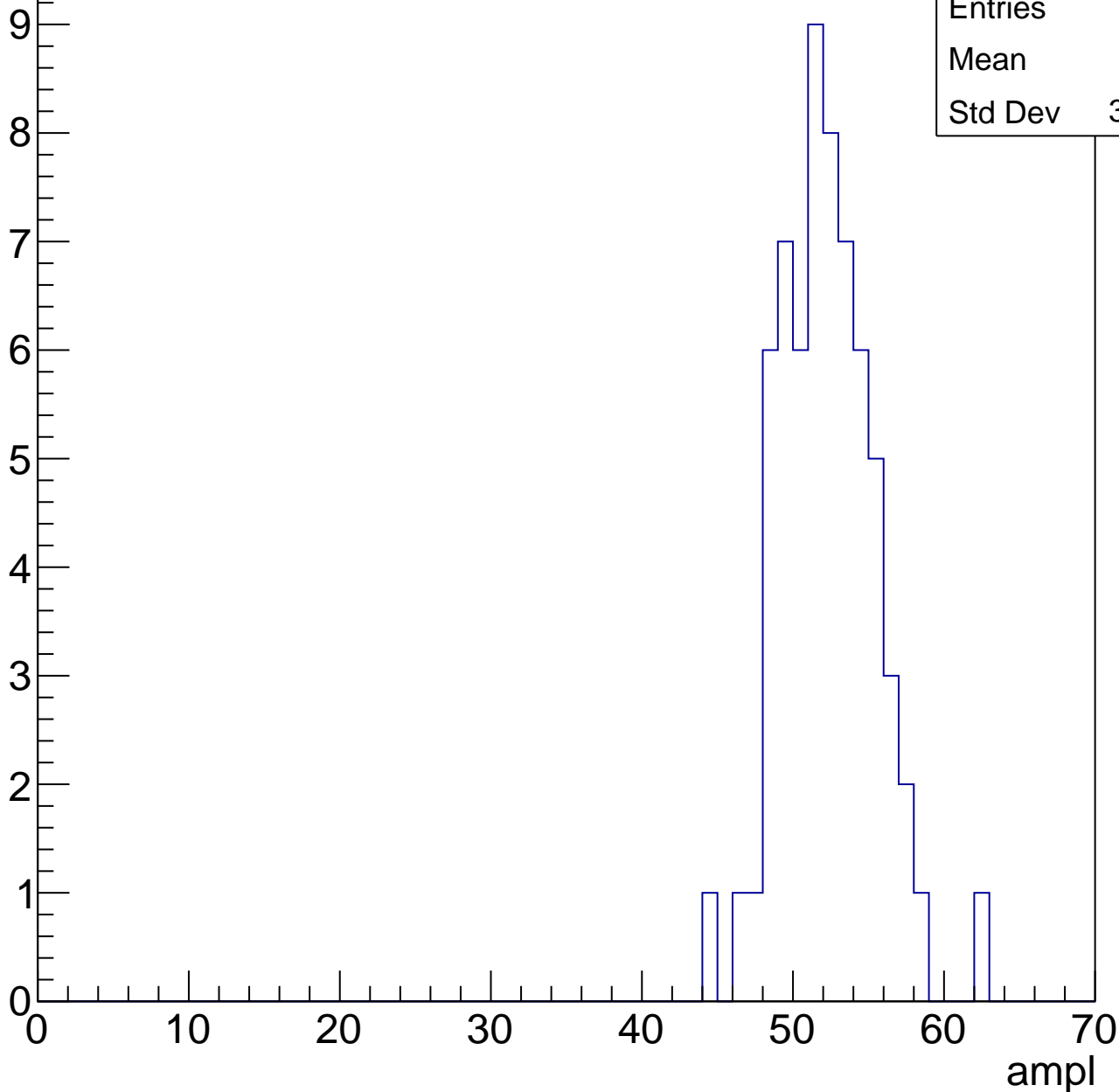


B0L102S, U20-ch18, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 51.8 |
| Std Dev | 3.123 |

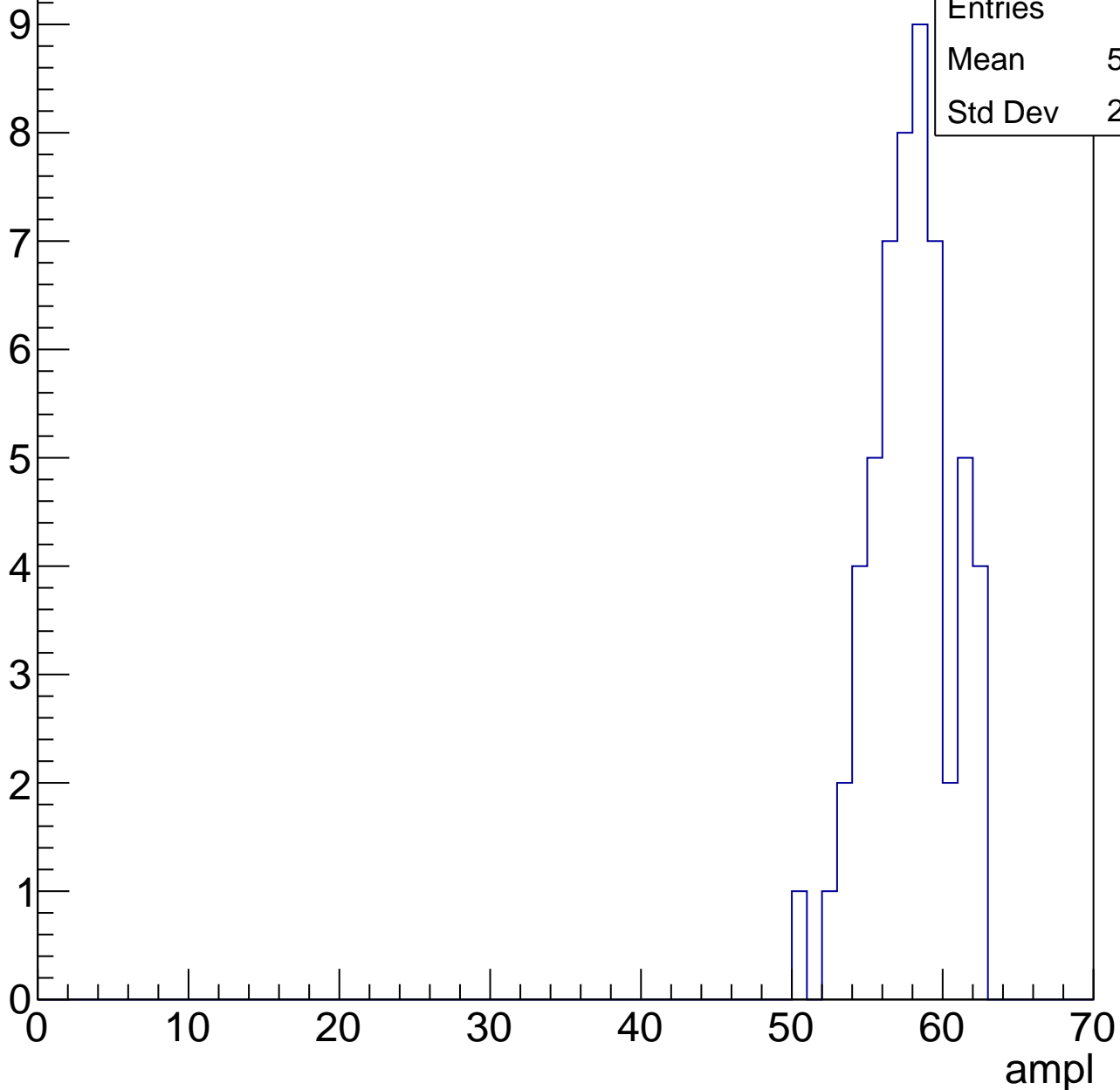


B0L102S, U20-ch18, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 57.36 |
| Std Dev | 2.686 |

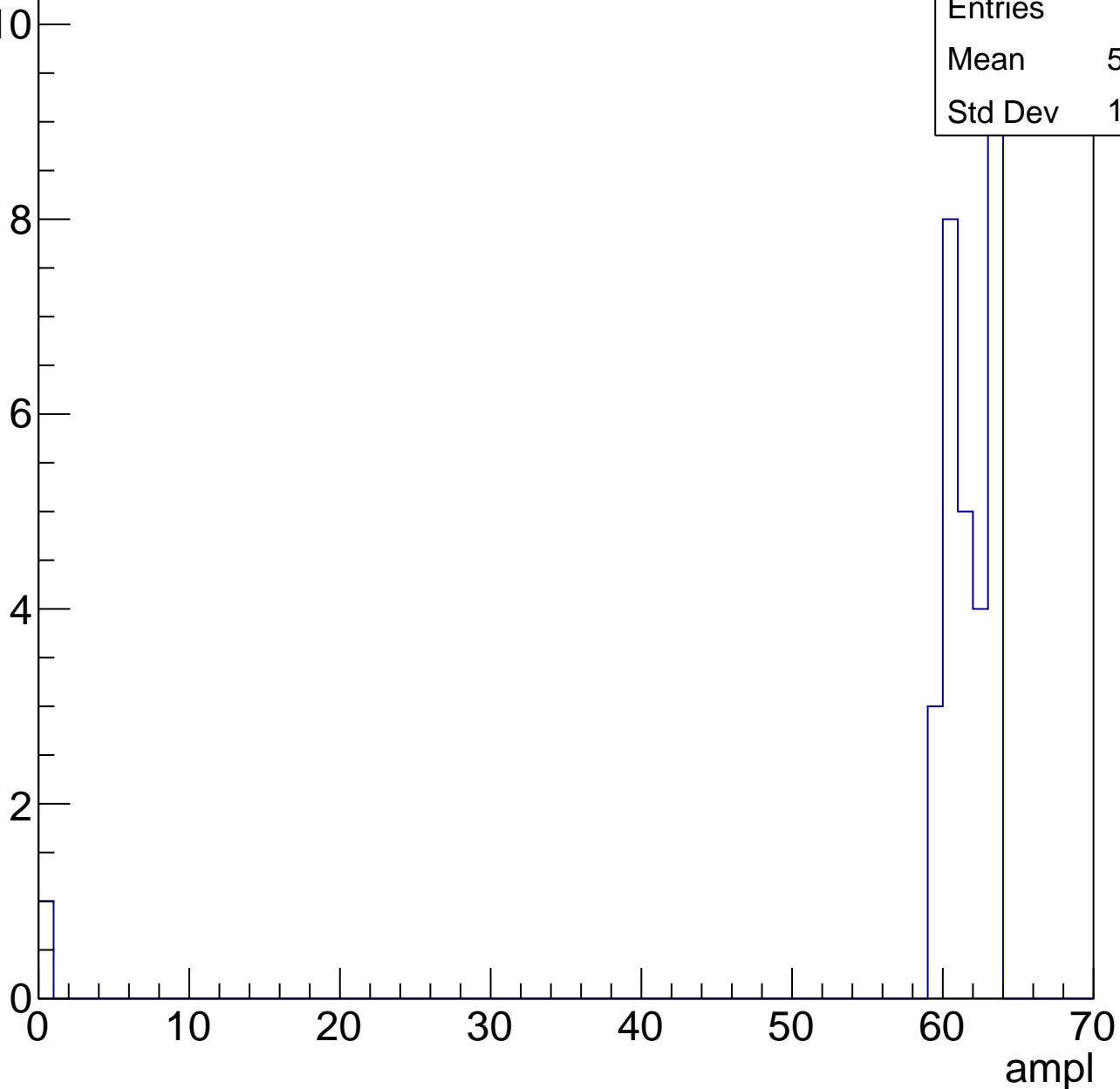


B0L102S, U20-ch18, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 31 |
| Mean | 59.35 |
| Std Dev | 10.93 |



B0L102S, U20-ch18, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch18, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch19, adc0

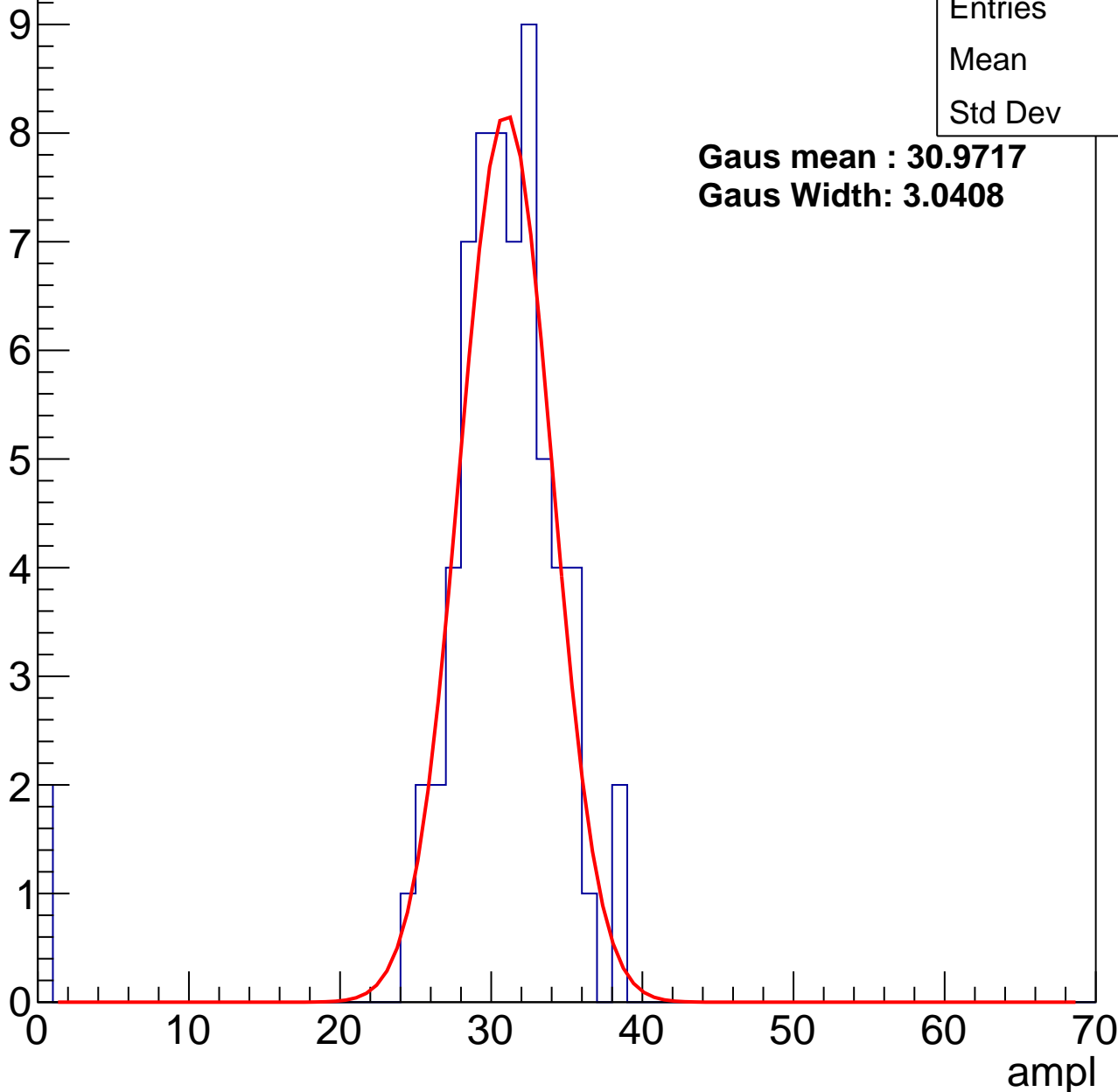
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 66 |
| Mean | 29.7 |
| Std Dev | 6.03 |

Gaus mean : 30.9717

Gaus Width: 3.0408



B0L102S, U20-ch19, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 37.23 |
| Std Dev | 3.341 |

Gaus mean : 37.6246

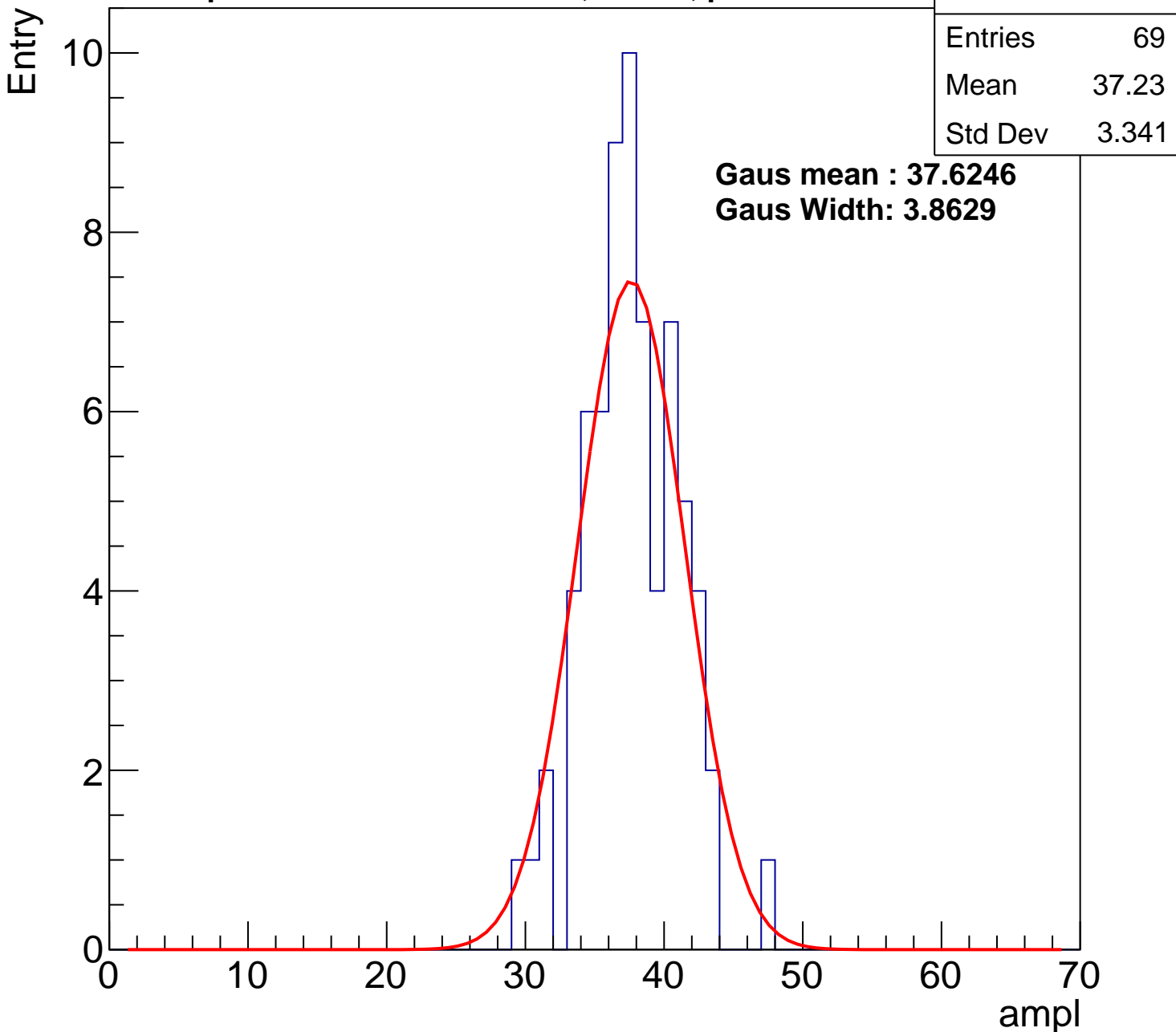
Gaus Width: 3.8629

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U20-ch19, adc2

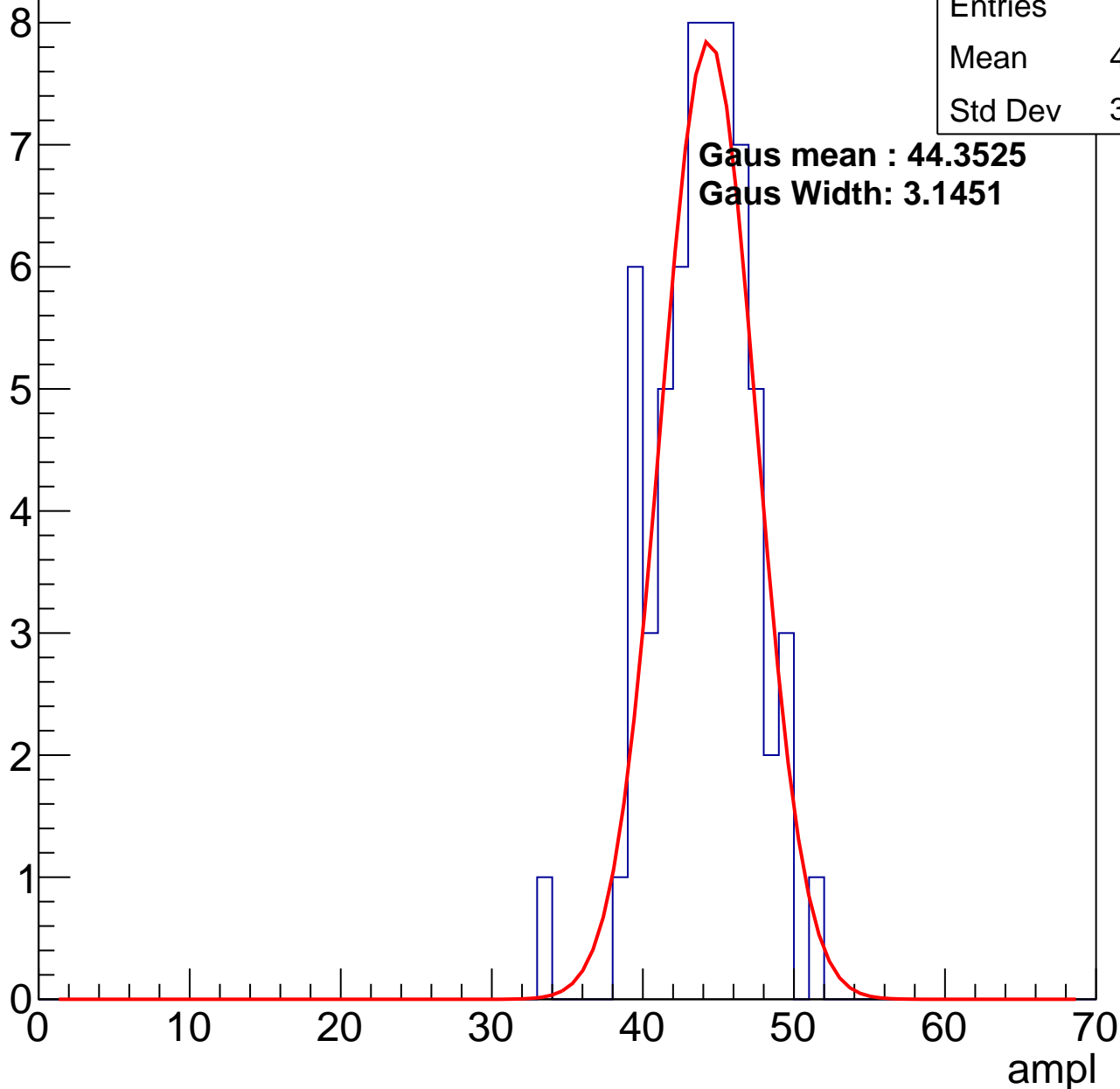
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.58 |
| Std Dev | 3.205 |

Gaus mean : 44.3525

Gaus Width: 3.1451

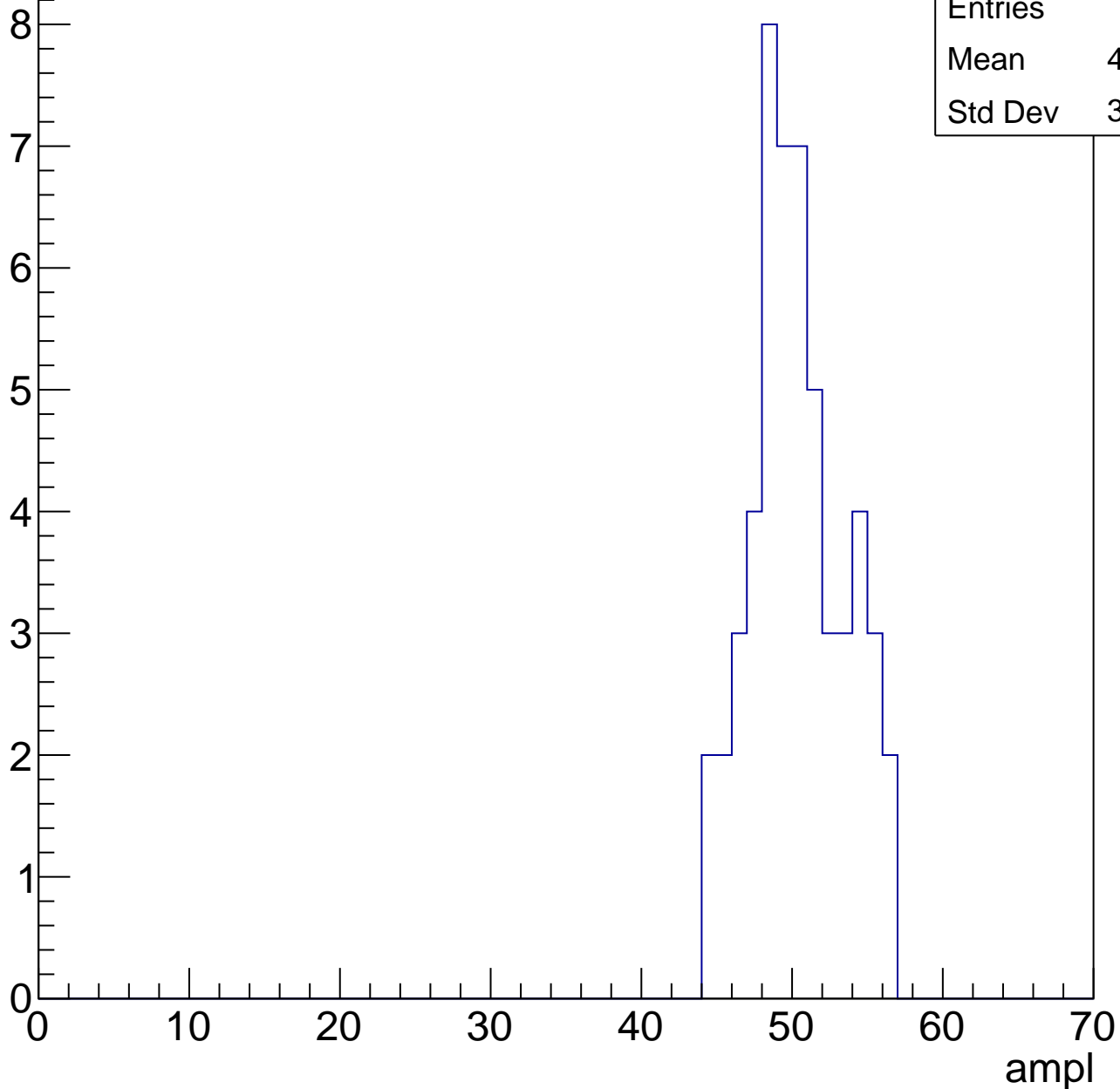


B0L102S, U20-ch19, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 49.89 |
| Std Dev | 3.069 |

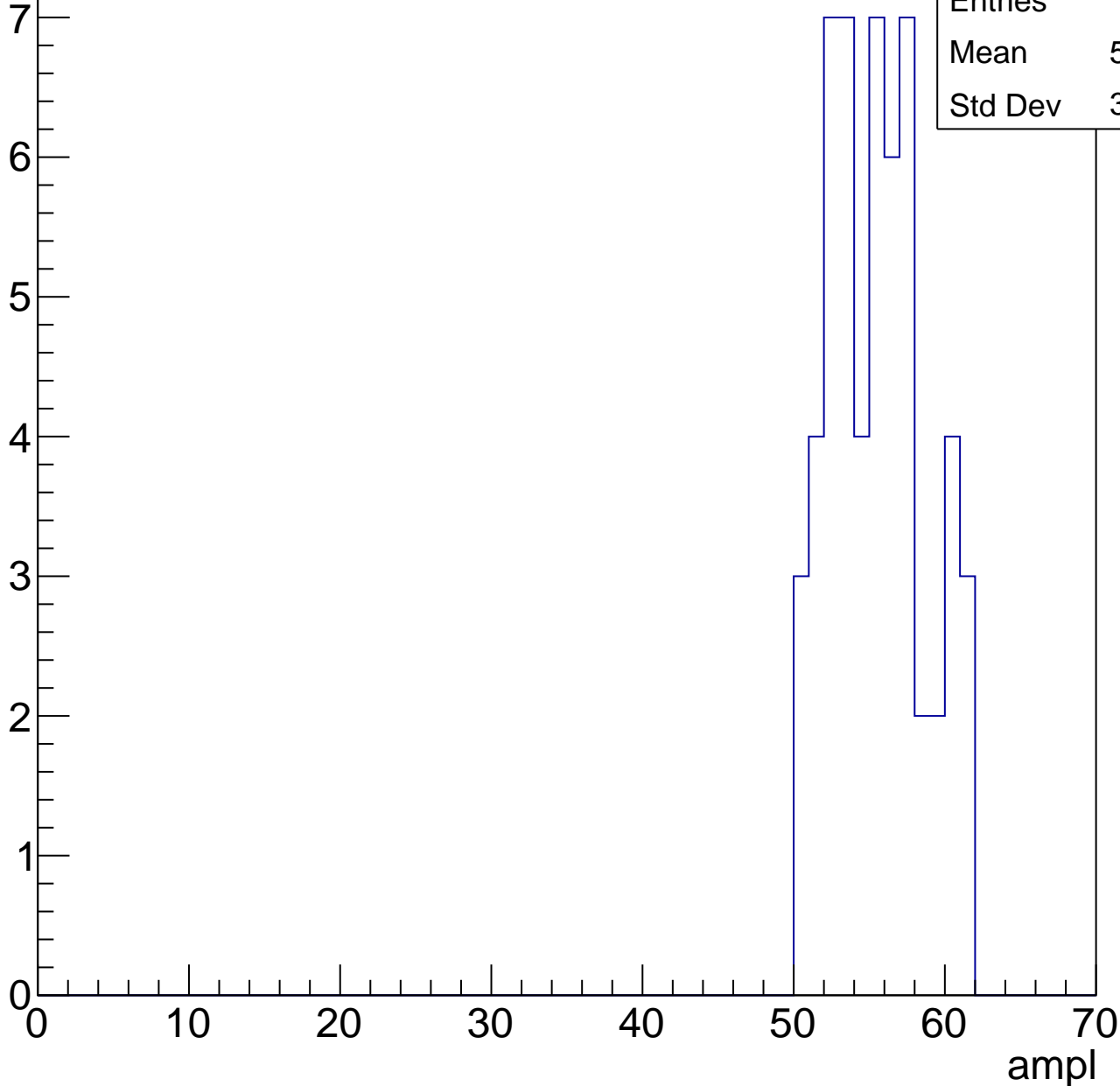


B0L102S, U20-ch19, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.04 |
| Std Dev | 3.065 |

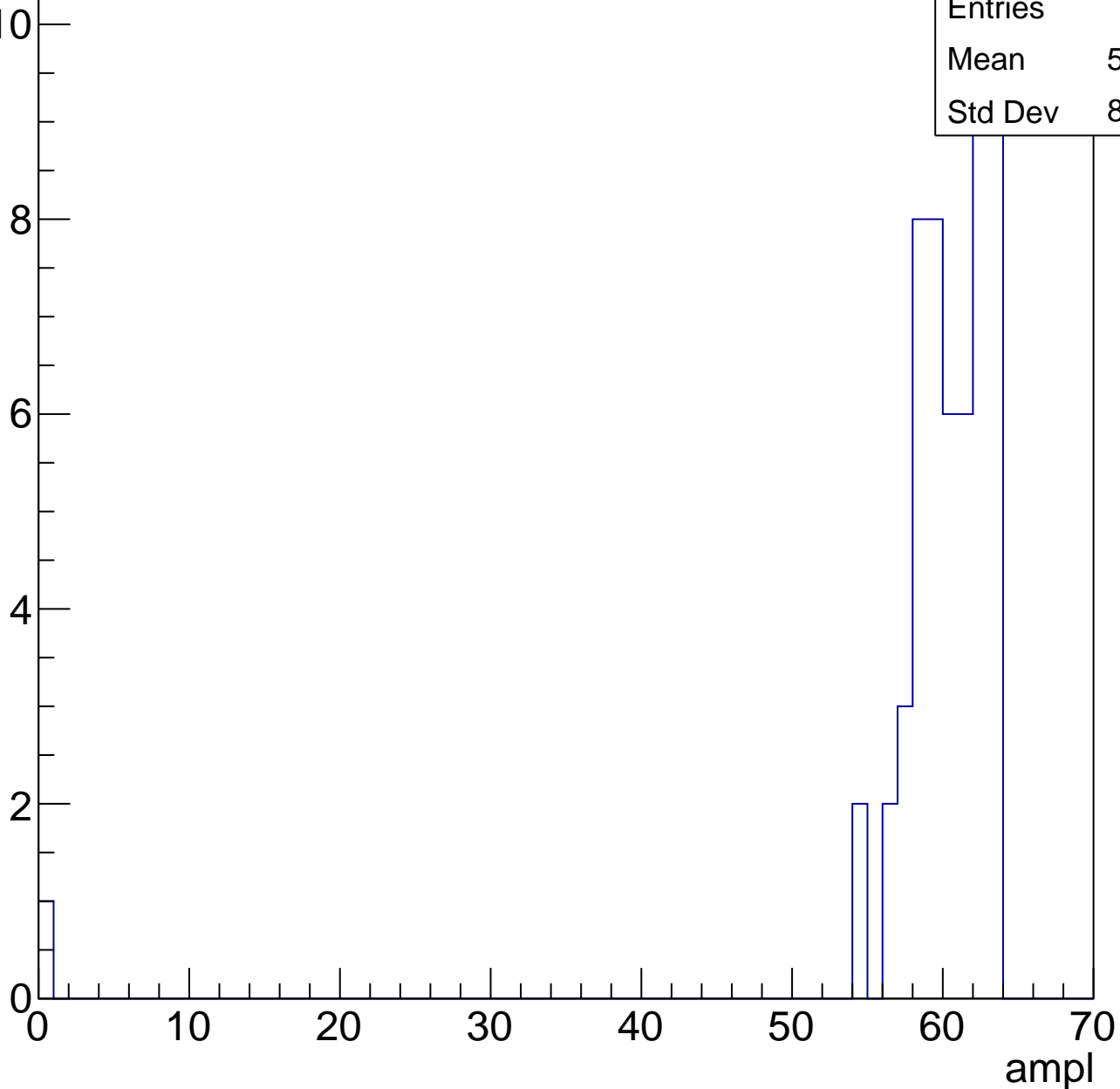


B0L102S, U20-ch19, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.93 |
| Std Dev | 8.355 |



B0L102S, U20-ch19, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch19, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B0L102S, U20-ch20, adc0

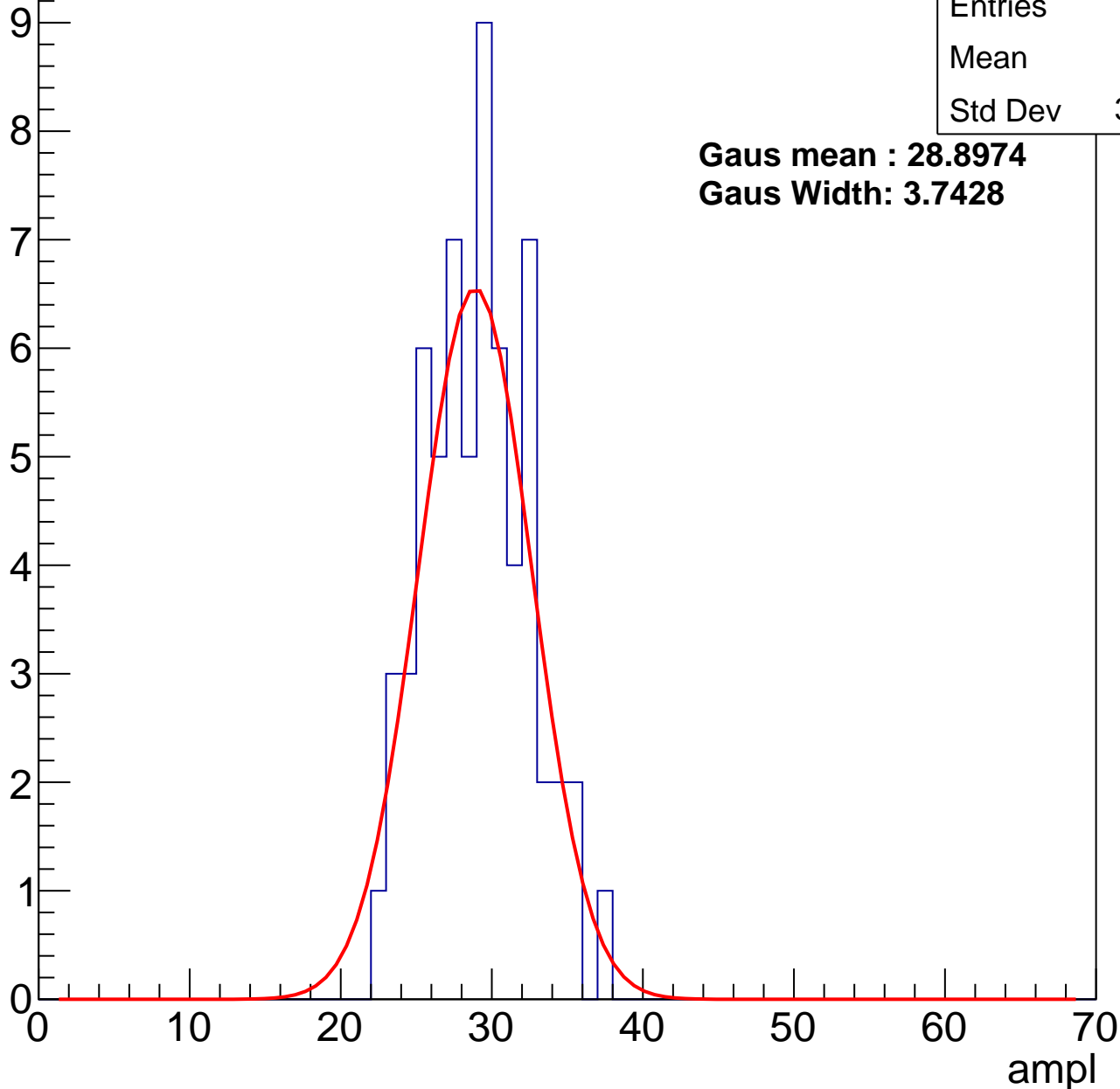
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.6 |
| Std Dev | 3.331 |

Gaus mean : 28.8974

Gaus Width: 3.7428



B0L102S, U20-ch20, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 79 |
| Mean | 35.47 |
| Std Dev | 3.532 |

Gaus mean : 36.3353

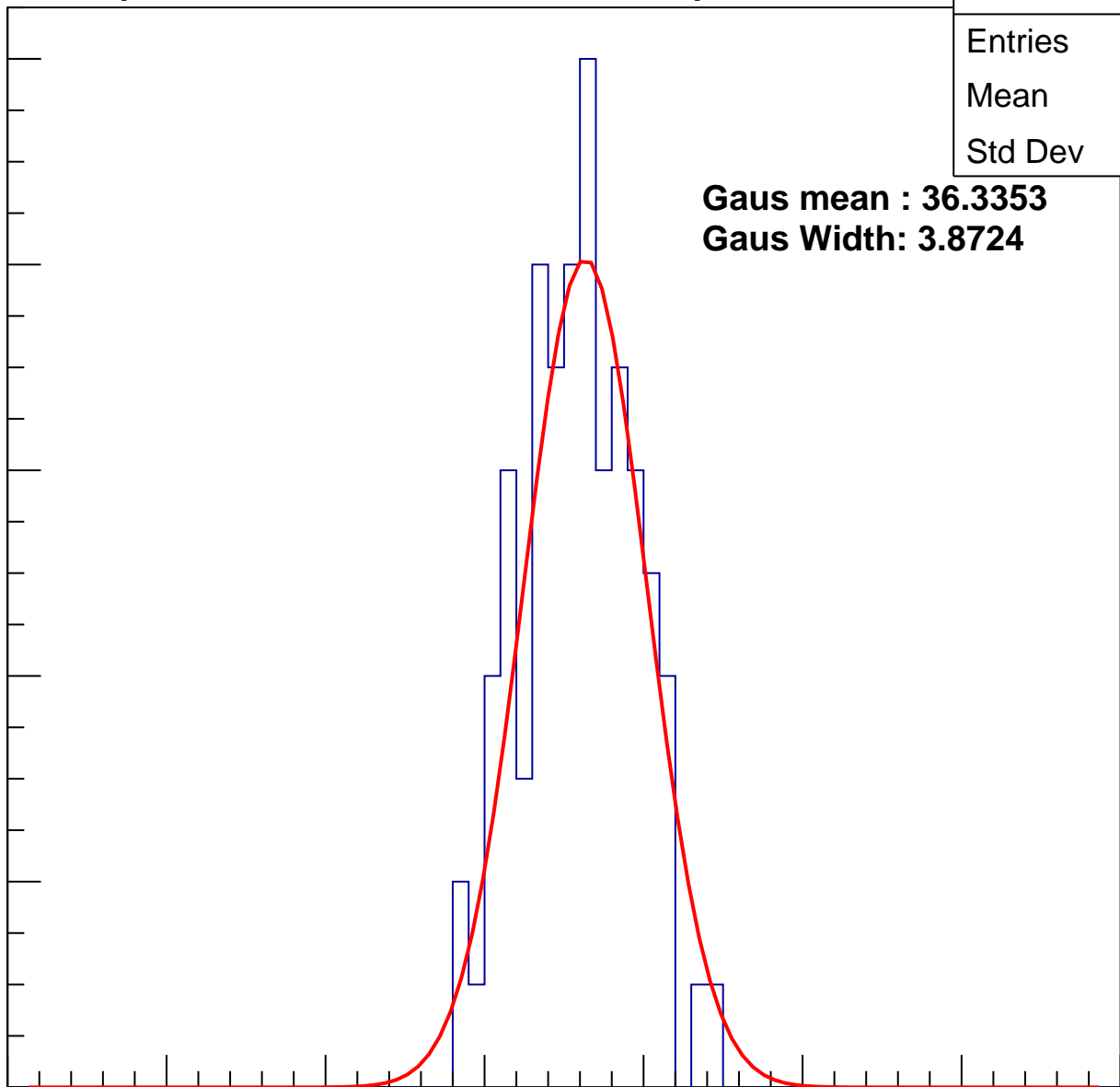
Gaus Width: 3.8724

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch20, adc2

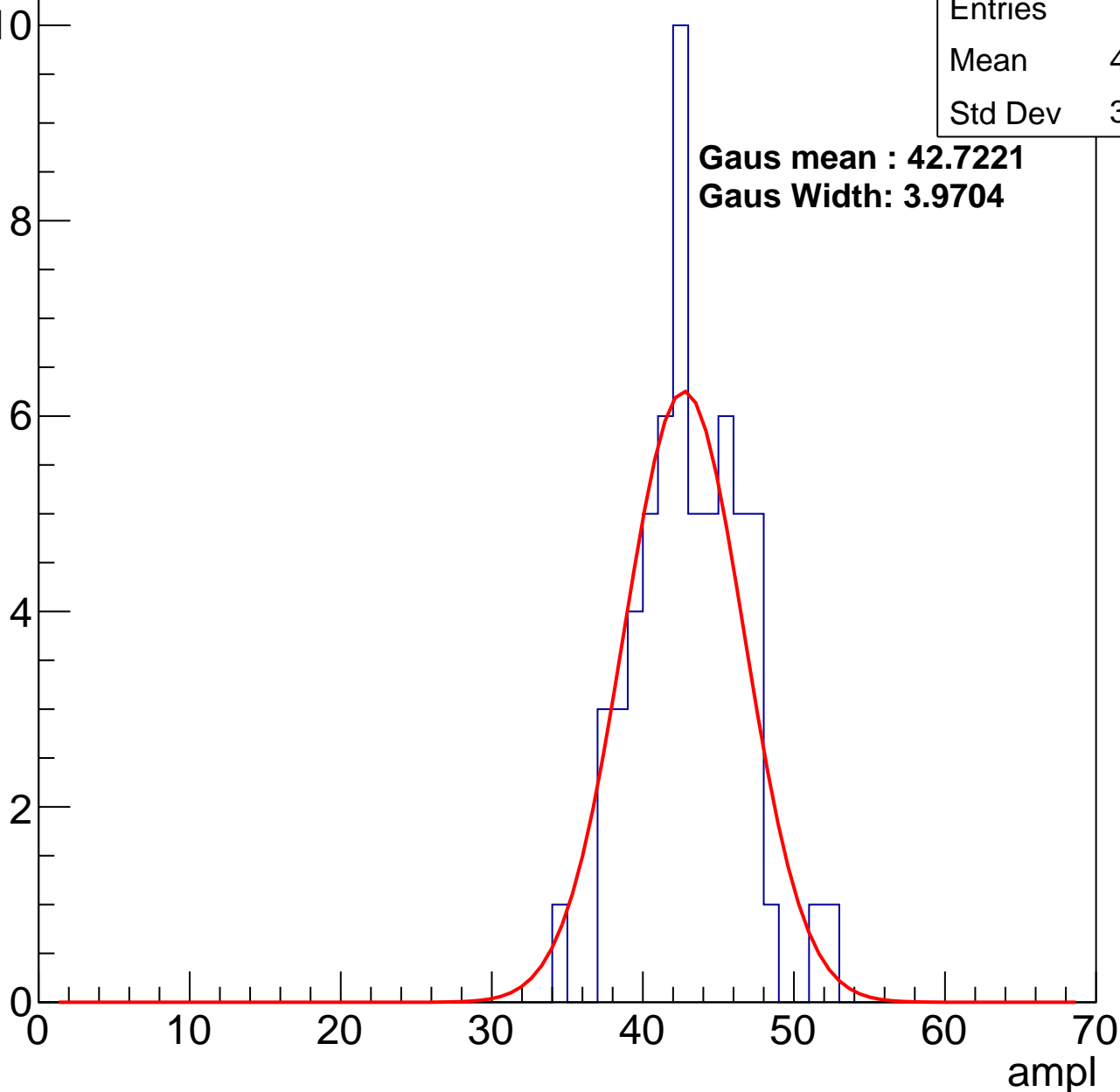
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.66 |
| Std Dev | 3.445 |

Gaus mean : 42.7221

Gaus Width: 3.9704

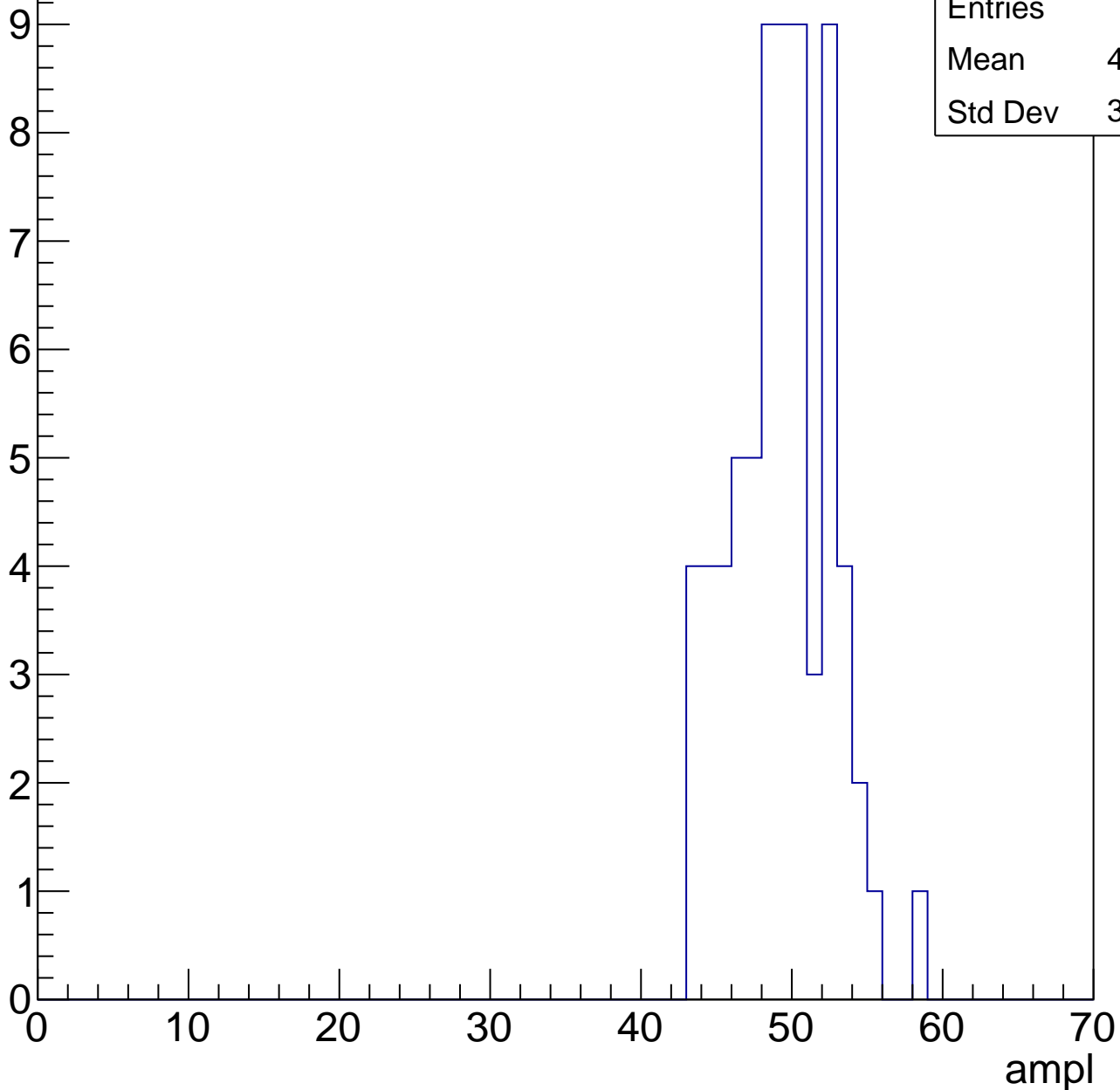


B0L102S, U20-ch20, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

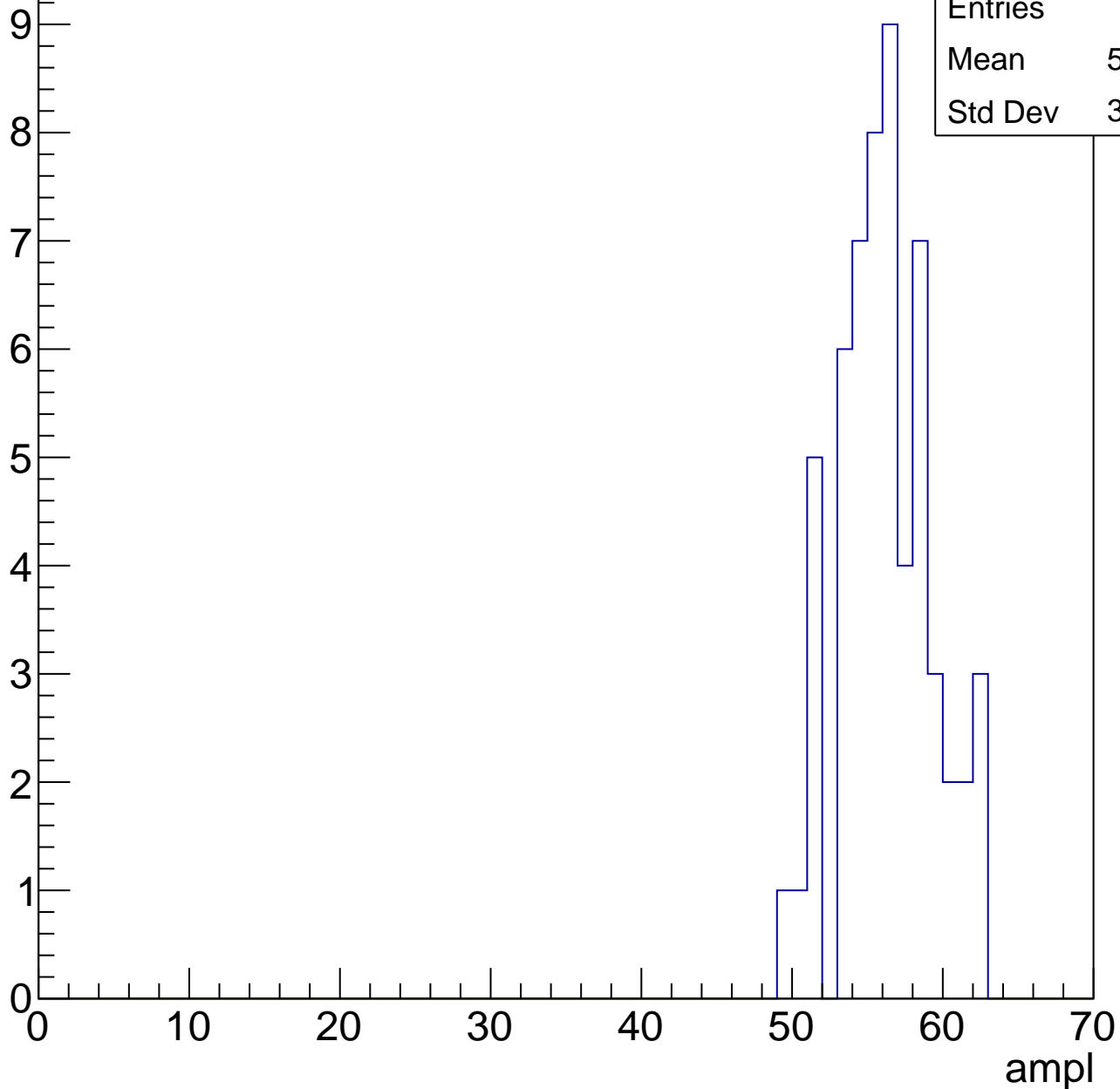
| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.84 |
| Std Dev | 3.215 |



B0L102S, U20-ch20, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



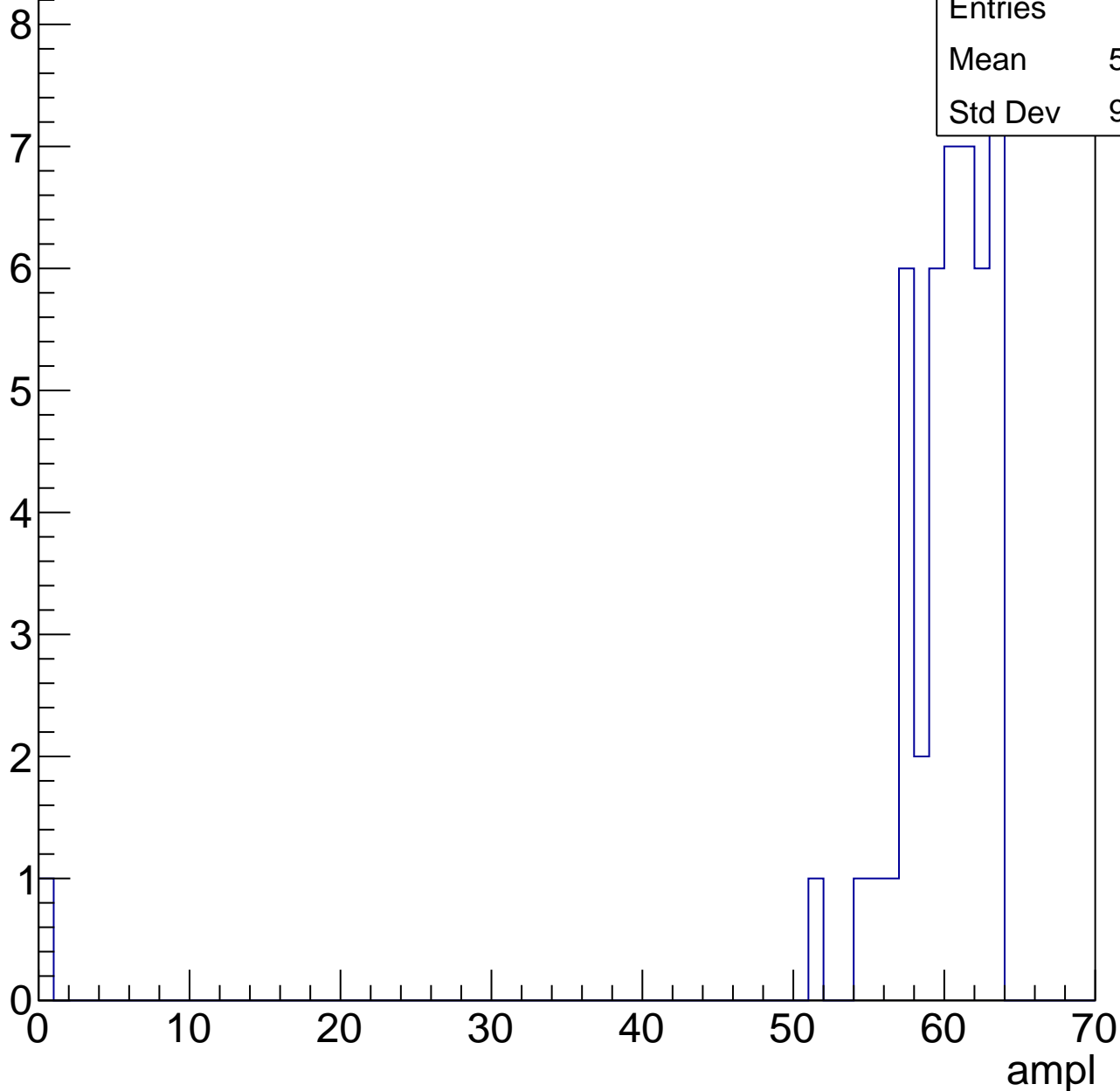
| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.74 |
| Std Dev | 3.066 |

B0L102S, U20-ch20, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

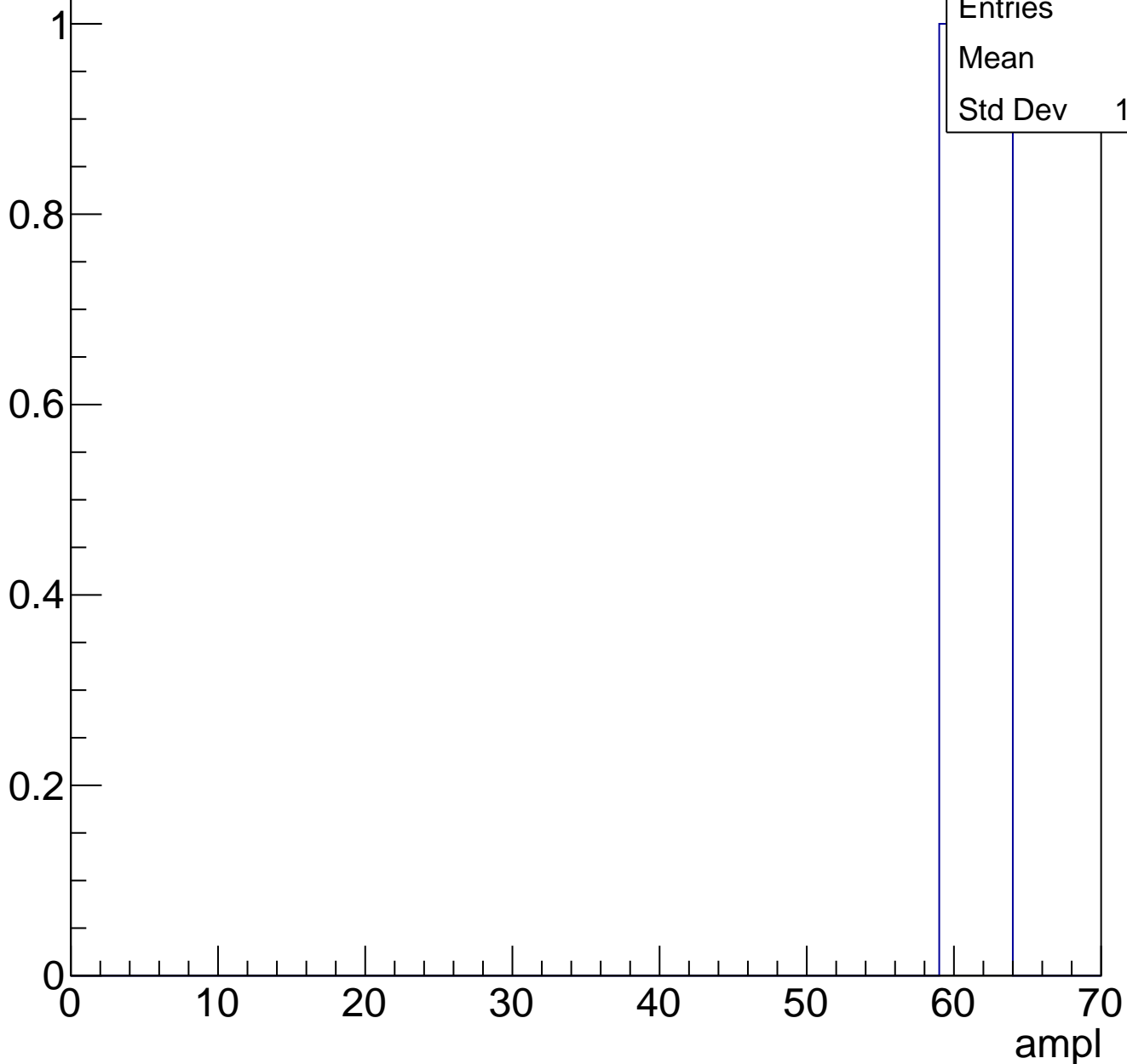
| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.53 |
| Std Dev | 9.024 |



B0L102S, U20-ch20, adc6

calib_packv5_042523_0143.root, FC#12, port B1

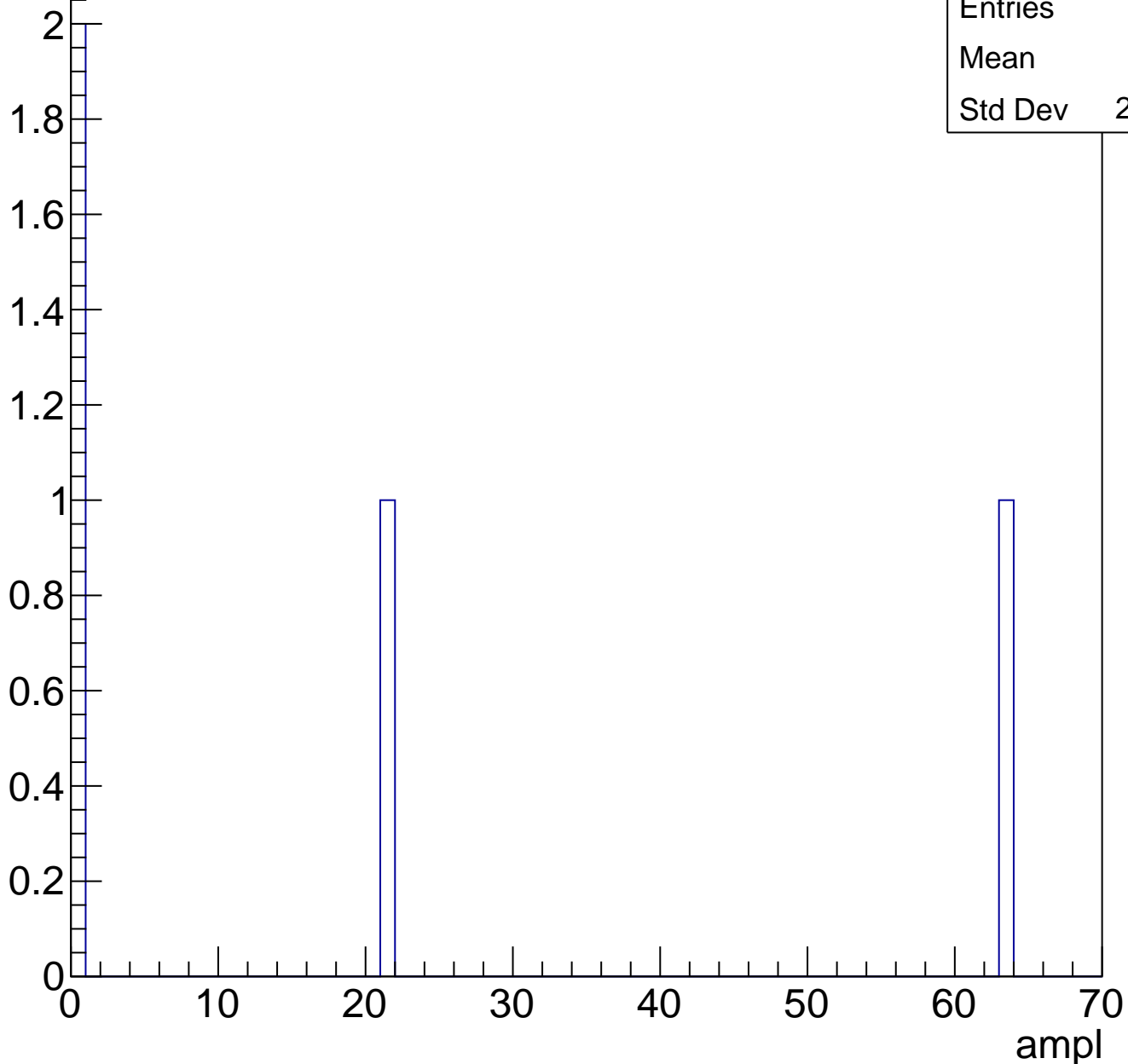
Entry



B0L102S, U20-ch20, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 21 |
| Std Dev | 25.72 |

B0L102S, U20-ch21, adc0

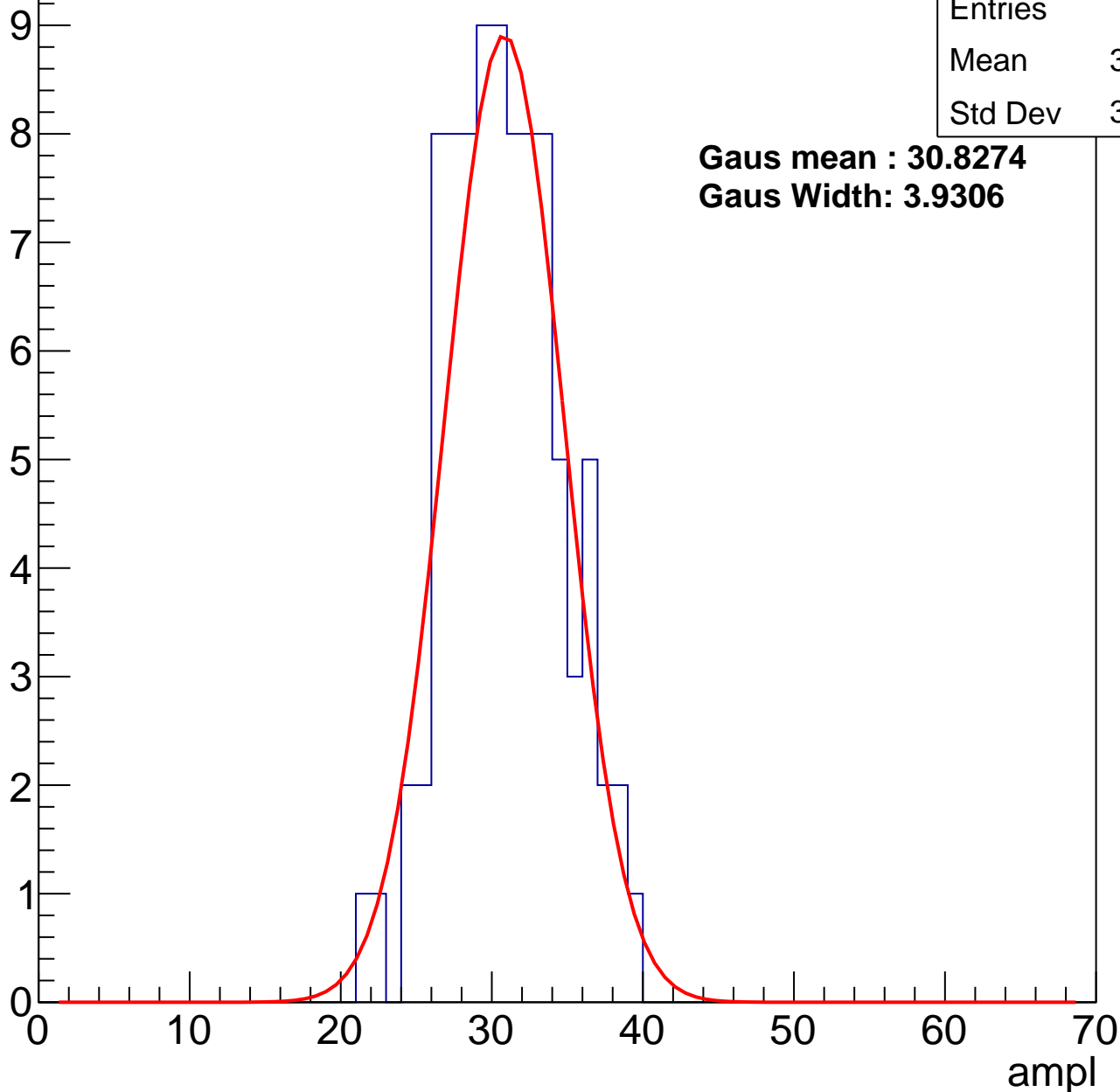
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 90 |
| Mean | 30.36 |
| Std Dev | 3.728 |

Gaus mean : 30.8274

Gaus Width: 3.9306



B0L102S, U20-ch21, adc1

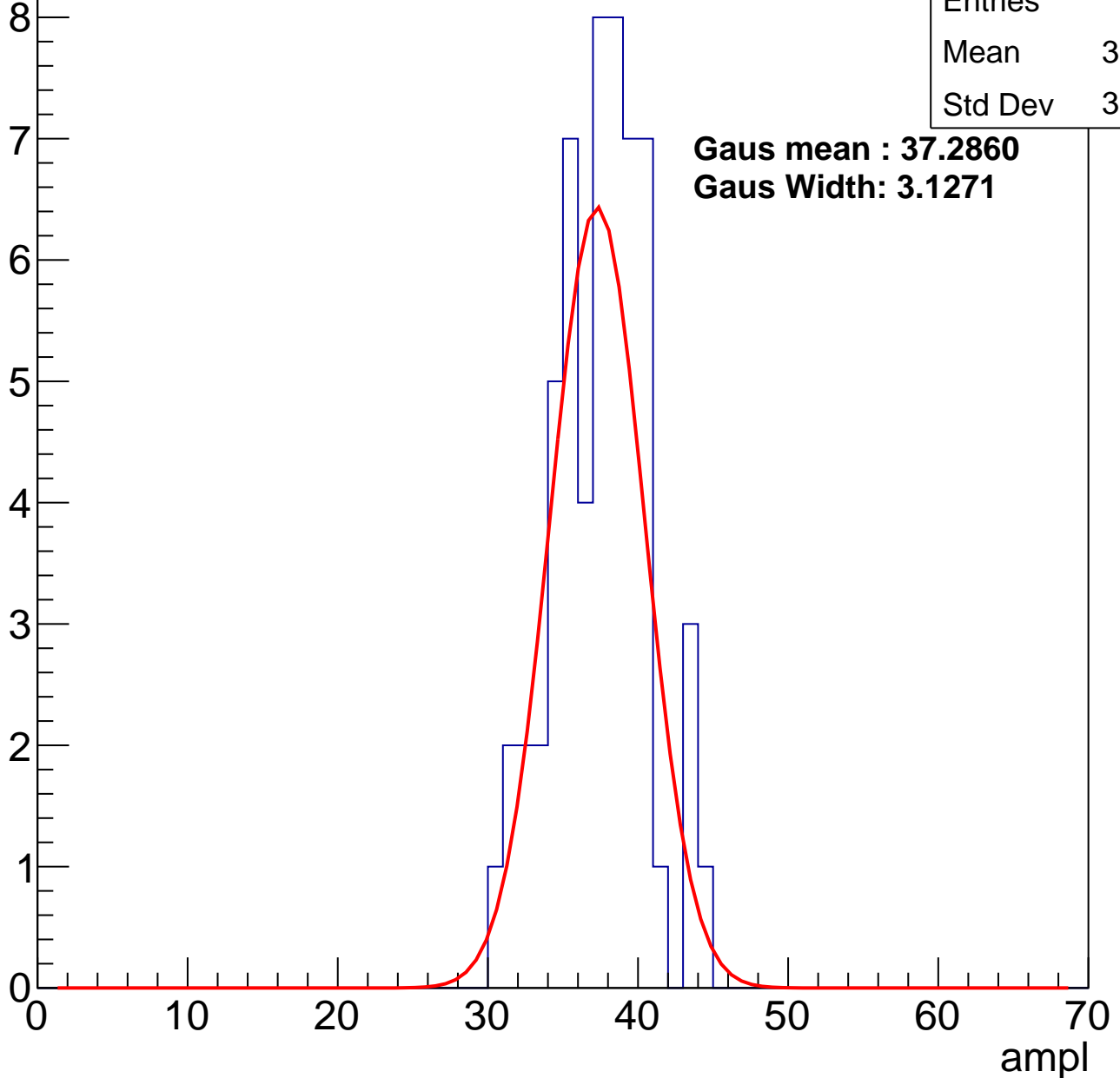
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 37.03 |
| Std Dev | 3.085 |

Gaus mean : 37.2860

Gaus Width: 3.1271



B0L102S, U20-ch21, adc2

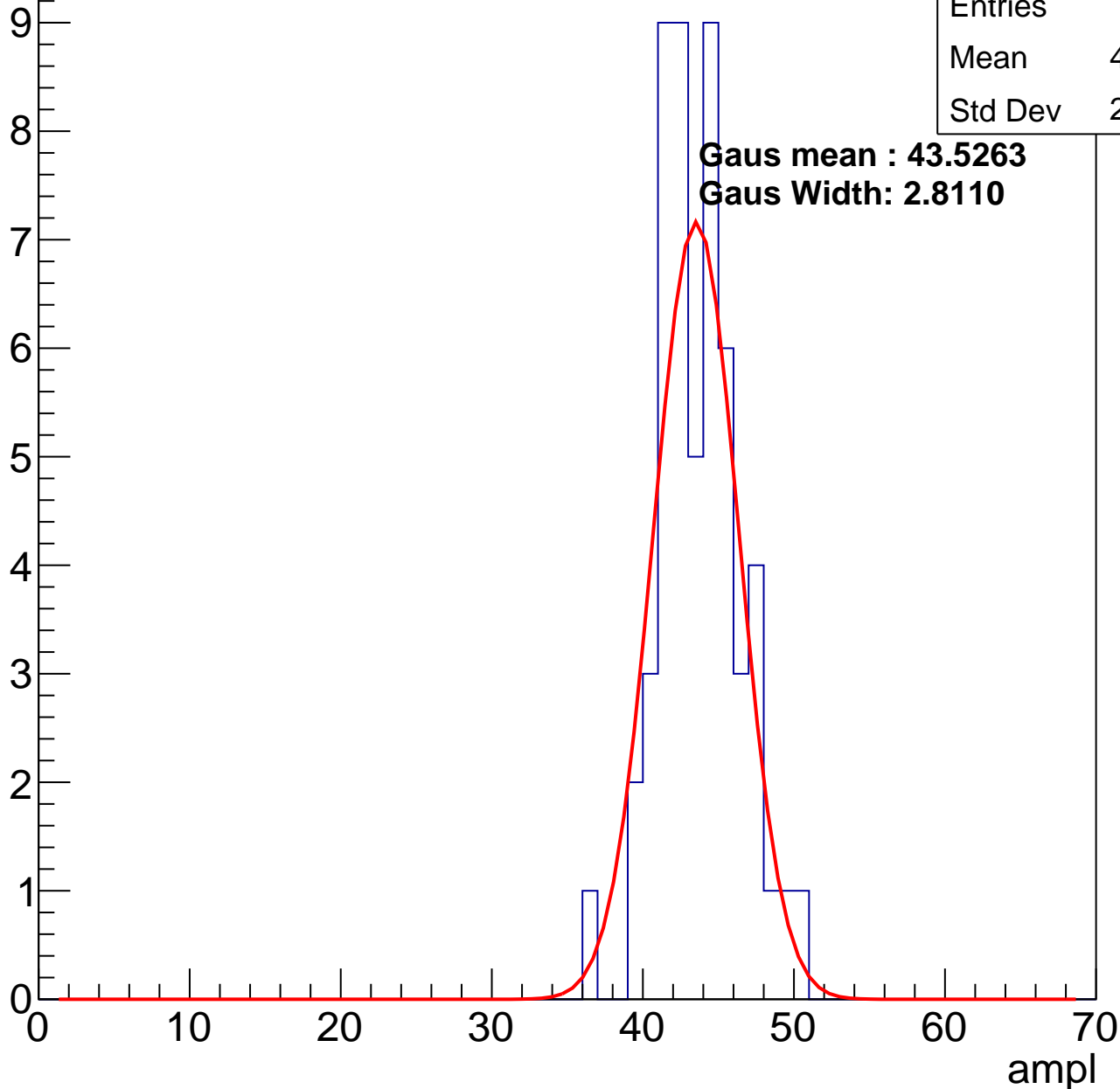
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.24 |
| Std Dev | 2.666 |

Gaus mean : 43.5263

Gaus Width: 2.8110

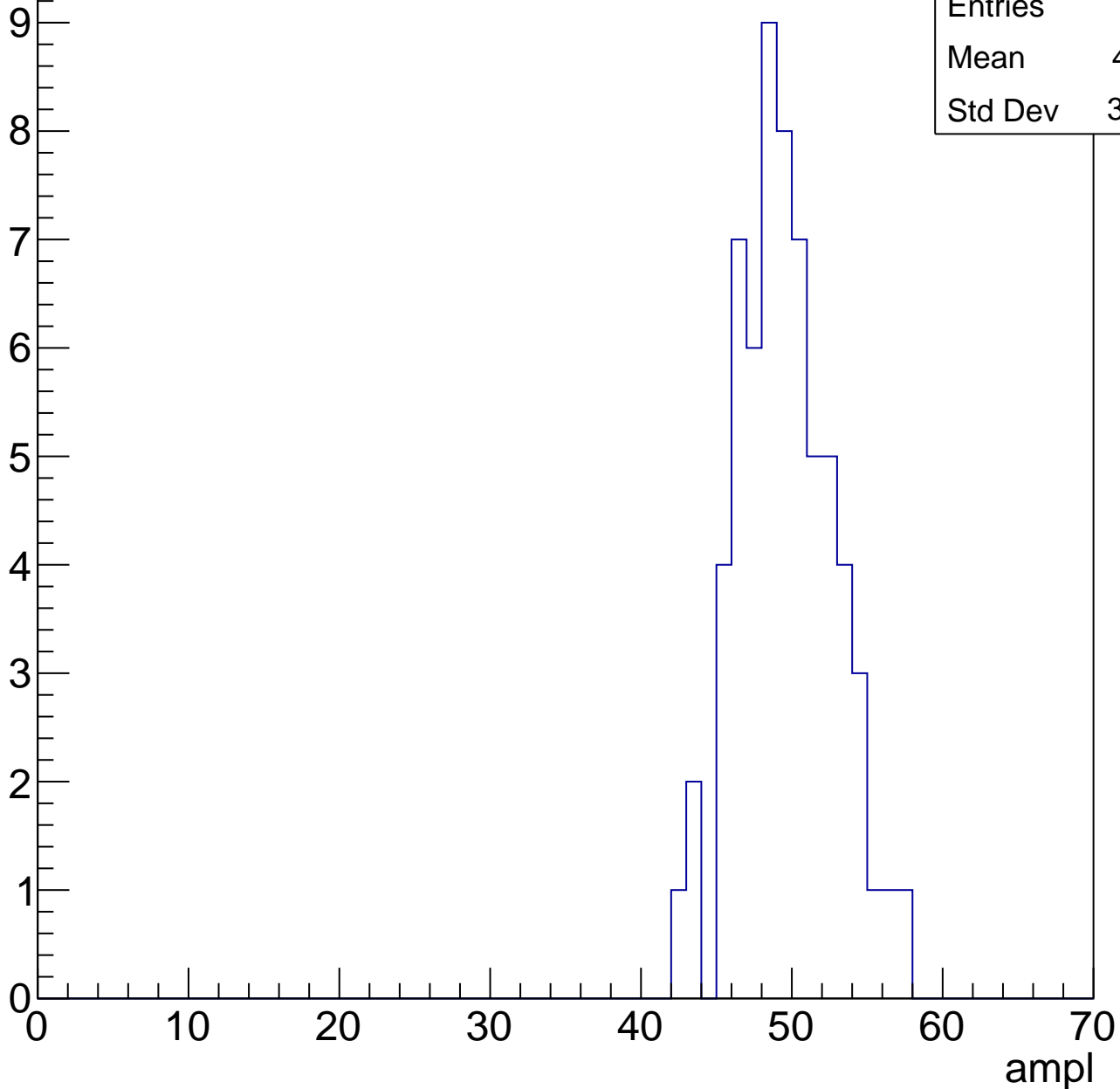


B0L102S, U20-ch21, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.11 |
| Std Dev | 3.163 |

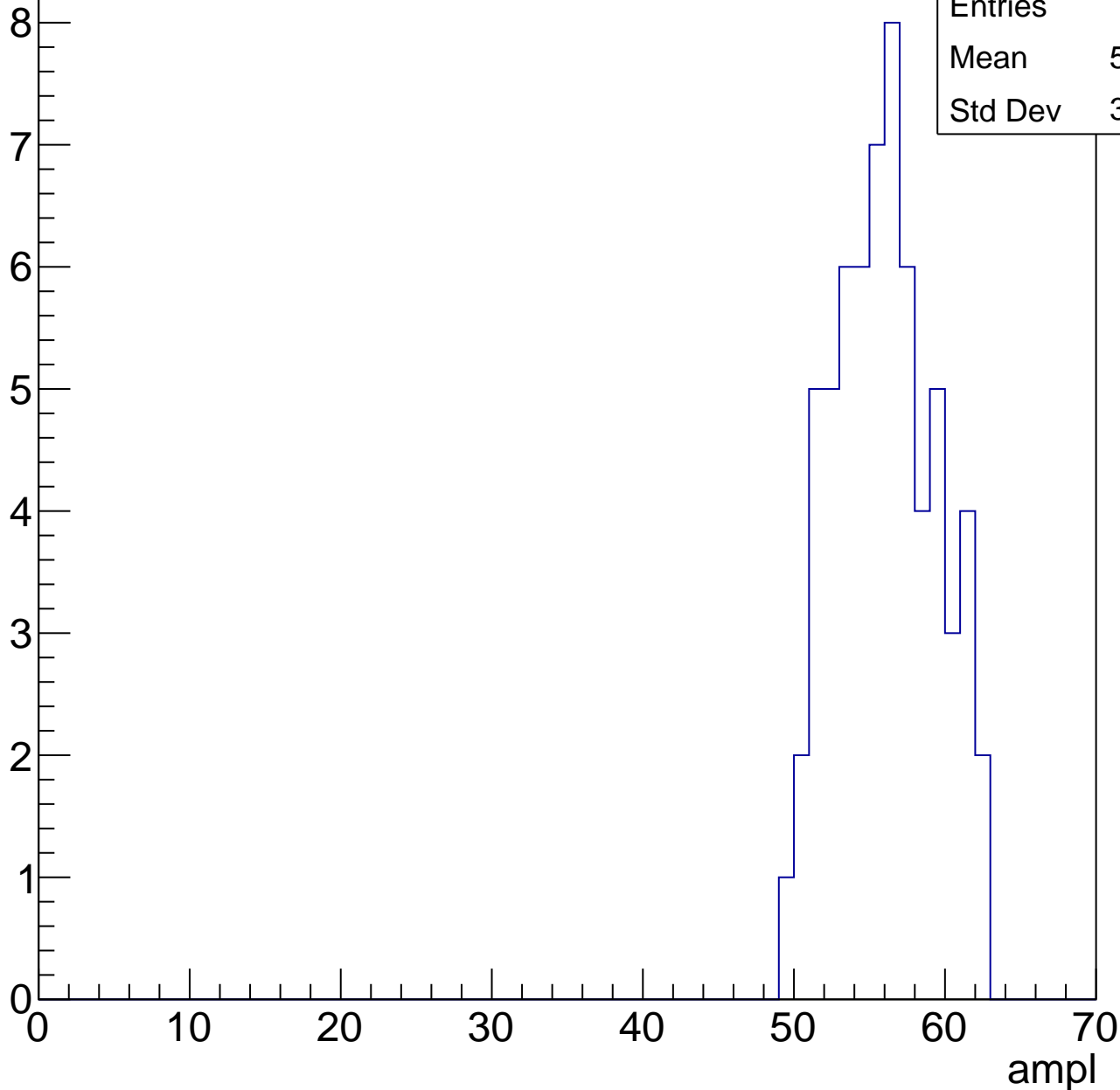


B0L102S, U20-ch21, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 55.56 |
| Std Dev | 3.273 |

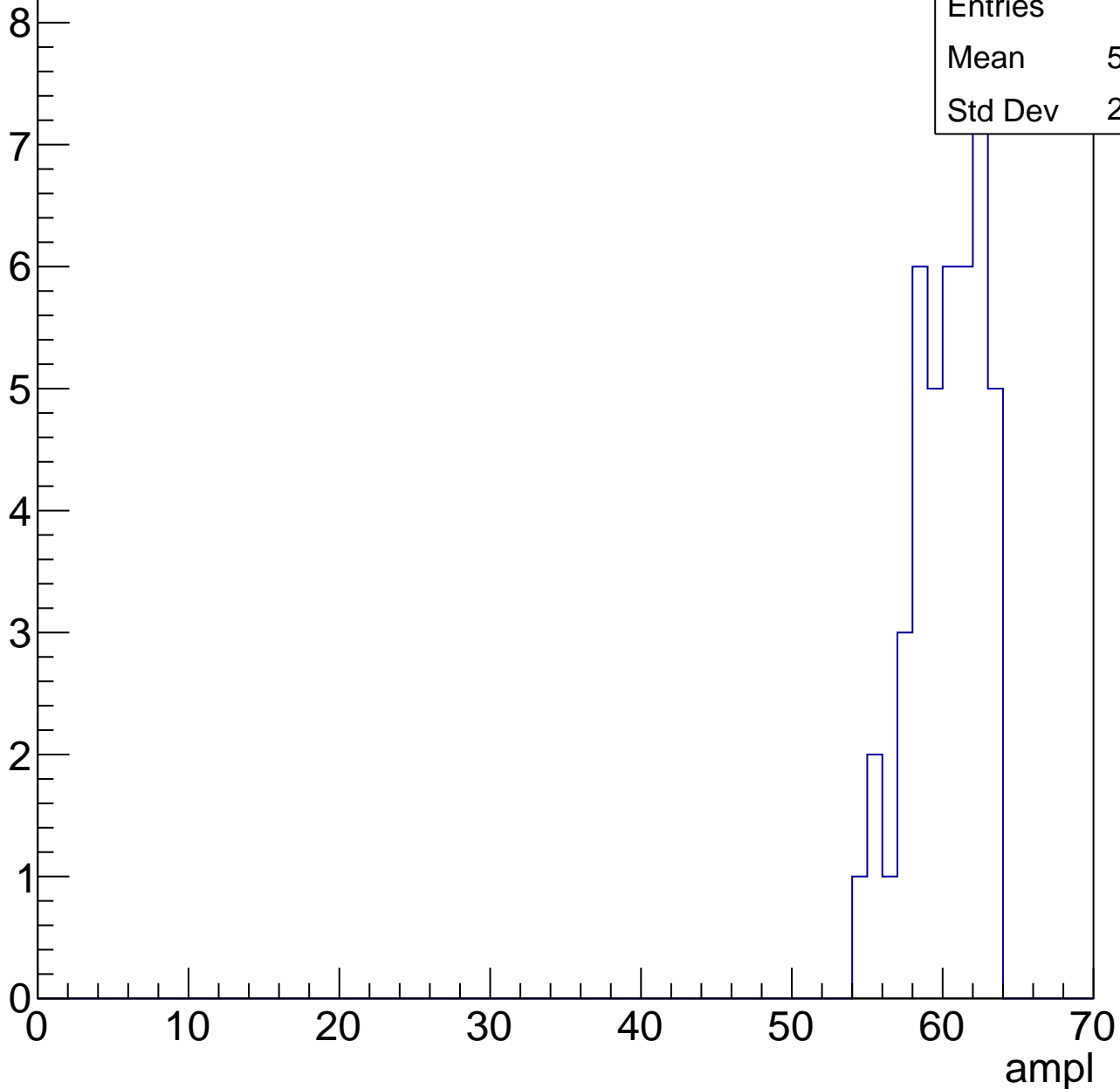


B0L102S, U20-ch21, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.79 |
| Std Dev | 2.358 |

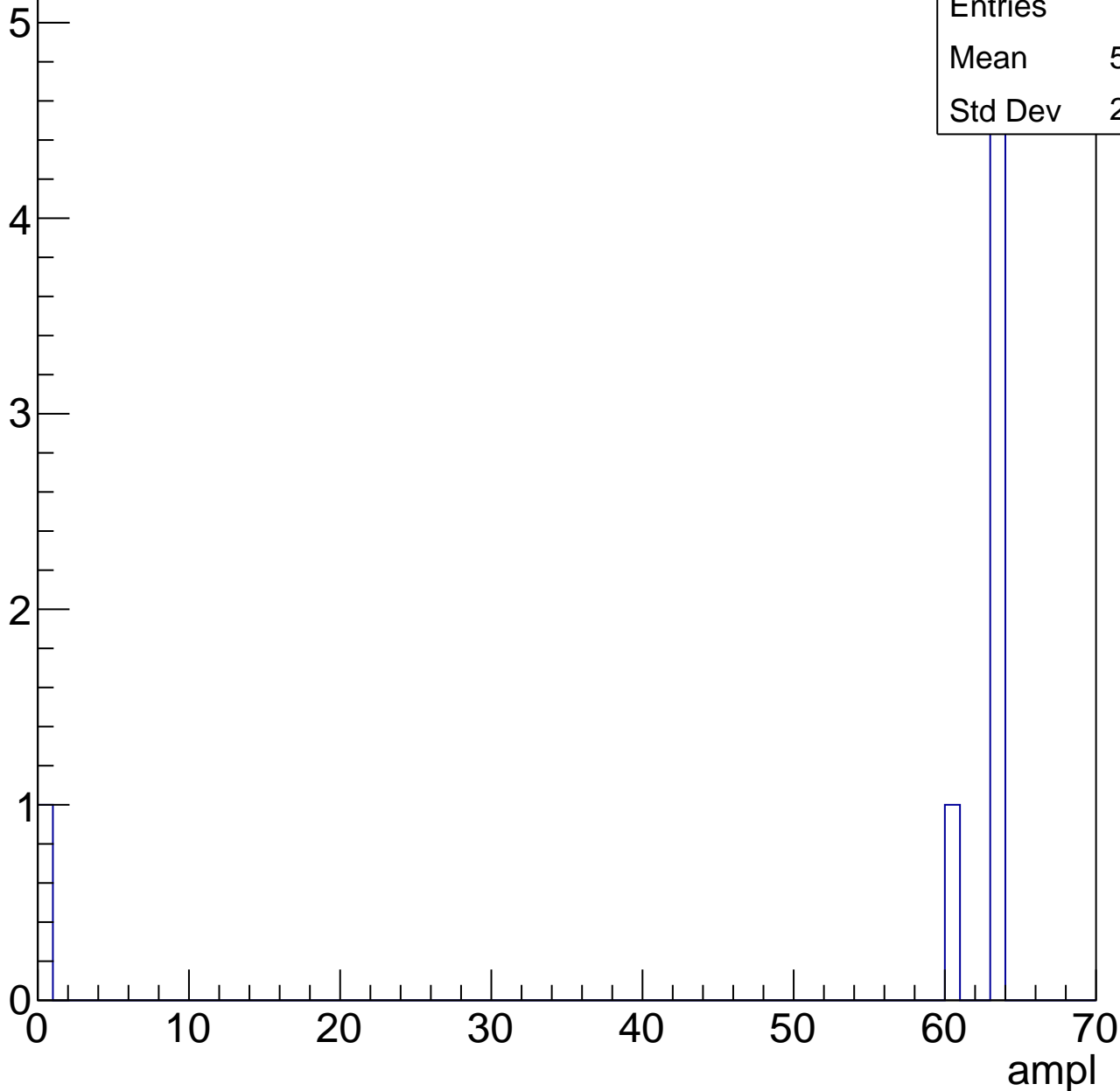


B0L102S, U20-ch21, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 7 |
| Mean | 53.57 |
| Std Dev | 21.89 |



B0L102S, U20-ch21, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U20-ch22, adc0

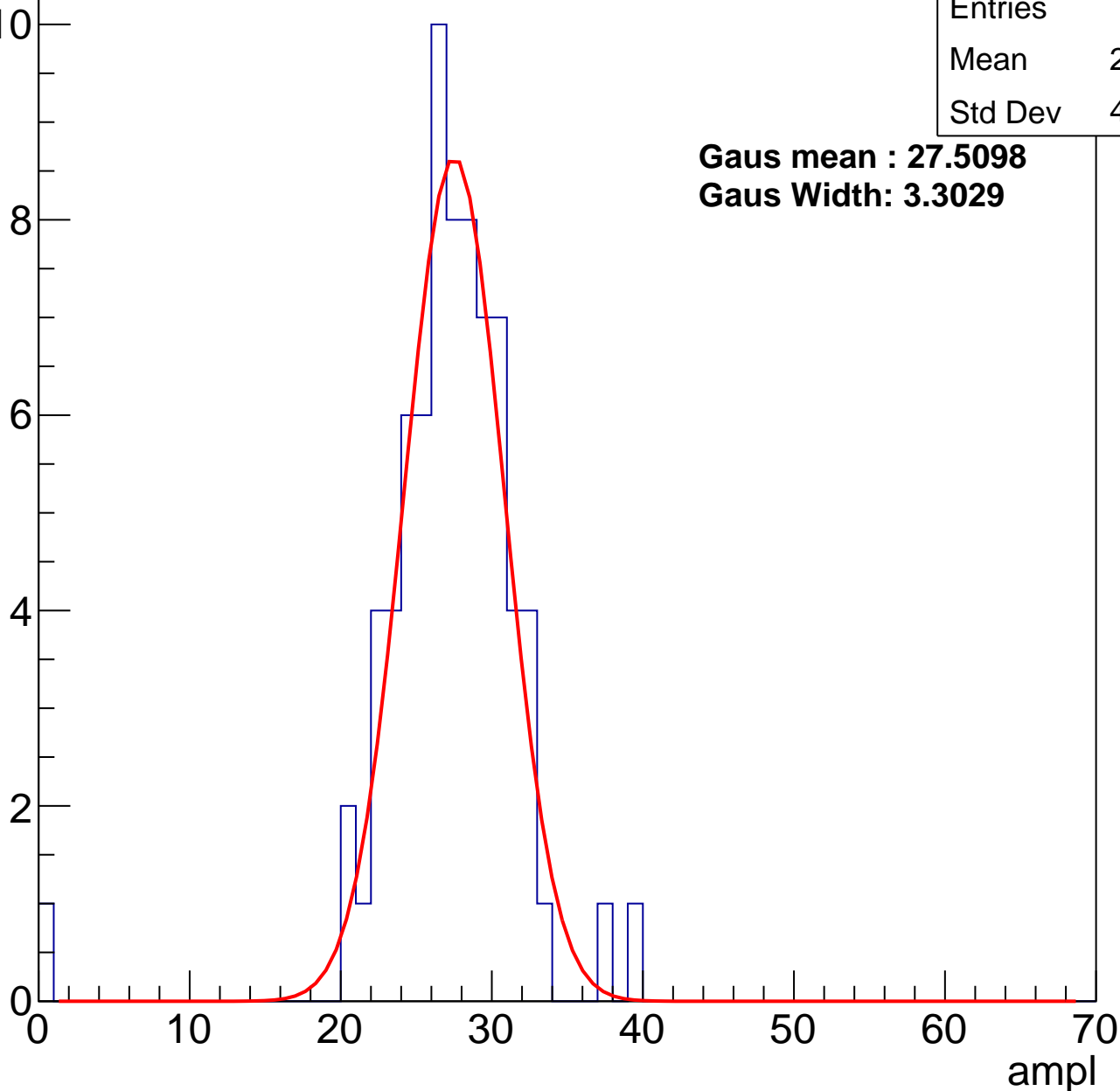
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 26.79 |
| Std Dev | 4.697 |

Gaus mean : 27.5098

Gaus Width: 3.3029



B0L102S, U20-ch22, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.36 |
| Std Dev | 2.859 |

Gaus mean : 34.9837

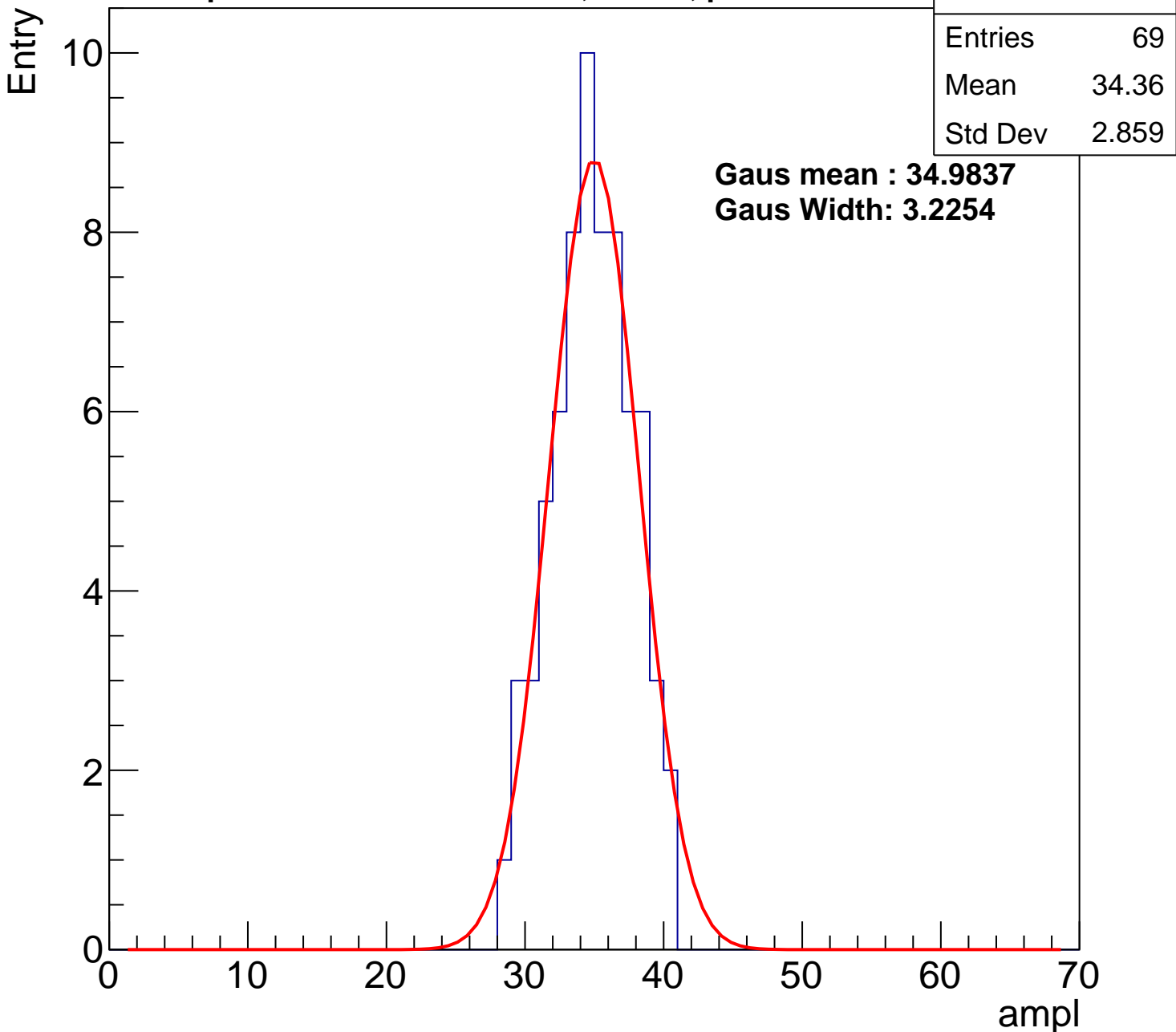
Gaus Width: 3.2254

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch22, adc2

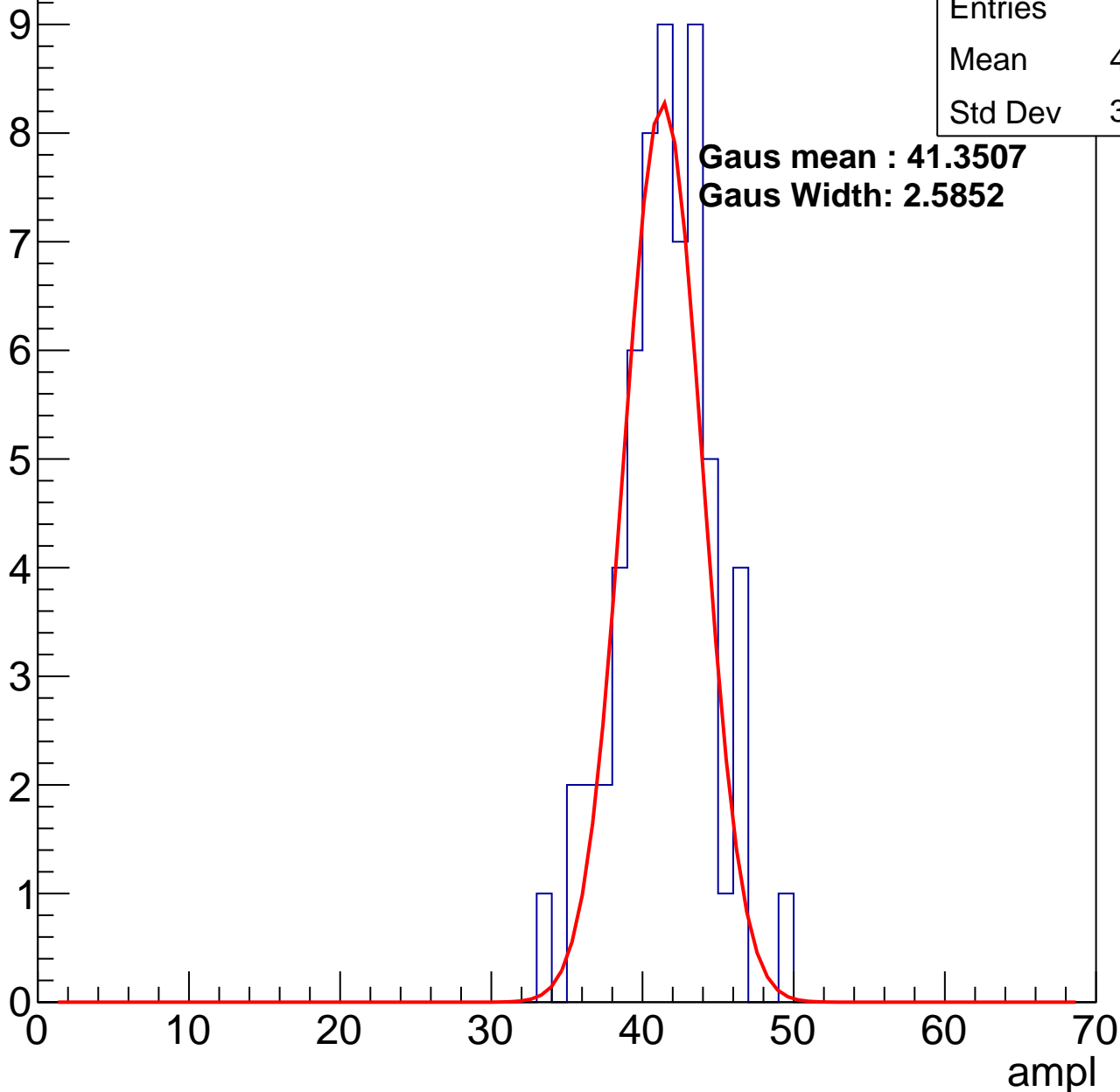
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.03 |
| Std Dev | 3.013 |

Gaus mean : 41.3507

Gaus Width: 2.5852

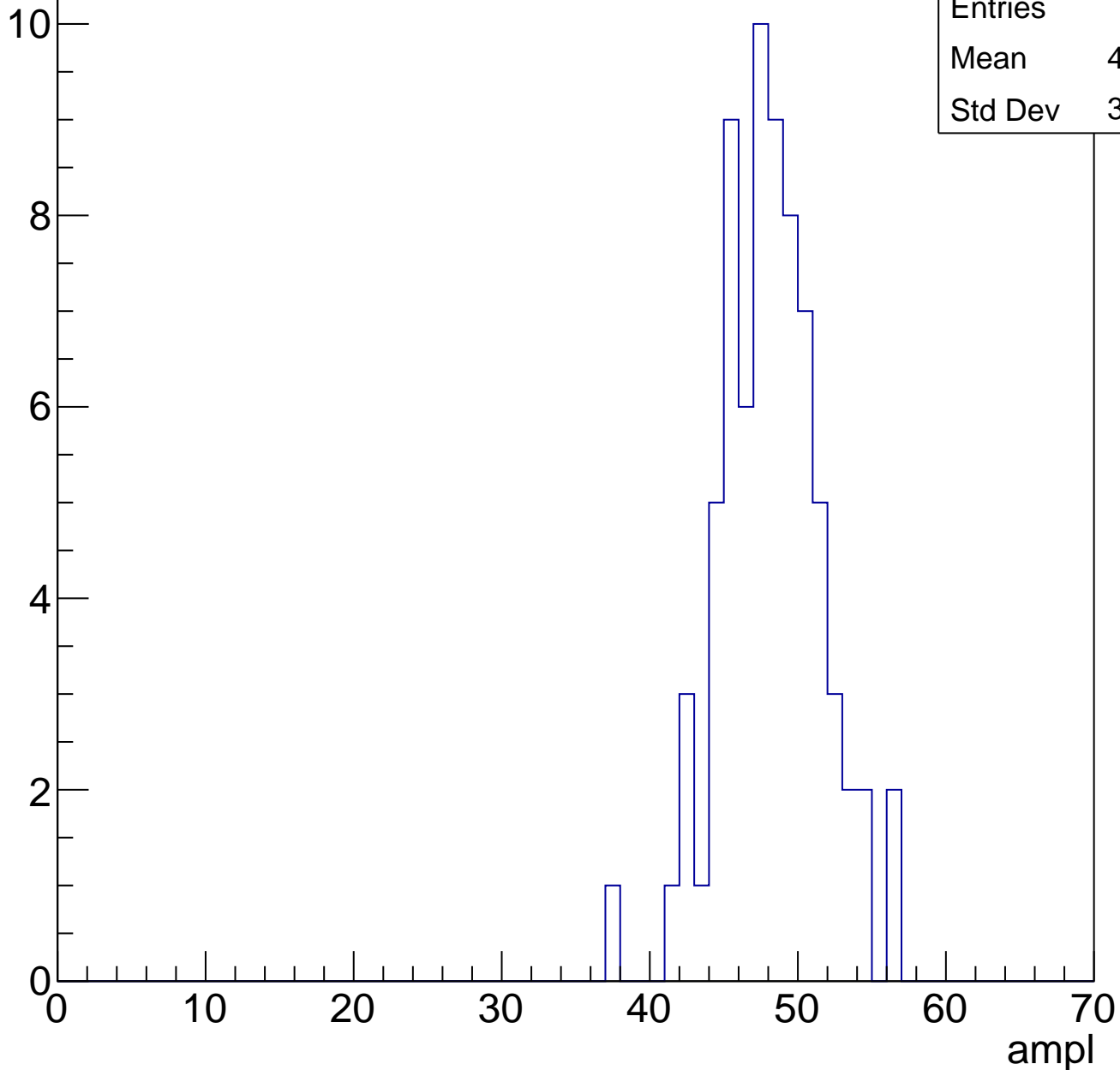


B0L102S, U20-ch22, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 47.69 |
| Std Dev | 3.429 |

Entry

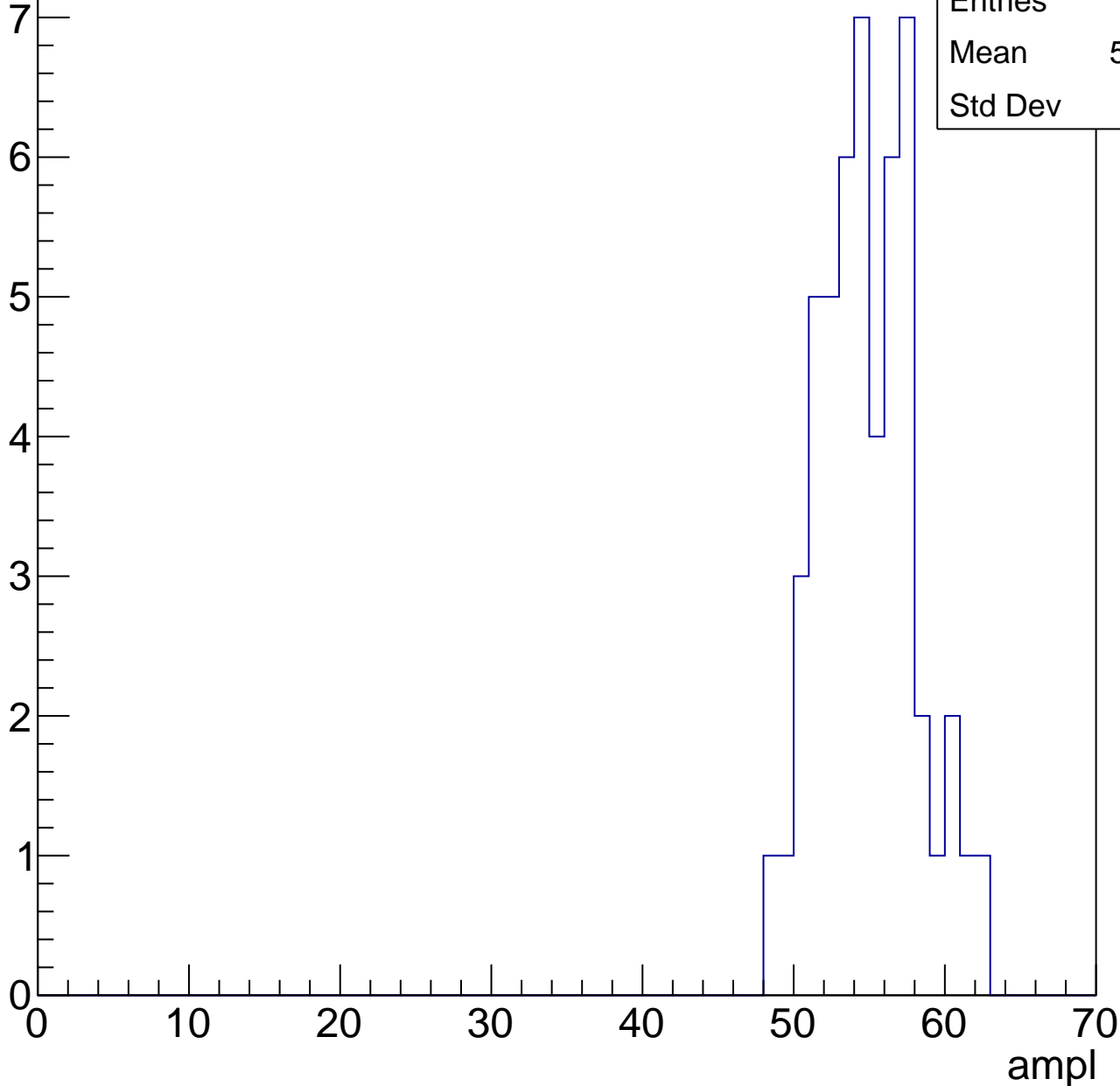


B0L102S, U20-ch22, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

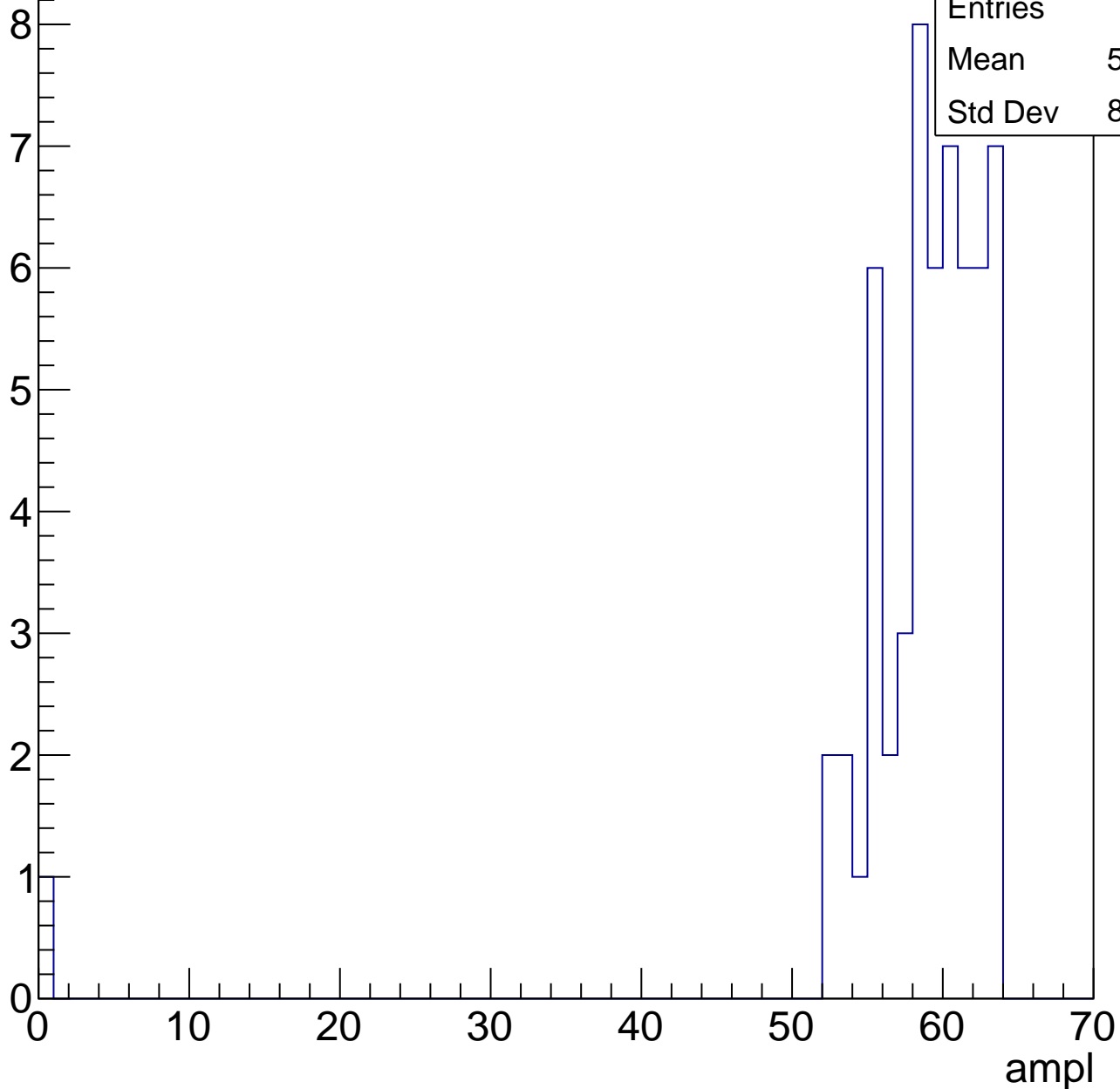
| | |
|---------|-------|
| Entries | 52 |
| Mean | 54.44 |
| Std Dev | 3.11 |



B0L102S, U20-ch22, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch22, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.15 |
| Std Dev | 1.46 |

B0L102S, U20-ch22, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch23, adc0

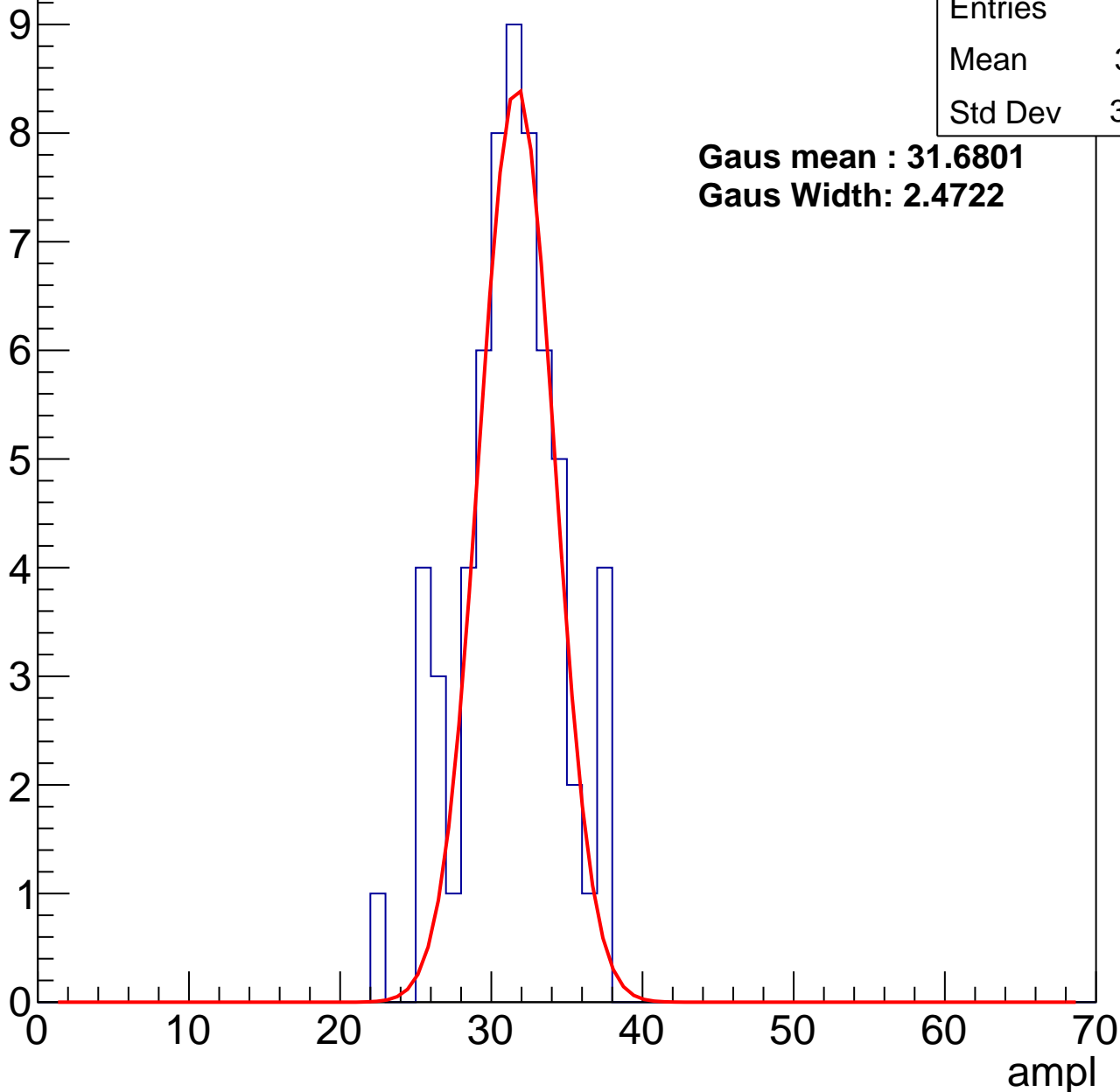
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.81 |
| Std Dev | 3.262 |

Gaus mean : 31.6801

Gaus Width: 2.4722



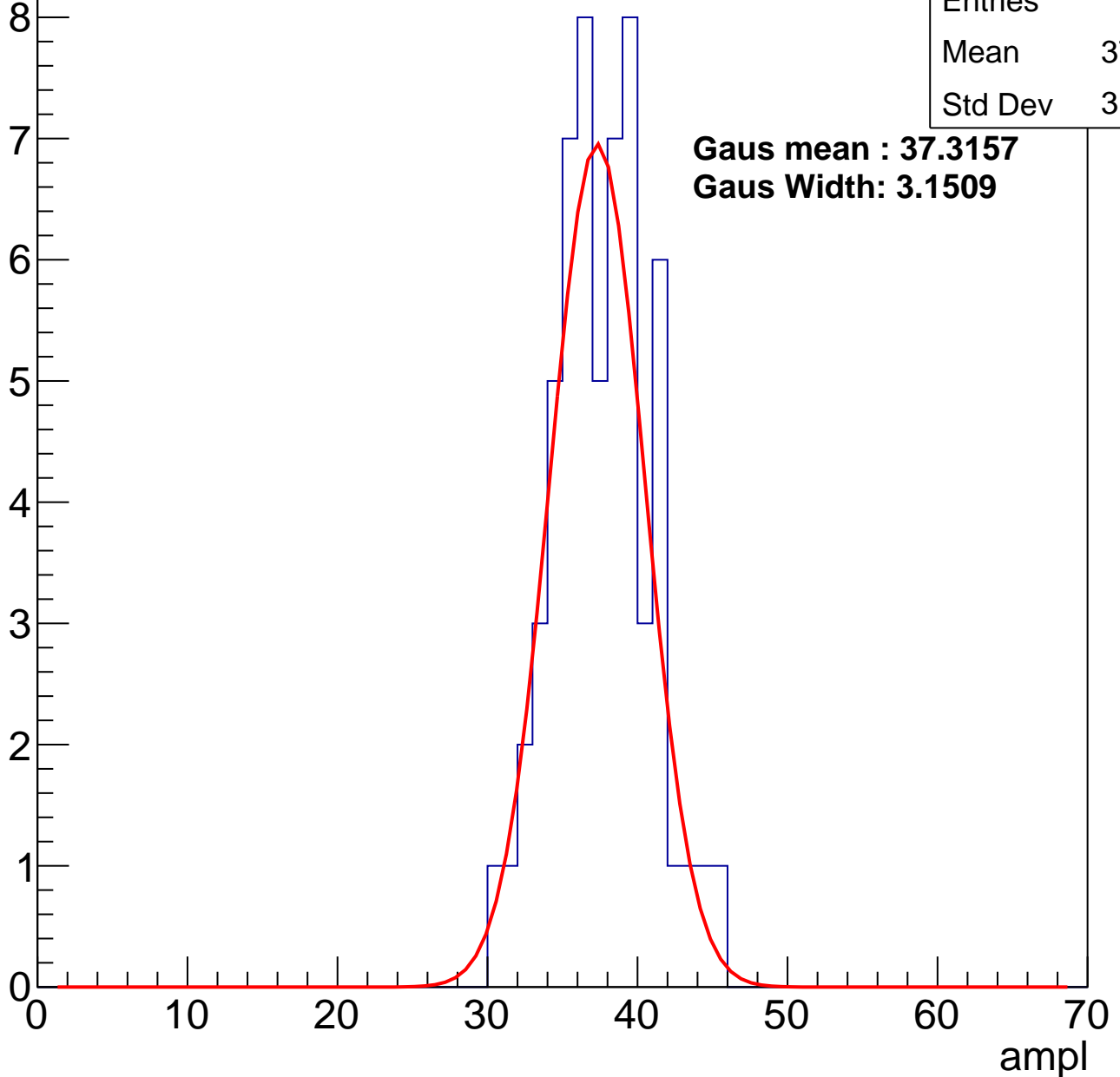
B0L102S, U20-ch23, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 37.17 |
| Std Dev | 3.158 |

Gaus mean : 37.3157
Gaus Width: 3.1509



B0L102S, U20-ch23, adc2

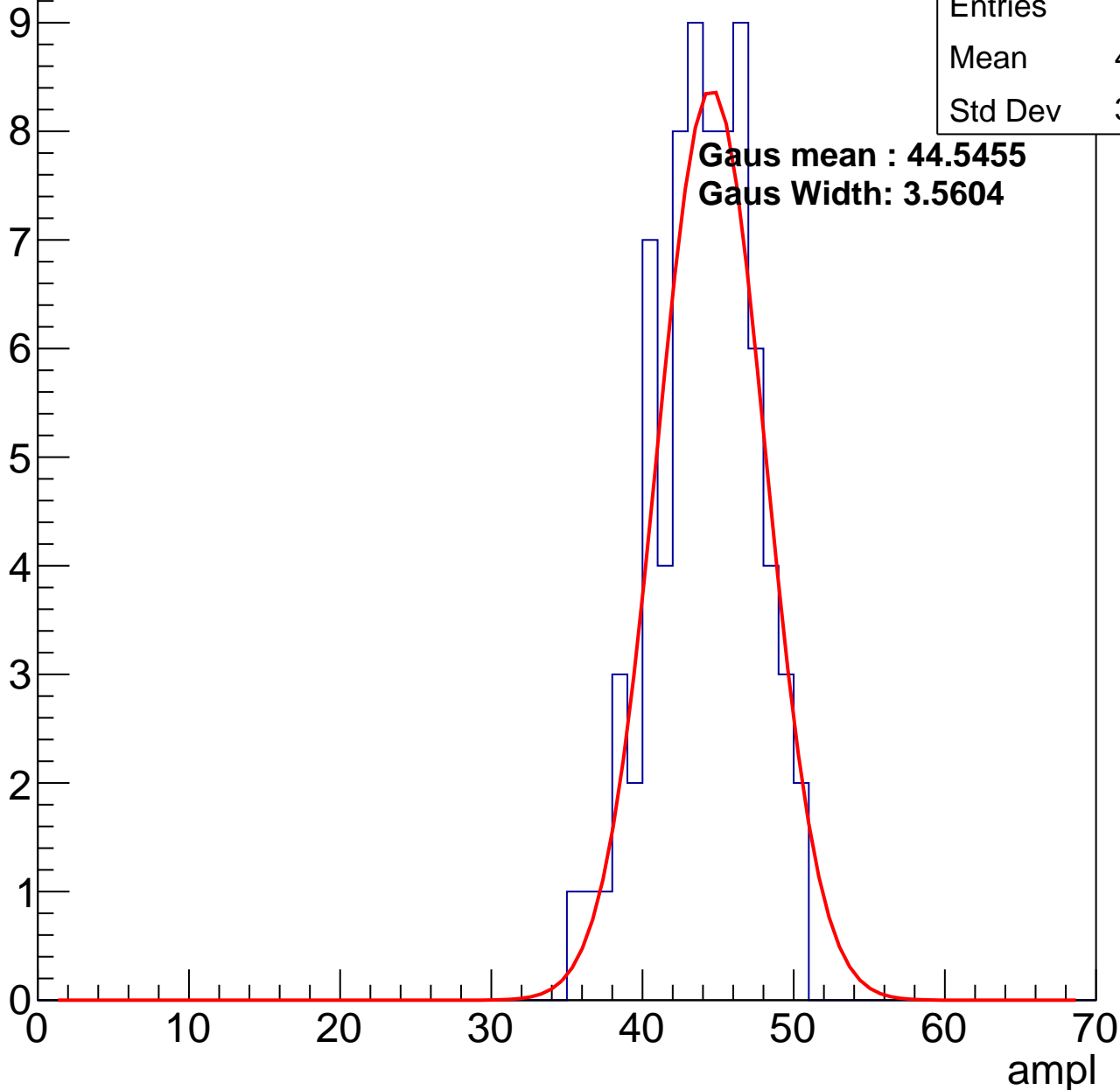
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 43.61 |
| Std Dev | 3.321 |

Gaus mean : 44.5455

Gaus Width: 3.5604

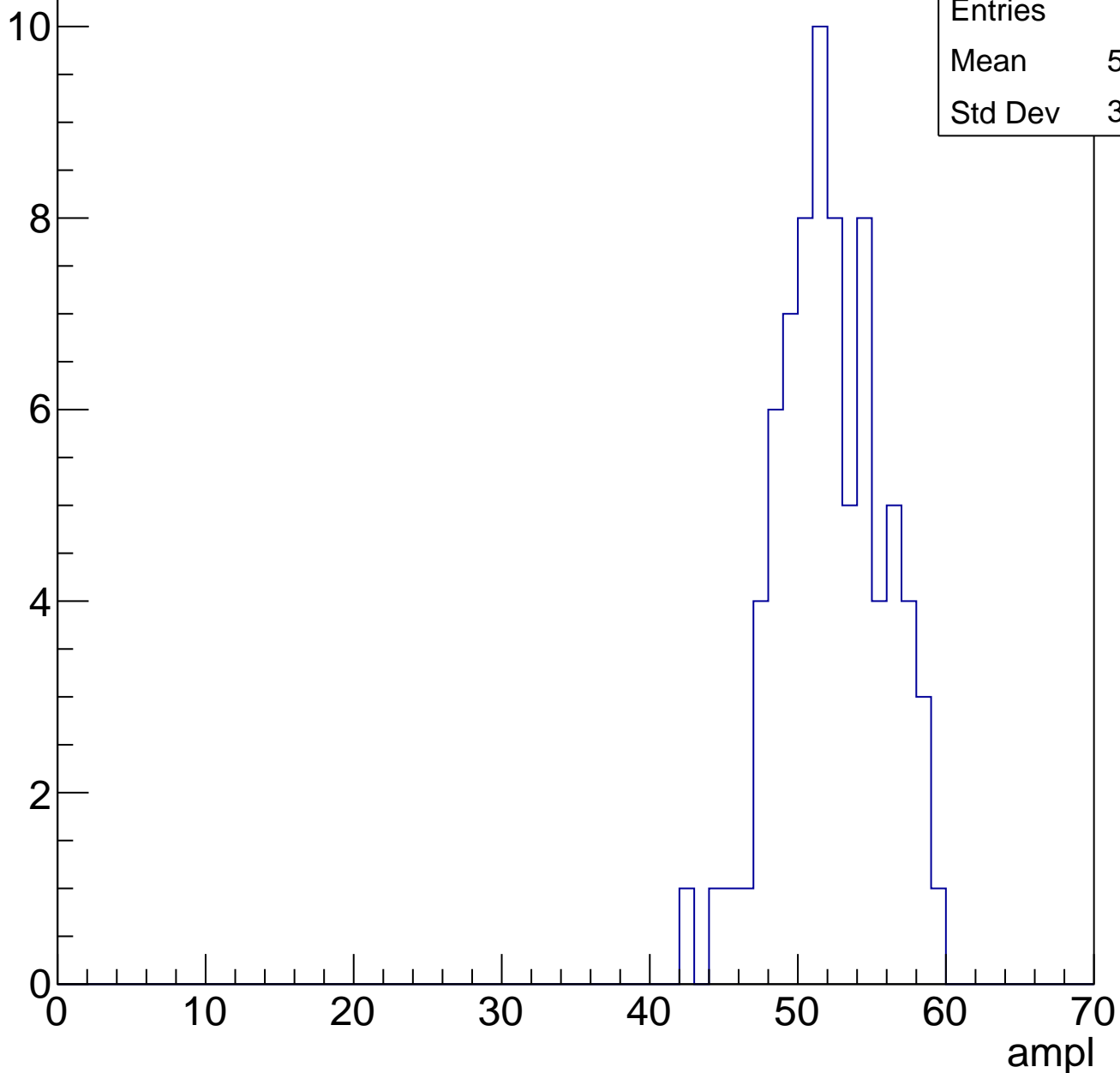


B0L102S, U20-ch23, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 51.69 |
| Std Dev | 3.517 |

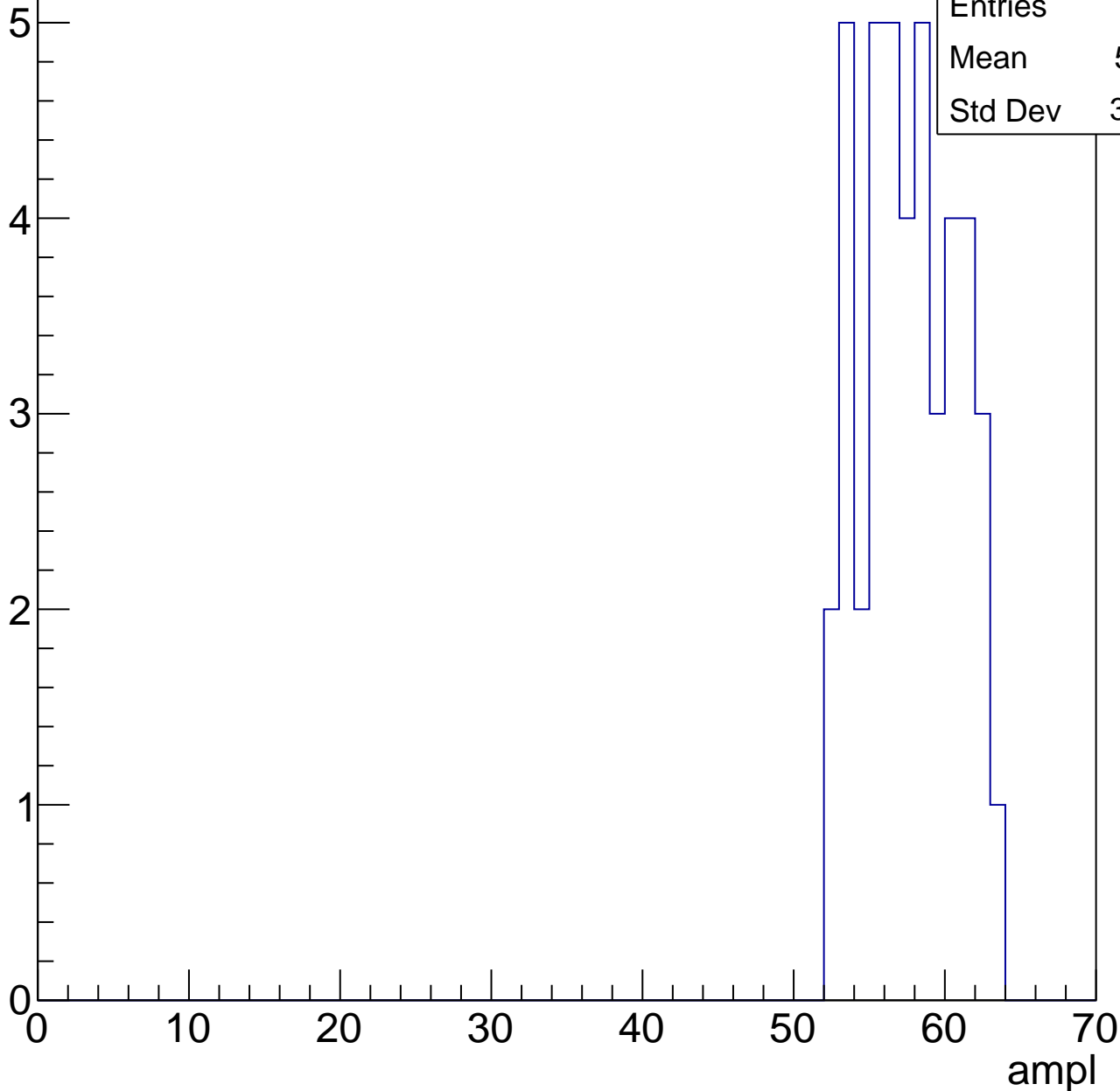


B0L102S, U20-ch23, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 57.21 |
| Std Dev | 3.047 |

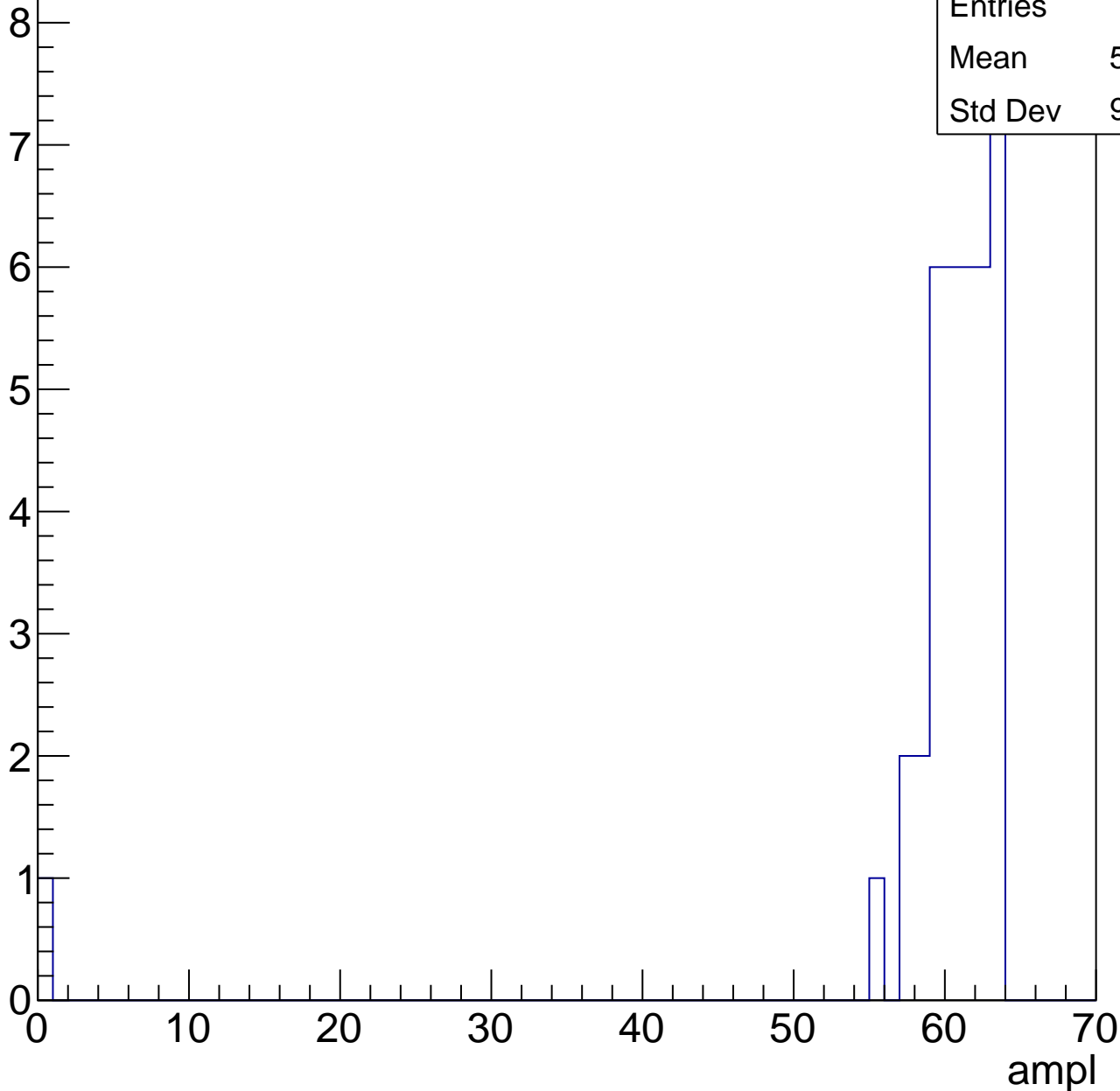


B0L102S, U20-ch23, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 58.97 |
| Std Dev | 9.893 |



B0L102S, U20-ch23, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch23, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch24, adc0

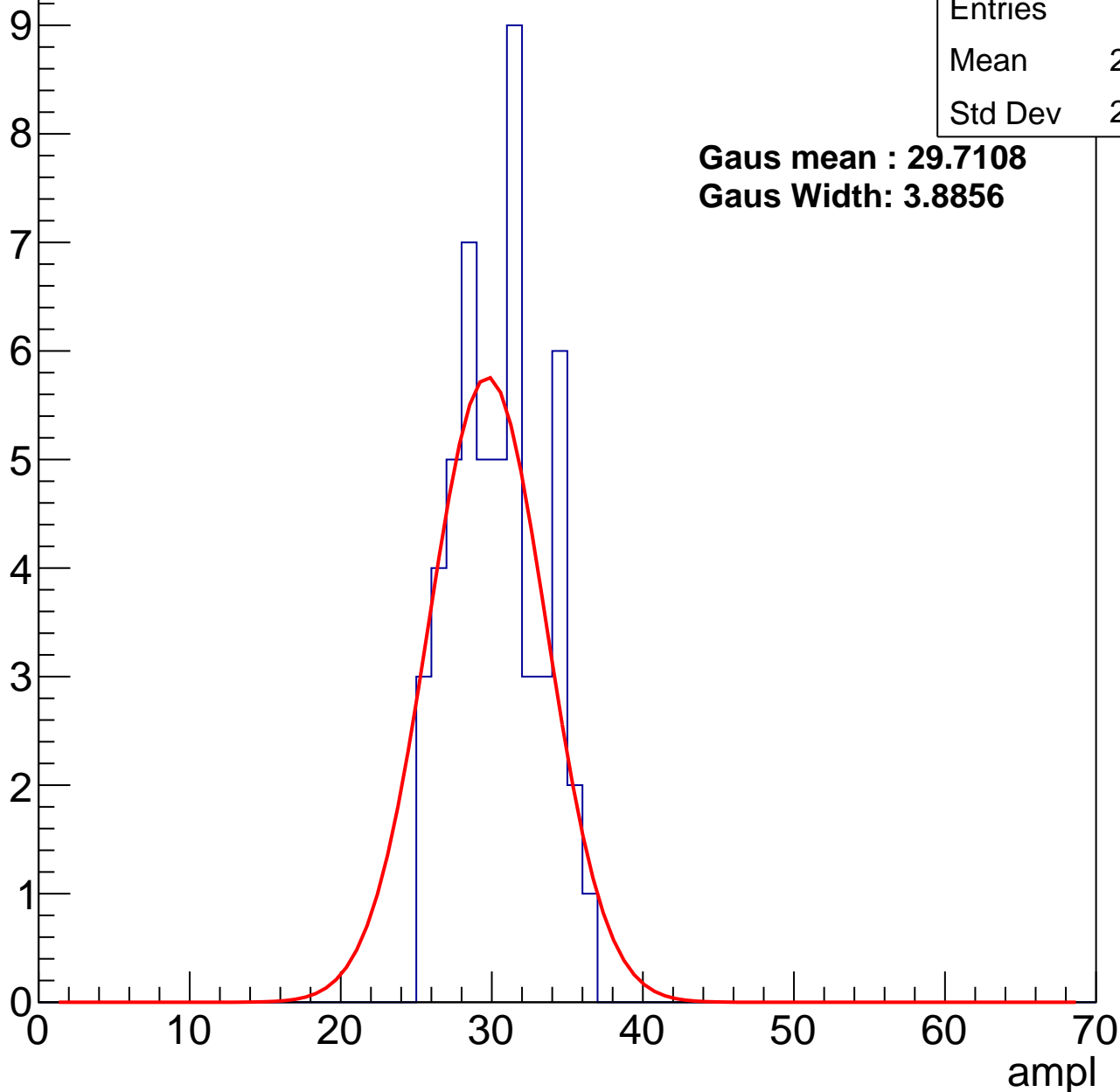
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.98 |
| Std Dev | 2.904 |

Gaus mean : 29.7108

Gaus Width: 3.8856



B0L102S, U20-ch24, adc1

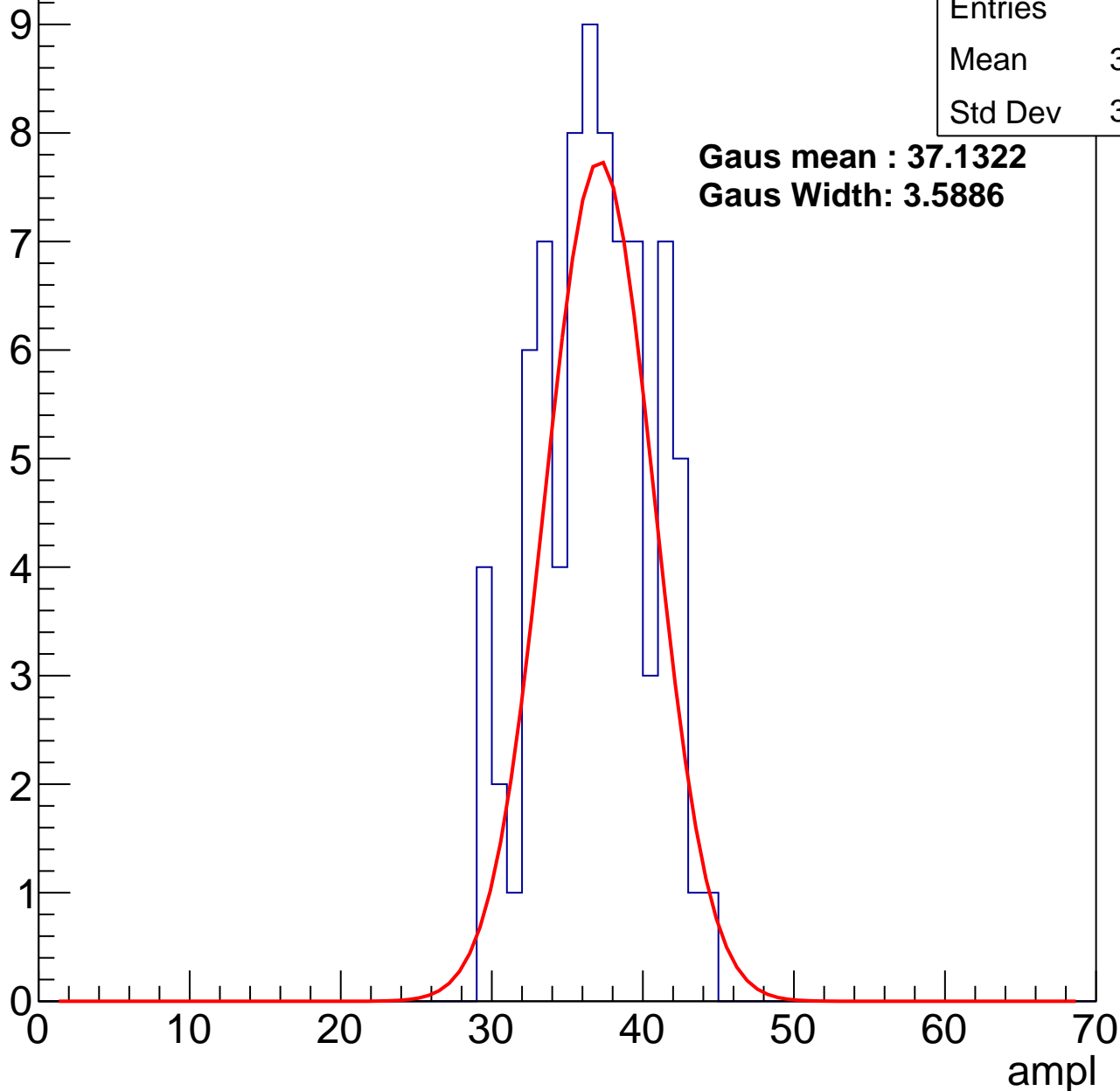
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.36 |
| Std Dev | 3.675 |

Gaus mean : 37.1322

Gaus Width: 3.5886



B0L102S, U20-ch24, adc2

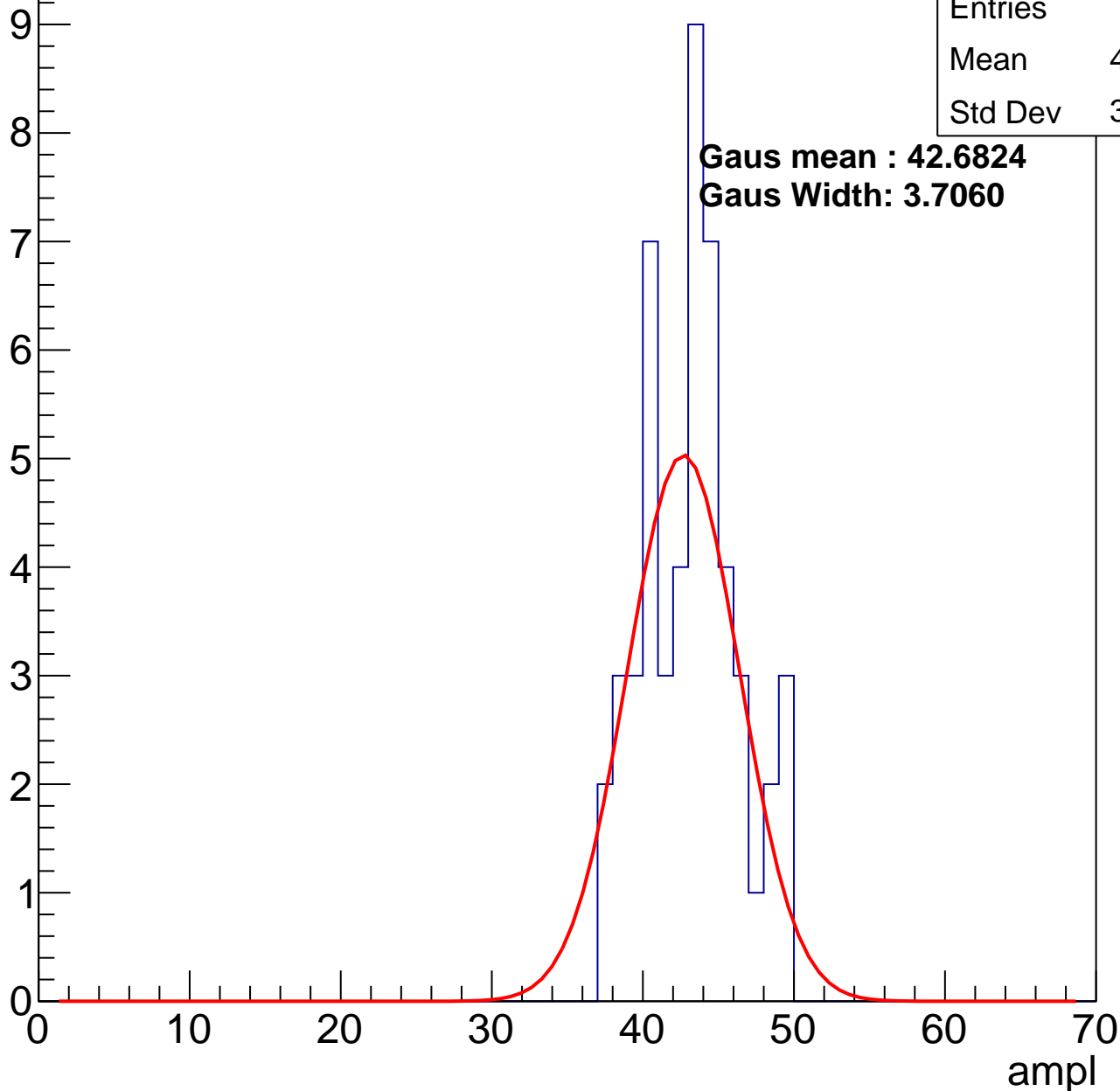
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 42.73 |
| Std Dev | 3.113 |

Gaus mean : 42.6824

Gaus Width: 3.7060

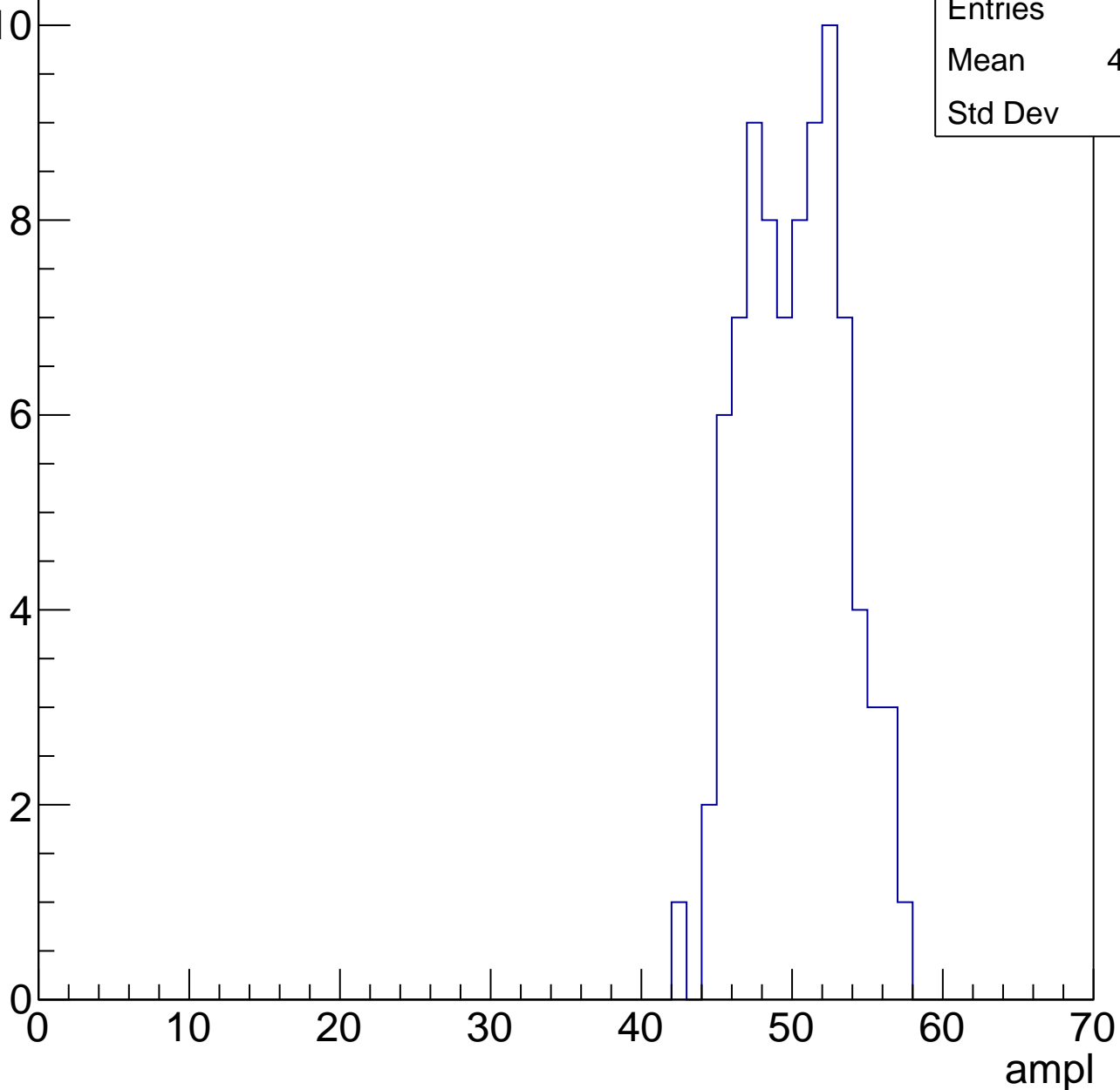


B0L102S, U20-ch24, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 49.74 |
| Std Dev | 3.29 |

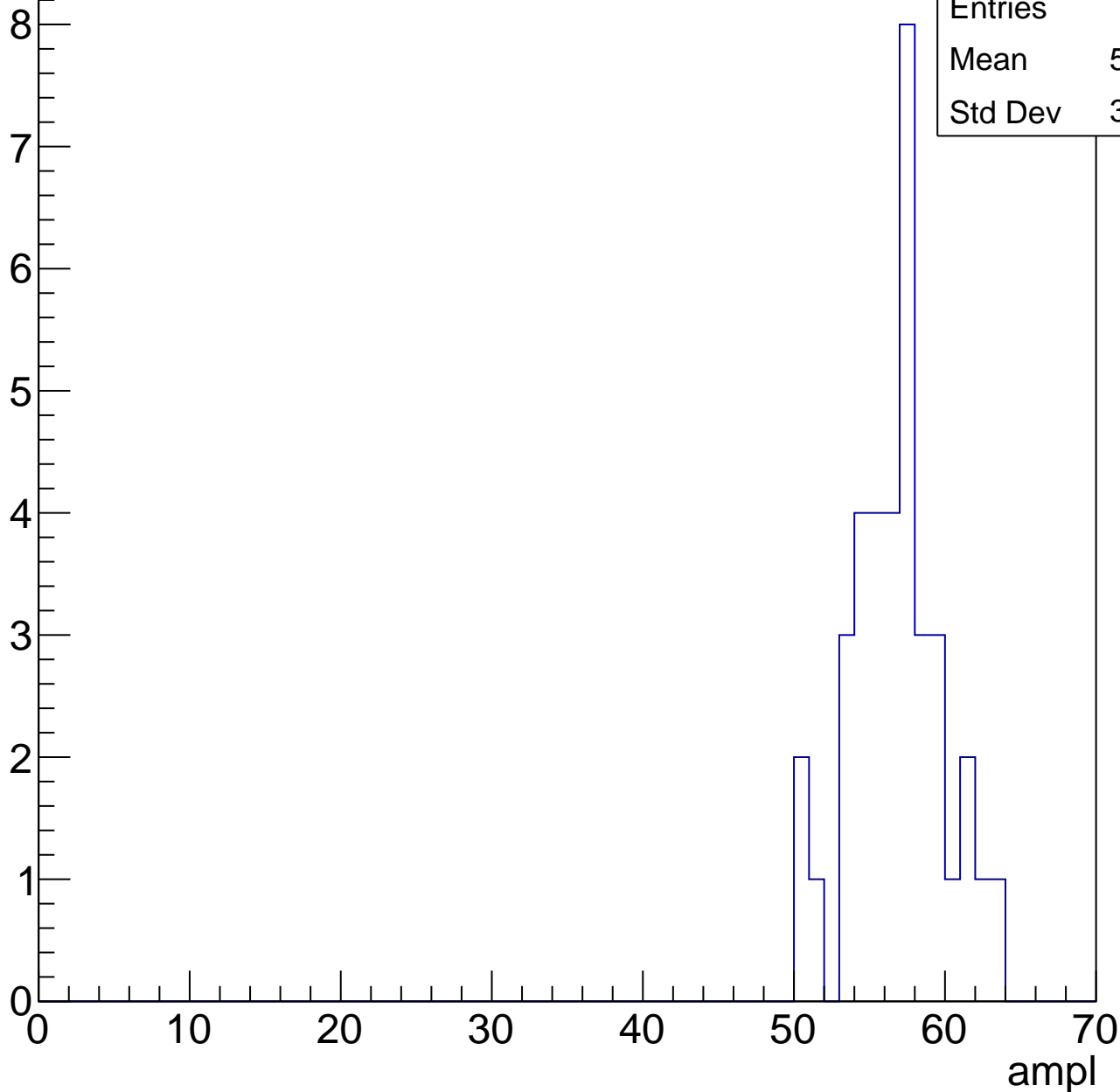


B0L102S, U20-ch24, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

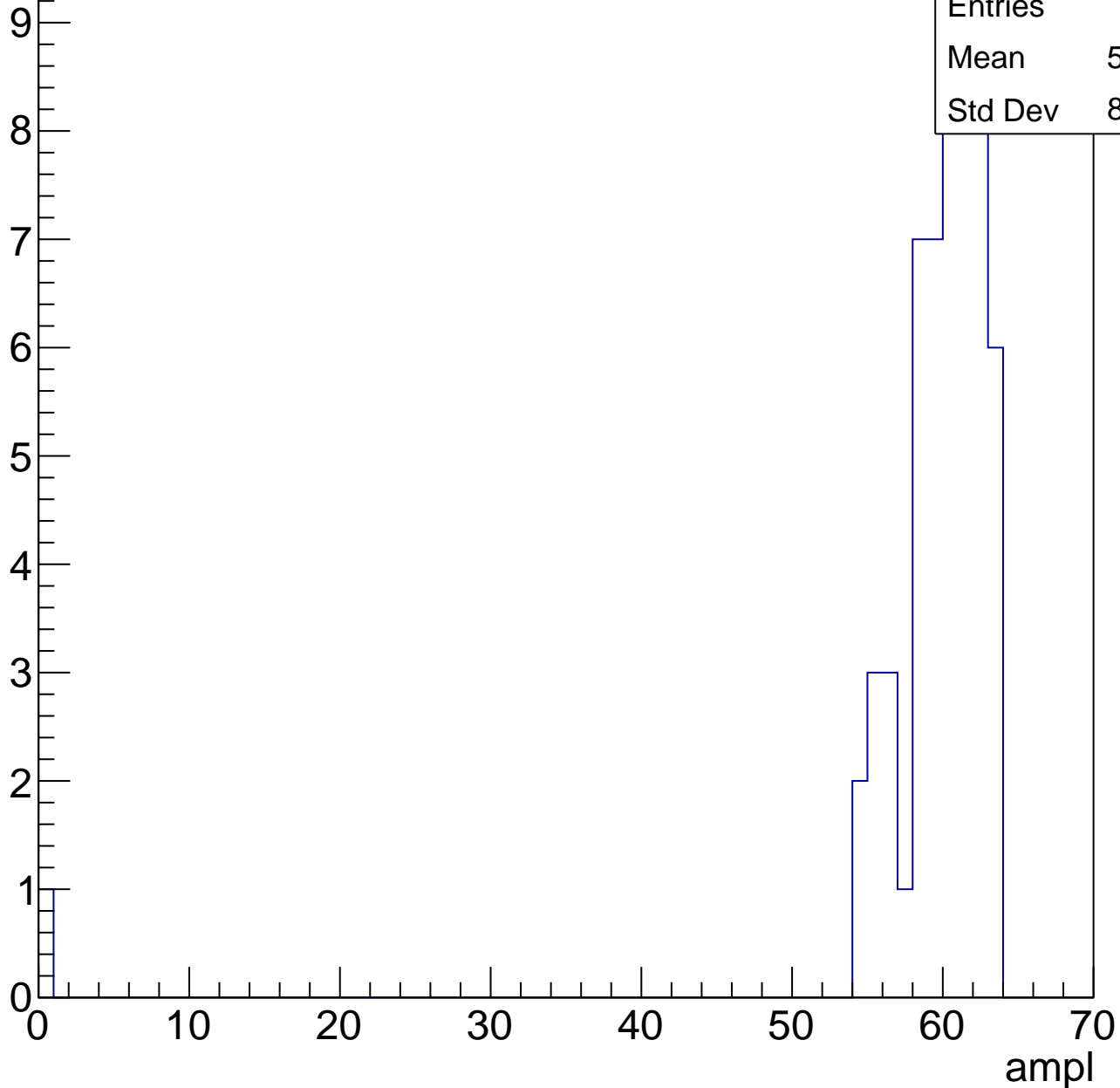
| | |
|---------|-------|
| Entries | 37 |
| Mean | 56.32 |
| Std Dev | 3.023 |



B0L102S, U20-ch24, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch24, adc6

calib_packv5_042523_0143.root, FC#12, port B1

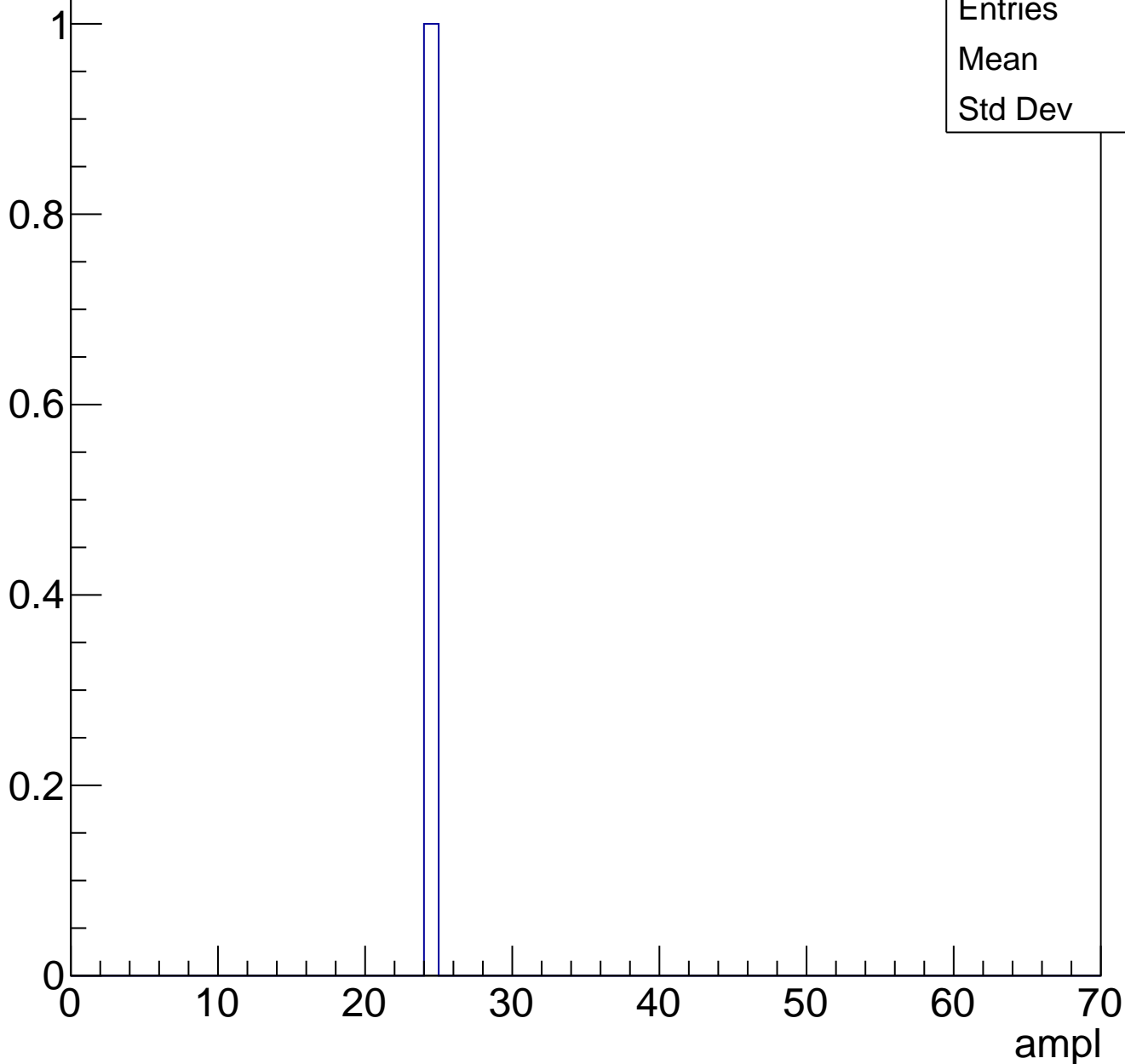
Entry



B0L102S, U20-ch24, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch25, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.51 |
| Std Dev | 3.015 |

Gaus mean : 29.7201

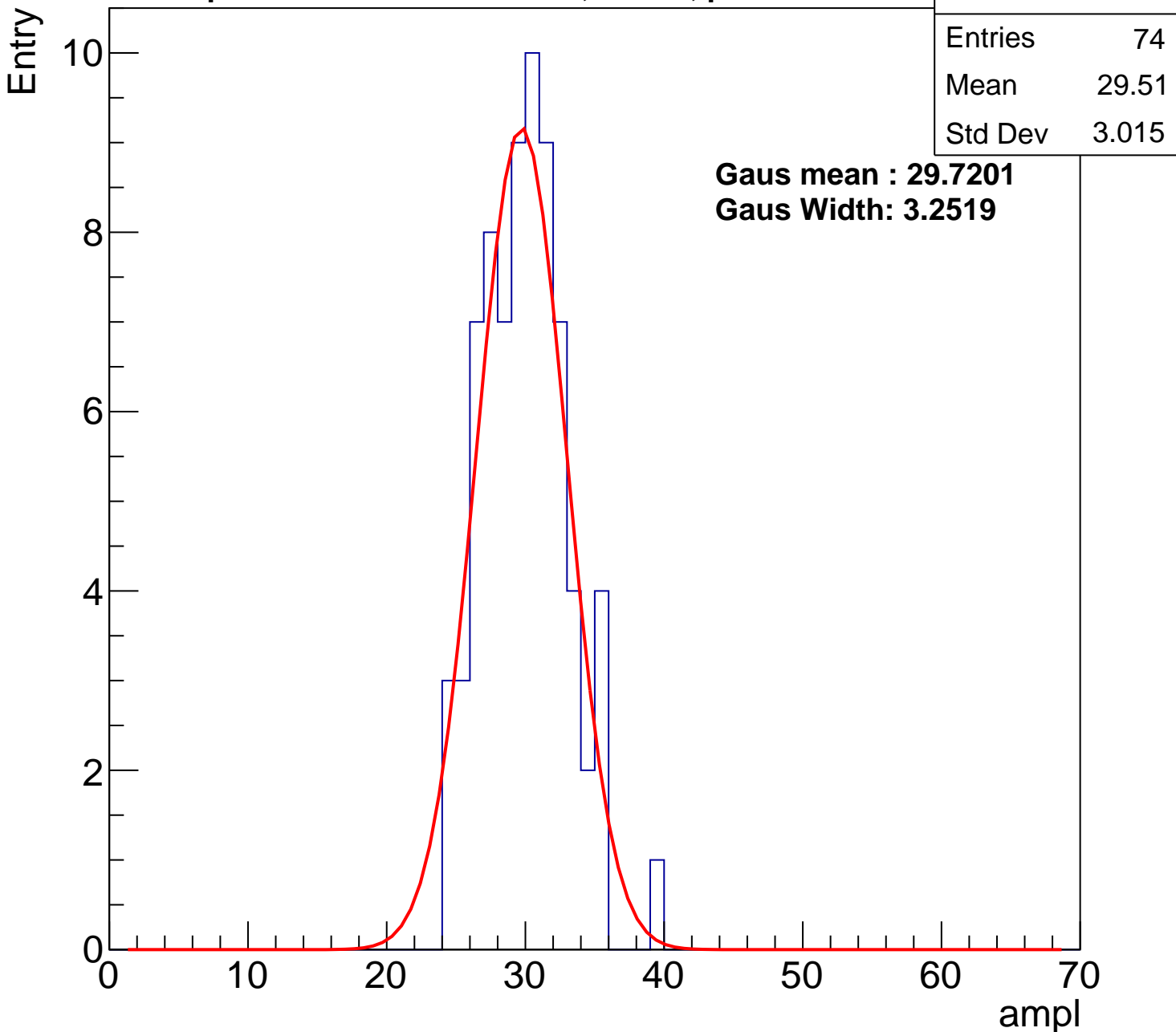
Gaus Width: 3.2519

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch25, adc1

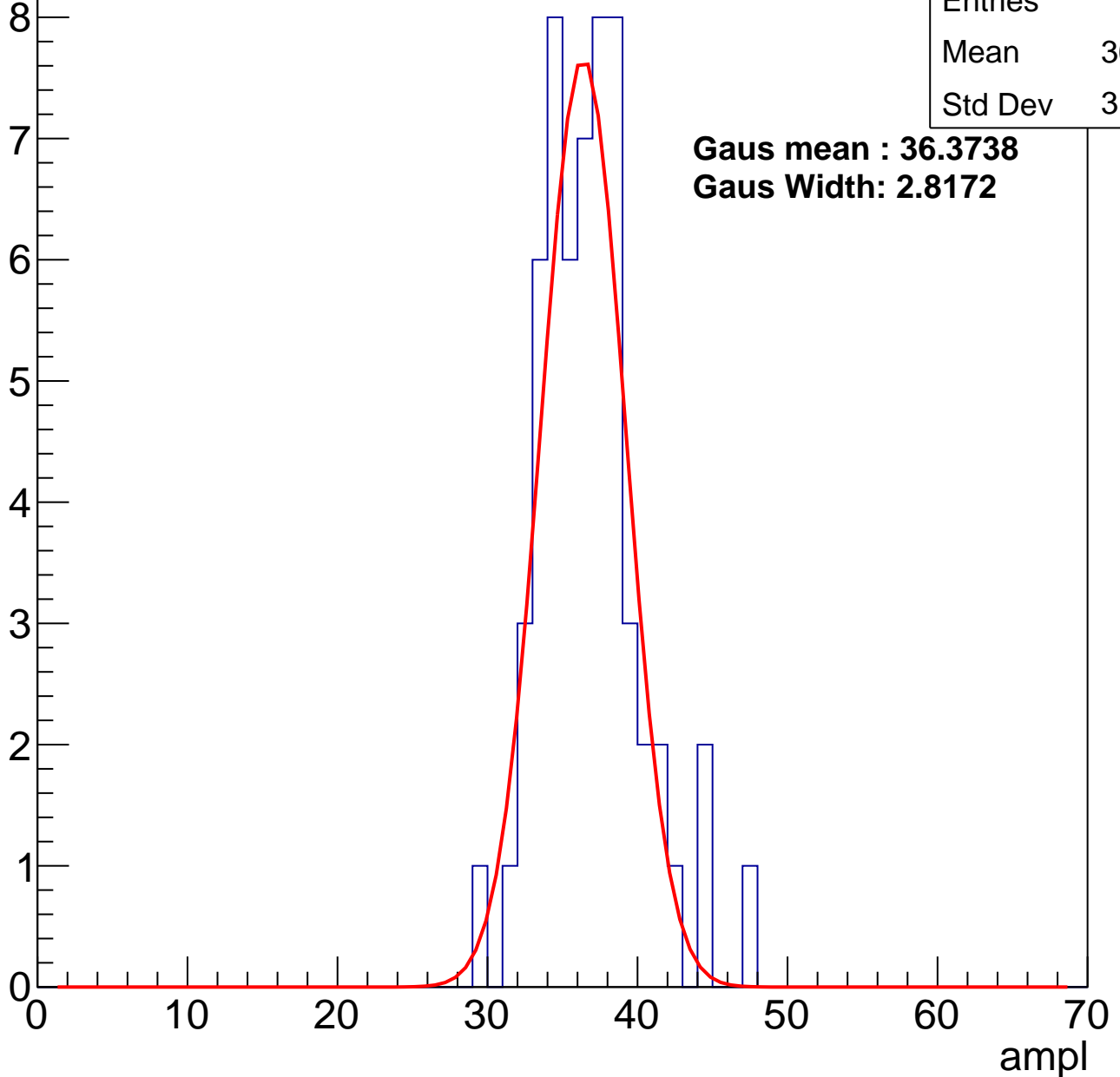
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 36.34 |
| Std Dev | 3.297 |

Gaus mean : 36.3738

Gaus Width: 2.8172



B0L102S, U20-ch25, adc2

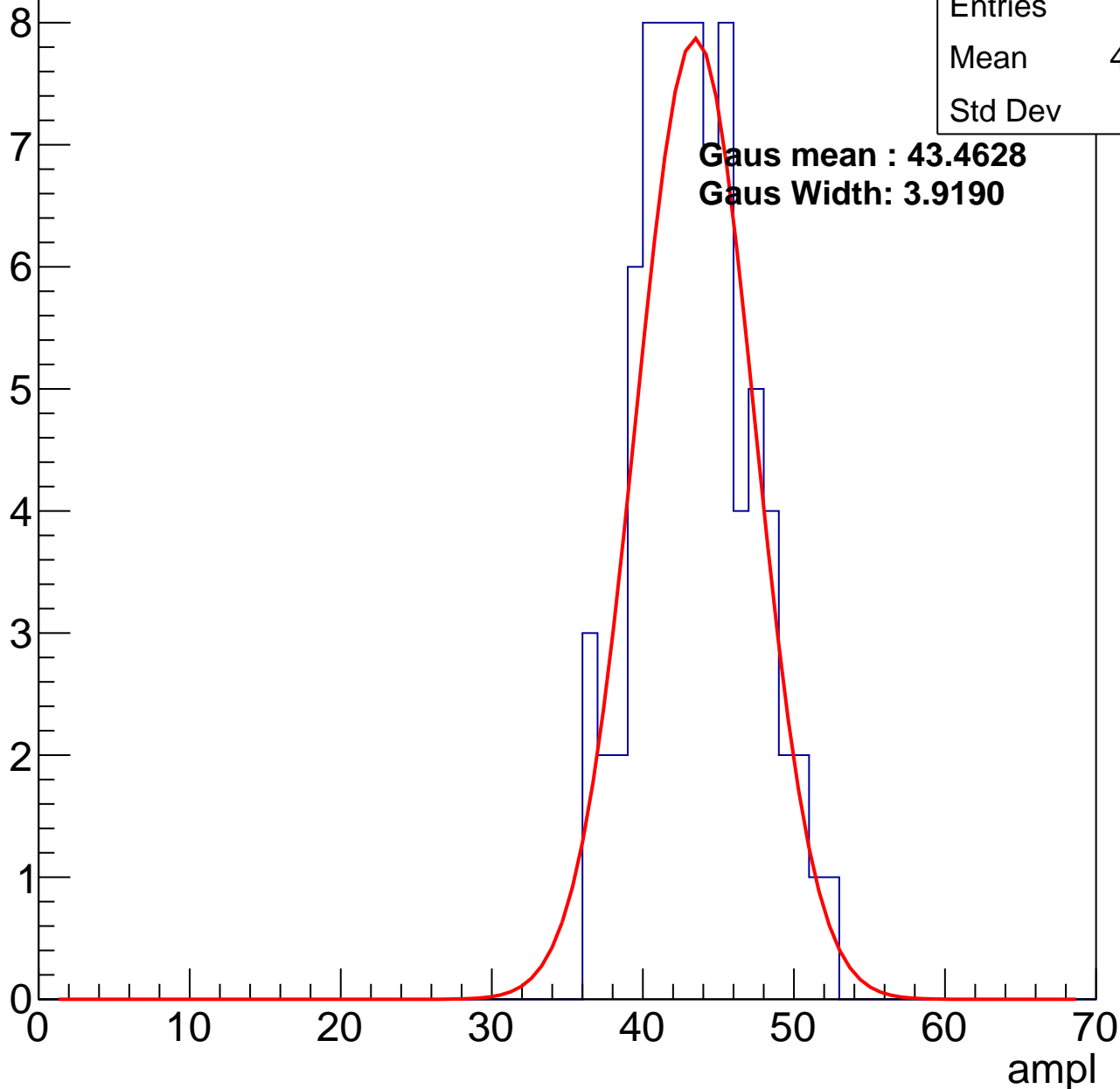
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 43.04 |
| Std Dev | 3.64 |

Gaus mean : 43.4628

Gaus Width: 3.9190

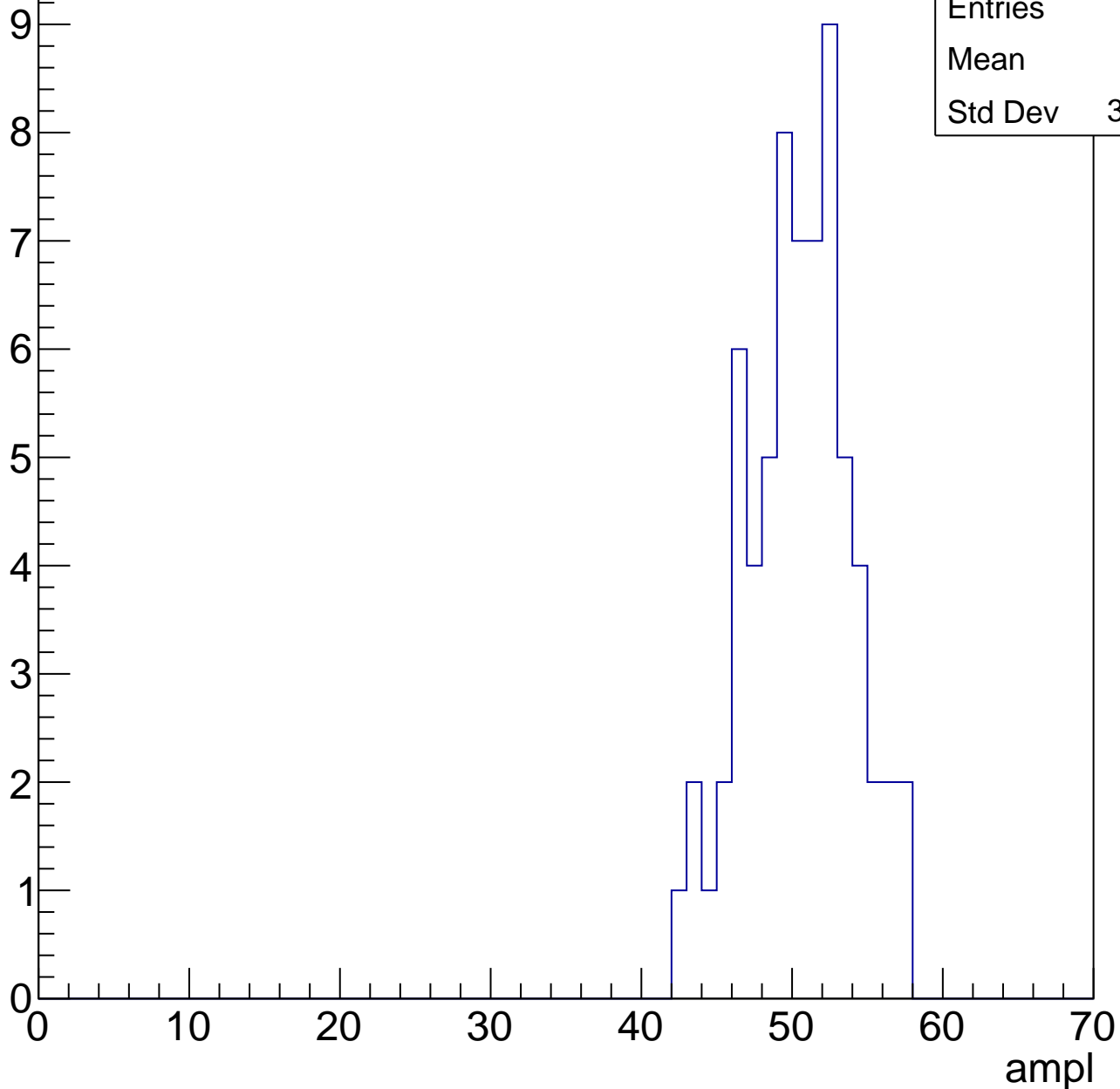


B0L102S, U20-ch25, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50 |
| Std Dev | 3.412 |

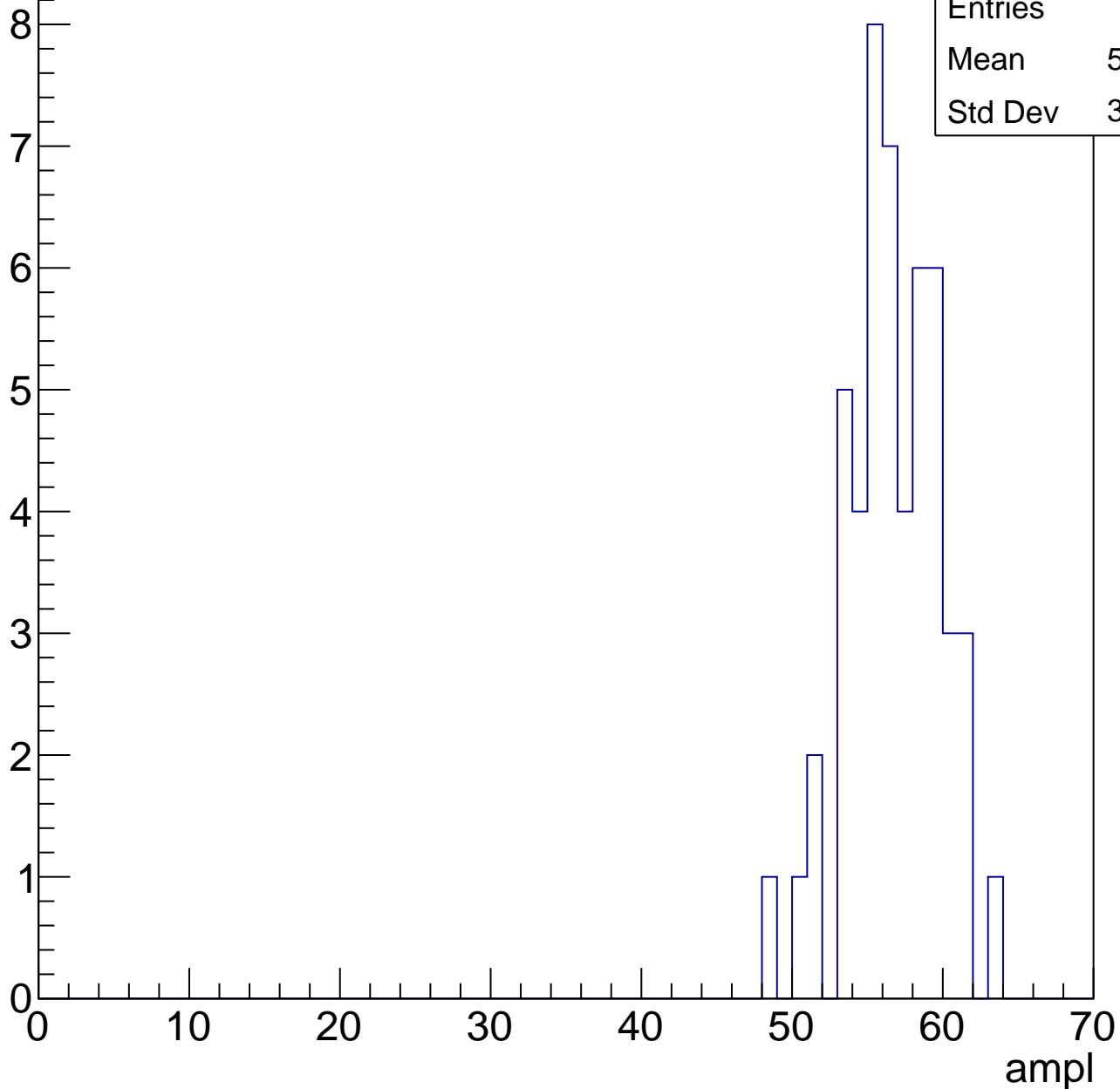


B0L102S, U20-ch25, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.25 |
| Std Dev | 3.035 |

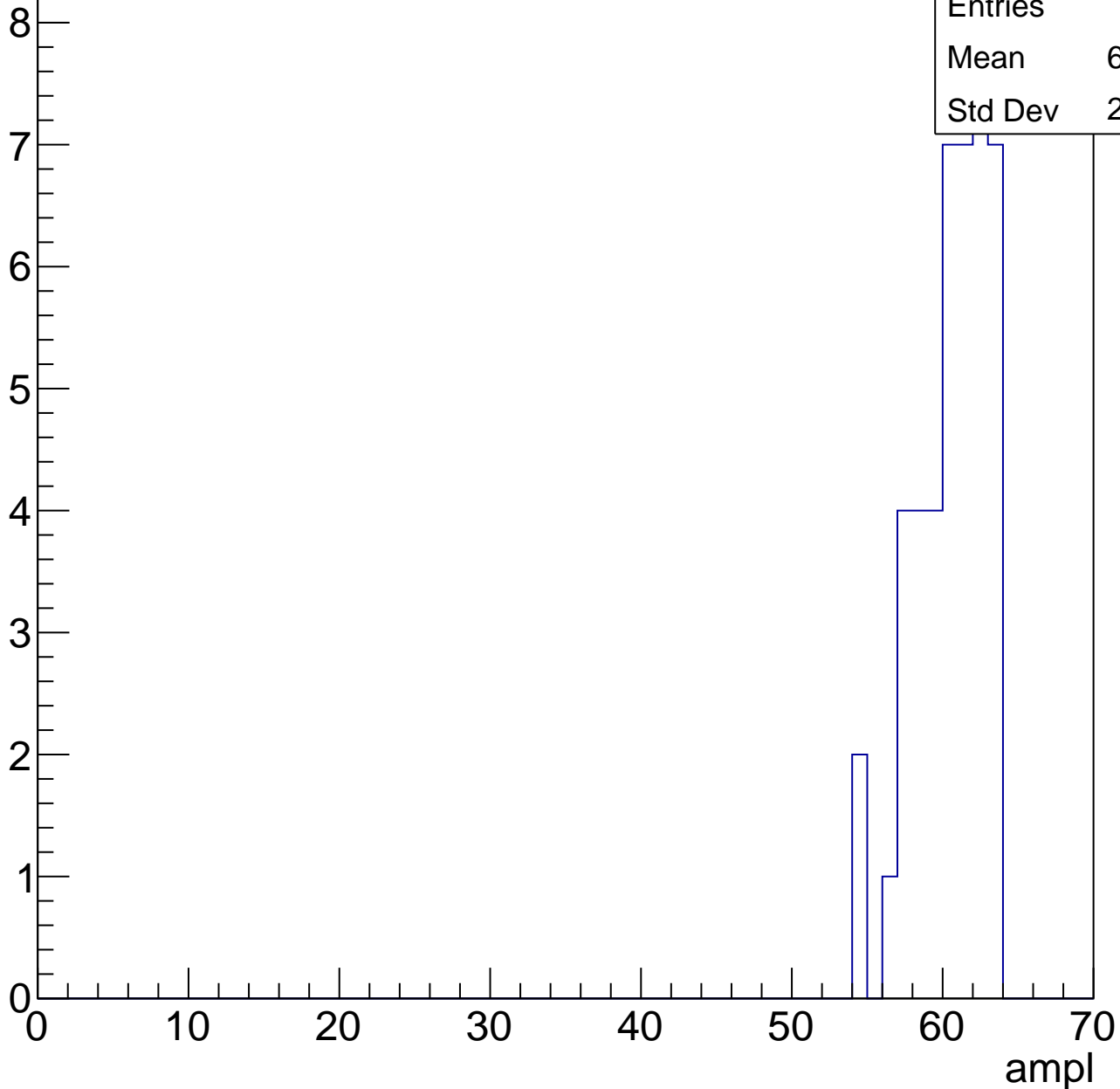


B0L102S, U20-ch25, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

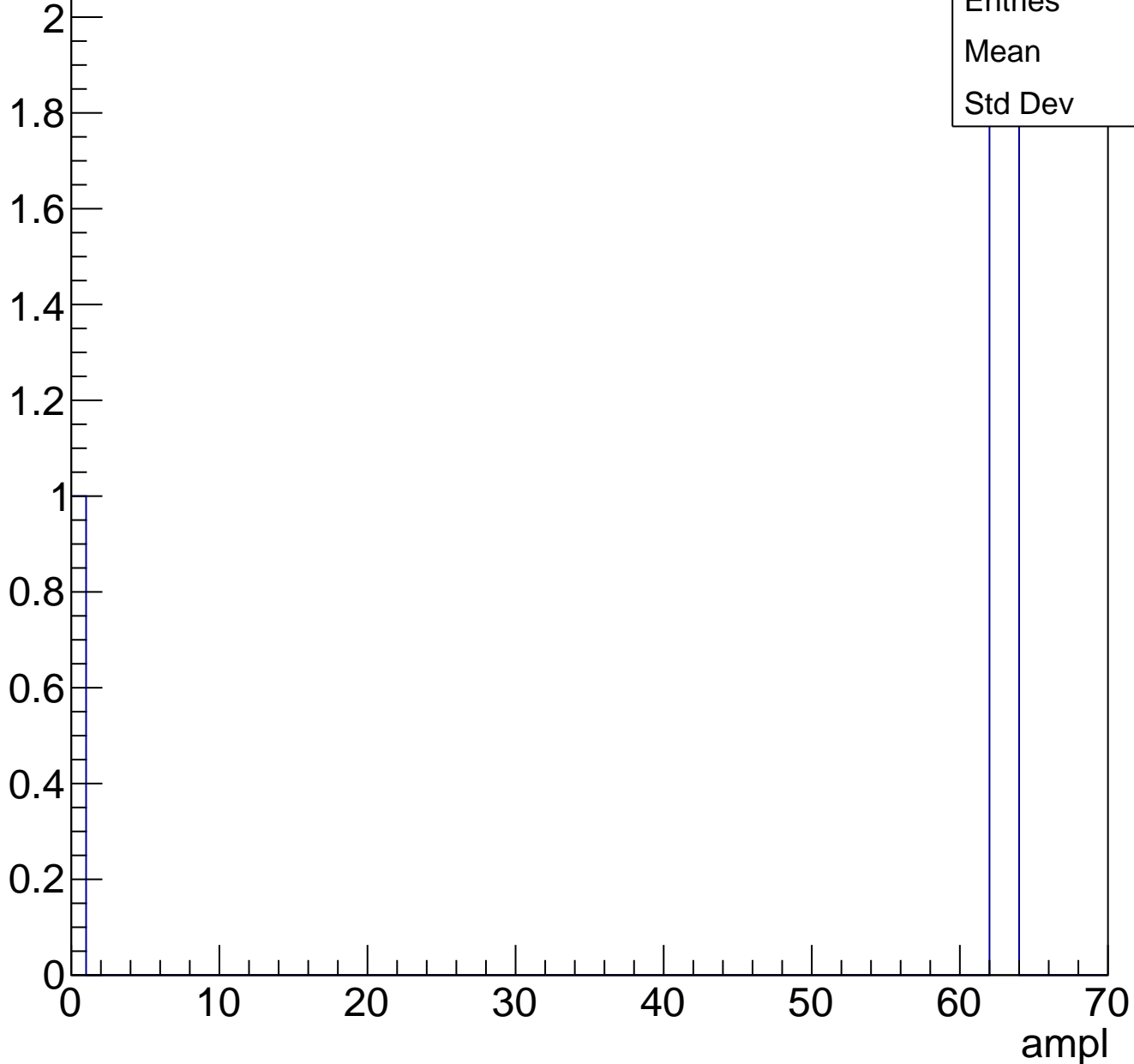
| | |
|---------|-------|
| Entries | 44 |
| Mean | 60.09 |
| Std Dev | 2.363 |



B0L102S, U20-ch25, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch25, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch26, adc0

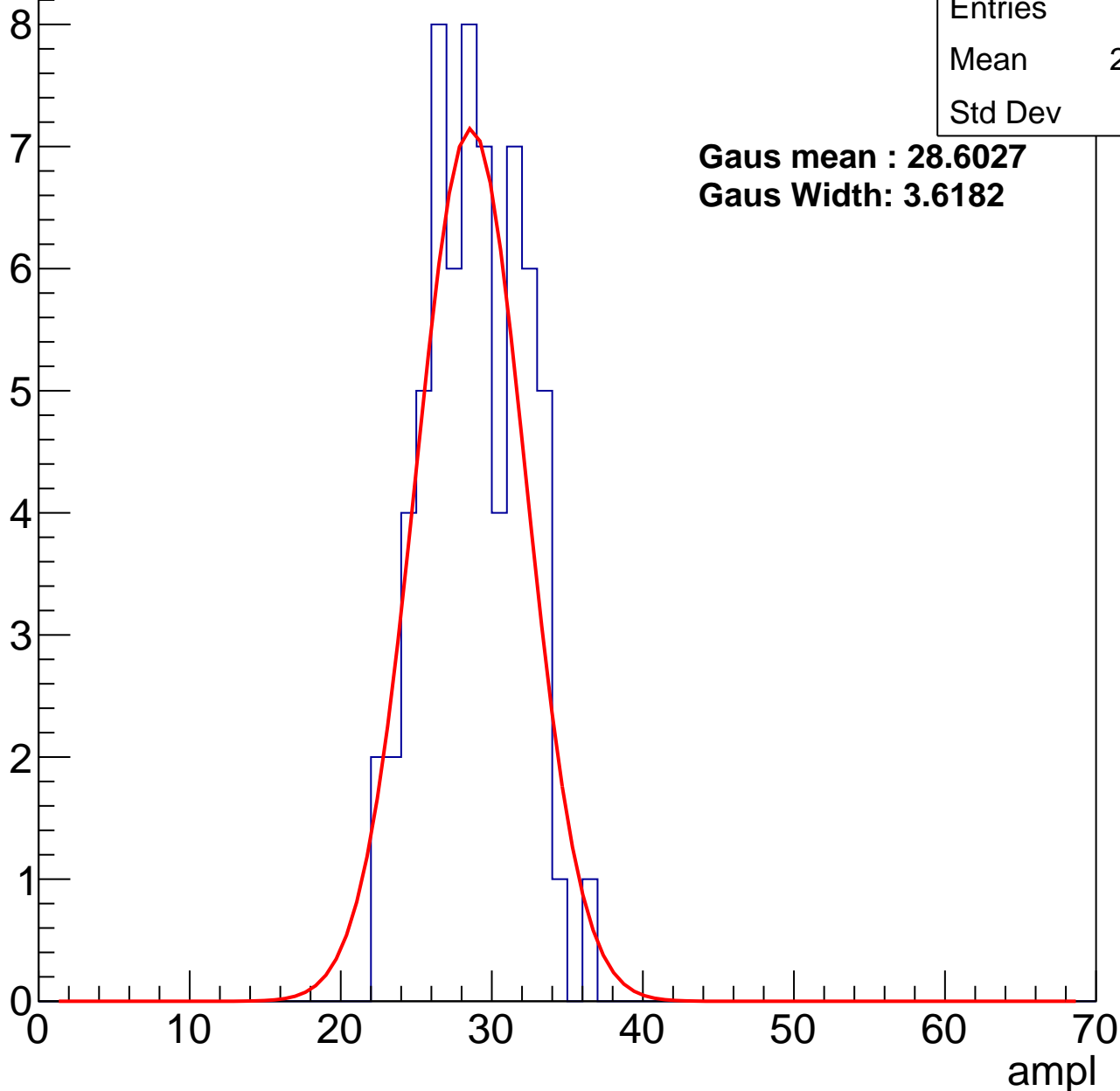
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.36 |
| Std Dev | 3.18 |

Gaus mean : 28.6027

Gaus Width: 3.6182



B0L102S, U20-ch26, adc1

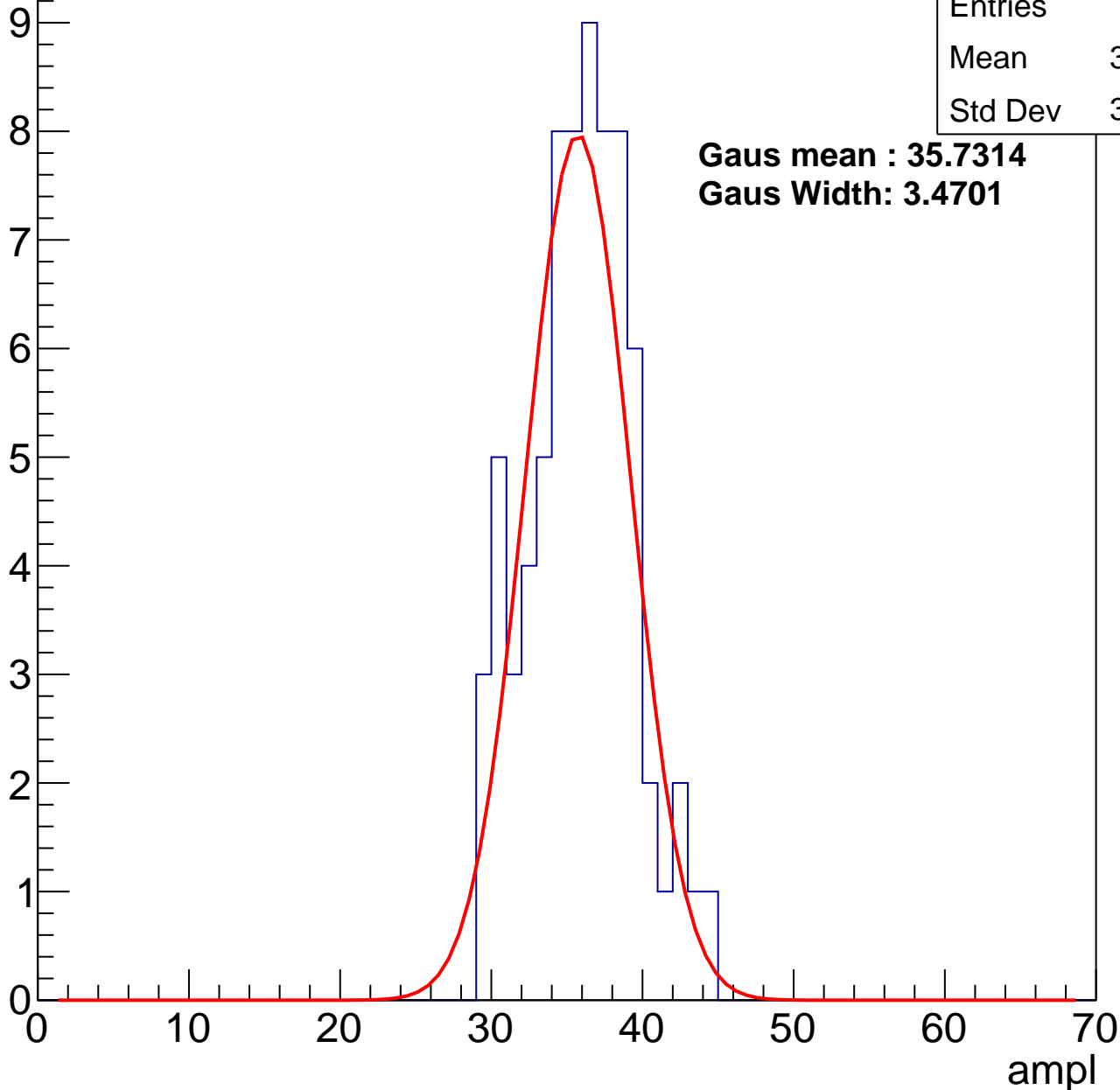
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.47 |
| Std Dev | 3.422 |

Gaus mean : 35.7314

Gaus Width: 3.4701



B0L102S, U20-ch26, adc2

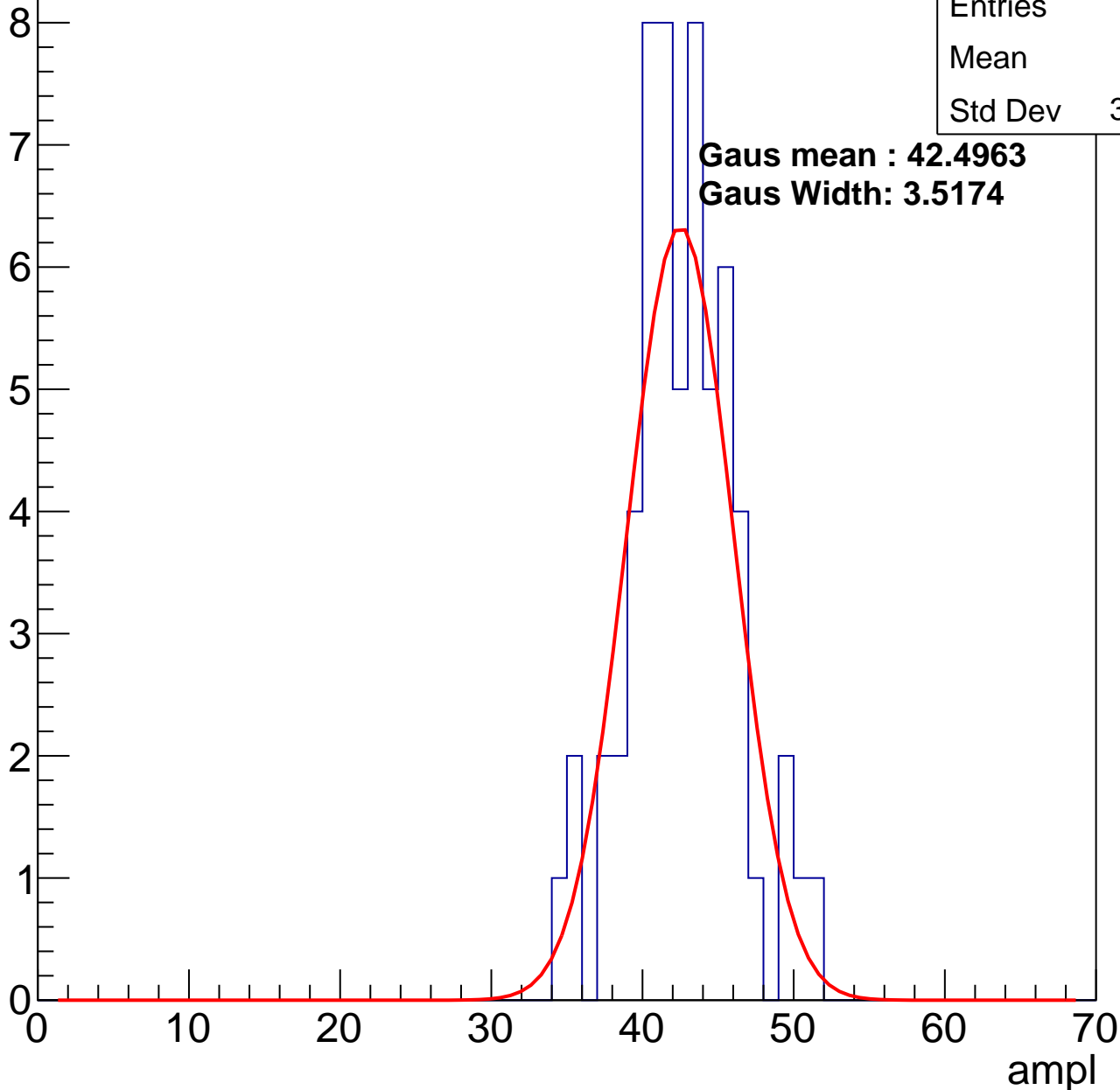
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.2 |
| Std Dev | 3.492 |

Gaus mean : 42.4963

Gaus Width: 3.5174

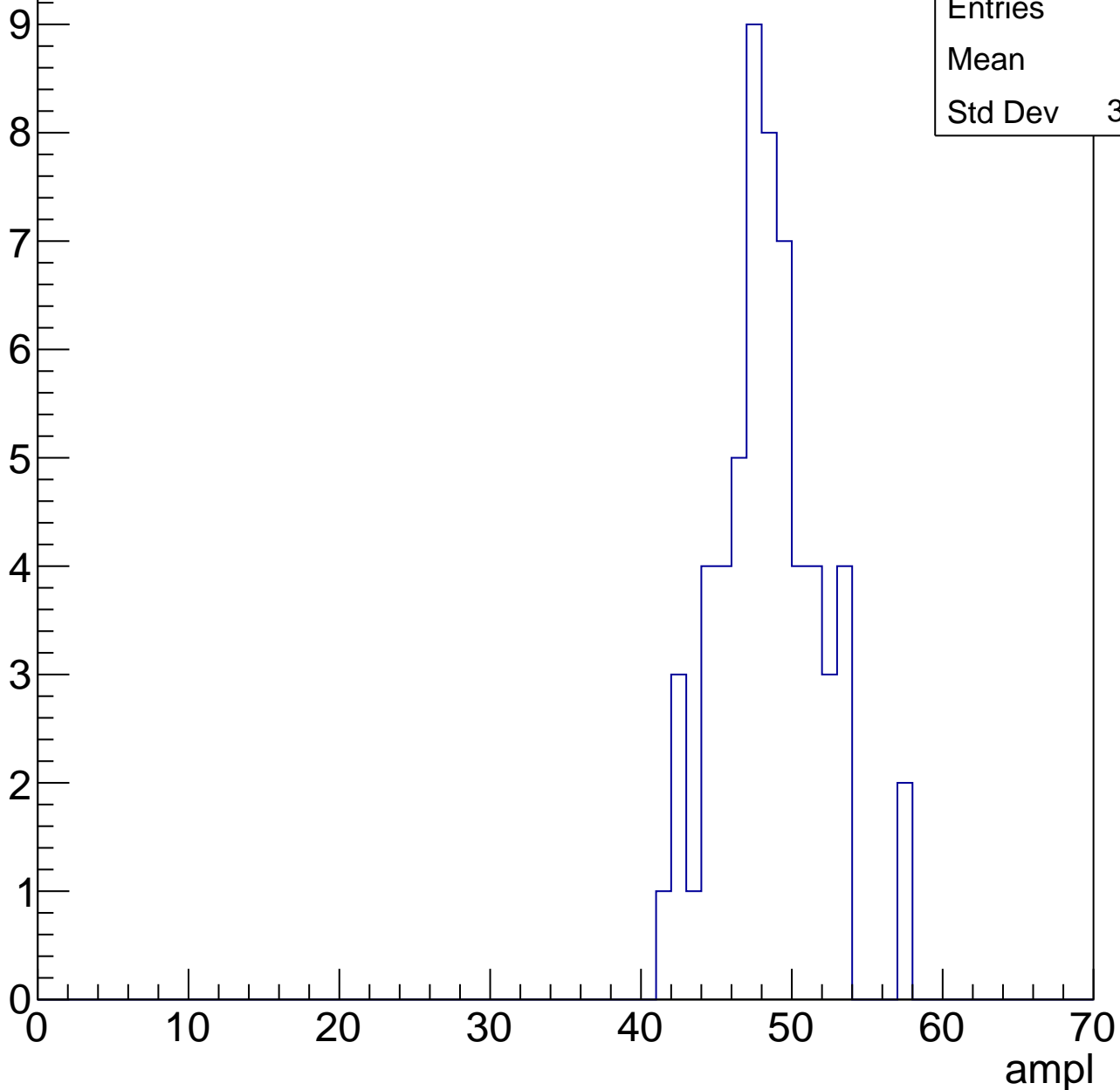


B0L102S, U20-ch26, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 48 |
| Std Dev | 3.395 |

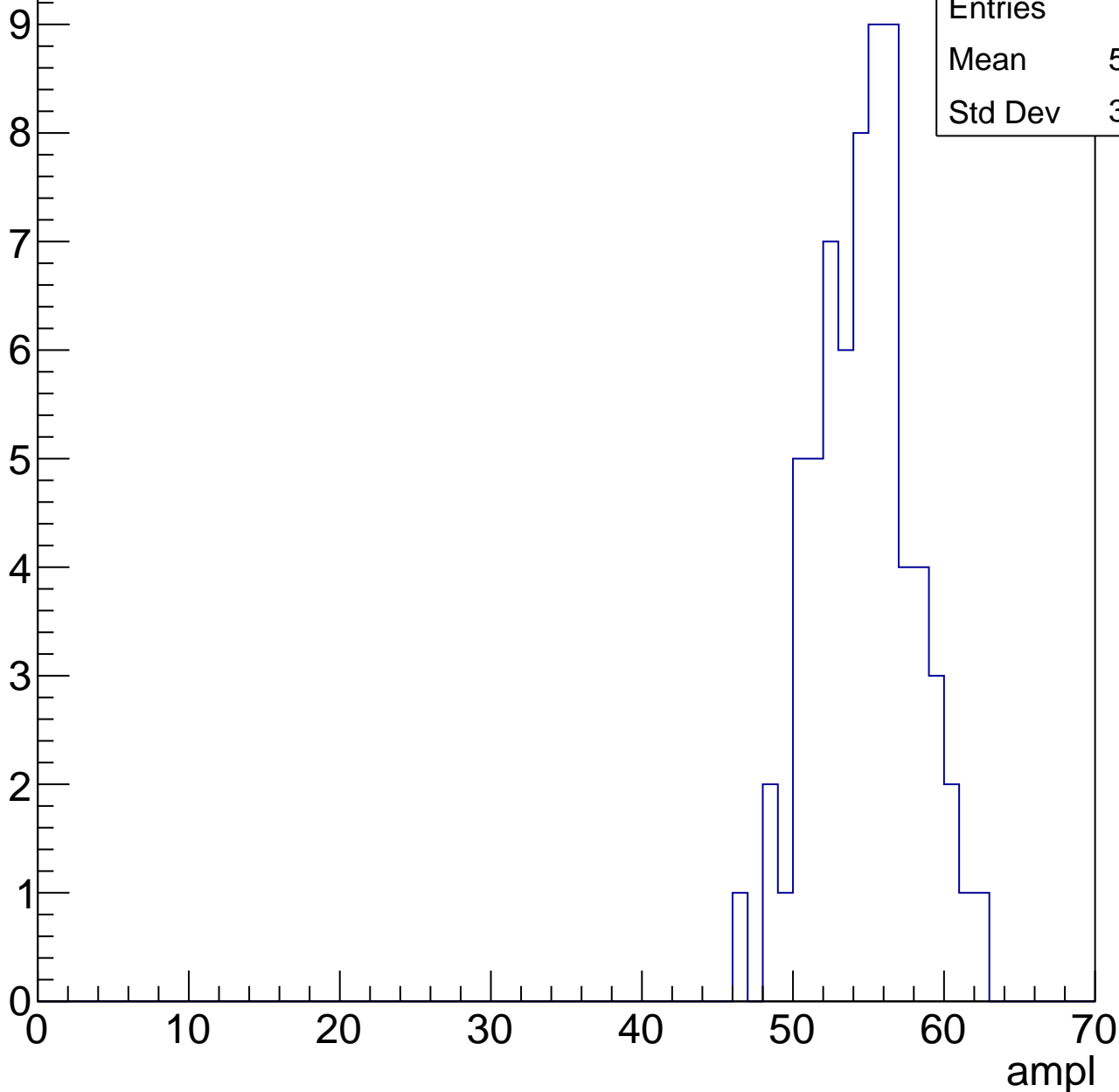


B0L102S, U20-ch26, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 54.25 |
| Std Dev | 3.256 |

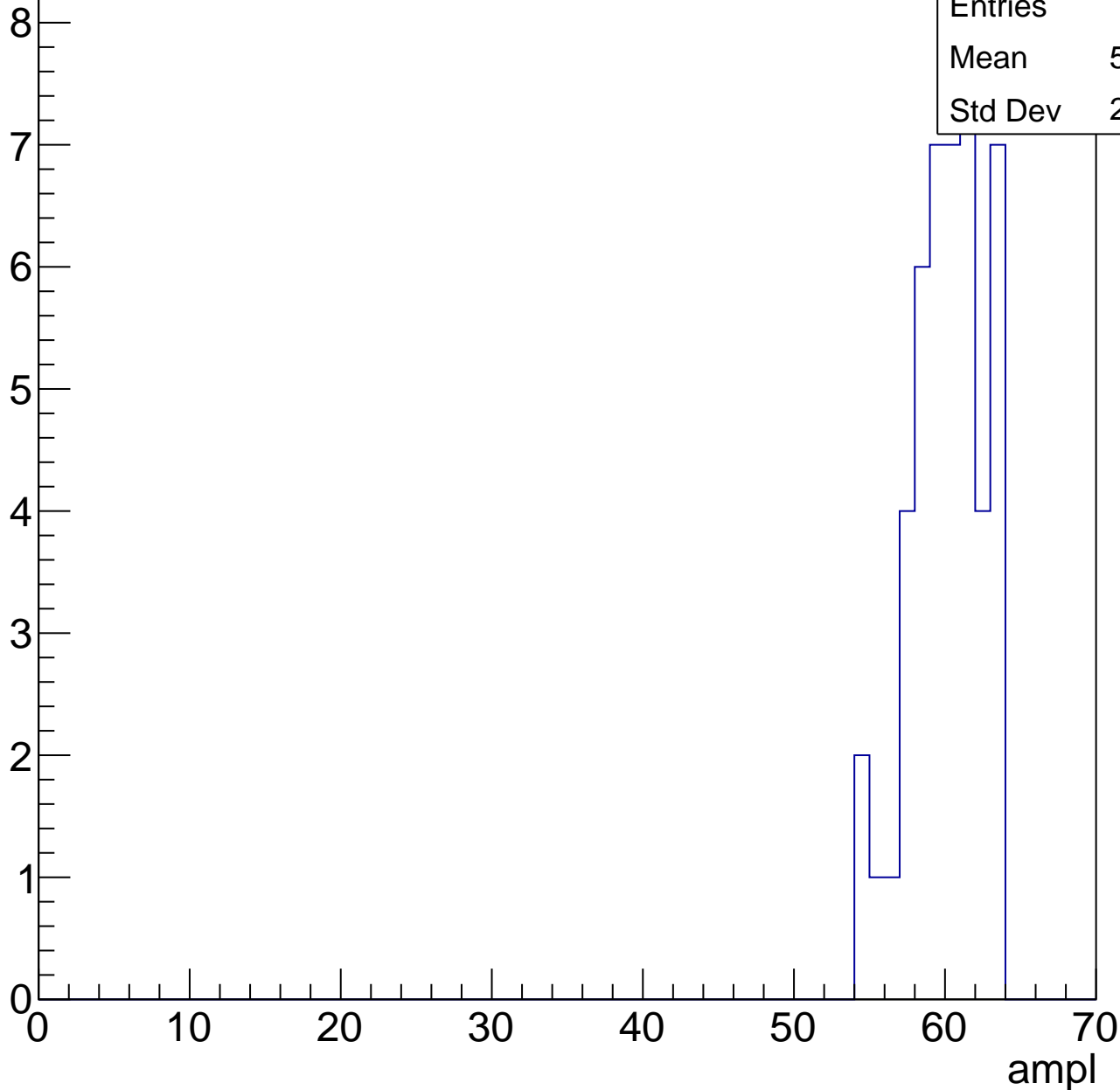


B0L102S, U20-ch26, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 59.68 |
| Std Dev | 2.362 |

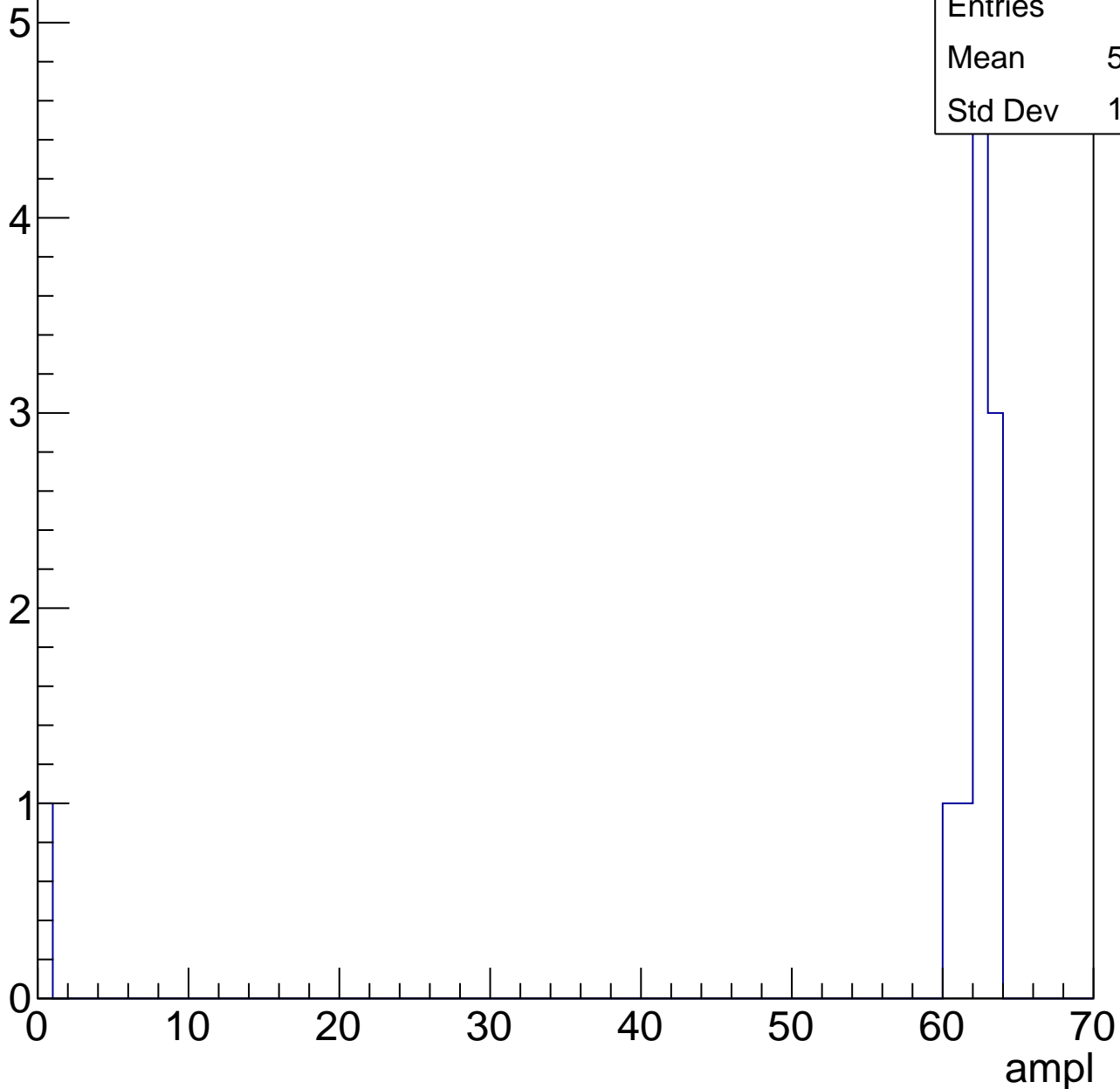


B0L102S, U20-ch26, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.36 |
| Std Dev | 17.84 |



B0L102S, U20-ch26, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch27, adc0

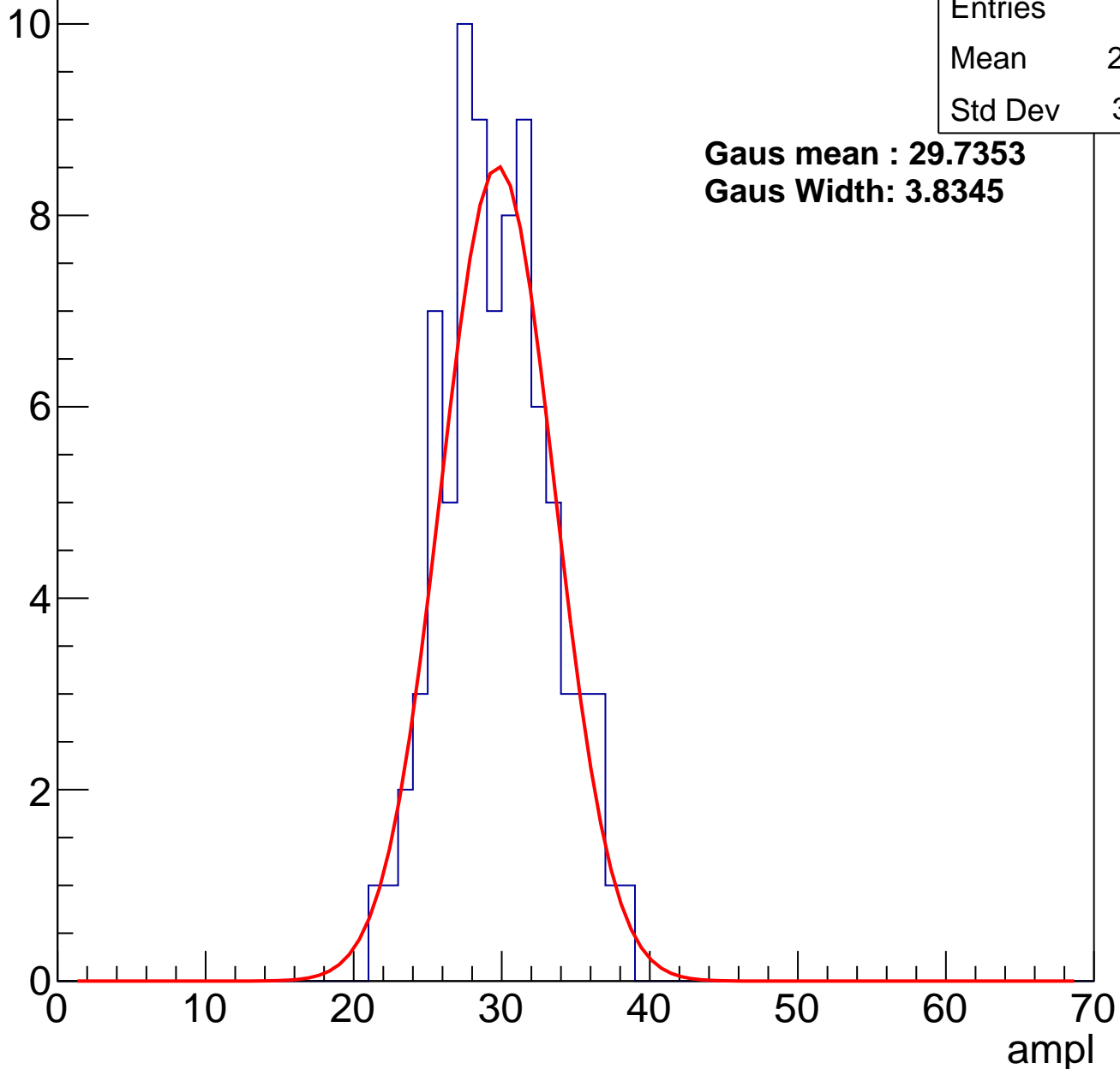
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 84 |
| Mean | 29.25 |
| Std Dev | 3.641 |

Gaus mean : 29.7353

Gaus Width: 3.8345

Entry



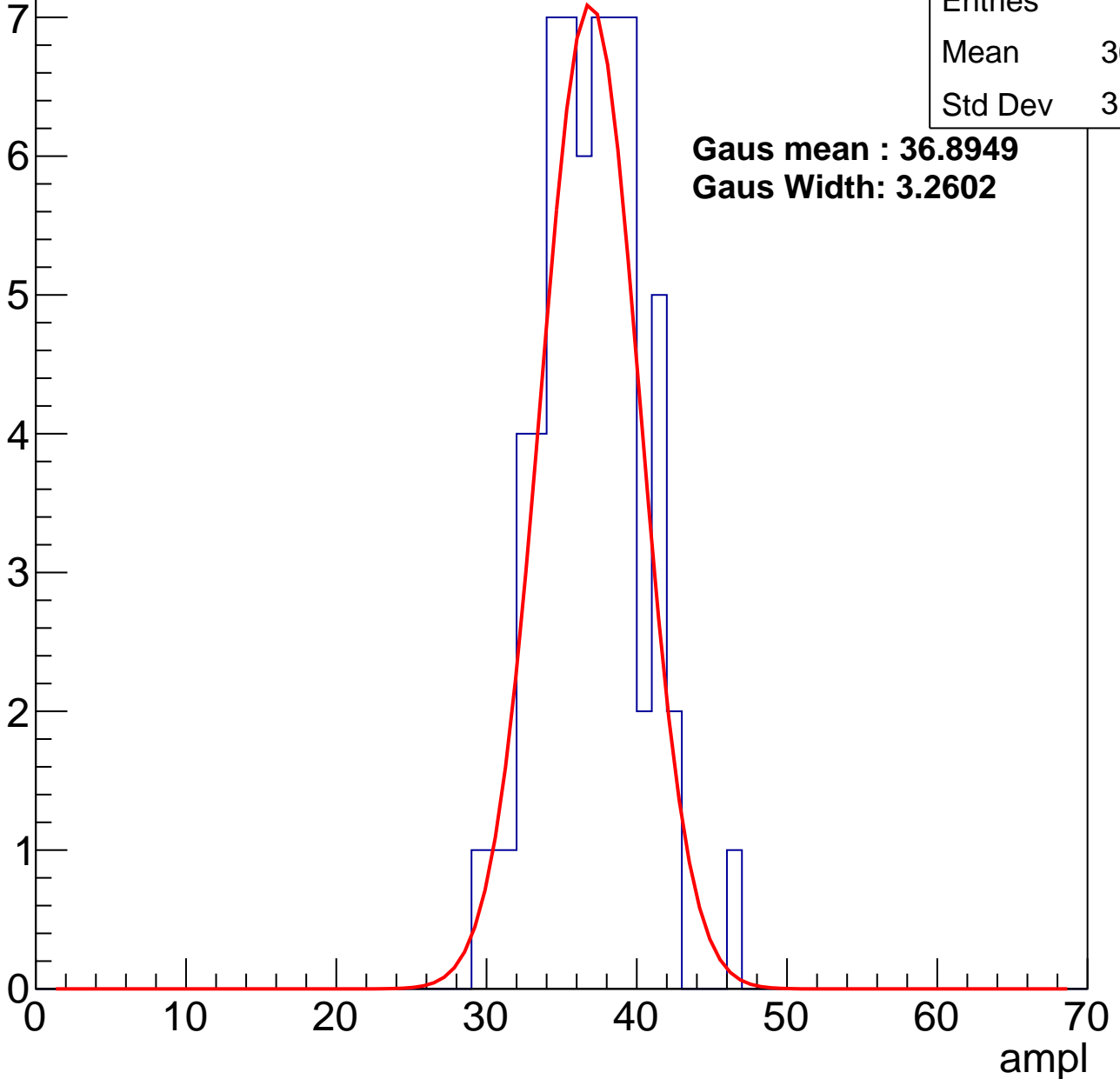
B0L102S, U20-ch27, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 36.48 |
| Std Dev | 3.256 |

Gaus mean : 36.8949
Gaus Width: 3.2602



B0L102S, U20-ch27, adc2

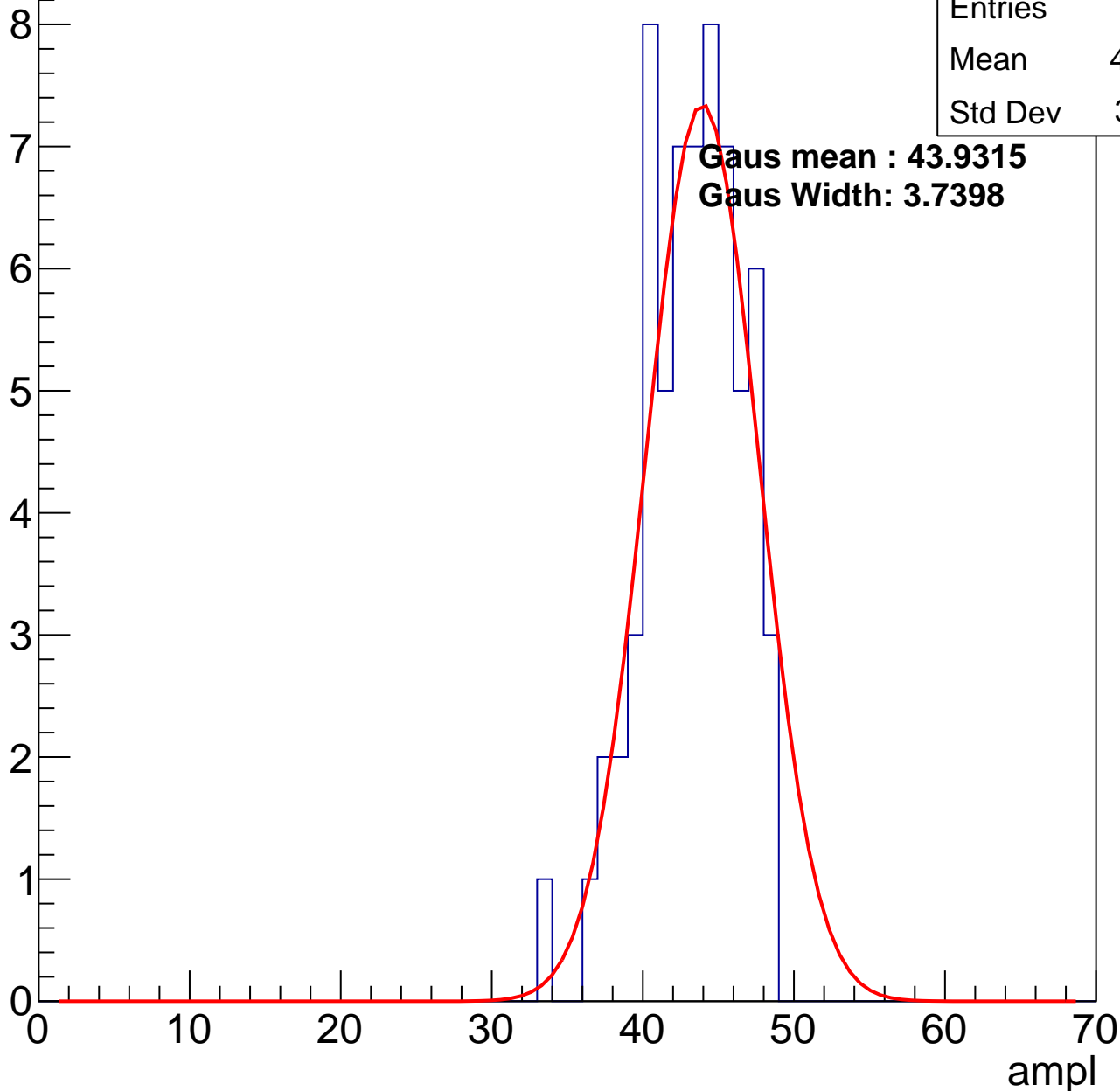
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 42.75 |
| Std Dev | 3.201 |

Gaus mean : 43.9315

Gaus Width: 3.7398



B0L102S, U20-ch27, adc3

calib_packv5_042523_0143.root, FC#12, port B1

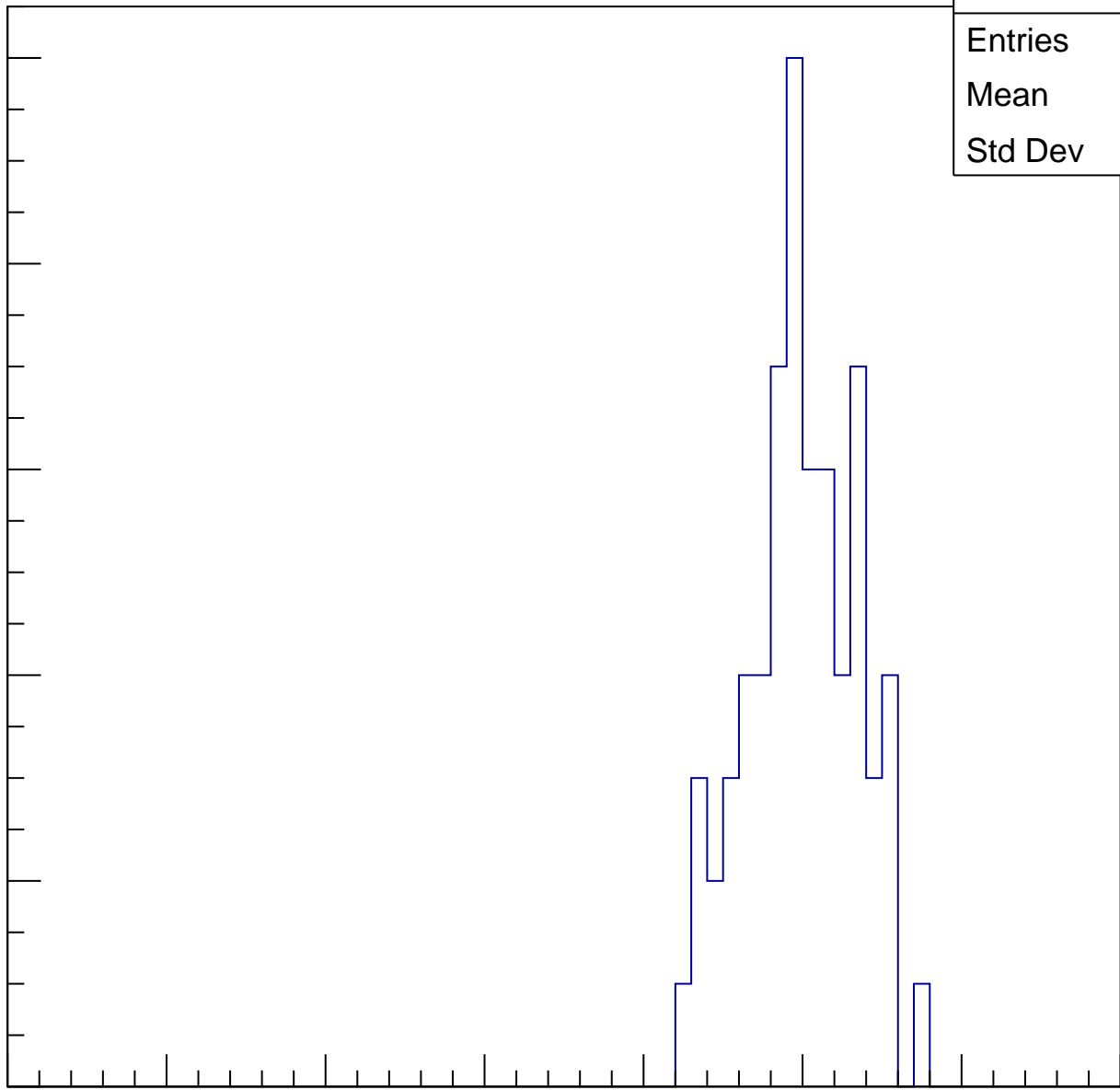
| | |
|---------|-------|
| Entries | 65 |
| Mean | 49.48 |
| Std Dev | 3.42 |

Entry

10
8
6
4
2
0

ampl

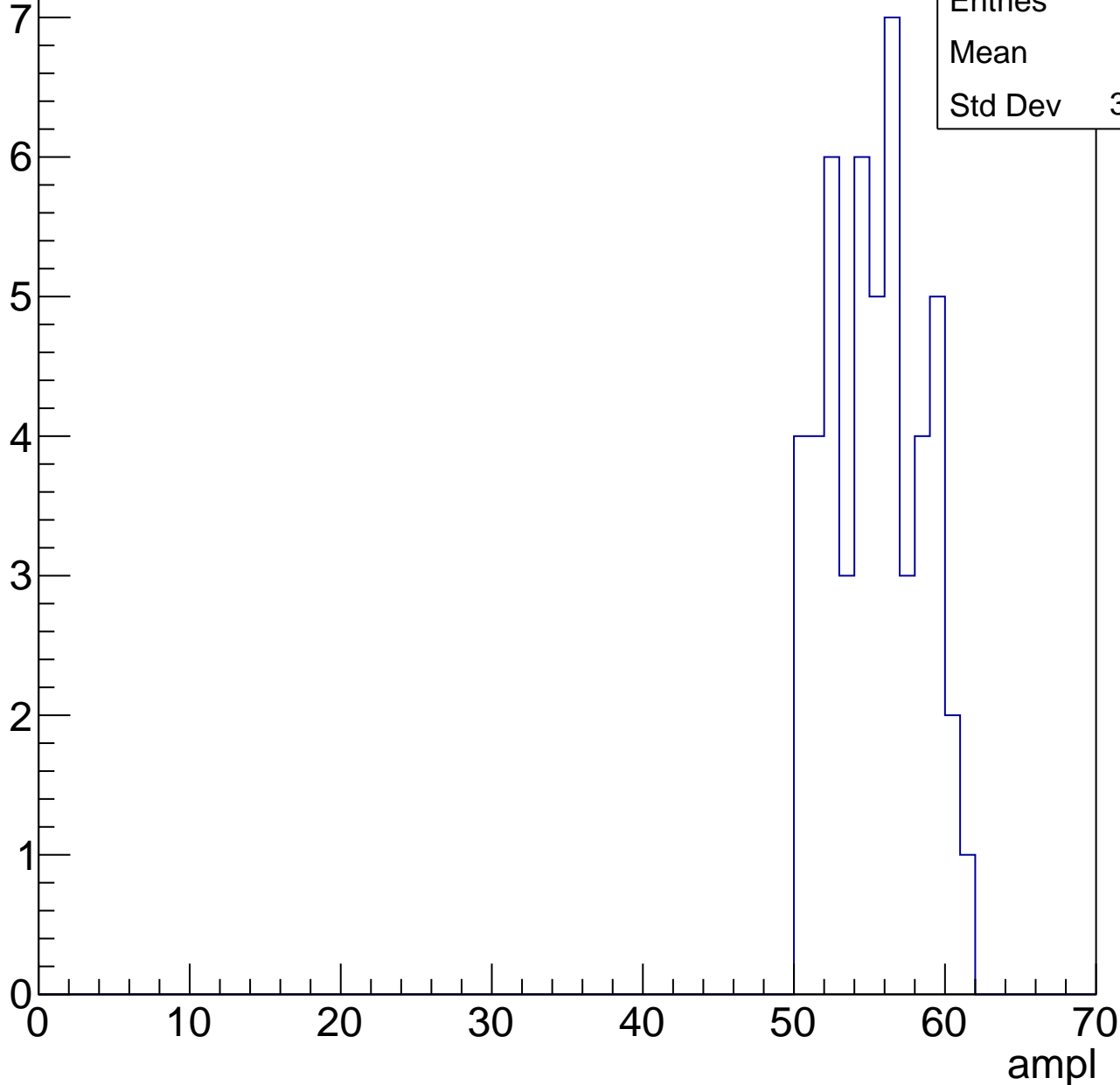
0 10 20 30 40 50 60 70



B0L102S, U20-ch27, adc4

calib_packv5_042523_0143.root, FC#12, port B1

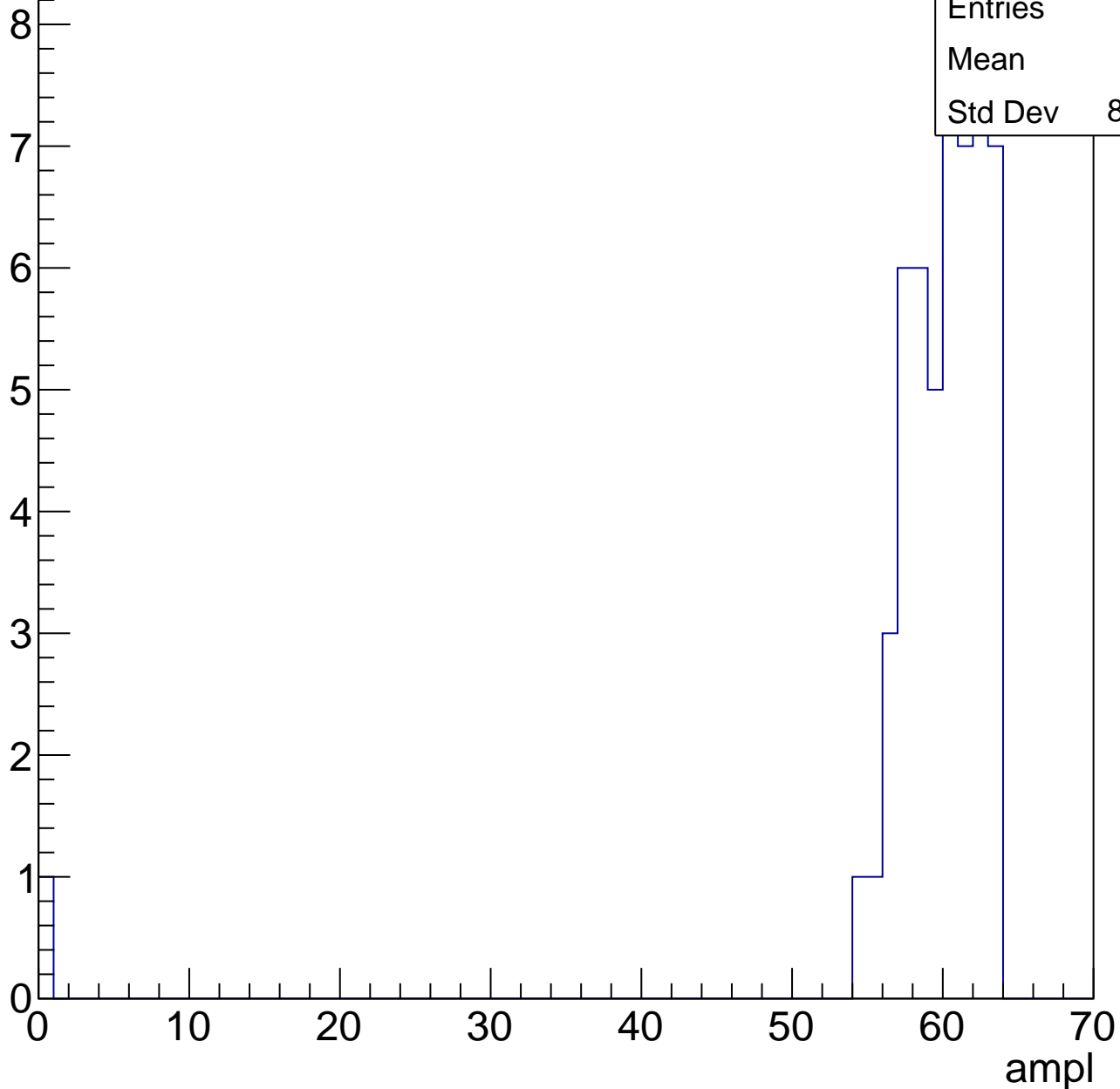
Entry



B0L102S, U20-ch27, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch27, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch27, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U20-ch28, adc0

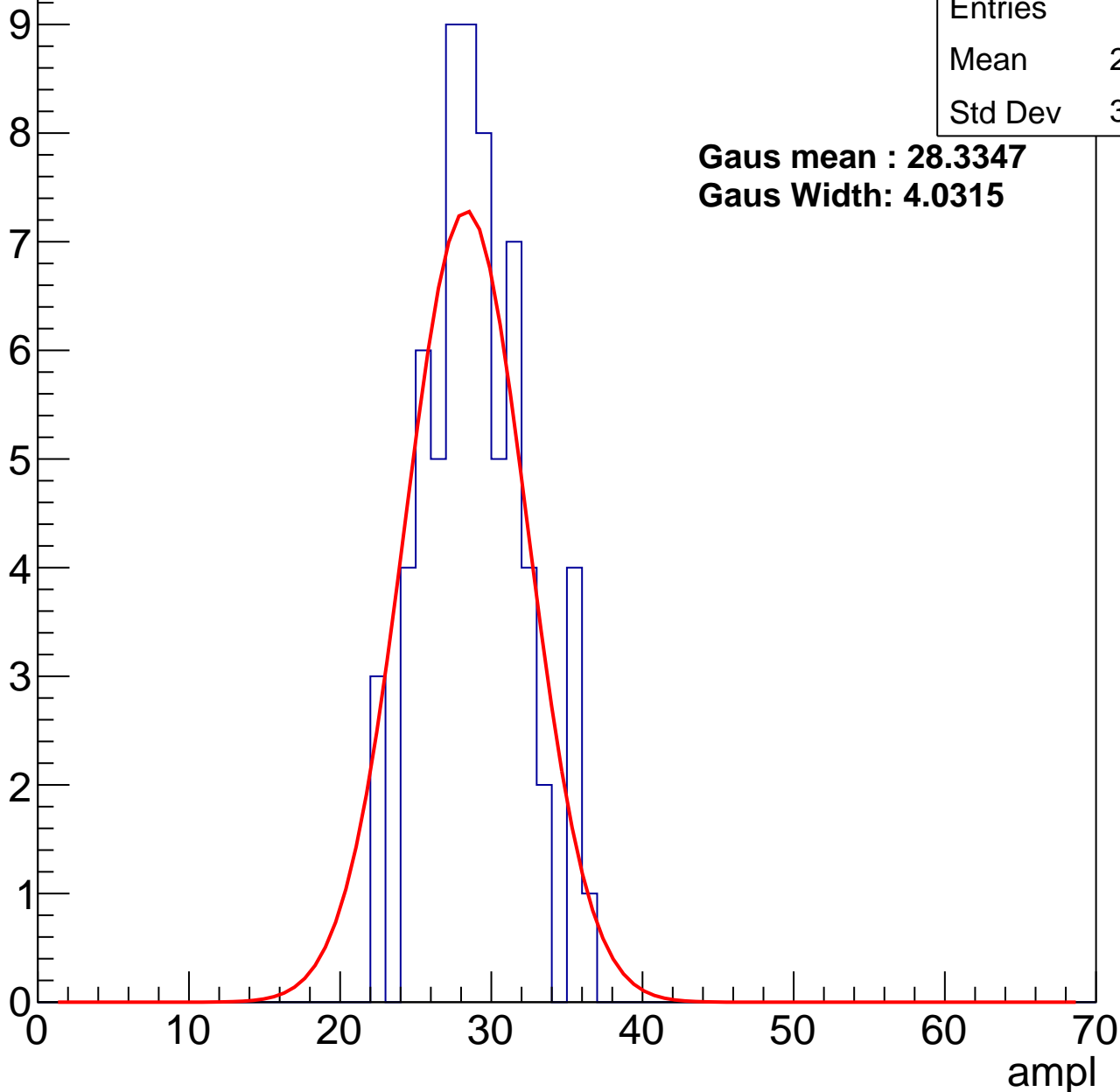
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 28.45 |
| Std Dev | 3.247 |

Gaus mean : 28.3347

Gaus Width: 4.0315



B0L102S, U20-ch28, adc1

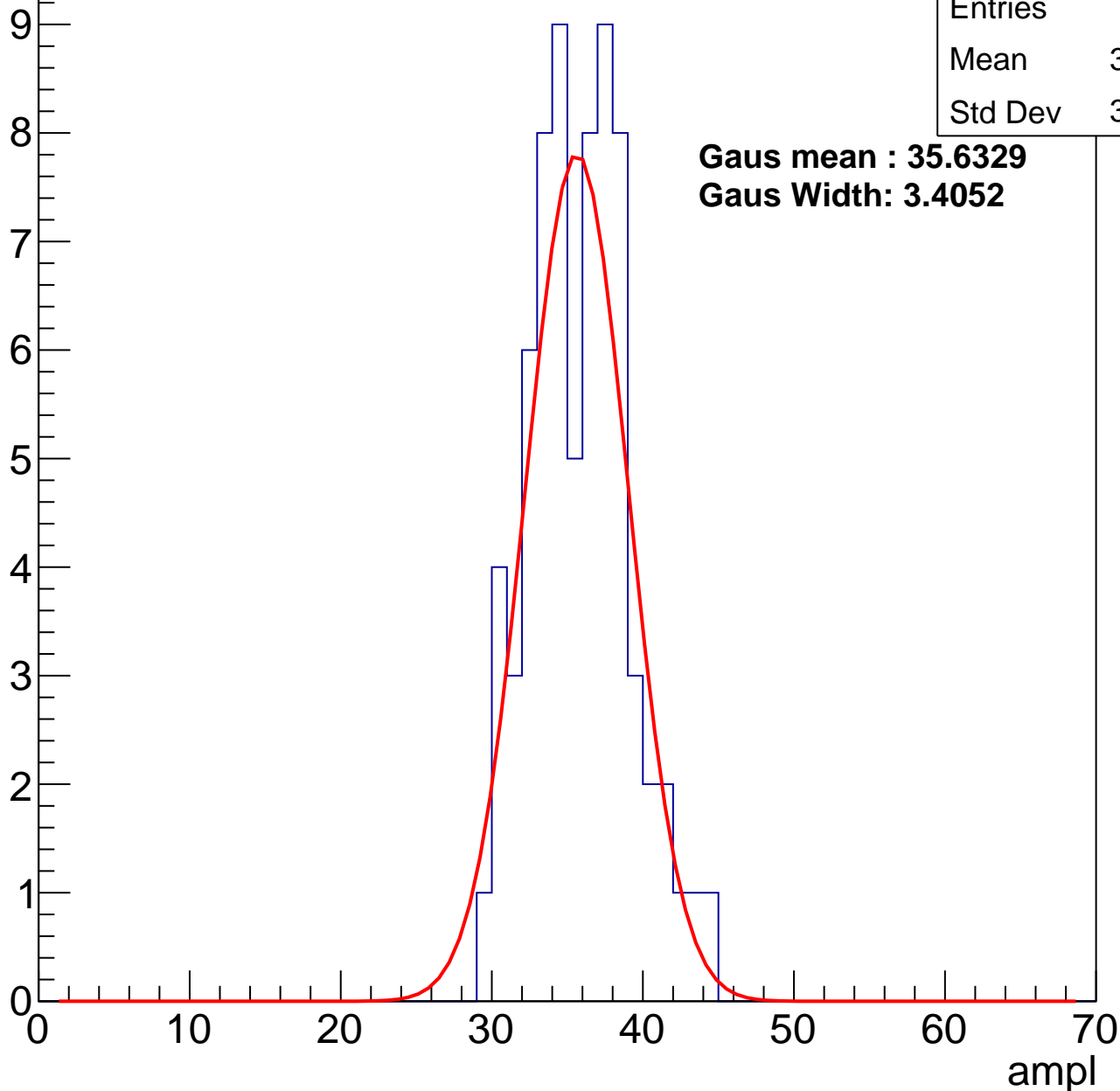
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.38 |
| Std Dev | 3.247 |

Gaus mean : 35.6329

Gaus Width: 3.4052



B0L102S, U20-ch28, adc2

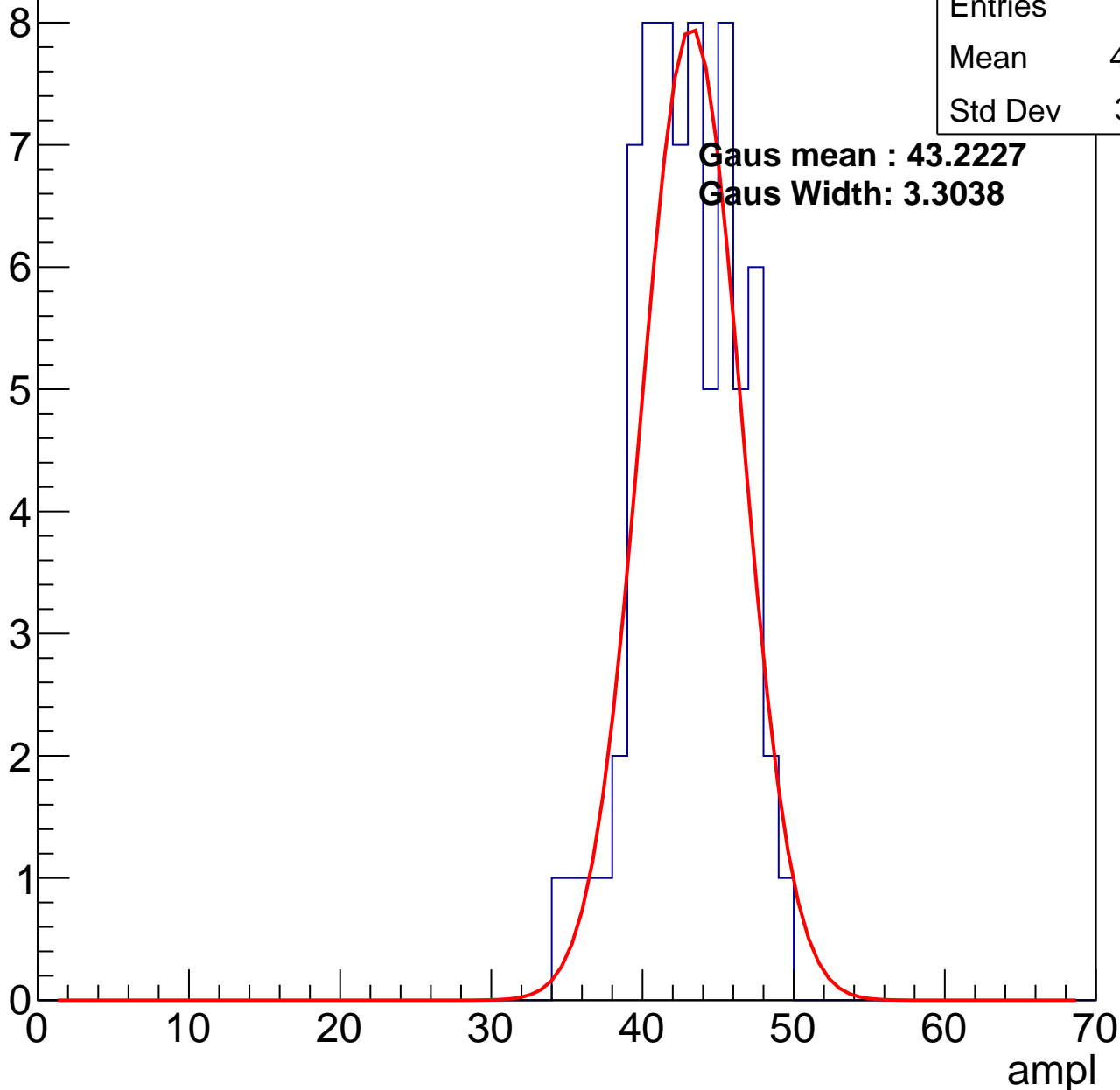
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.45 |
| Std Dev | 3.241 |

Gaus mean : 43.2227

Gaus Width: 3.3038



B0L102S, U20-ch28, adc3

calib_packv5_042523_0143.root, FC#12, port B1

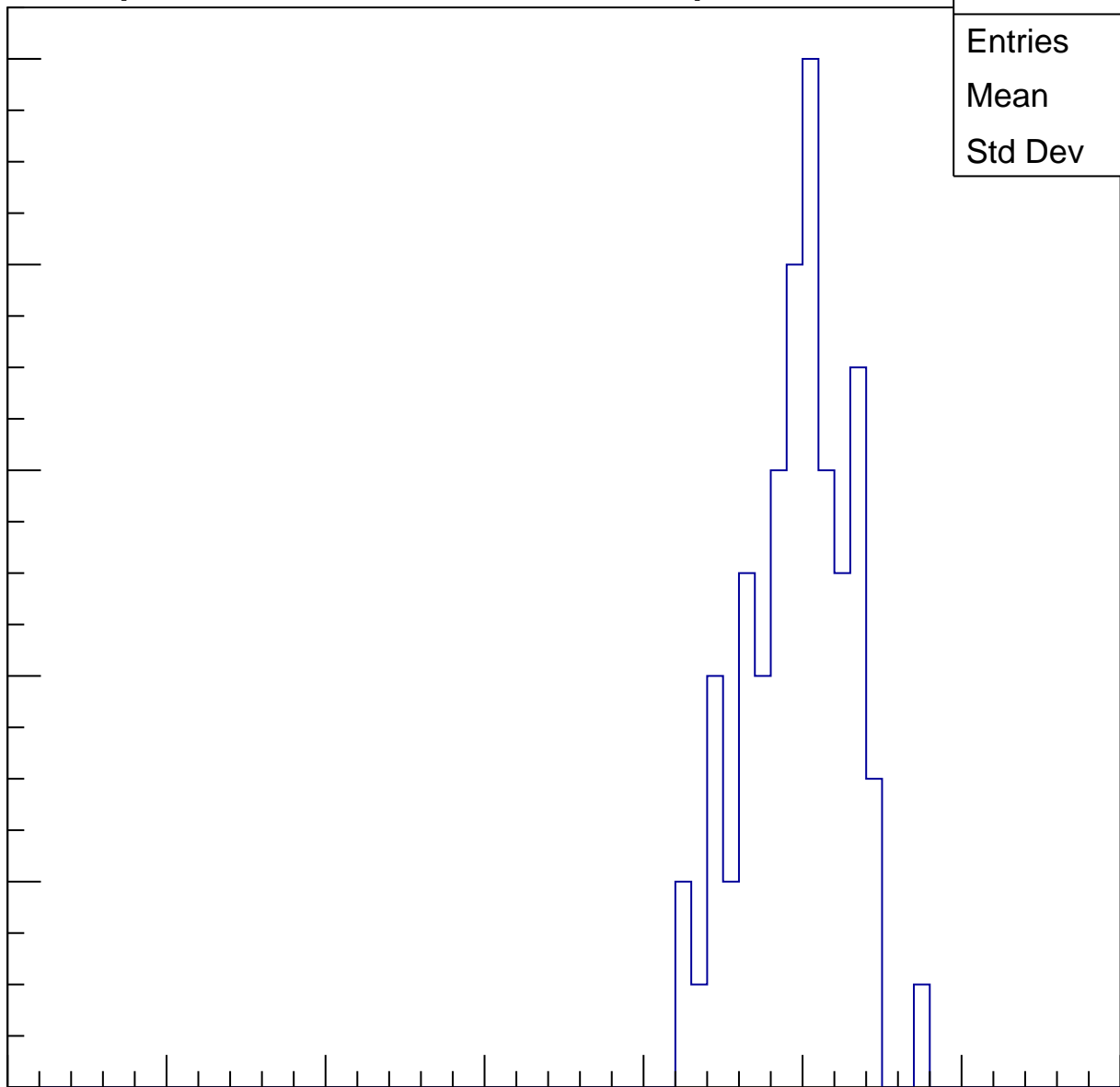
| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.17 |
| Std Dev | 3.214 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

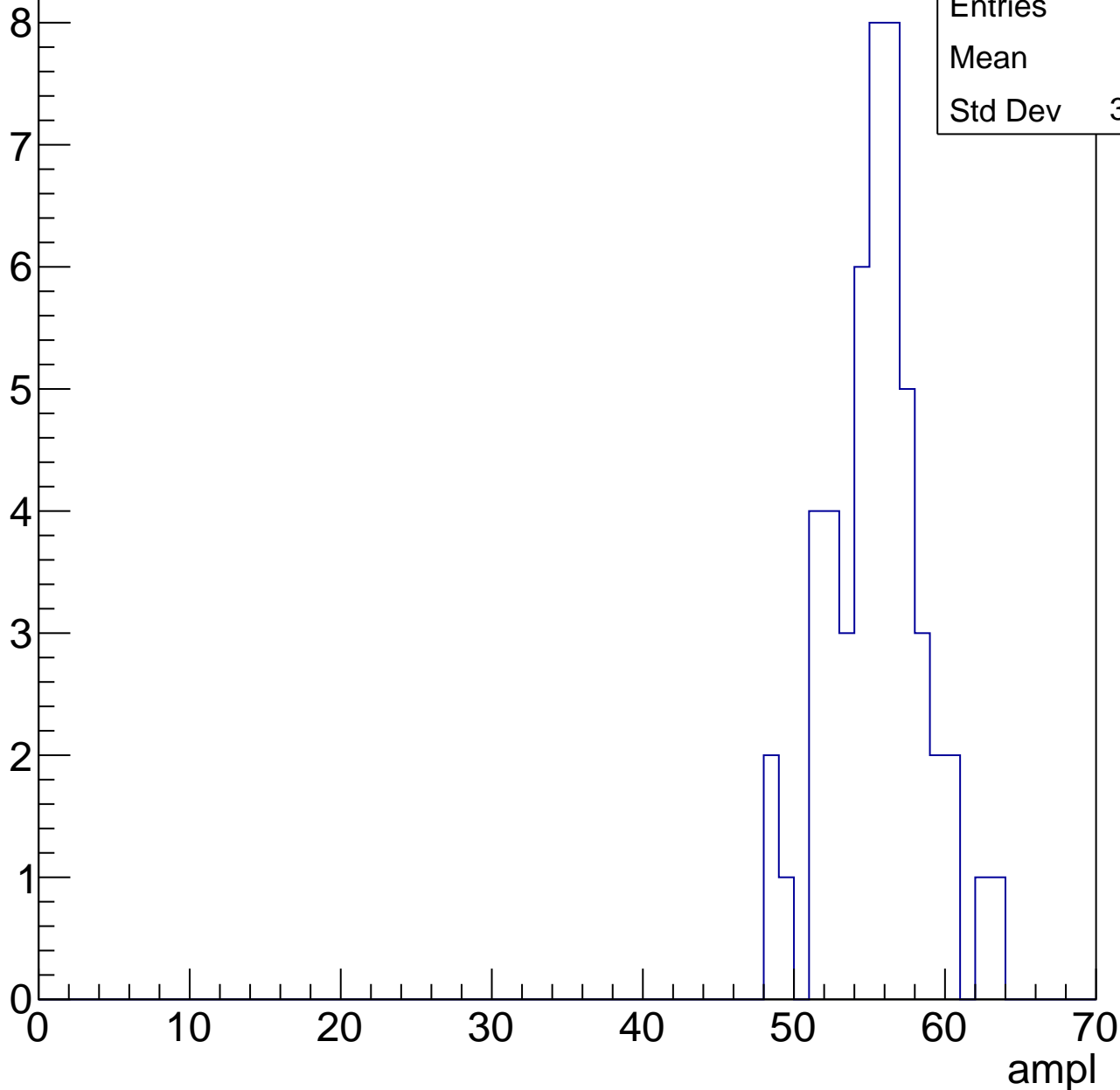


B0L102S, U20-ch28, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

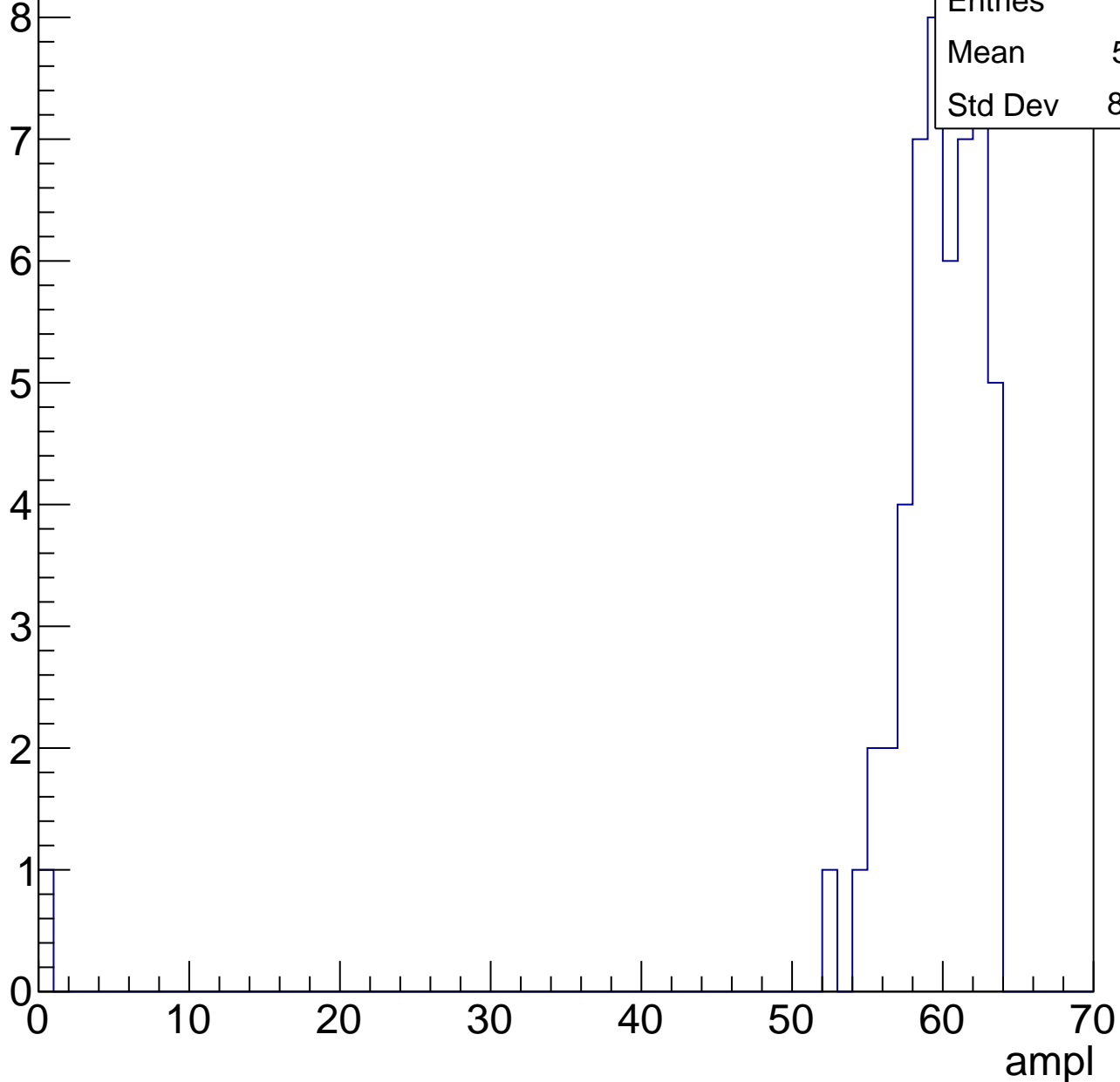
| | |
|---------|-------|
| Entries | 50 |
| Mean | 55 |
| Std Dev | 3.169 |



B0L102S, U20-ch28, adc5

calib_packv5_042523_0143.root, FC#12, port B1

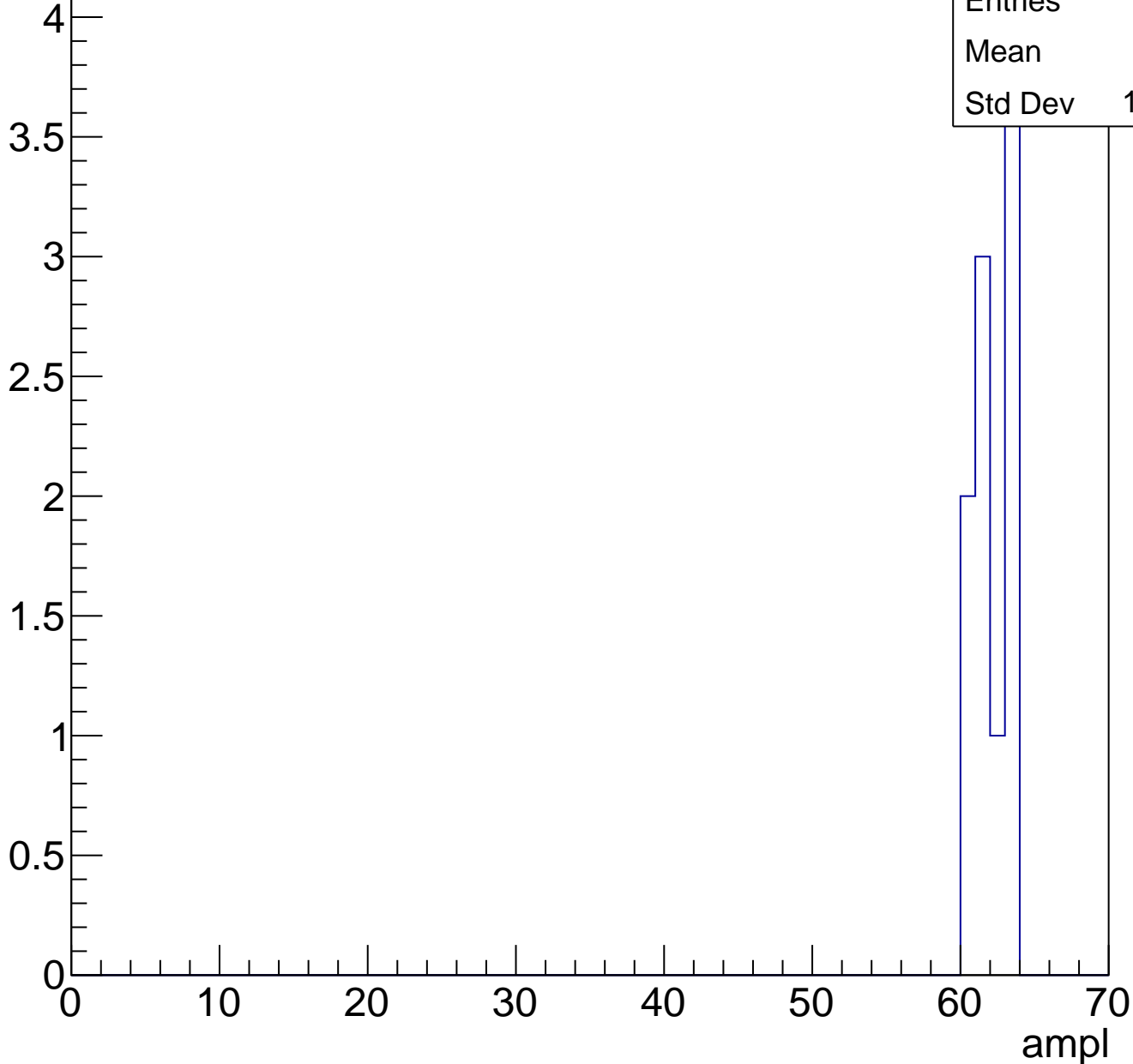
Entry



B0L102S, U20-ch28, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch28, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch29, adc0

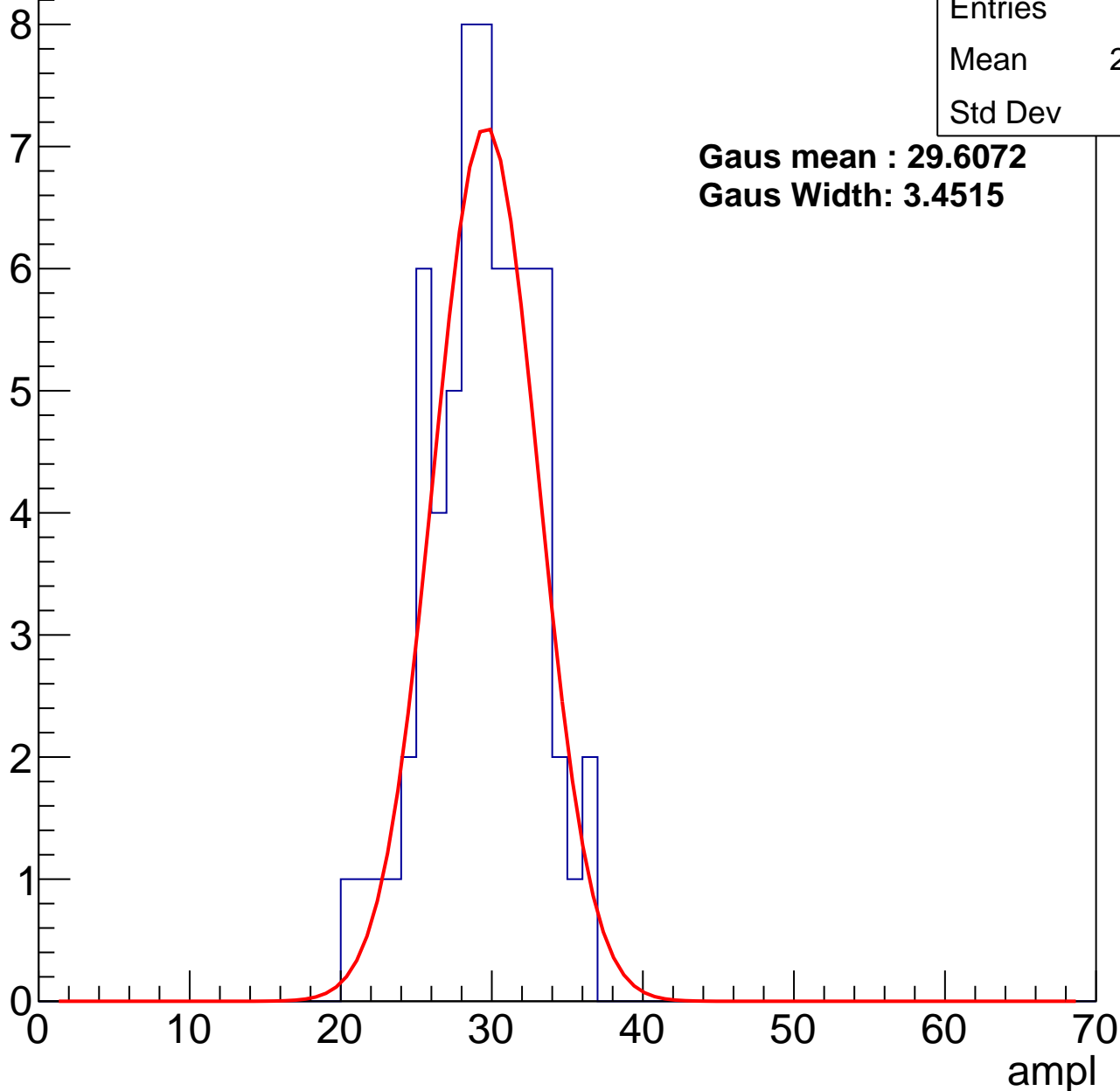
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.94 |
| Std Dev | 3.49 |

Gaus mean : 29.6072

Gaus Width: 3.4515



B0L102S, U20-ch29, adc1

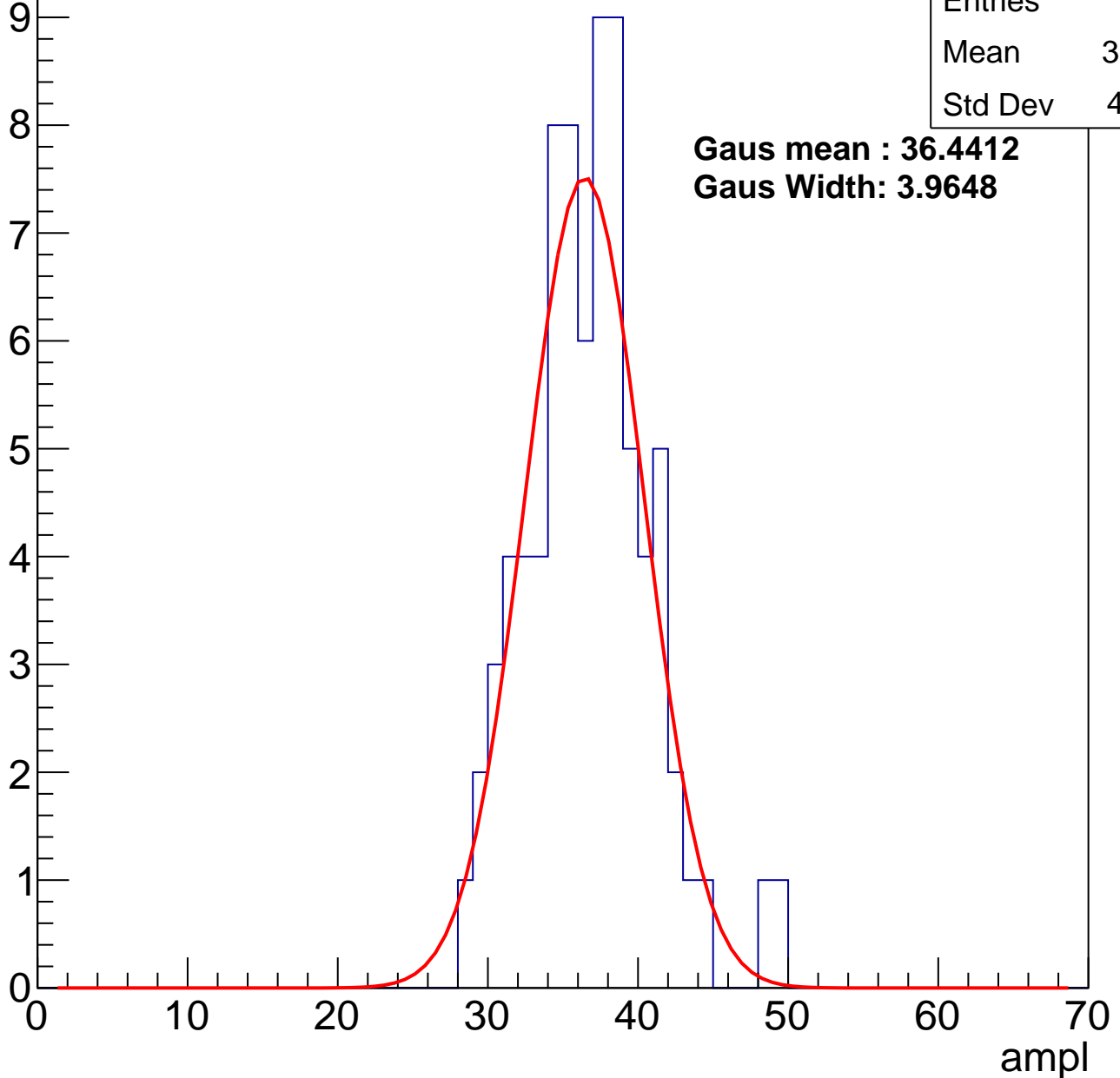
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 36.29 |
| Std Dev | 4.051 |

Gaus mean : 36.4412

Gaus Width: 3.9648



B0L102S, U20-ch29, adc2

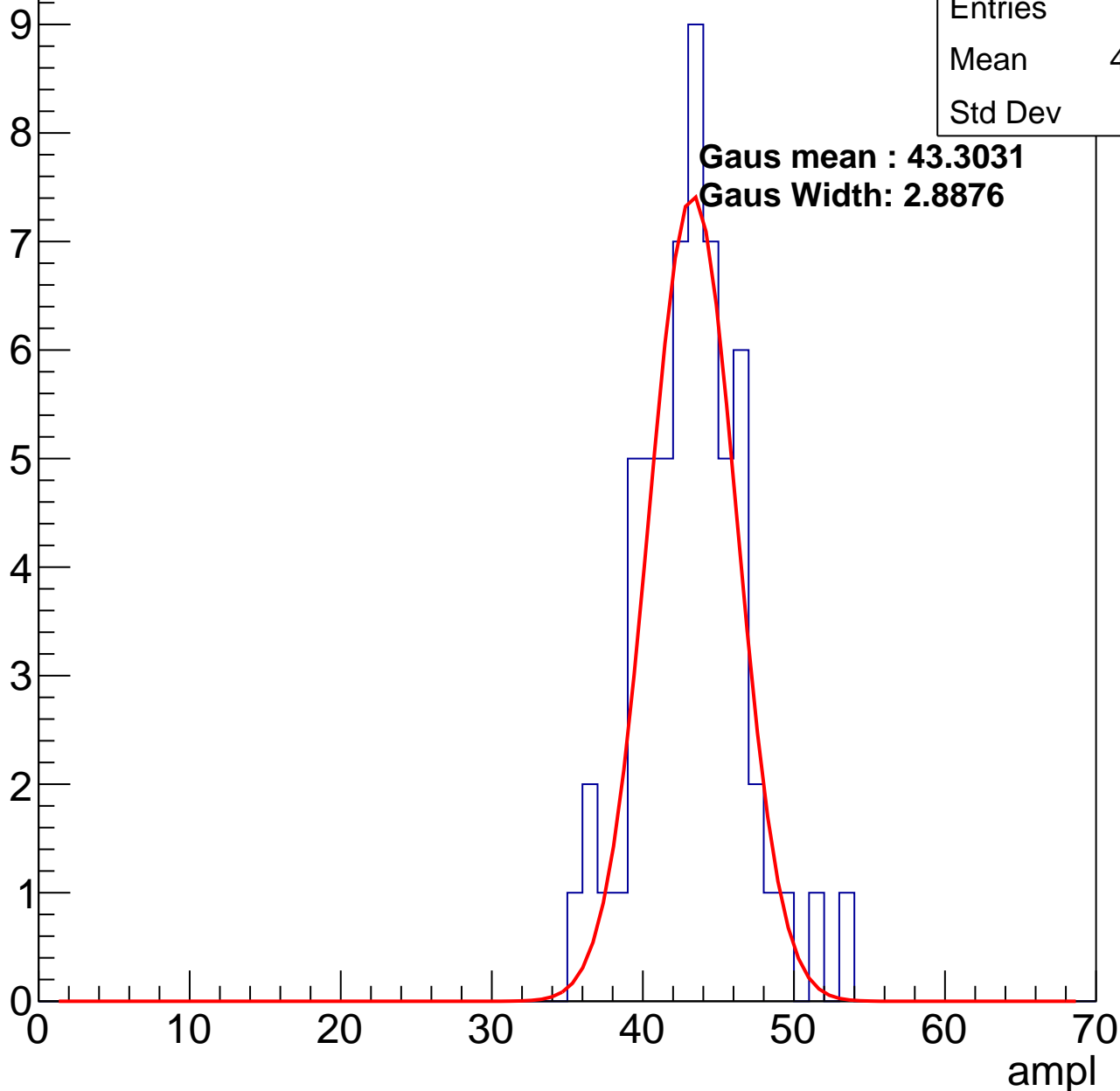
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.78 |
| Std Dev | 3.44 |

Gaus mean : 43.3031

Gaus Width: 2.8876

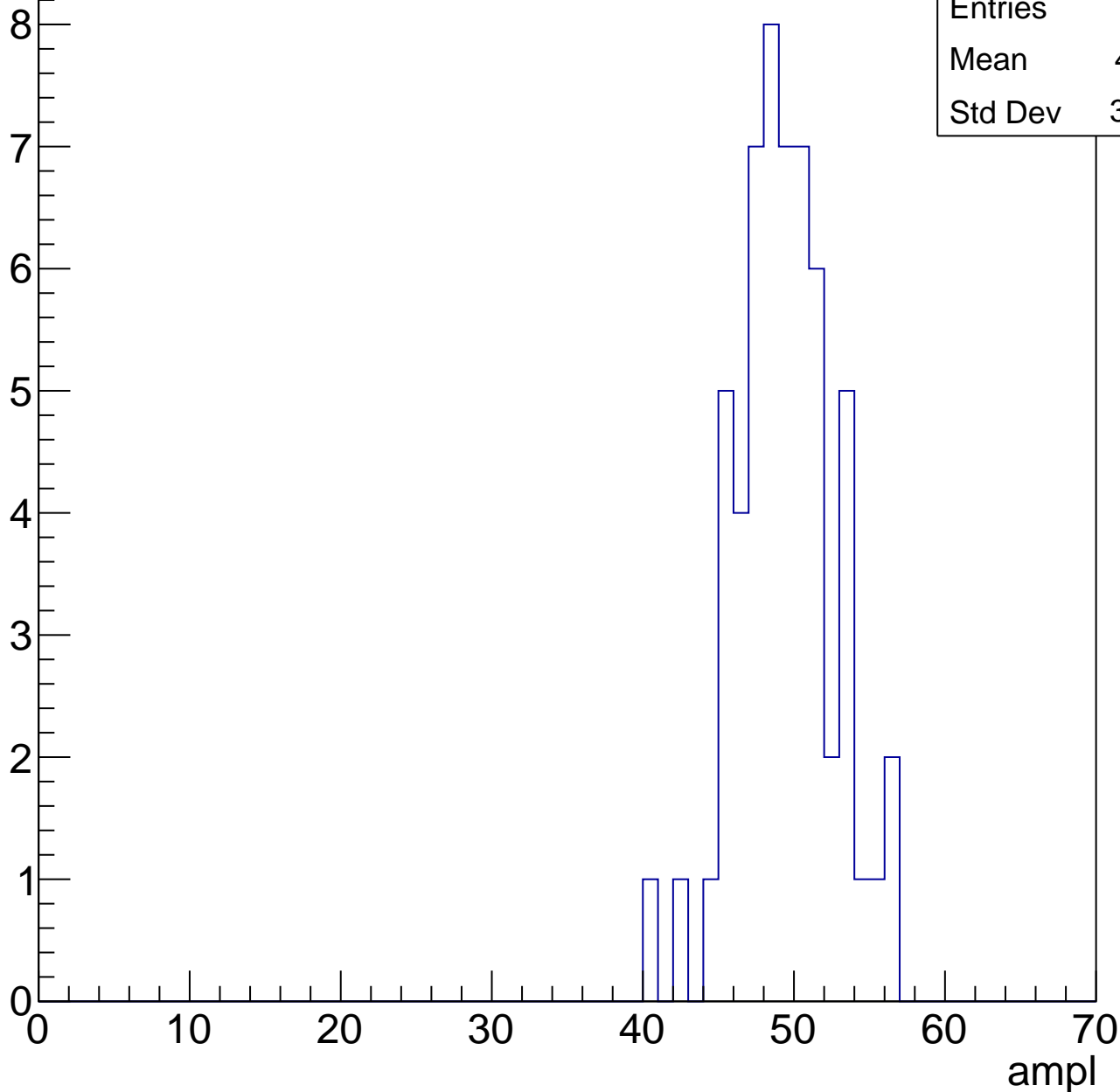


B0L102S, U20-ch29, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 48.91 |
| Std Dev | 3.202 |

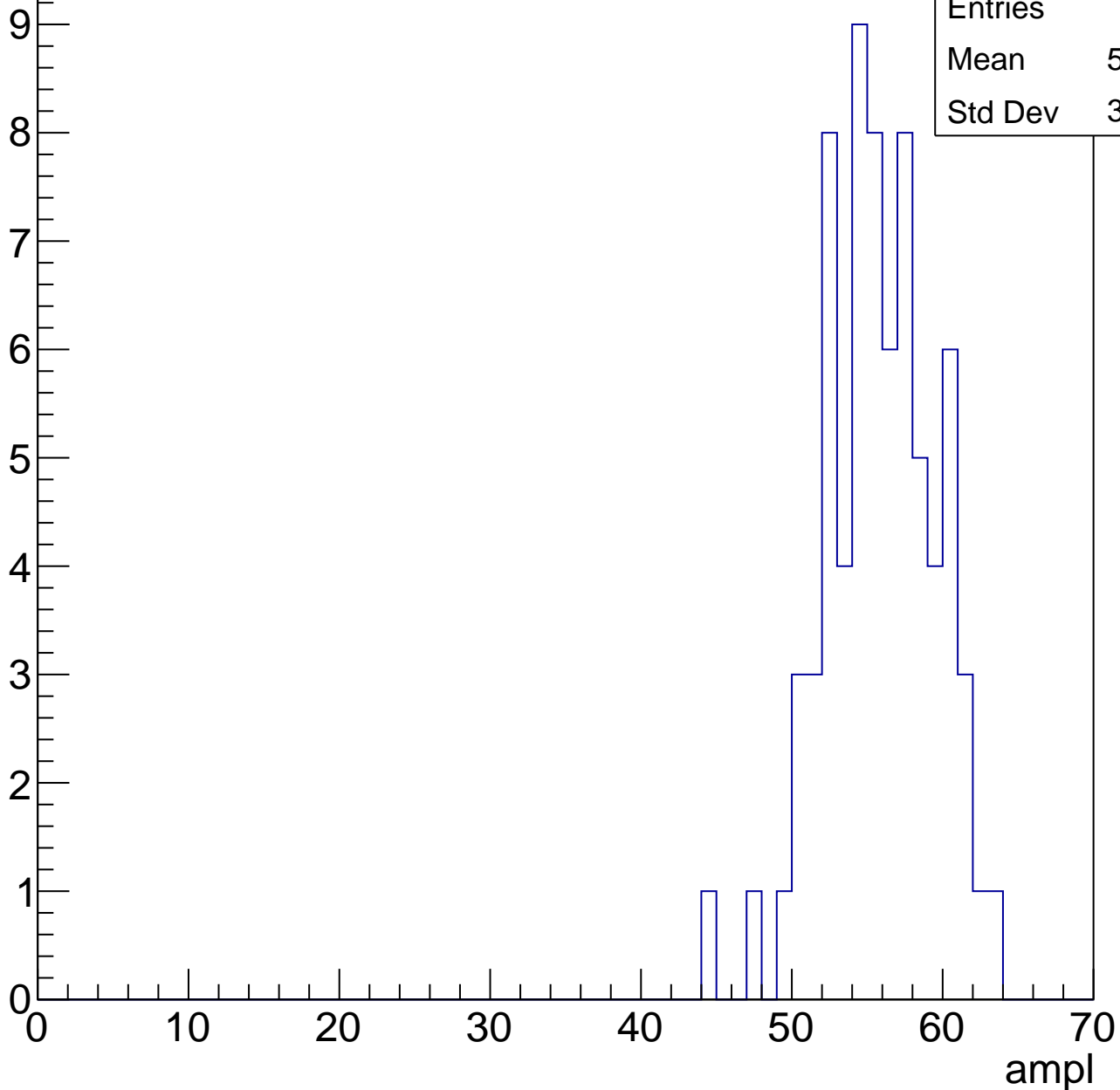


B0L102S, U20-ch29, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

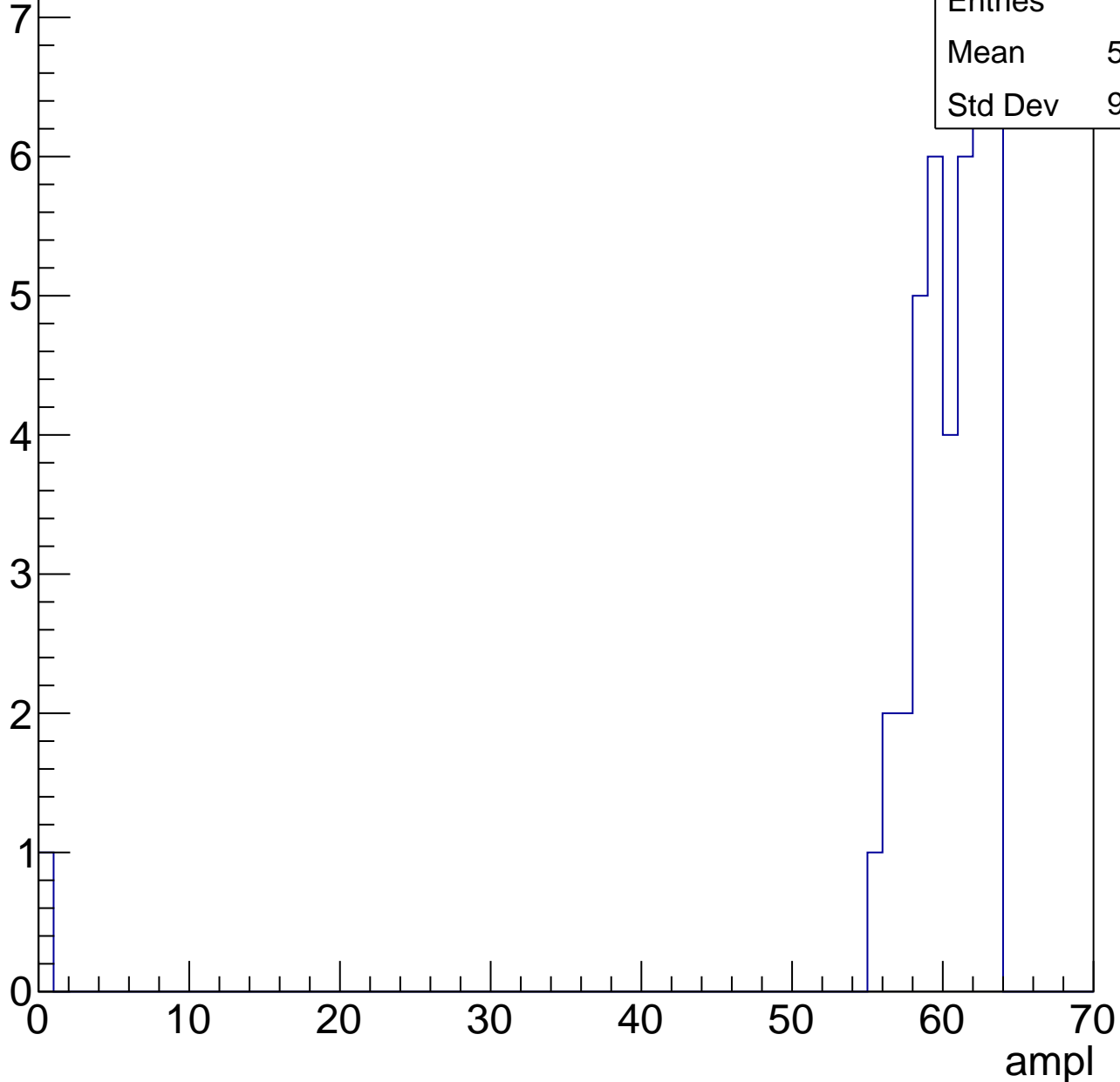
| | |
|---------|-------|
| Entries | 72 |
| Mean | 55.32 |
| Std Dev | 3.636 |



B0L102S, U20-ch29, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch29, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch29, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch30, adc0

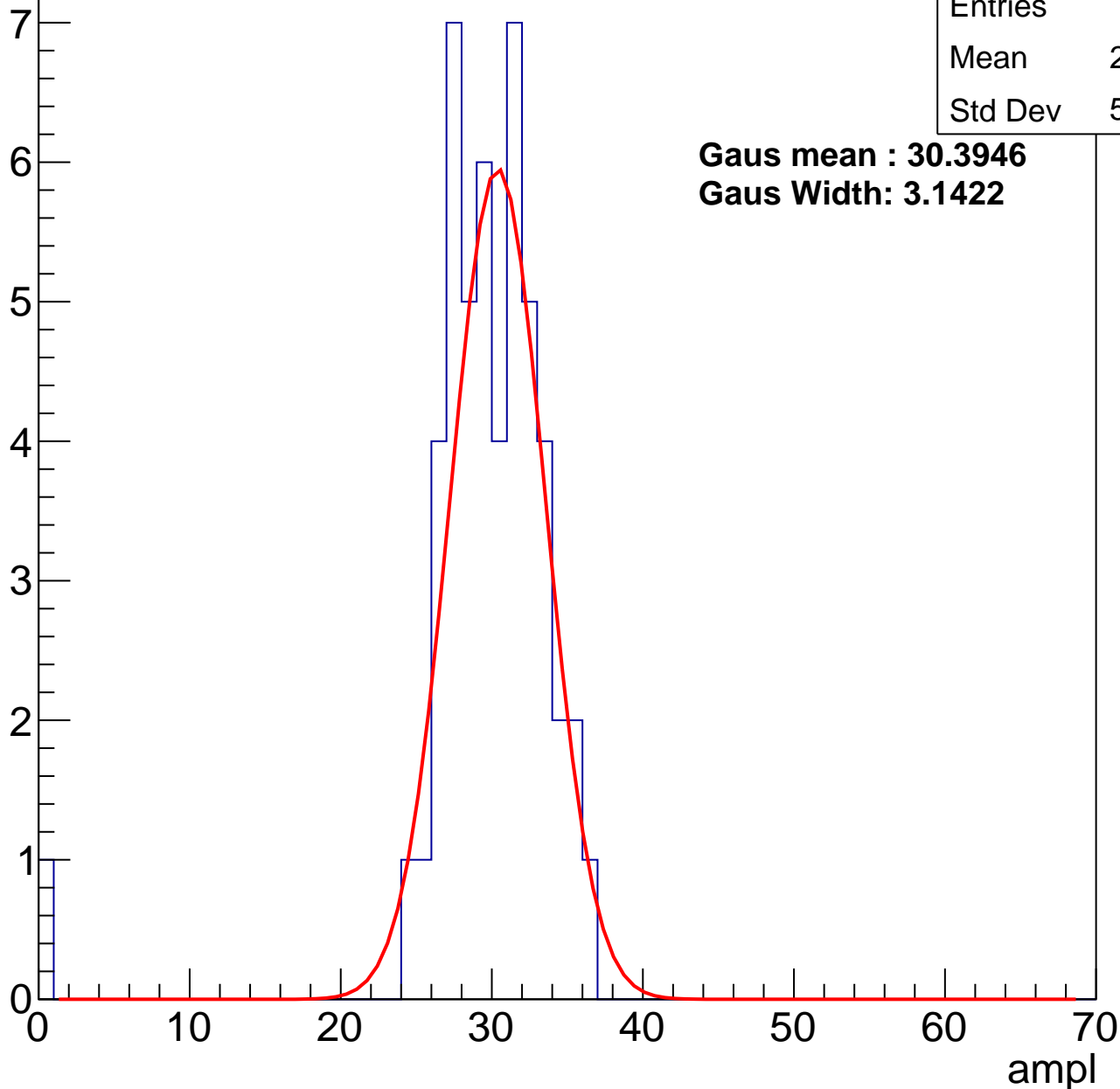
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 29.18 |
| Std Dev | 5.023 |

Gaus mean : 30.3946

Gaus Width: 3.1422

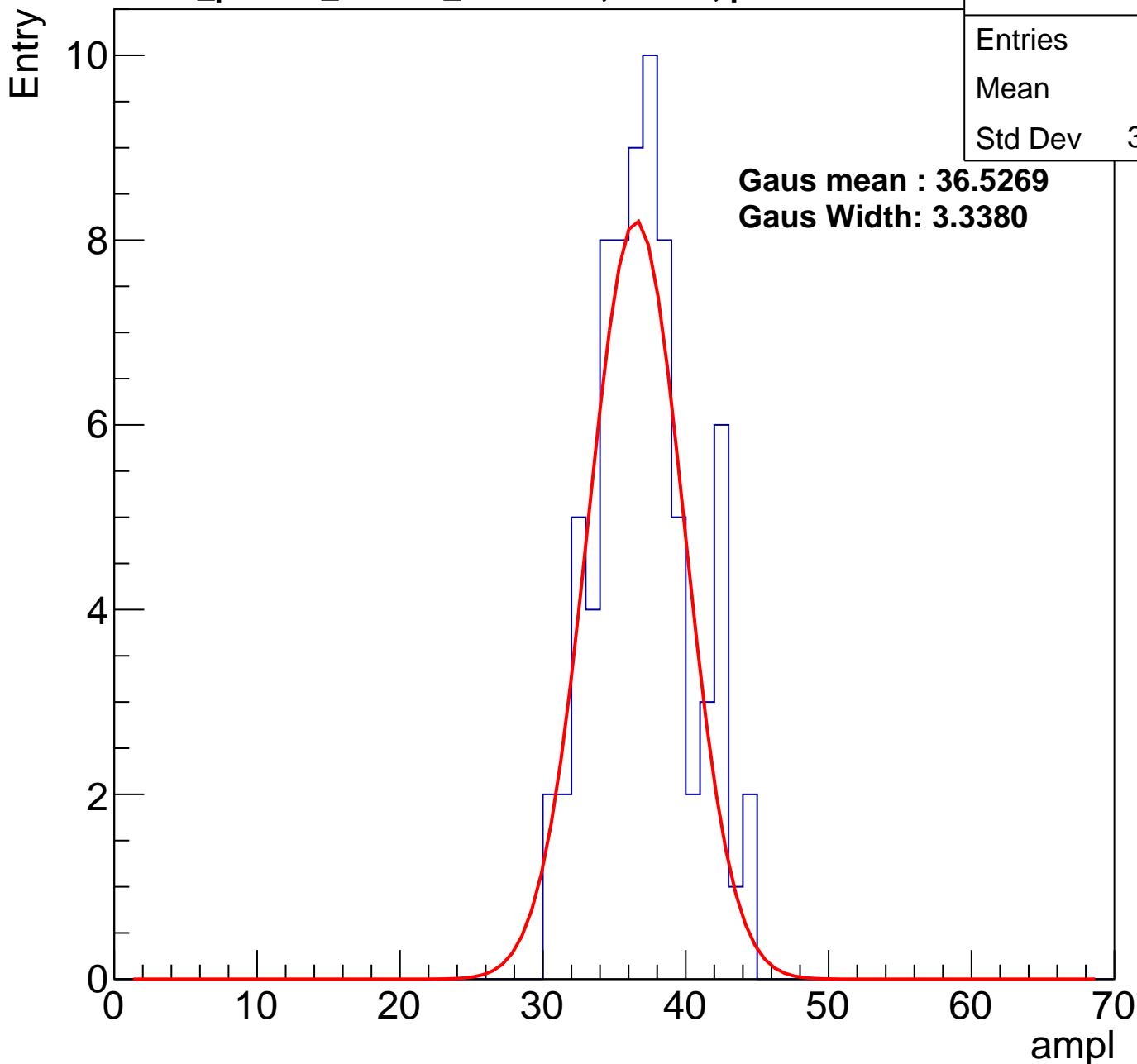


B0L102S, U20-ch30, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 36.6 |
| Std Dev | 3.343 |

Gaus mean : 36.5269
Gaus Width: 3.3380



B0L102S, U20-ch30, adc2

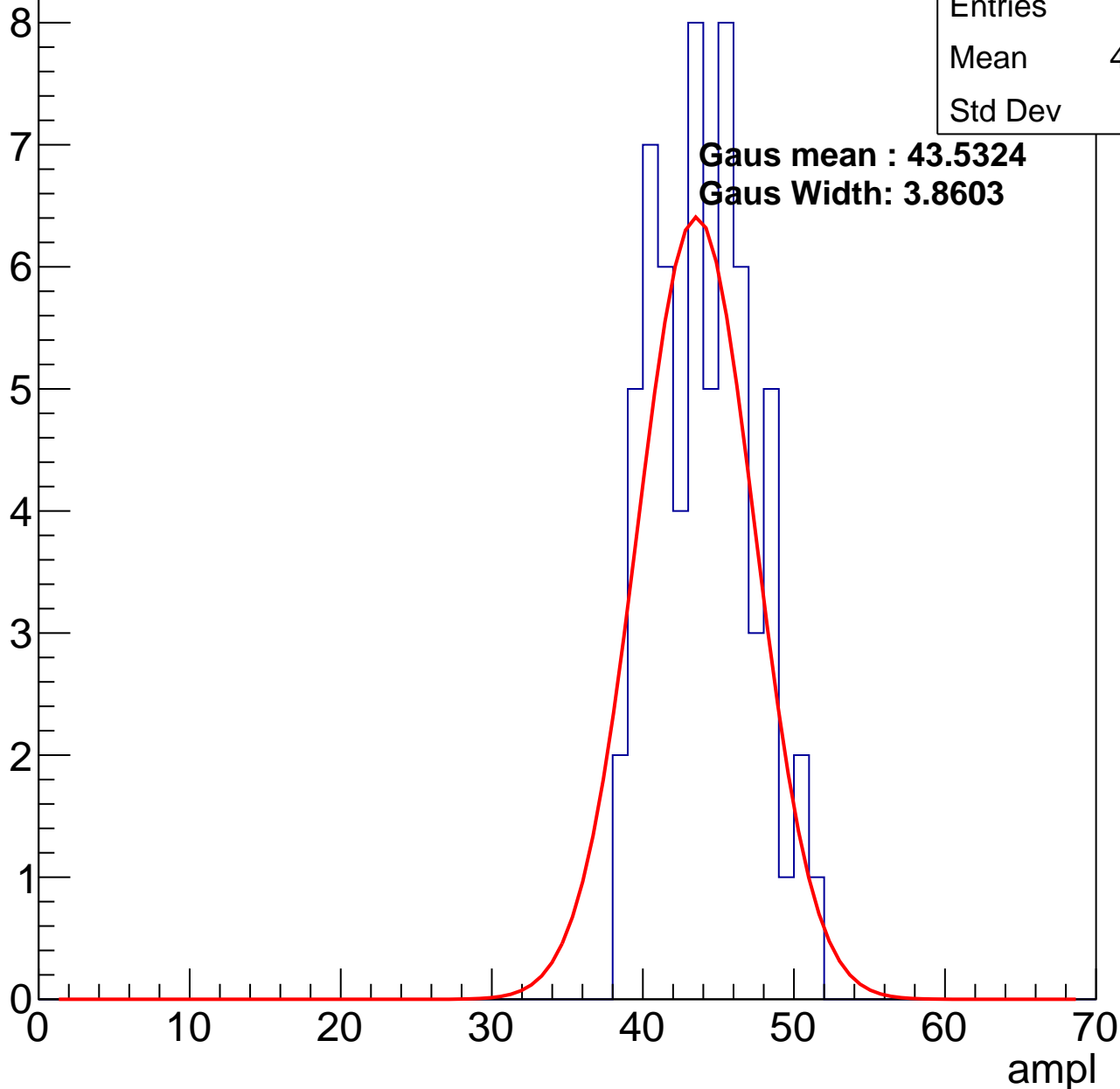
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.59 |
| Std Dev | 3.24 |

Gaus mean : 43.5324

Gaus Width: 3.8603

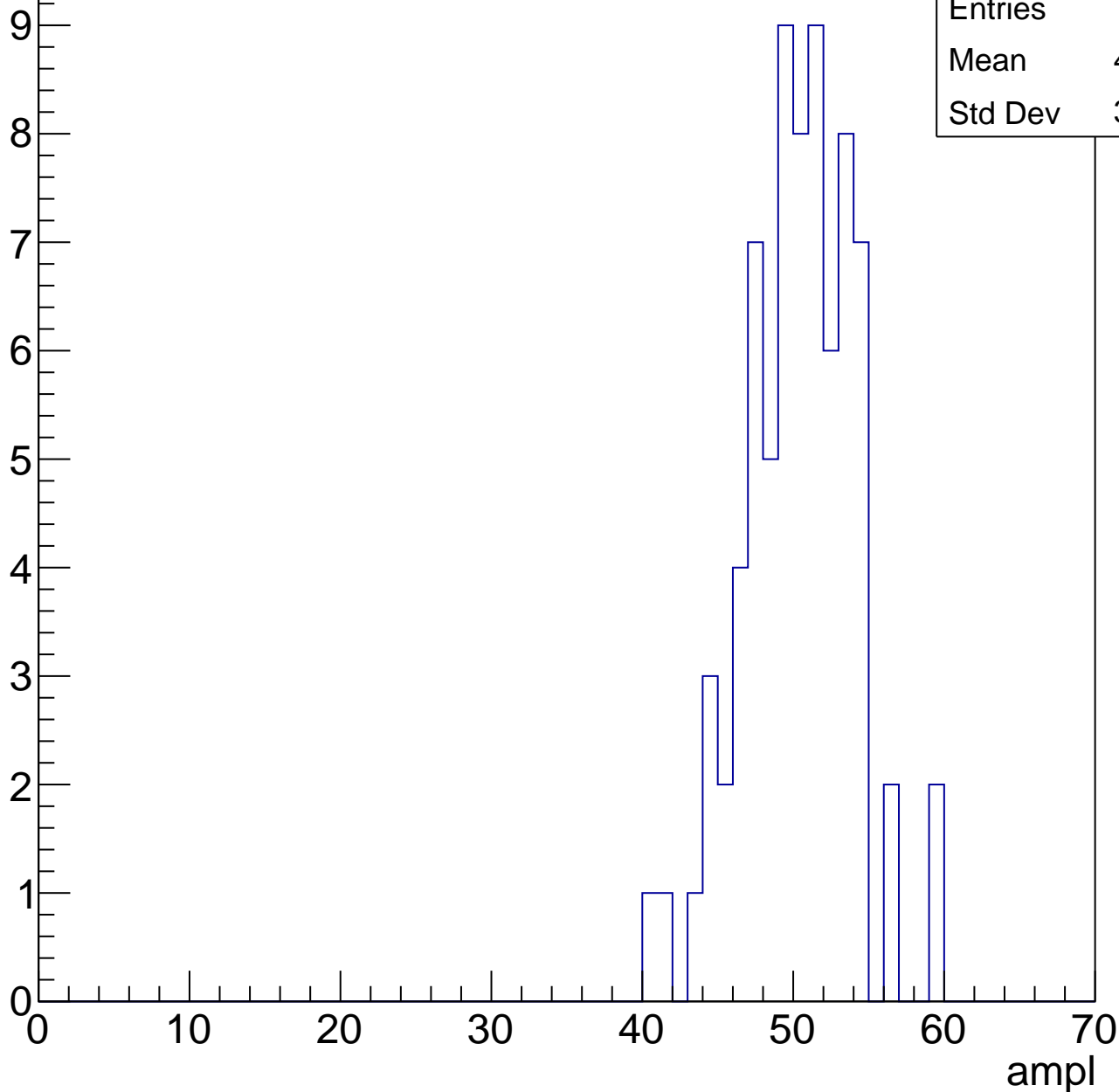


B0L102S, U20-ch30, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.91 |
| Std Dev | 3.641 |

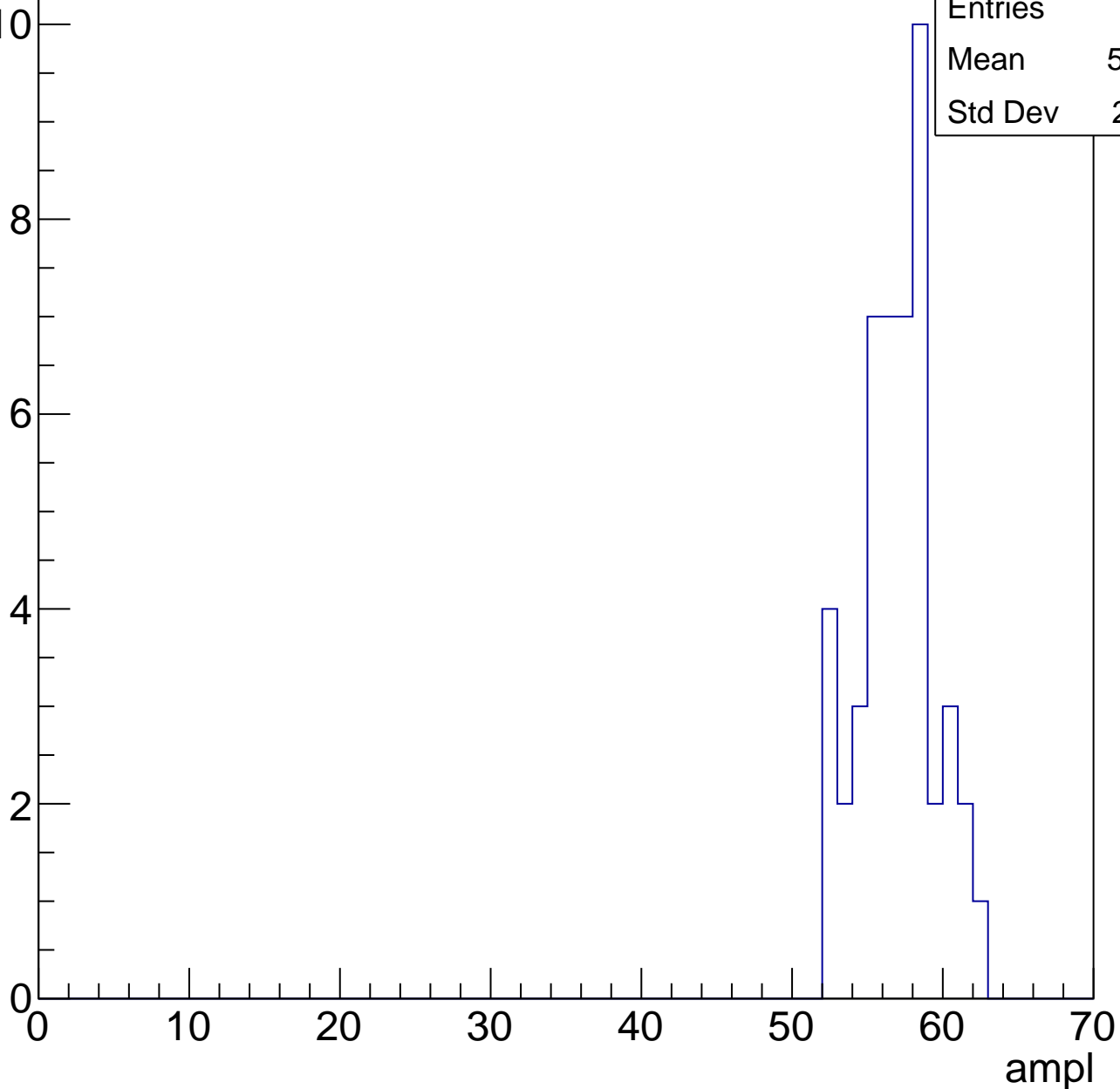


B0L102S, U20-ch30, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 56.54 |
| Std Dev | 2.441 |

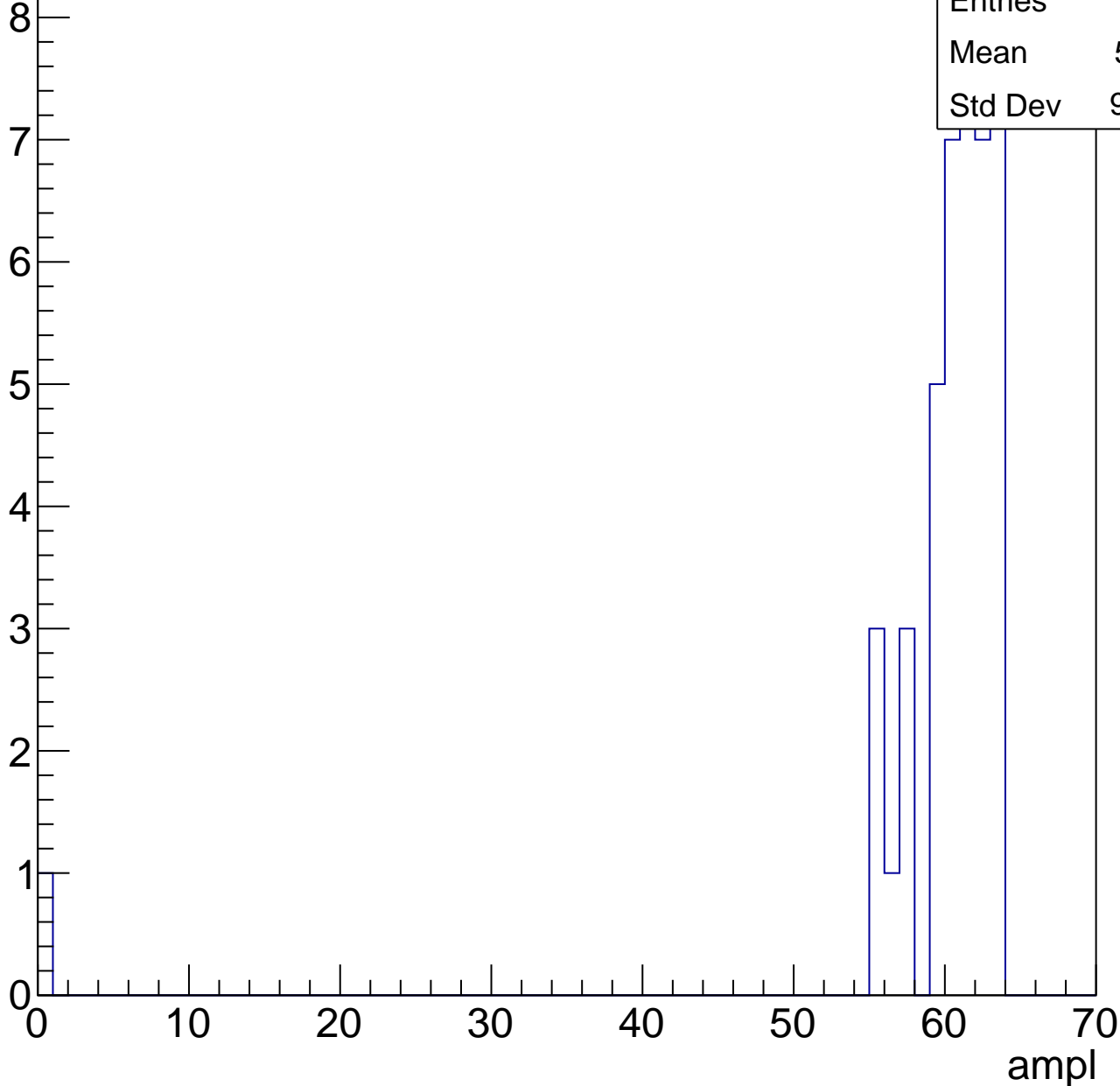


B0L102S, U20-ch30, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.91 |
| Std Dev | 9.375 |



B0L102S, U20-ch30, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch30, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch31, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.84 |
| Std Dev | 3.124 |

Gaus mean : 31.2978

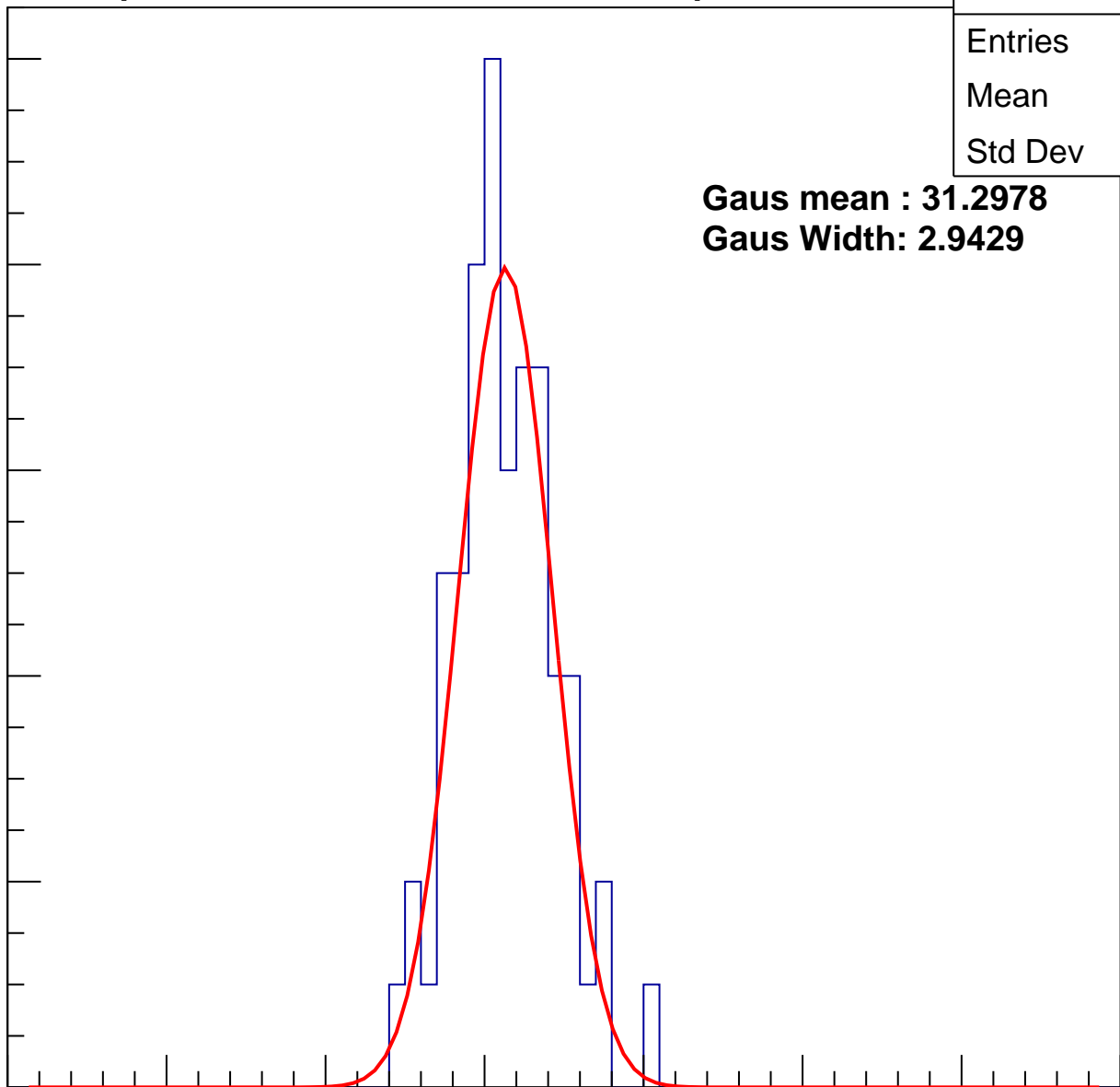
Gaus Width: 2.9429

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch31, adc1

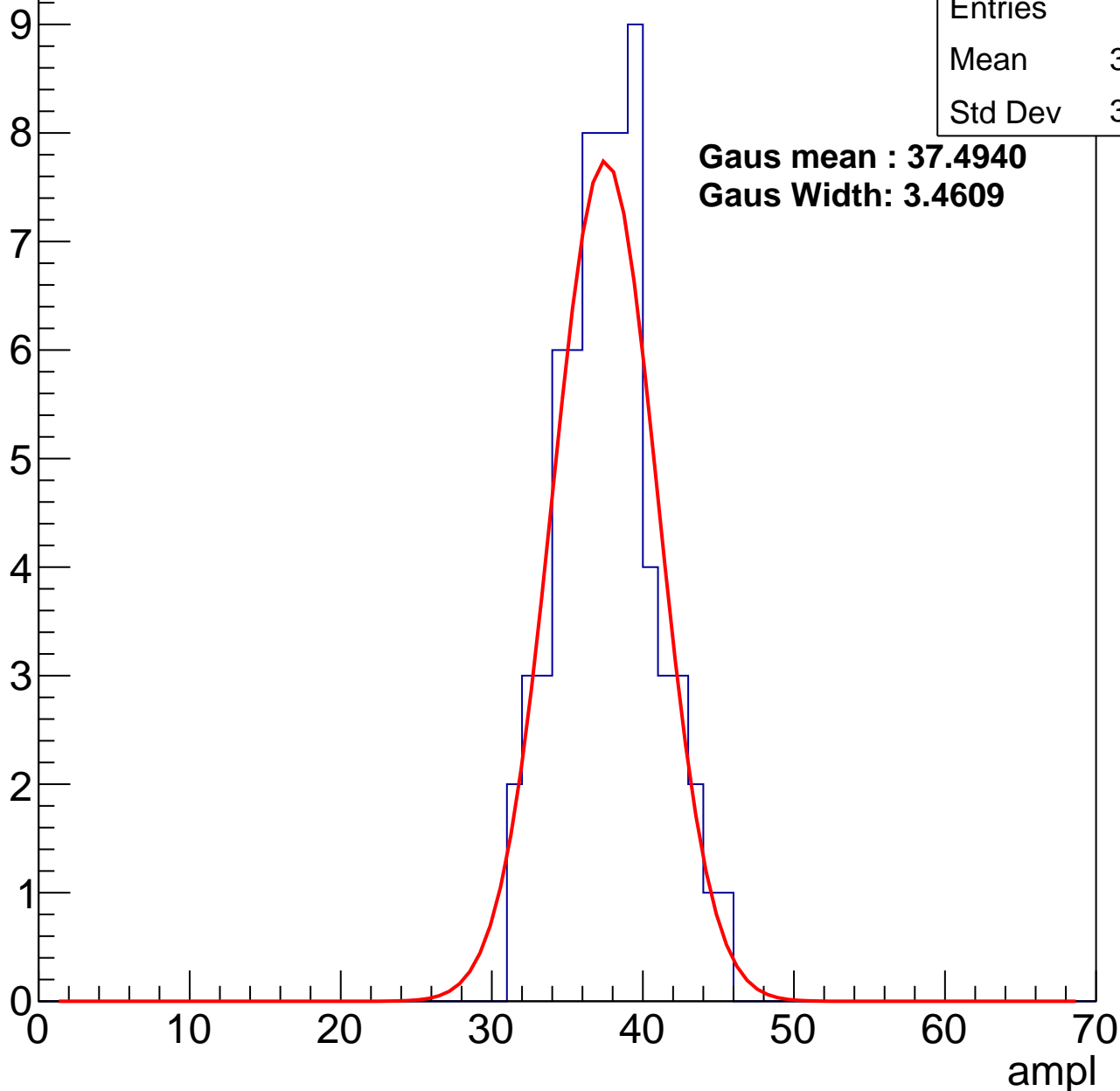
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.22 |
| Std Dev | 3.152 |

Gaus mean : 37.4940

Gaus Width: 3.4609



B0L102S, U20-ch31, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

8

7

6

5

4

3

2

1

0

Entries 71

Mean 44.25

Std Dev 3.401

Gaus mean : 44.6765

Gaus Width: 3.1307

0

10

20

30

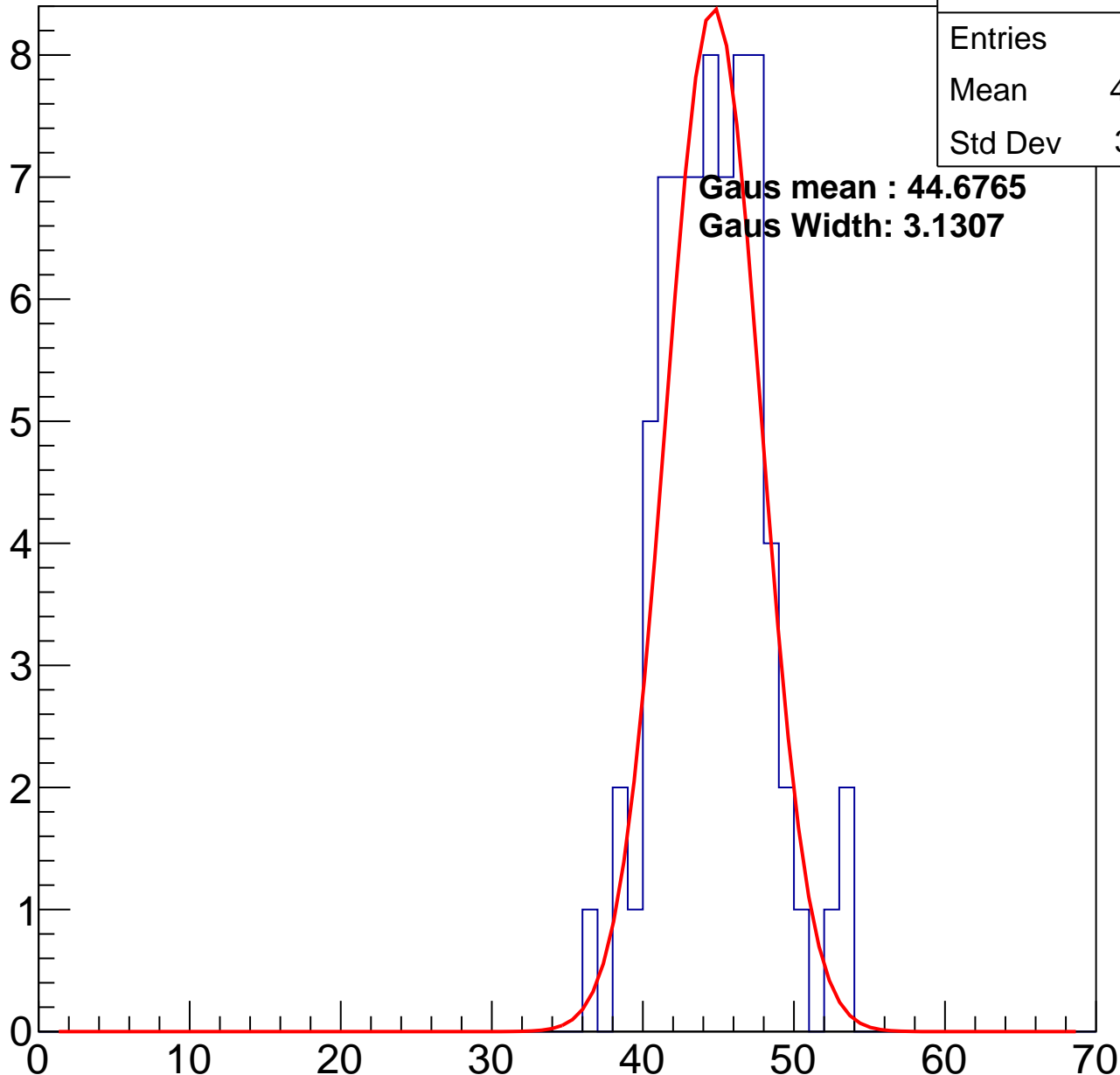
40

50

60

70

ampl

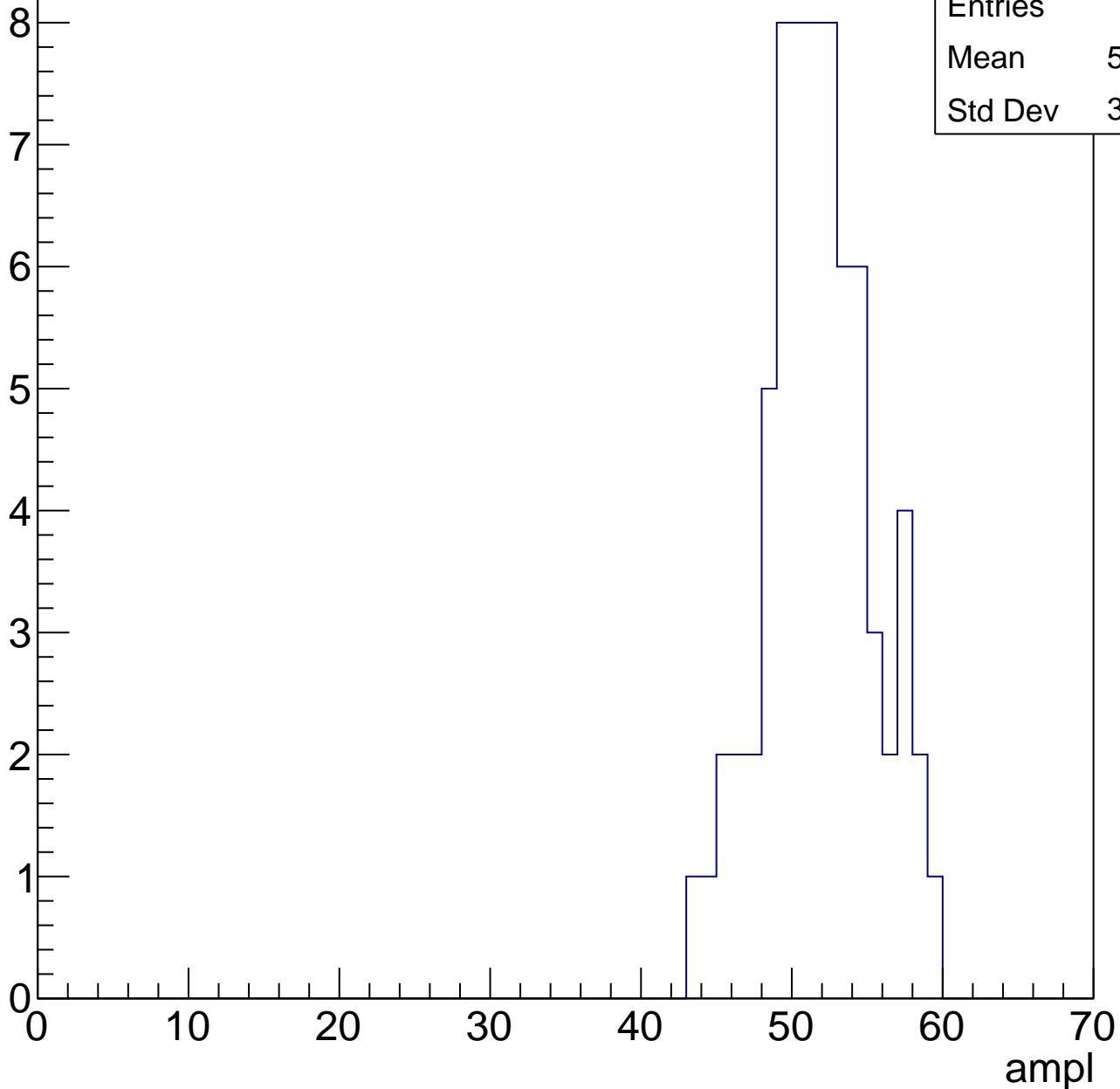


B0L102S, U20-ch31, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 51.32 |
| Std Dev | 3.479 |

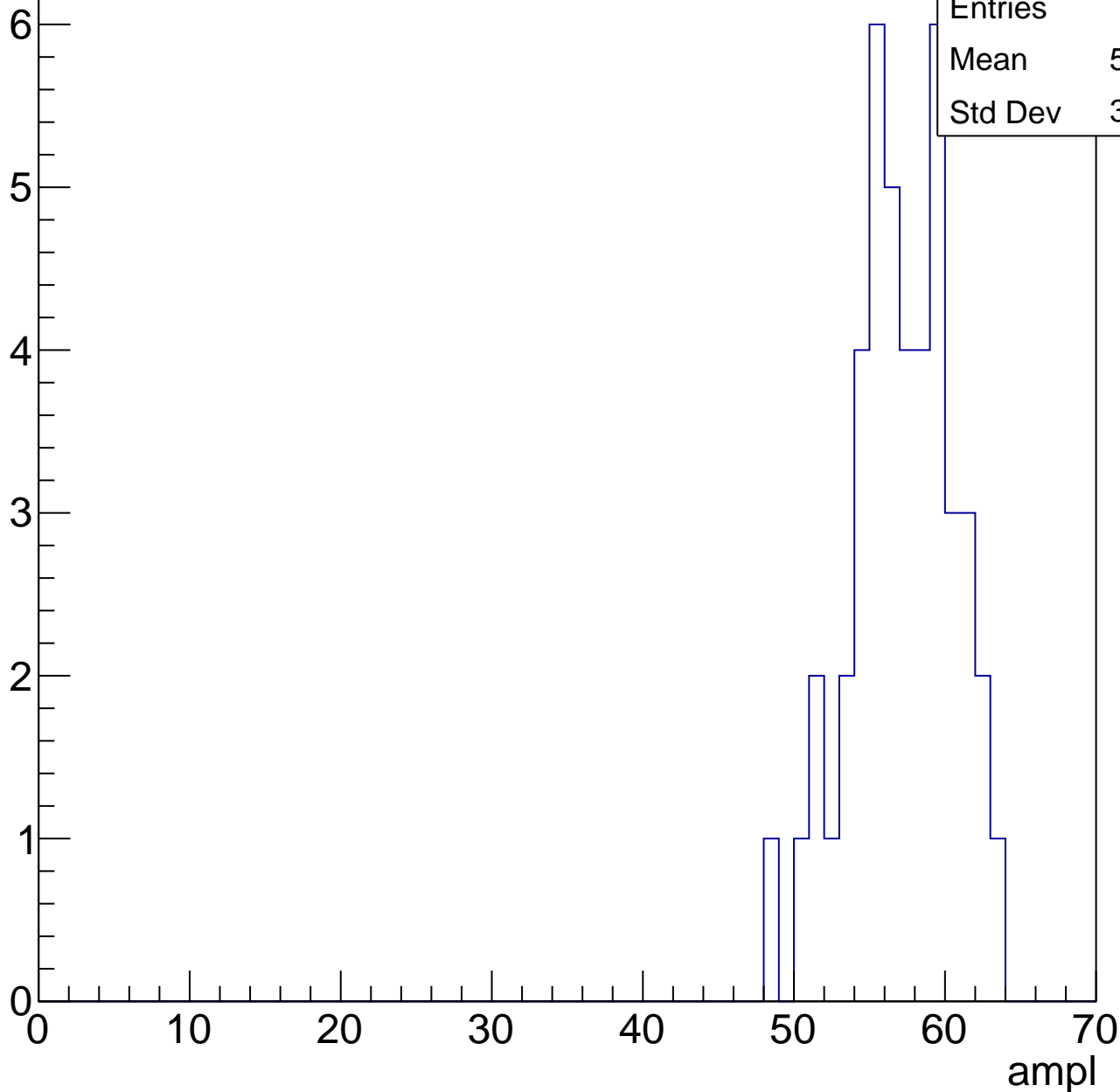


B0L102S, U20-ch31, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 56.62 |
| Std Dev | 3.355 |

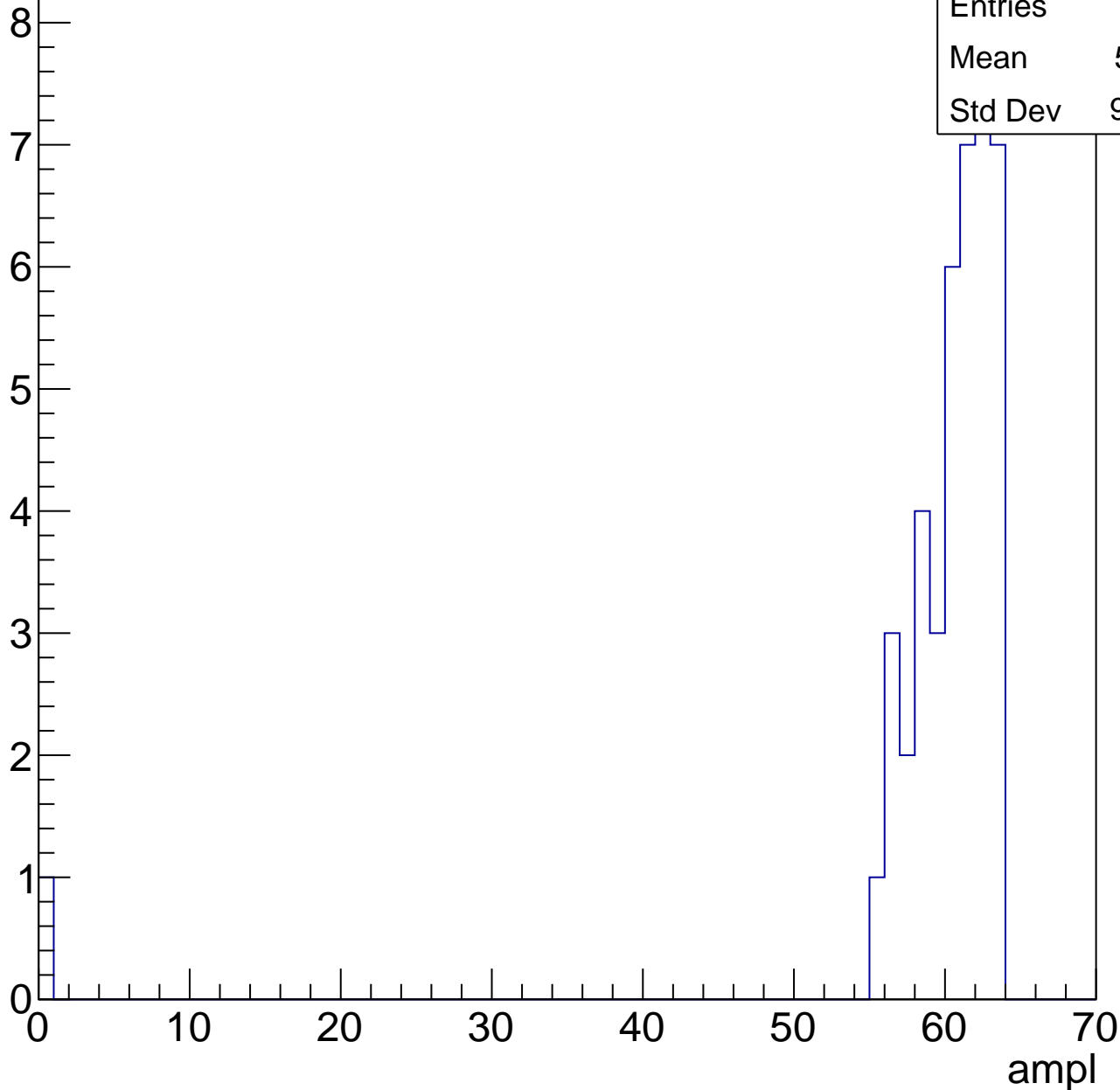


B0L102S, U20-ch31, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.81 |
| Std Dev | 9.452 |



B0L102S, U20-ch31, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 62 |
| Std Dev | 1.414 |

B0L102S, U20-ch31, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch32, adc0

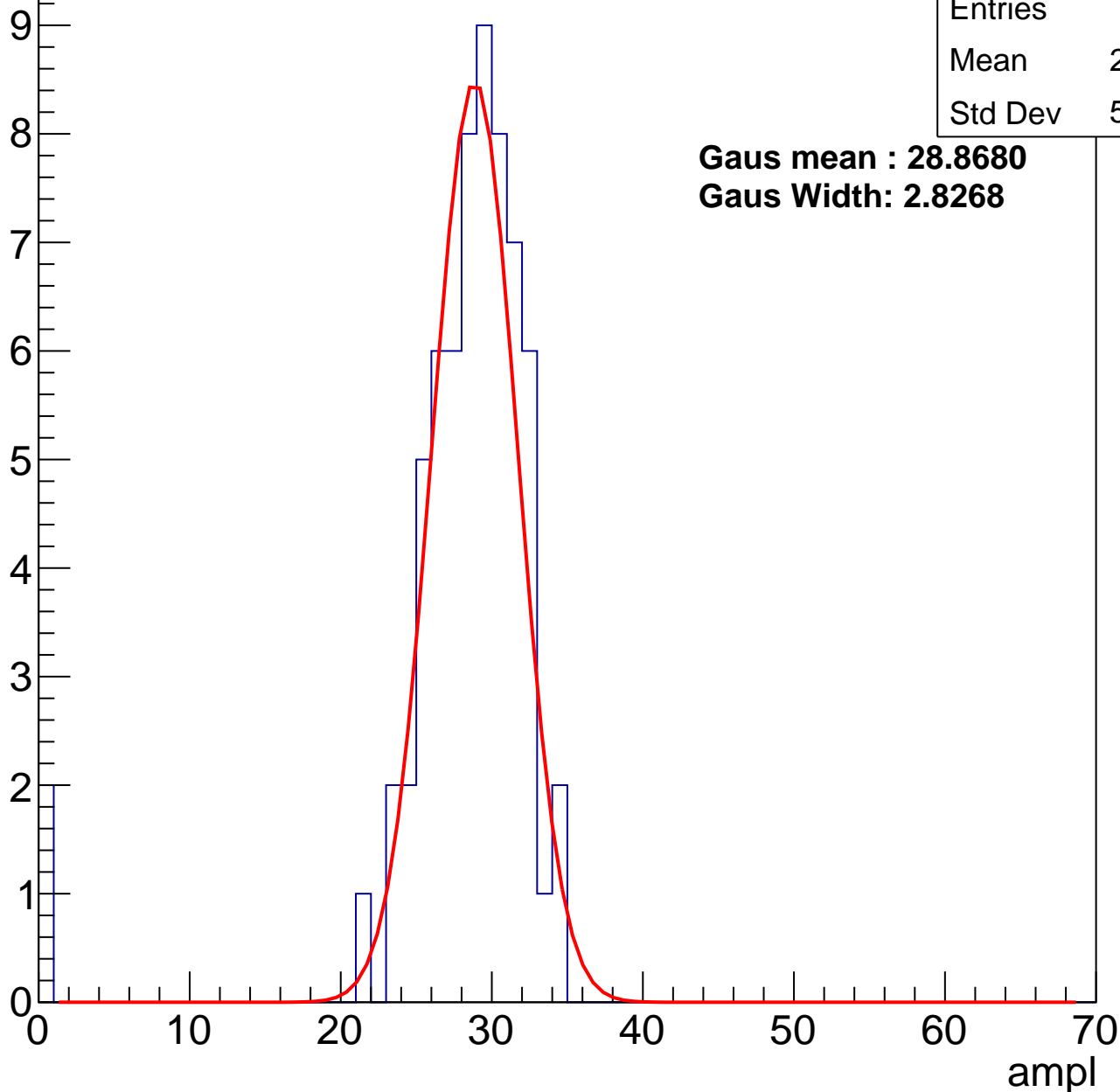
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 27.58 |
| Std Dev | 5.629 |

Gaus mean : 28.8680

Gaus Width: 2.8268



B0L102S, U20-ch32, adc1

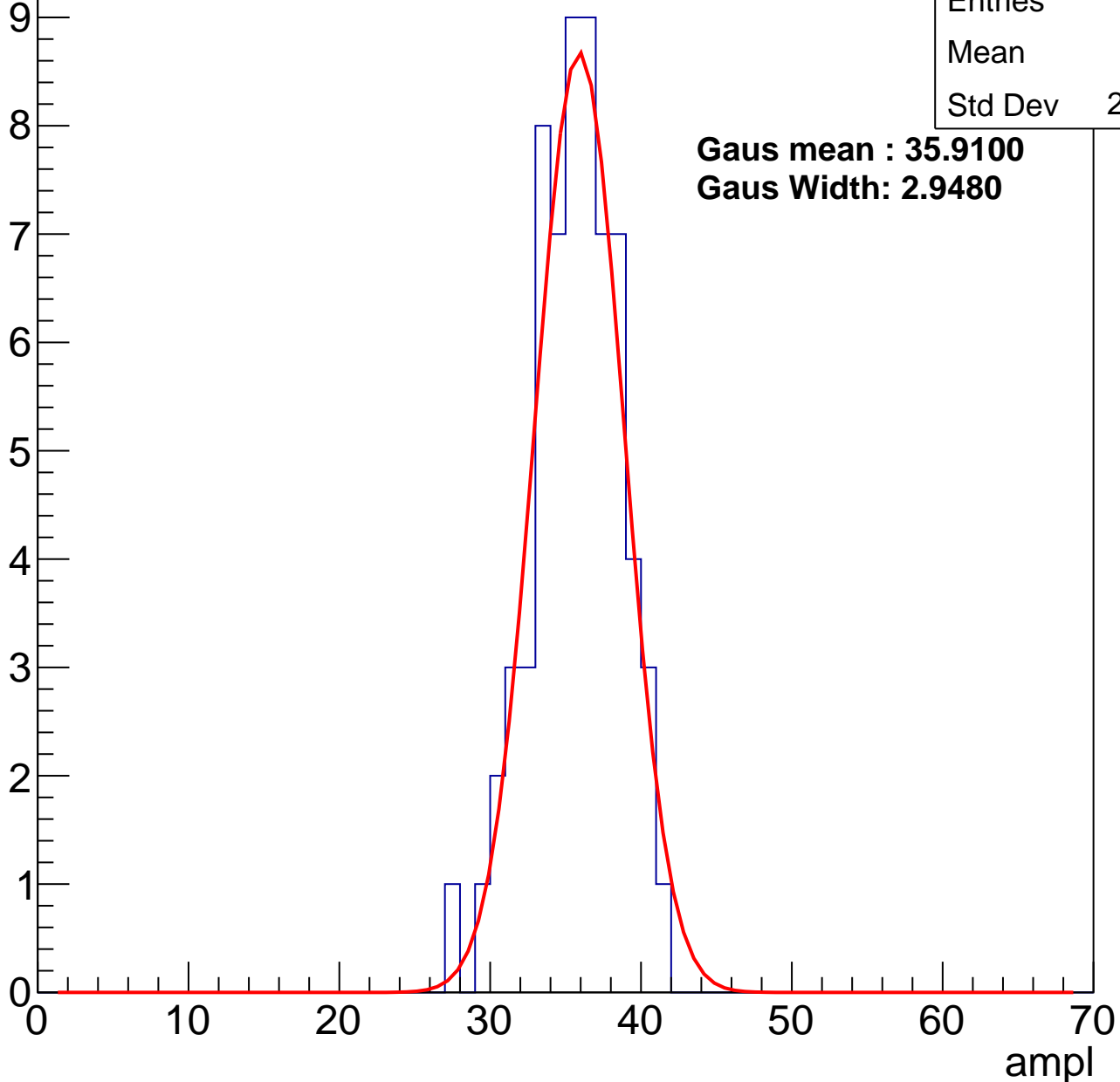
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.2 |
| Std Dev | 2.873 |

Gaus mean : 35.9100

Gaus Width: 2.9480



B0L102S, U20-ch32, adc2

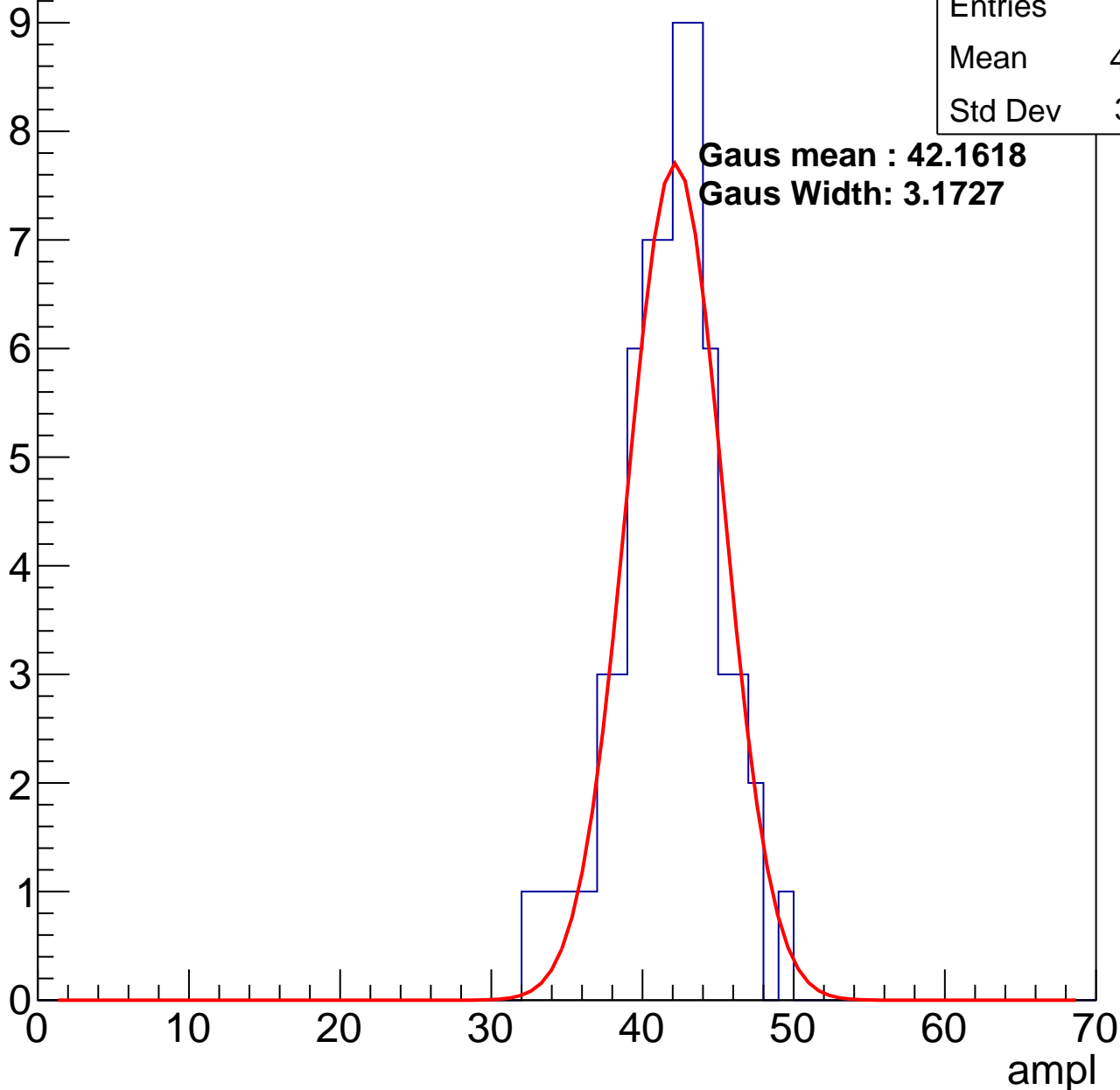
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 41.27 |
| Std Dev | 3.341 |

Gaus mean : 42.1618

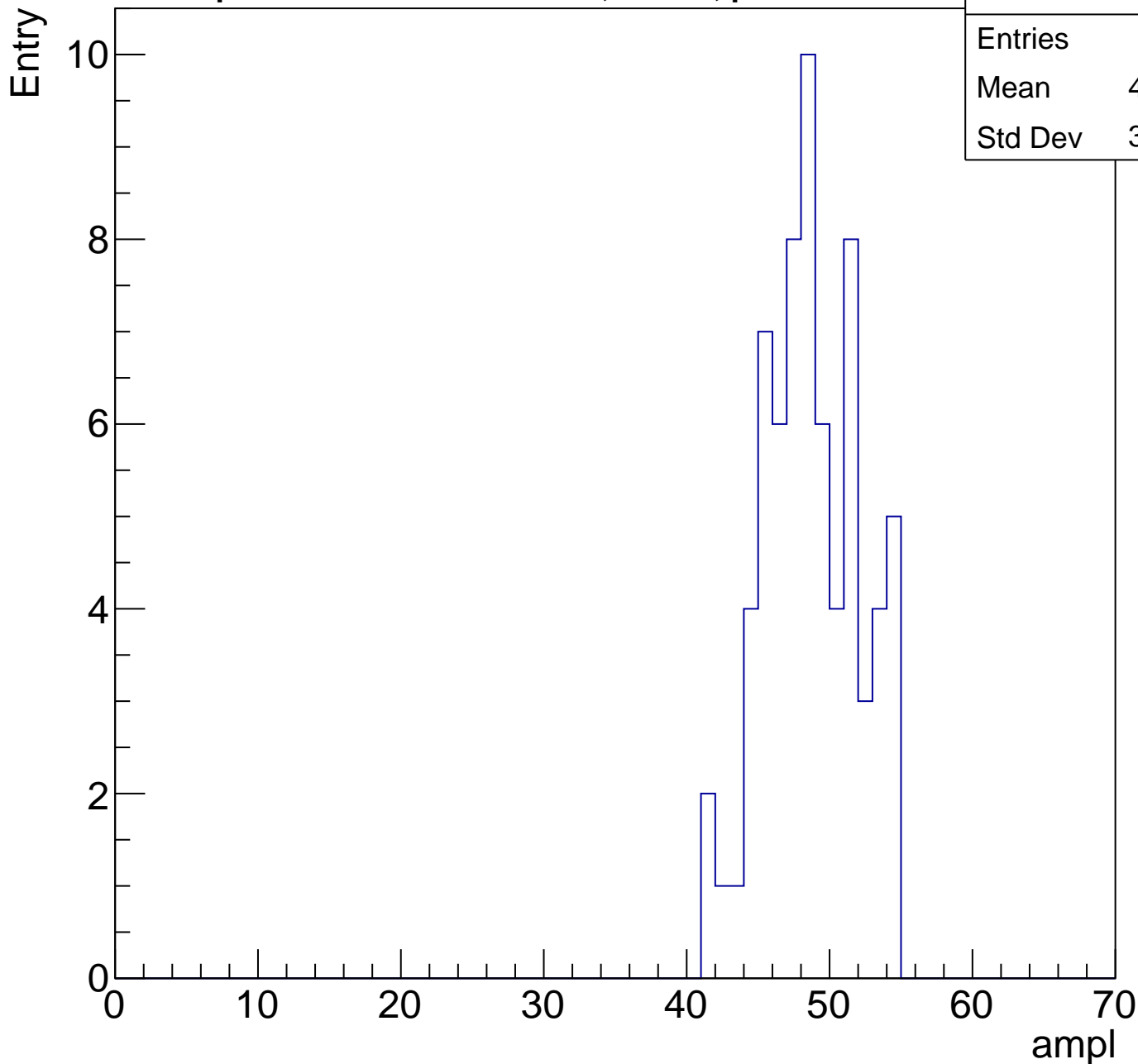
Gaus Width: 3.1727



B0L102S, U20-ch32, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.26 |
| Std Dev | 3.264 |

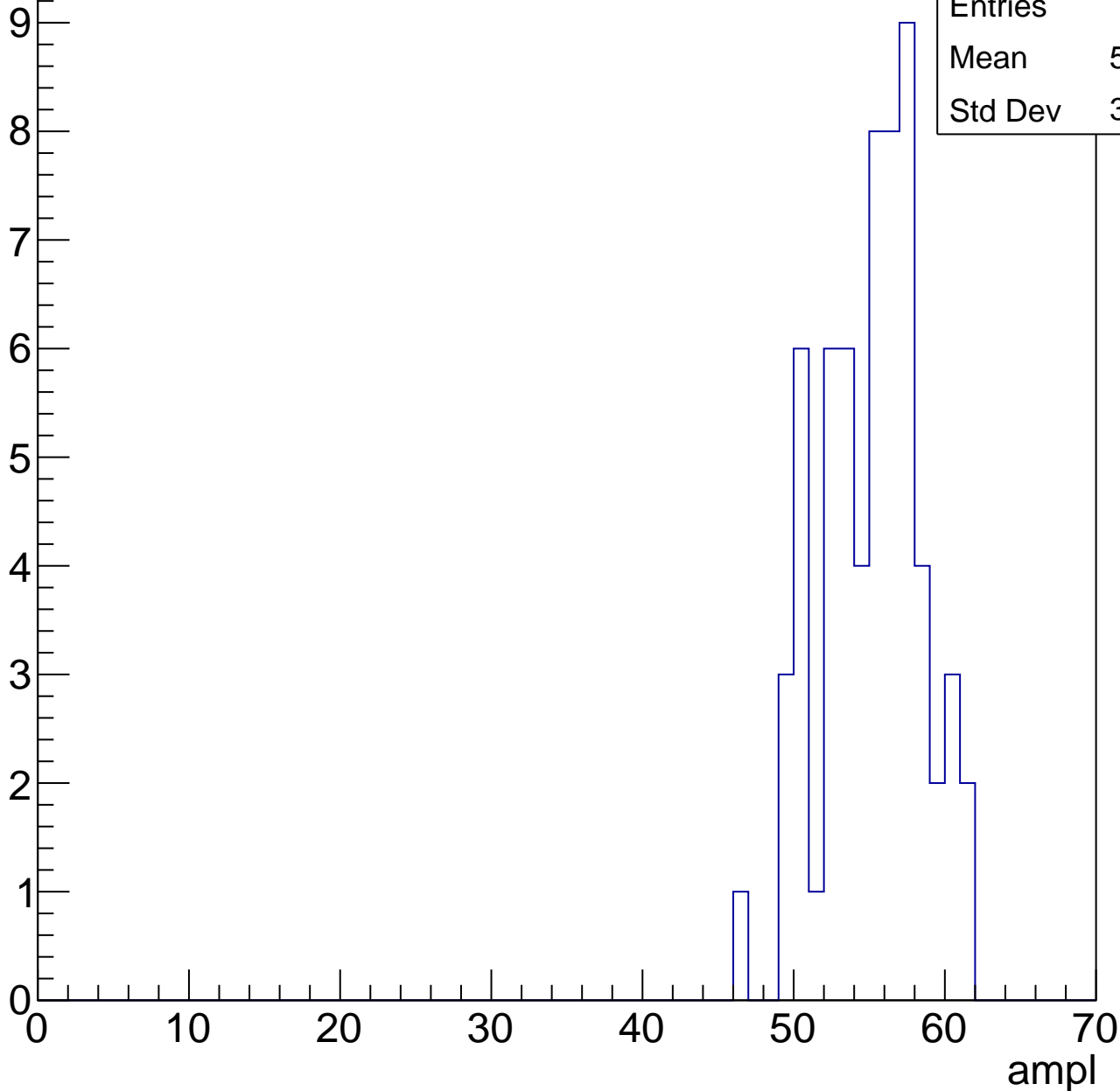


B0L102S, U20-ch32, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

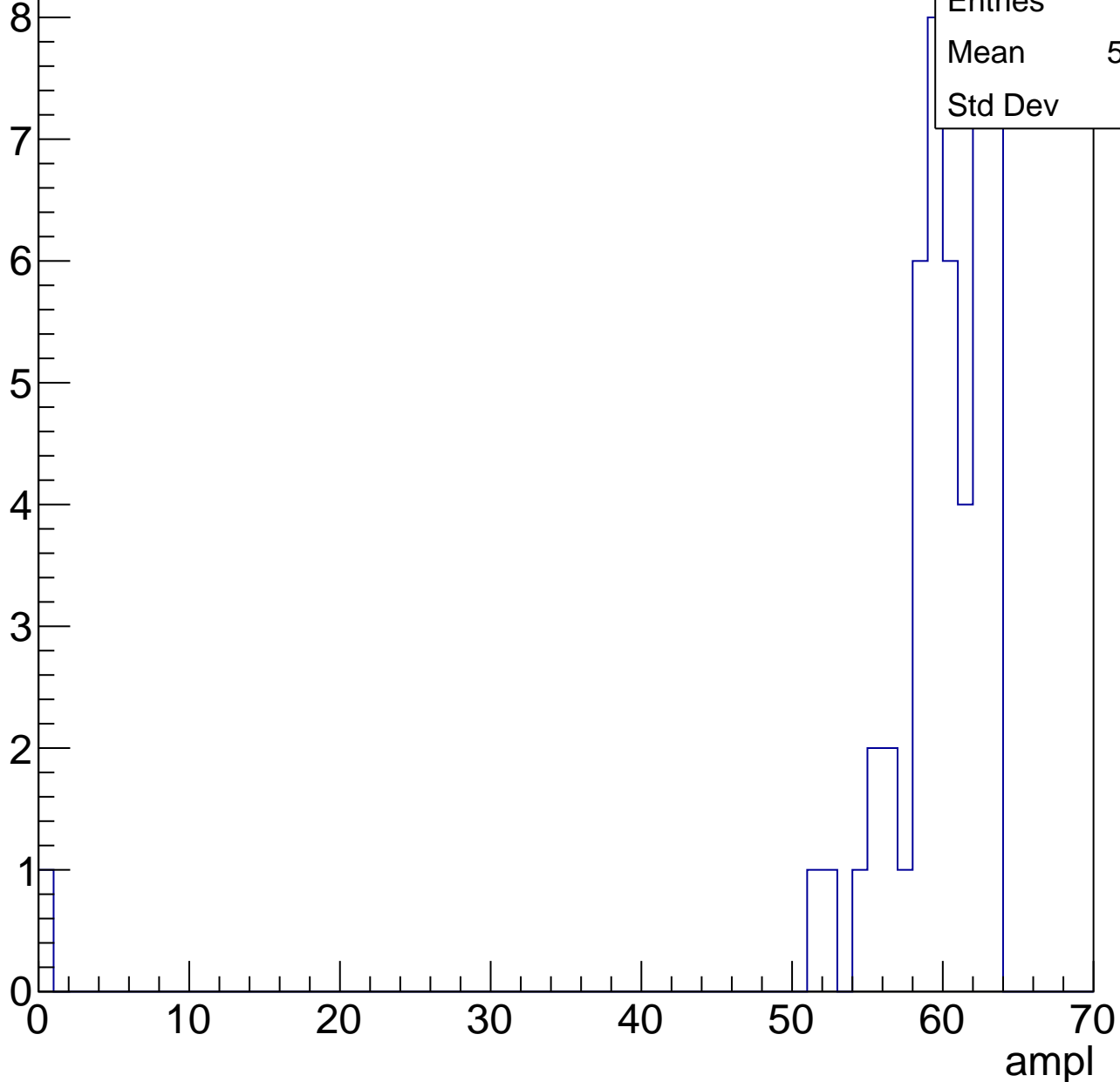
| | |
|---------|-------|
| Entries | 63 |
| Mean | 54.65 |
| Std Dev | 3.305 |



B0L102S, U20-ch32, adc5

calib_packv5_042523_0143.root, FC#12, port B1

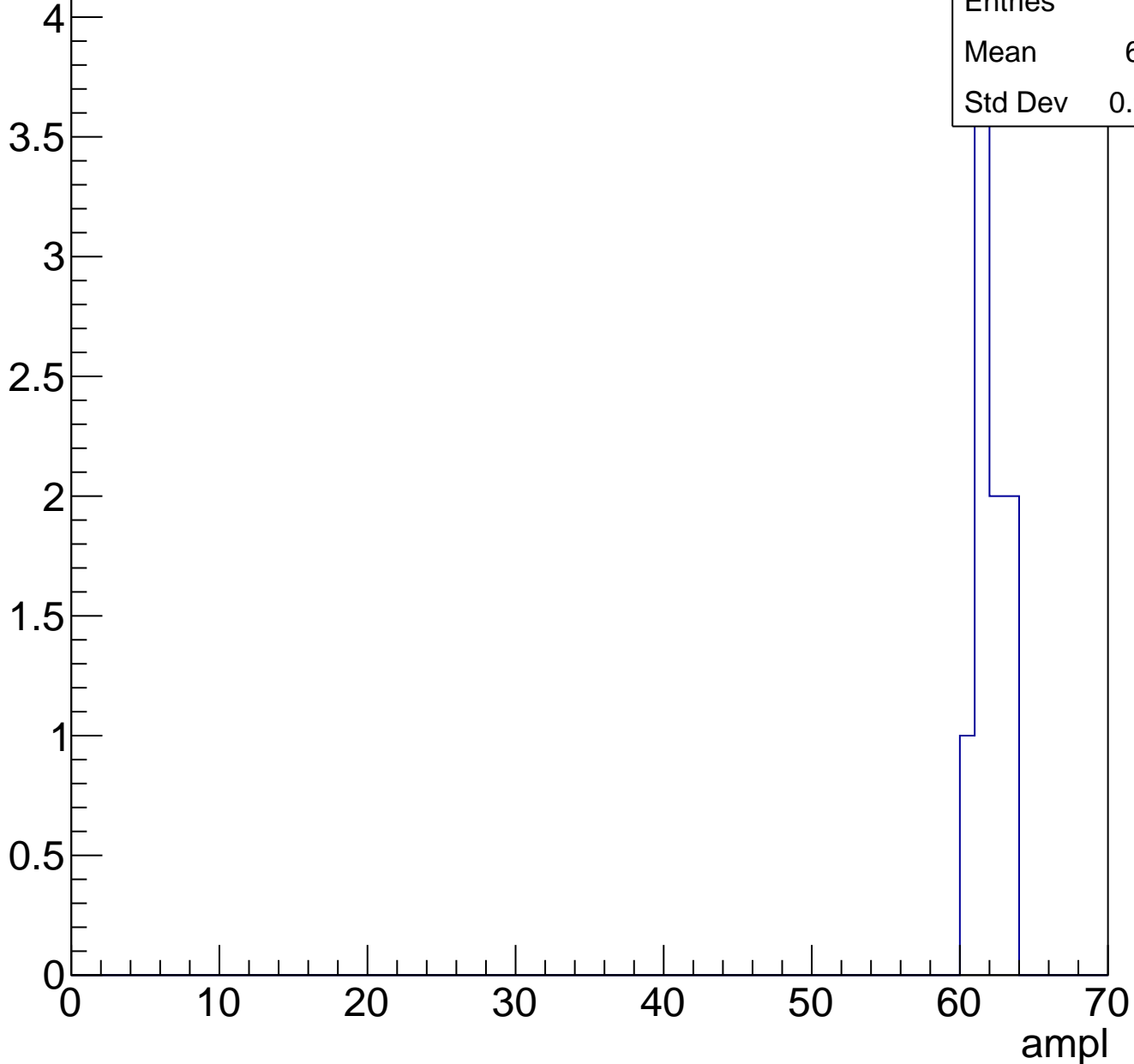
Entry



B0L102S, U20-ch32, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch32, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch33, adc0

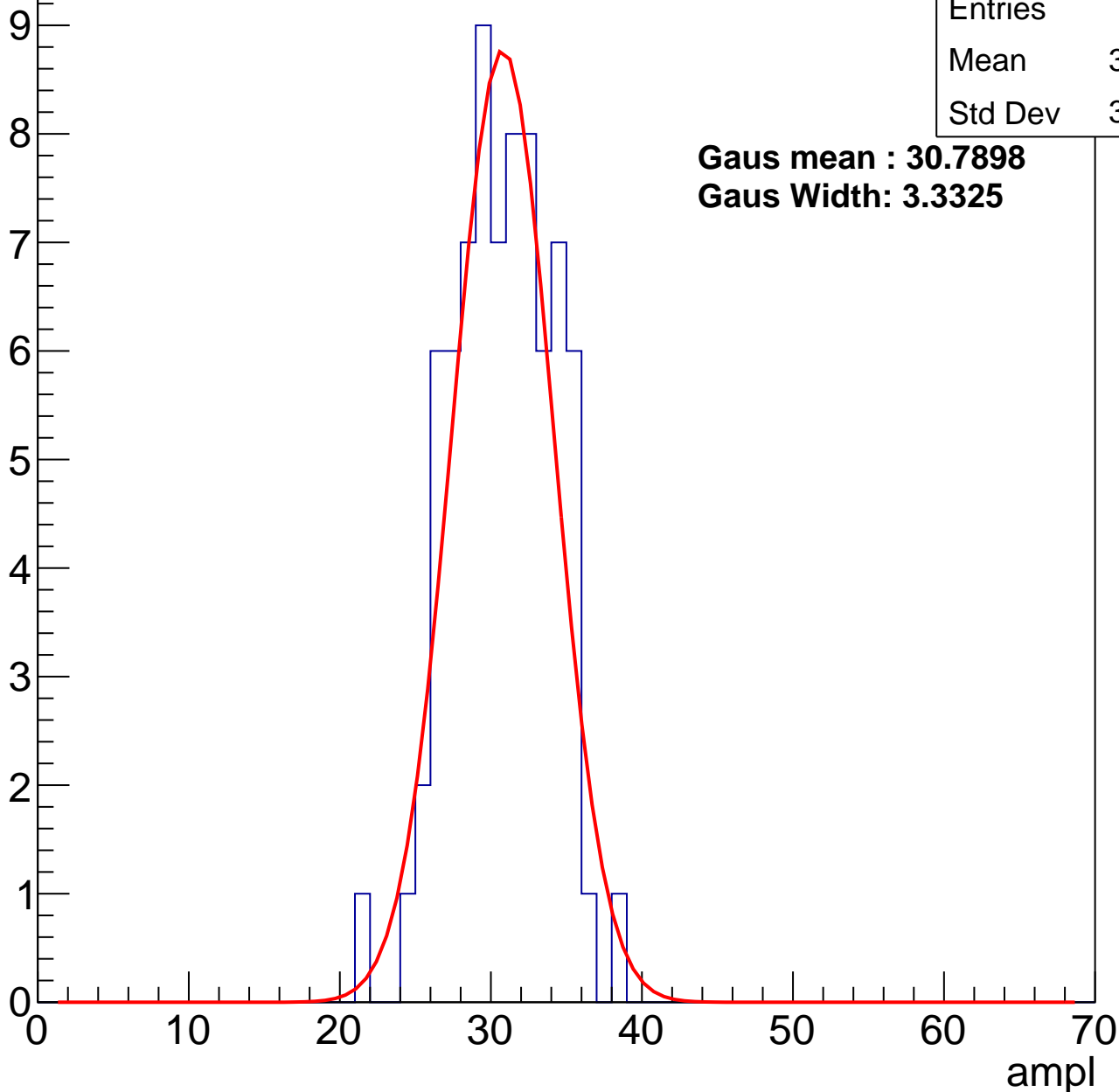
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.32 |
| Std Dev | 3.249 |

Gaus mean : 30.7898

Gaus Width: 3.3325



B0L102S, U20-ch33, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 73 |
| Mean | 37.95 |
| Std Dev | 3.448 |

Gaus mean : 38.4140

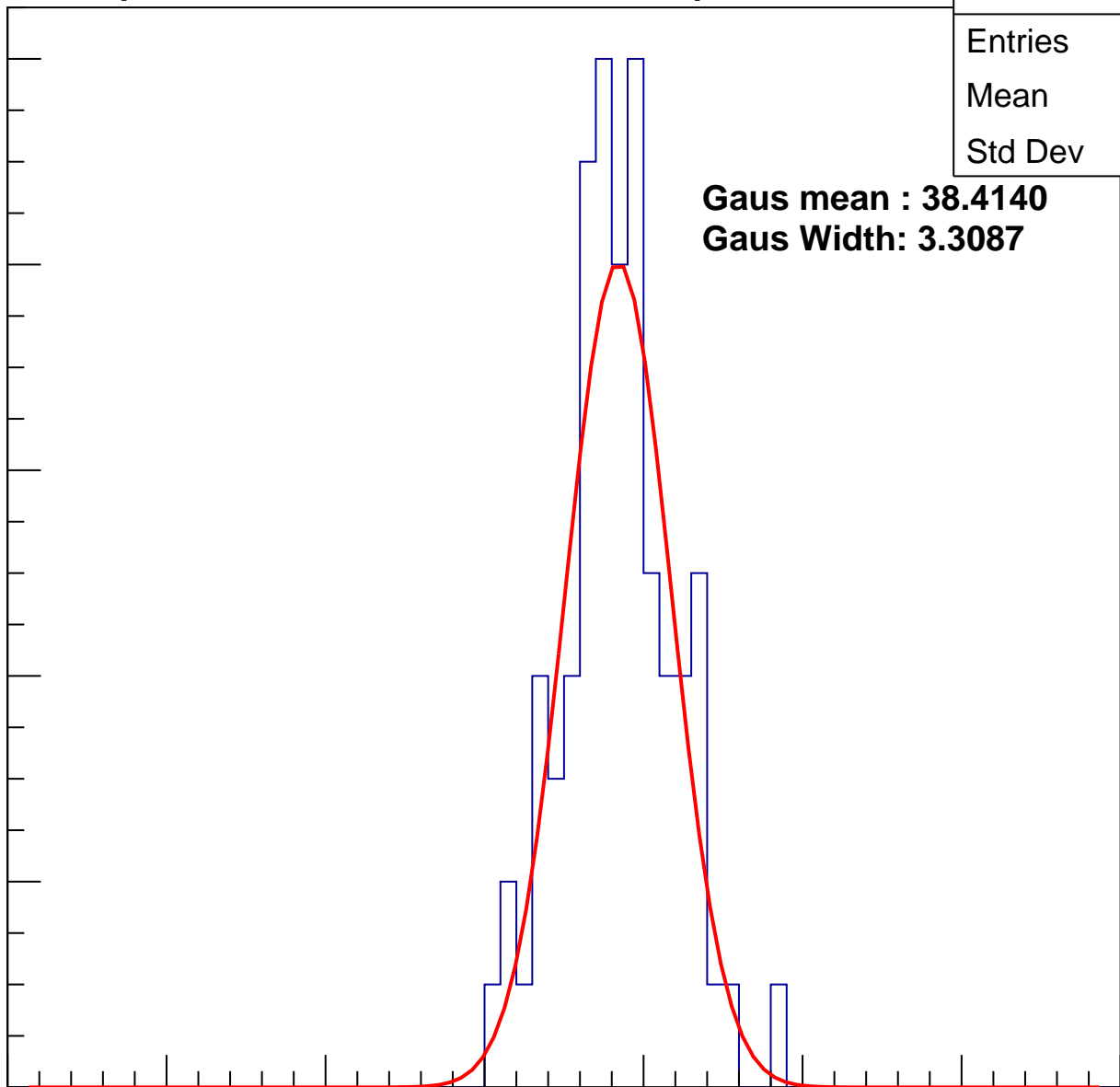
Gaus Width: 3.3087

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch33, adc2

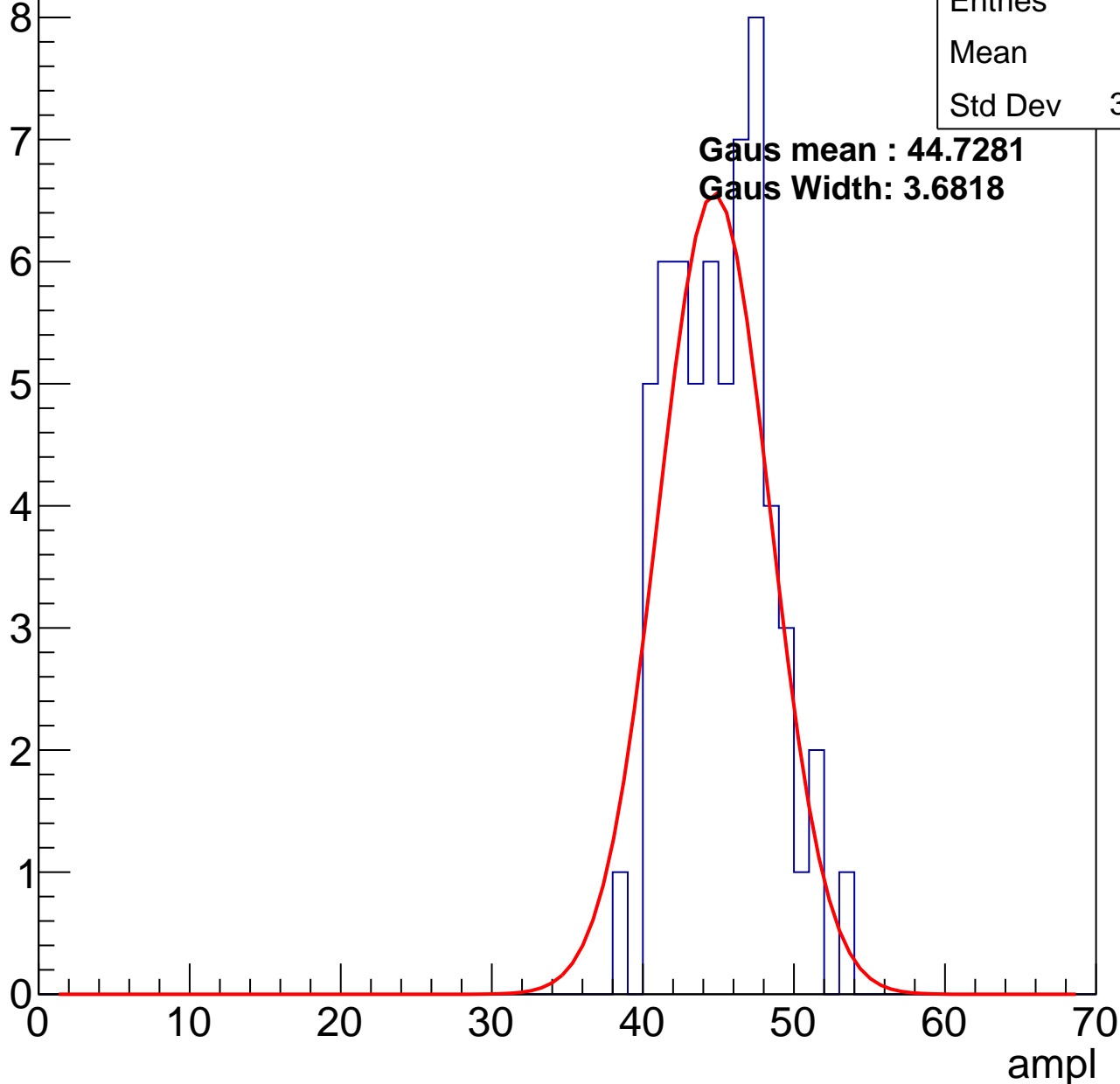
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 44.7 |
| Std Dev | 3.237 |

Gaus mean : 44.7281

Gaus Width: 3.6818

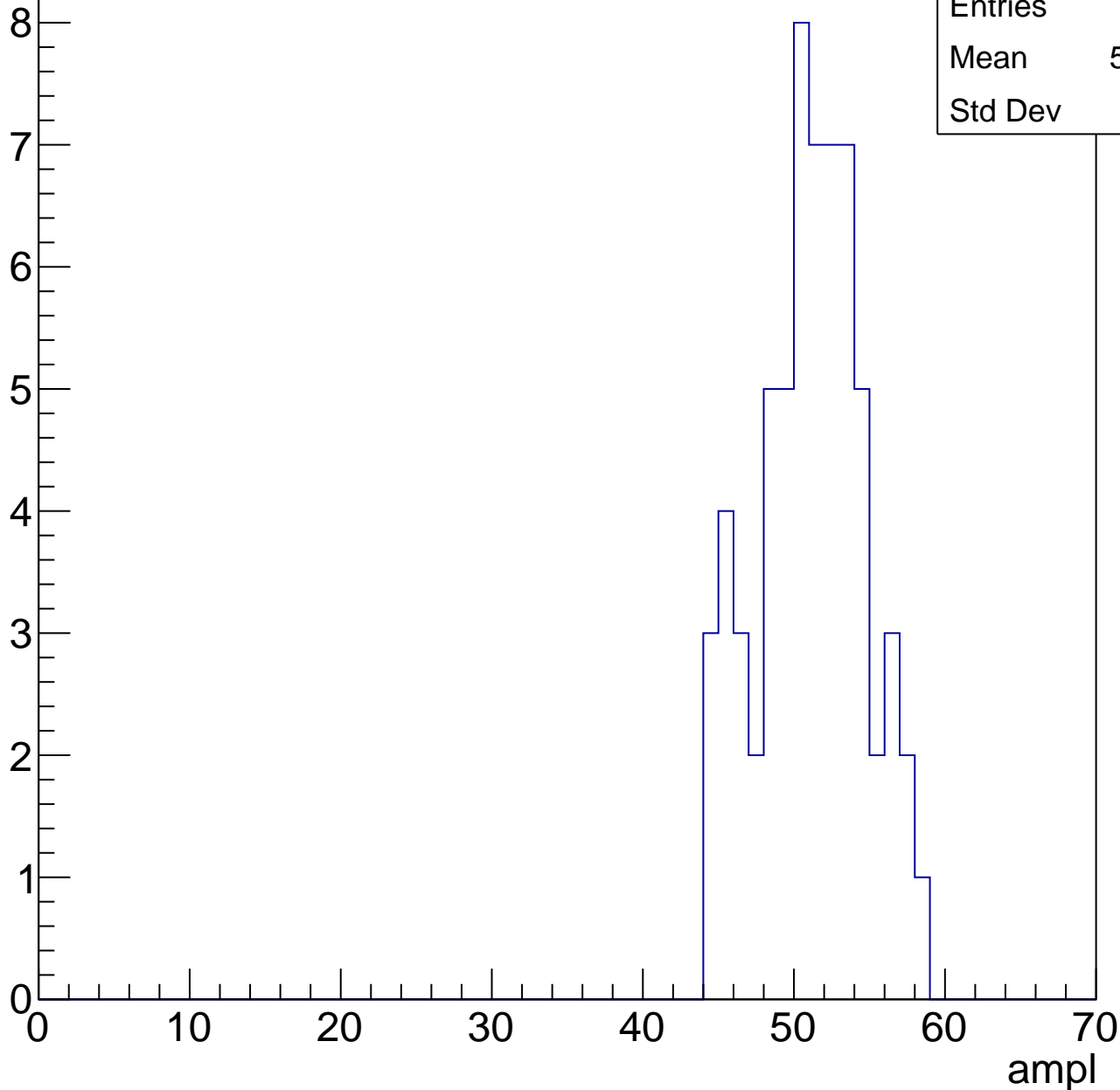


B0L102S, U20-ch33, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.64 |
| Std Dev | 3.47 |

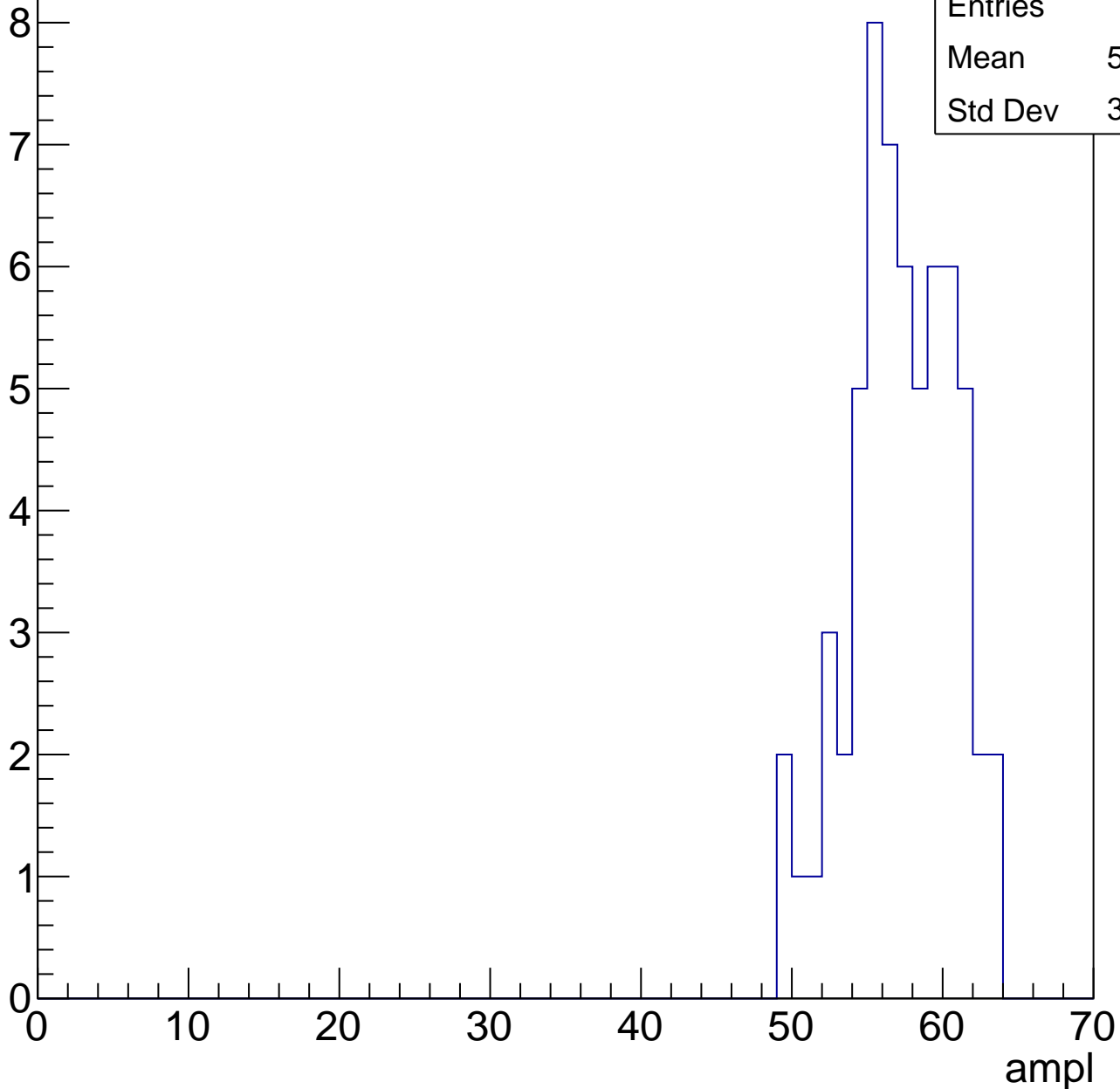


B0L102S, U20-ch33, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 56.79 |
| Std Dev | 3.354 |

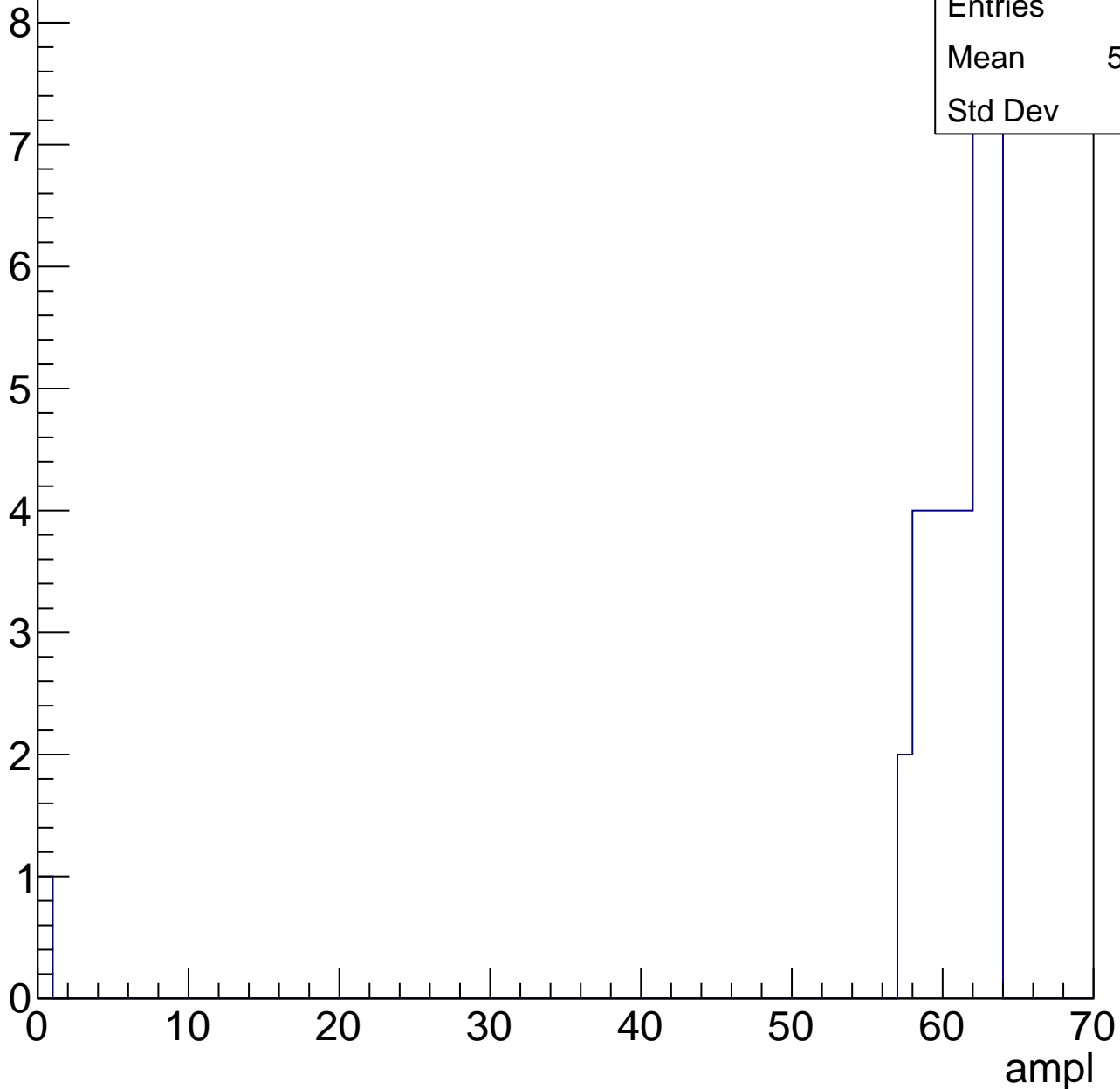


B0L102S, U20-ch33, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 59.03 |
| Std Dev | 10.3 |



B0L102S, U20-ch33, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch33, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch34, adc0

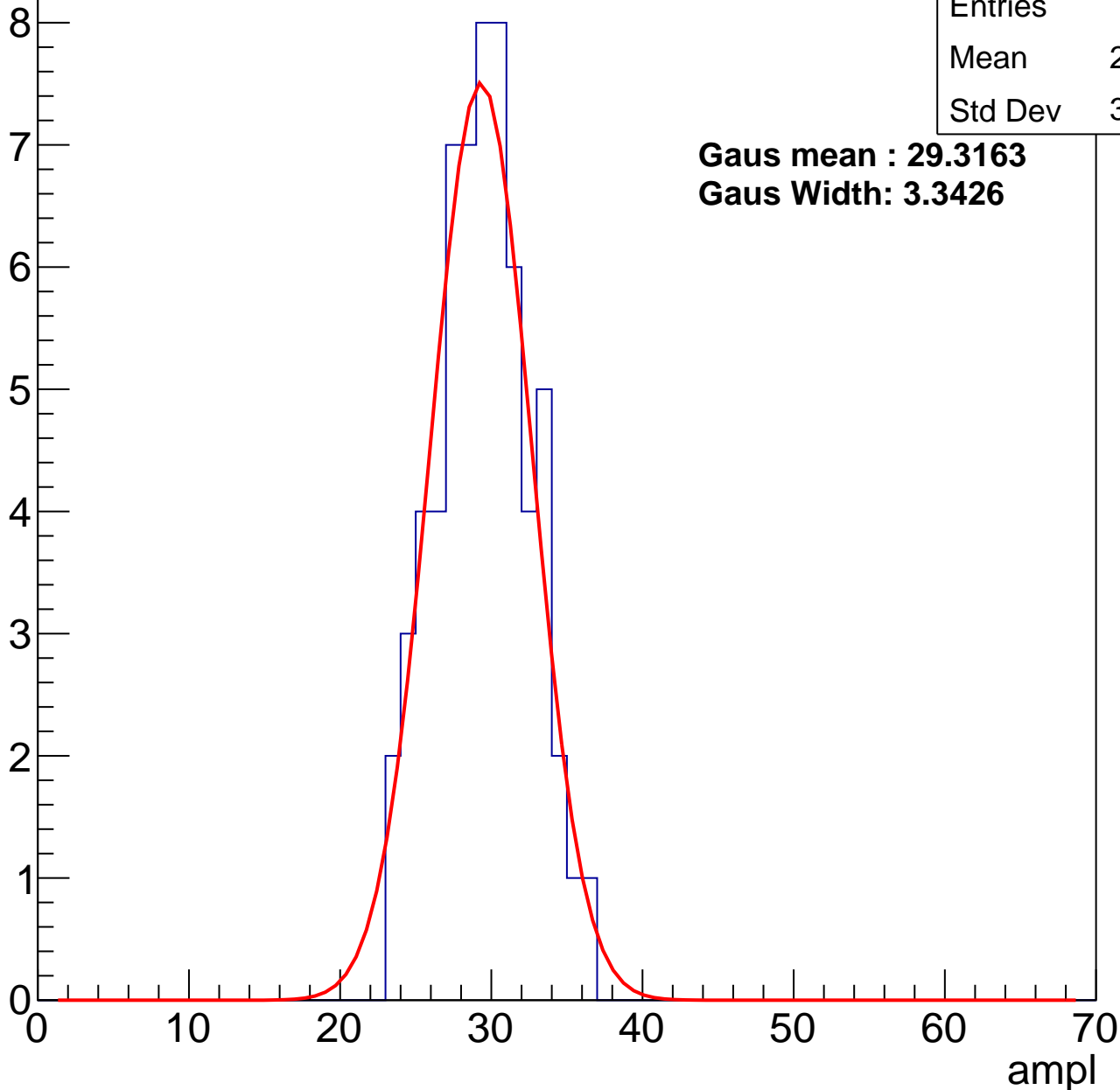
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.98 |
| Std Dev | 3.019 |

Gaus mean : 29.3163

Gaus Width: 3.3426



B0L102S, U20-ch34, adc1

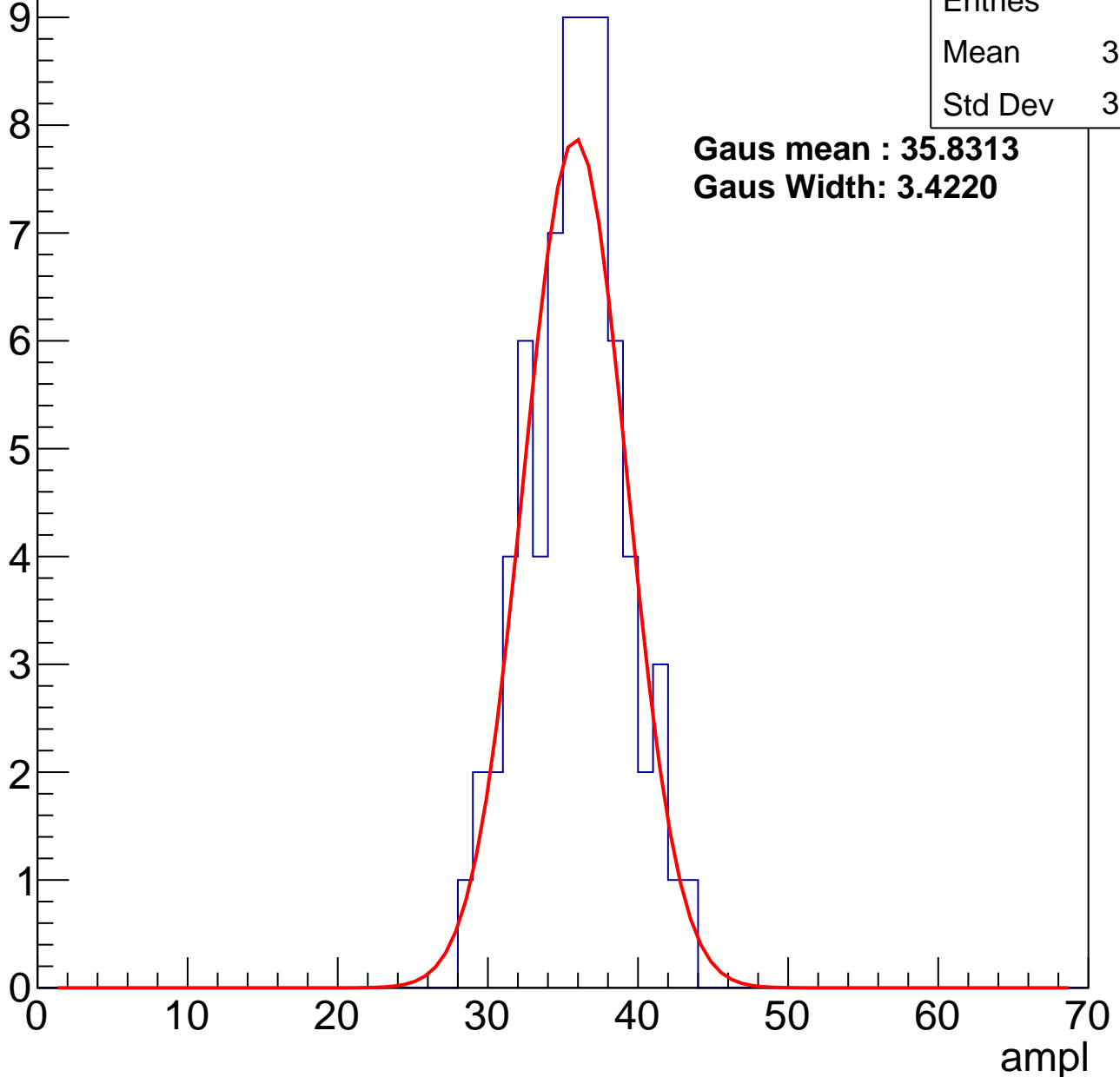
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.37 |
| Std Dev | 3.243 |

Gaus mean : 35.8313

Gaus Width: 3.4220



B0L102S, U20-ch34, adc2

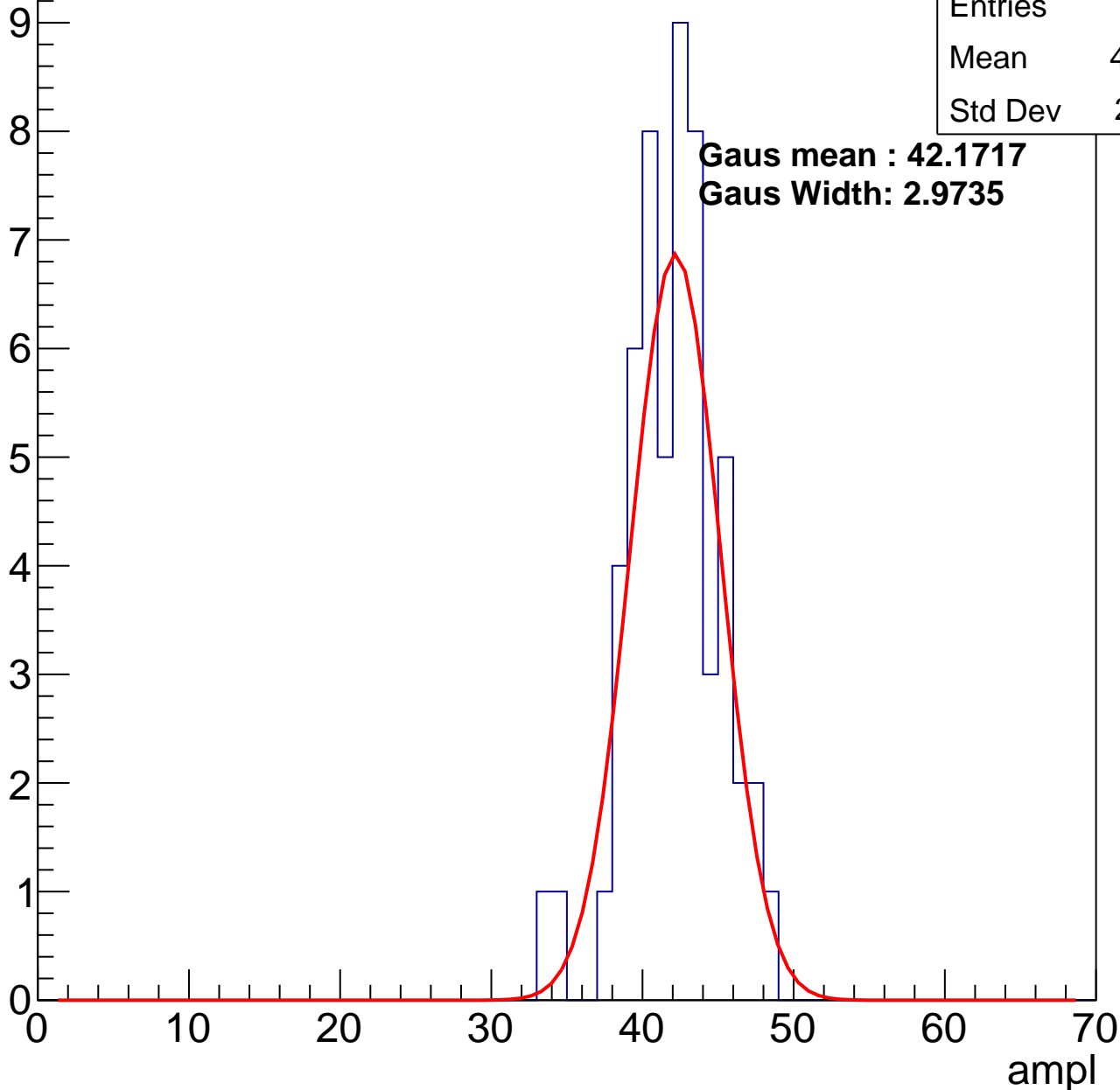
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 41.57 |
| Std Dev | 2.981 |

Gaus mean : 42.1717

Gaus Width: 2.9735

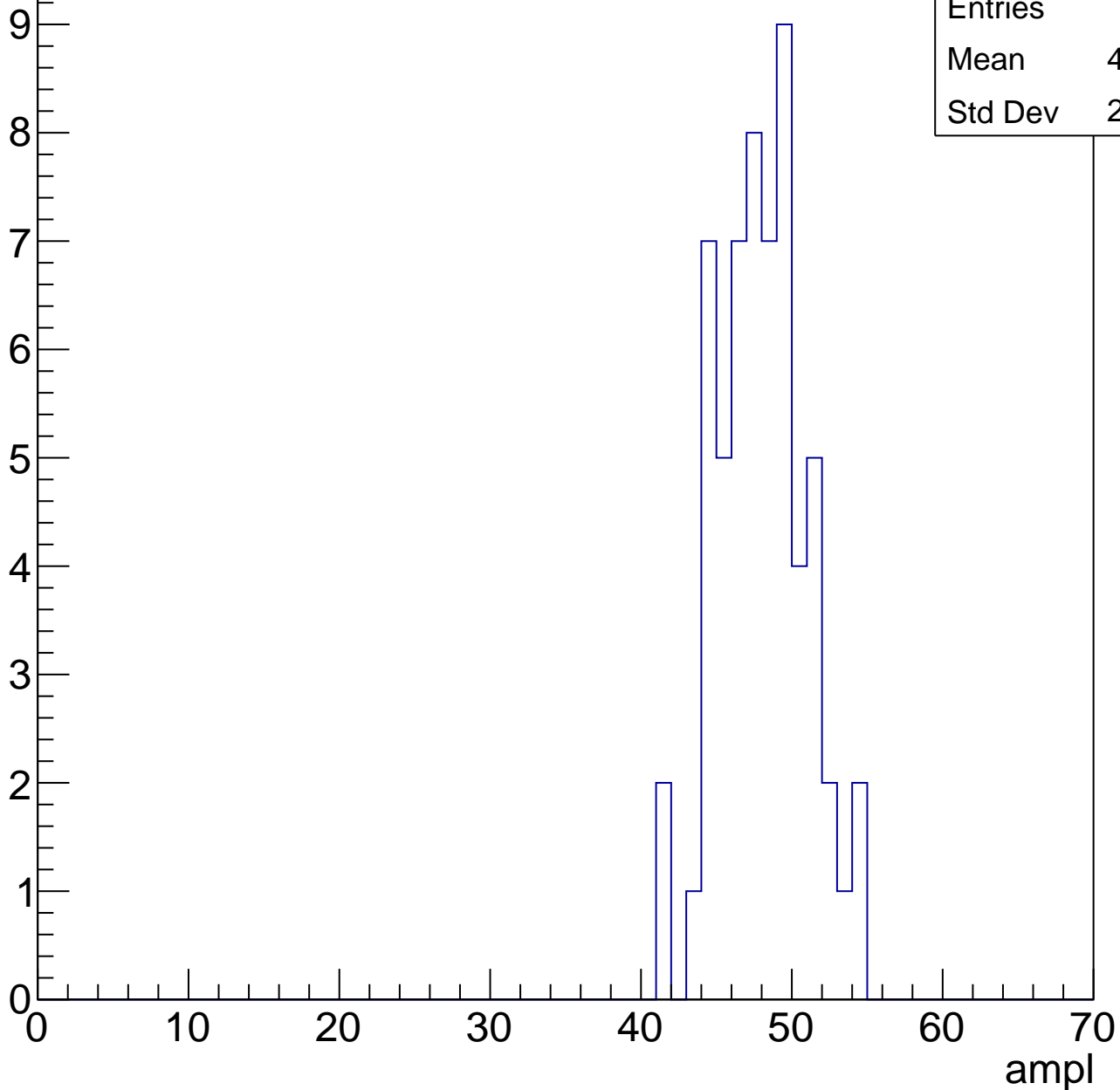


B0L102S, U20-ch34, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

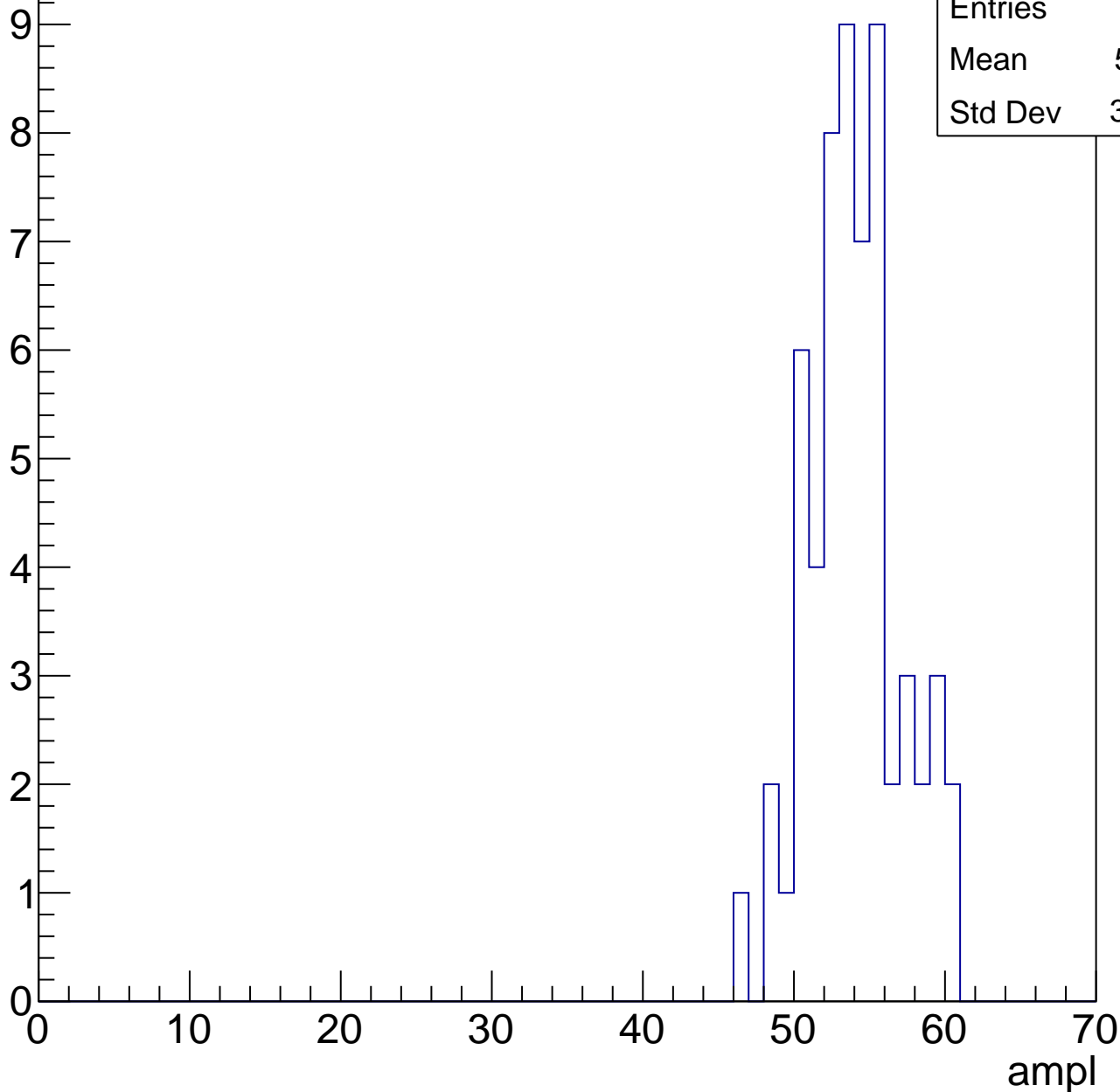
| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.55 |
| Std Dev | 2.895 |



B0L102S, U20-ch34, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 59 |
| Mean | 53.51 |
| Std Dev | 3.033 |

B0L102S, U20-ch34, adc5

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.32 |
| Std Dev | 2.74 |

Entry

10

8

6

4

2

0

0

10

20

30

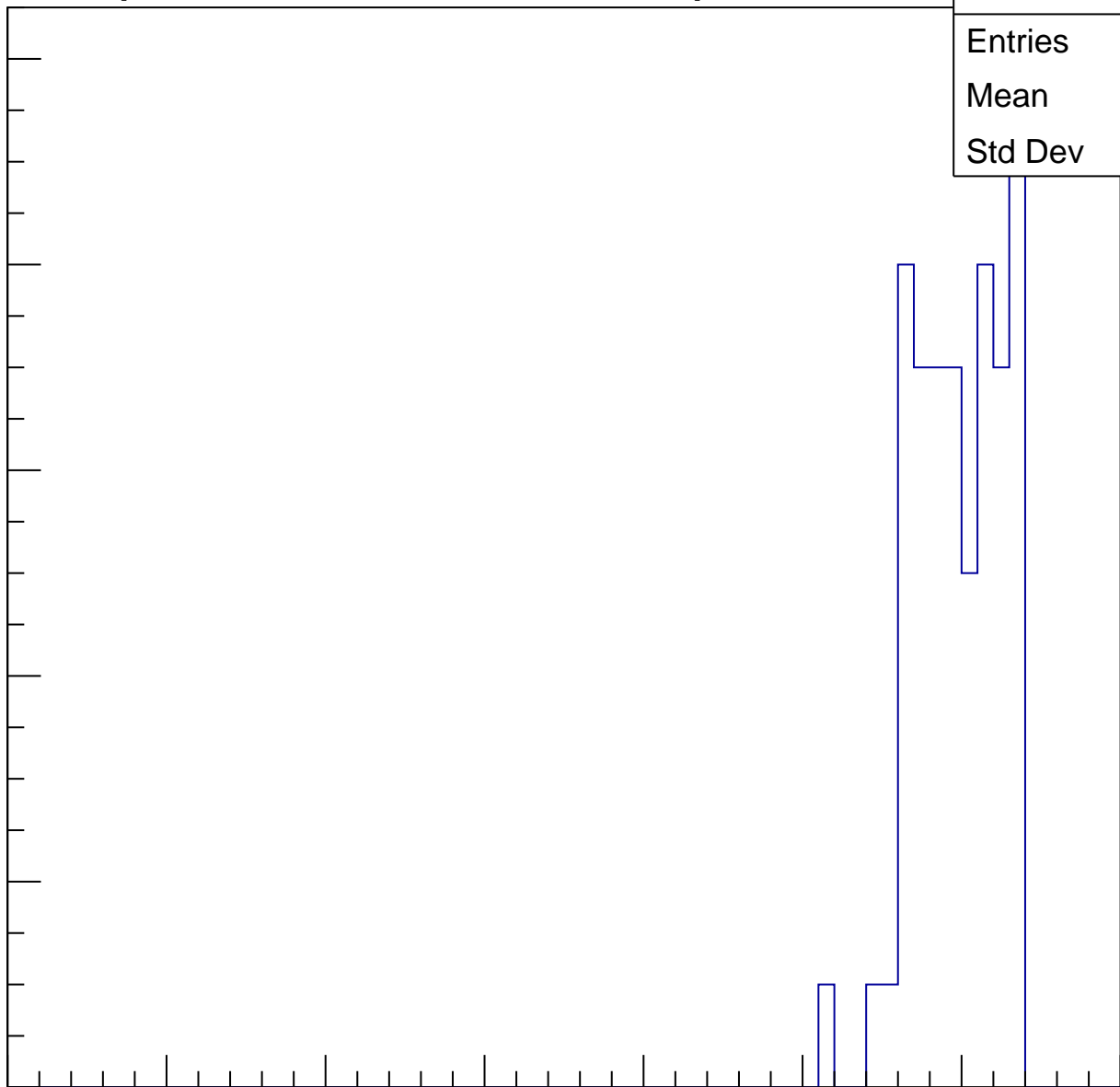
40

50

60

70

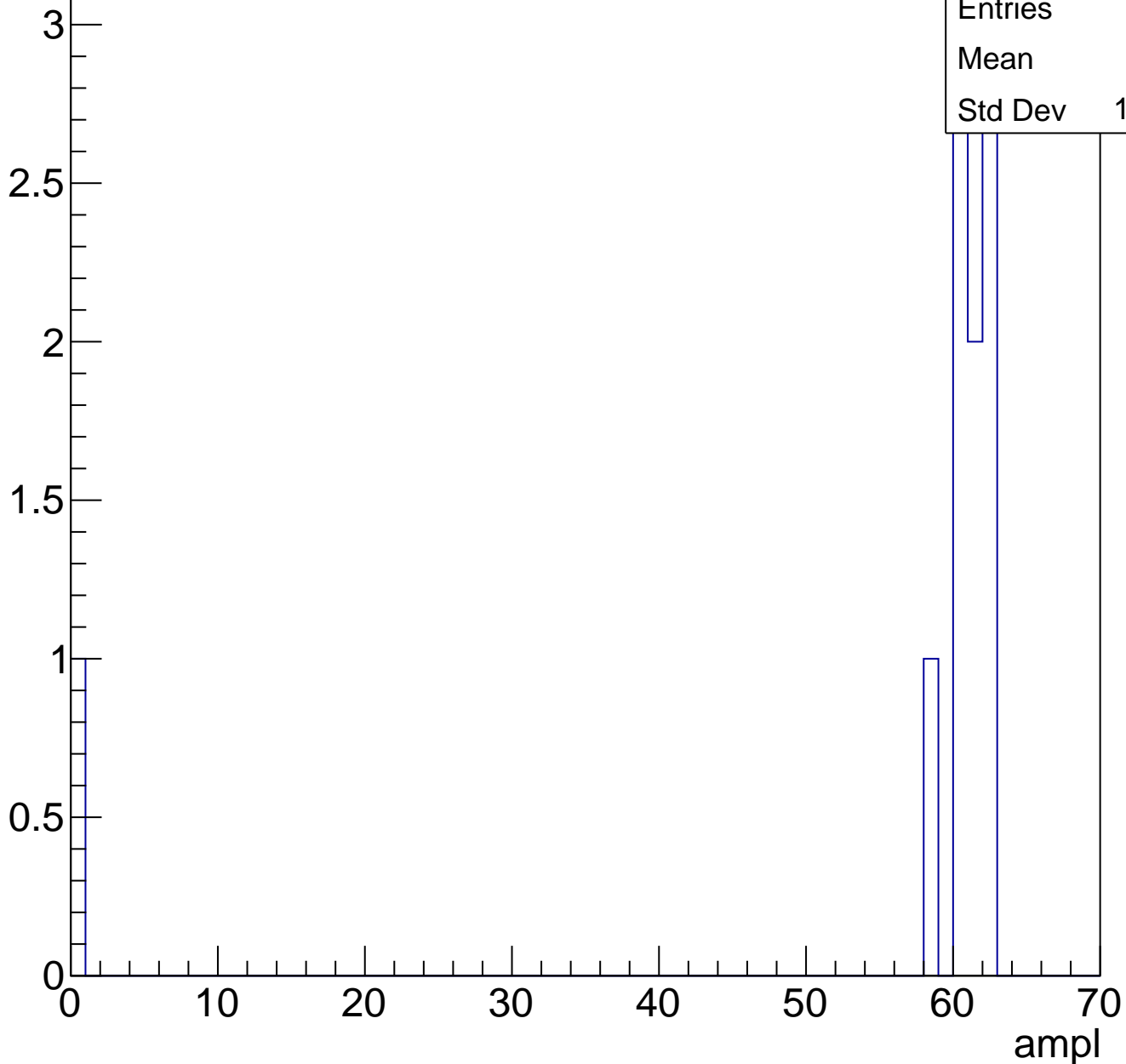
ampl



B0L102S, U20-ch34, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch34, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch35, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 30.57 |
| Std Dev | 4.903 |

Gaus mean : 31.5055

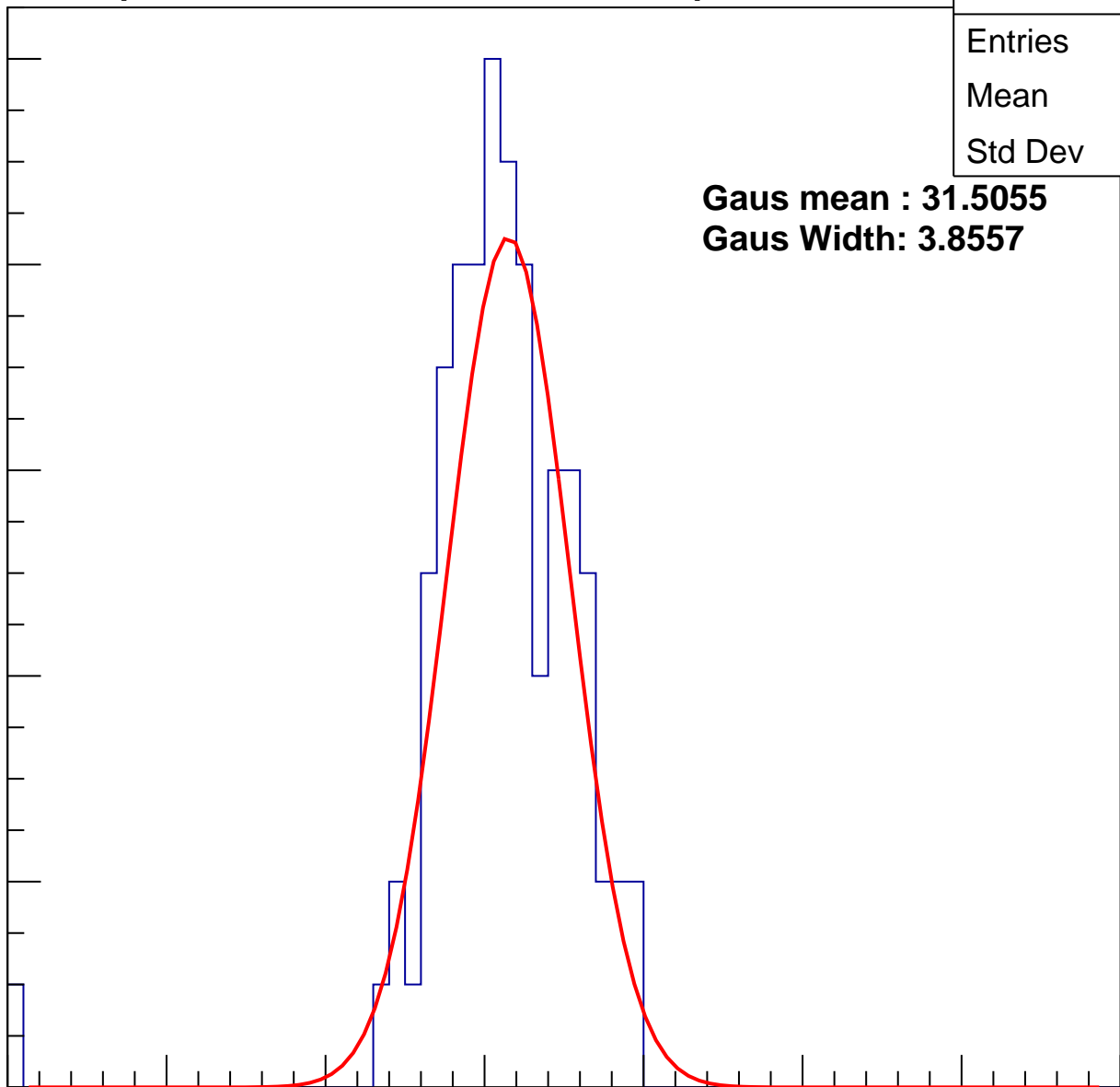
Gaus Width: 3.8557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch35, adc1

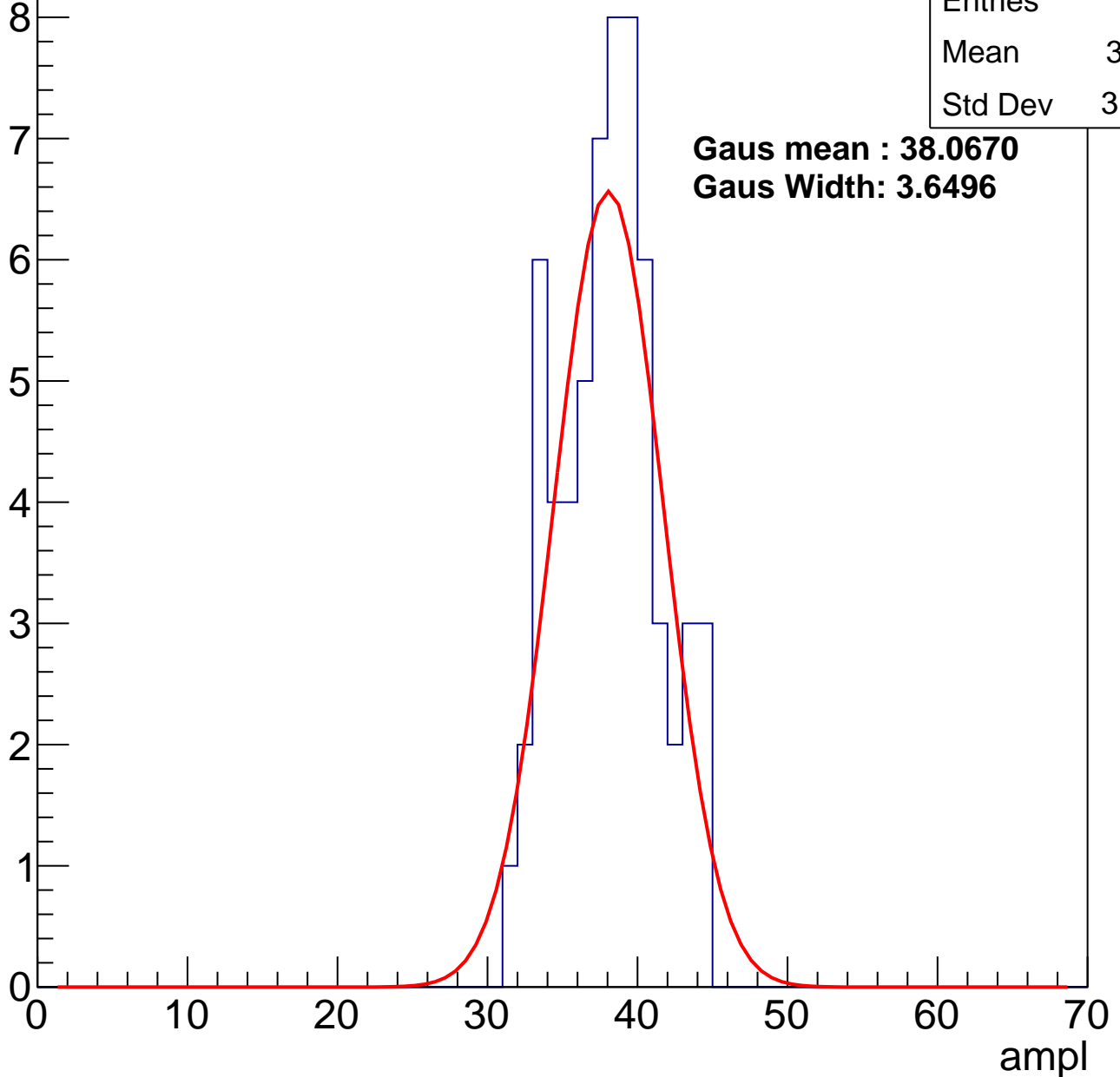
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 37.61 |
| Std Dev | 3.269 |

Gaus mean : 38.0670

Gaus Width: 3.6496

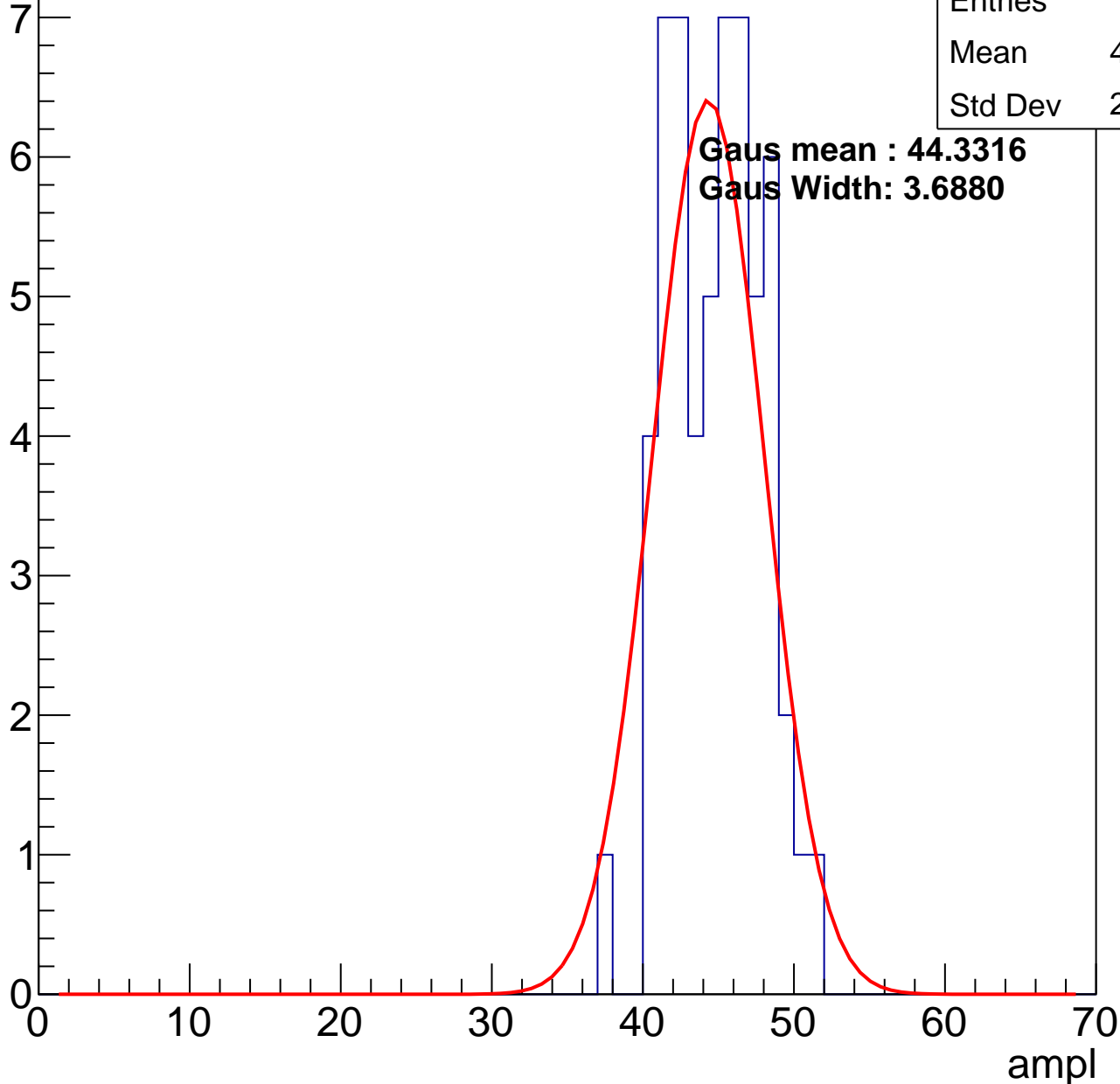


B0L102S, U20-ch35, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

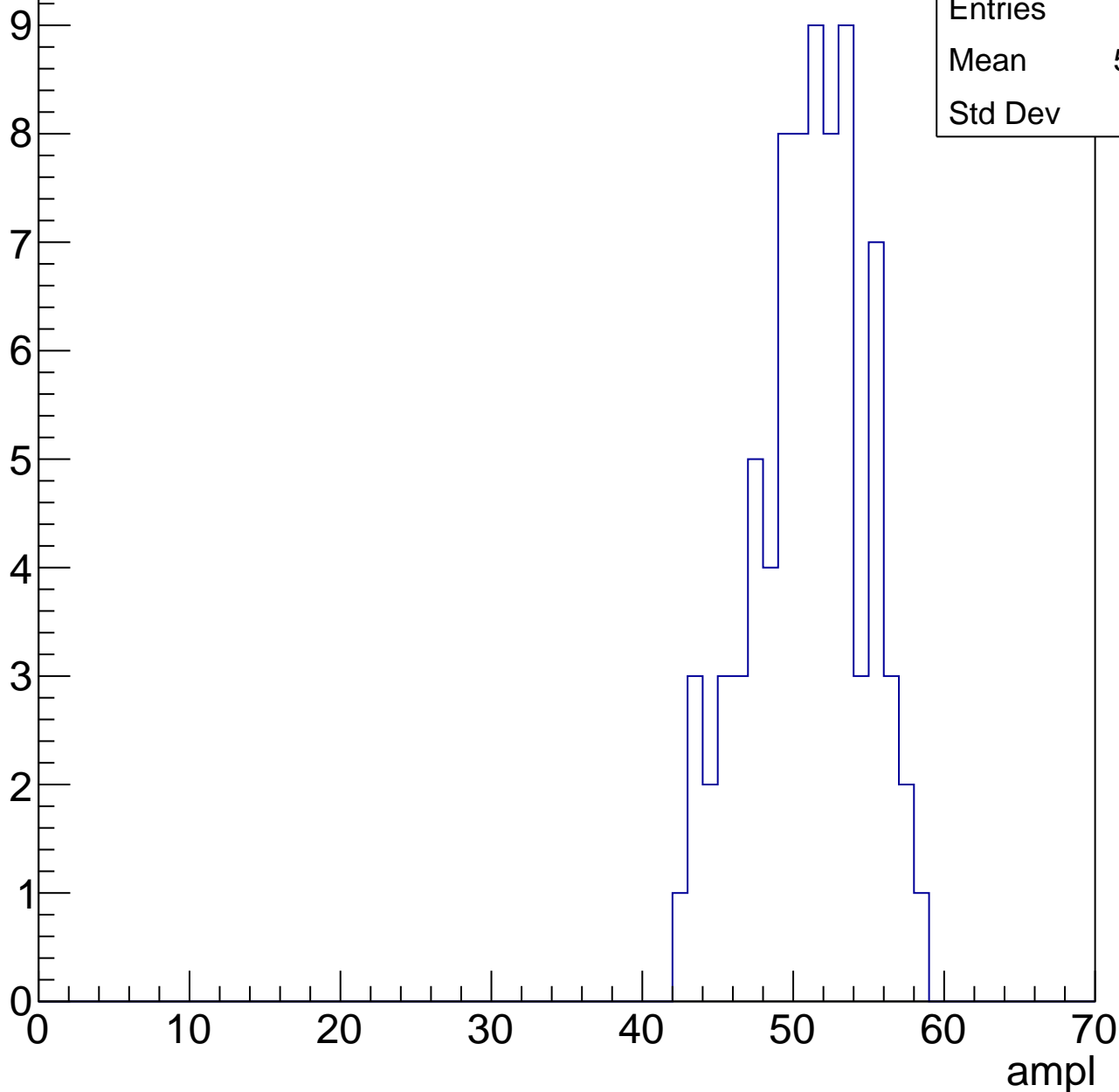
| | |
|---------|-------|
| Entries | 57 |
| Mean | 44.37 |
| Std Dev | 2.995 |



B0L102S, U20-ch35, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

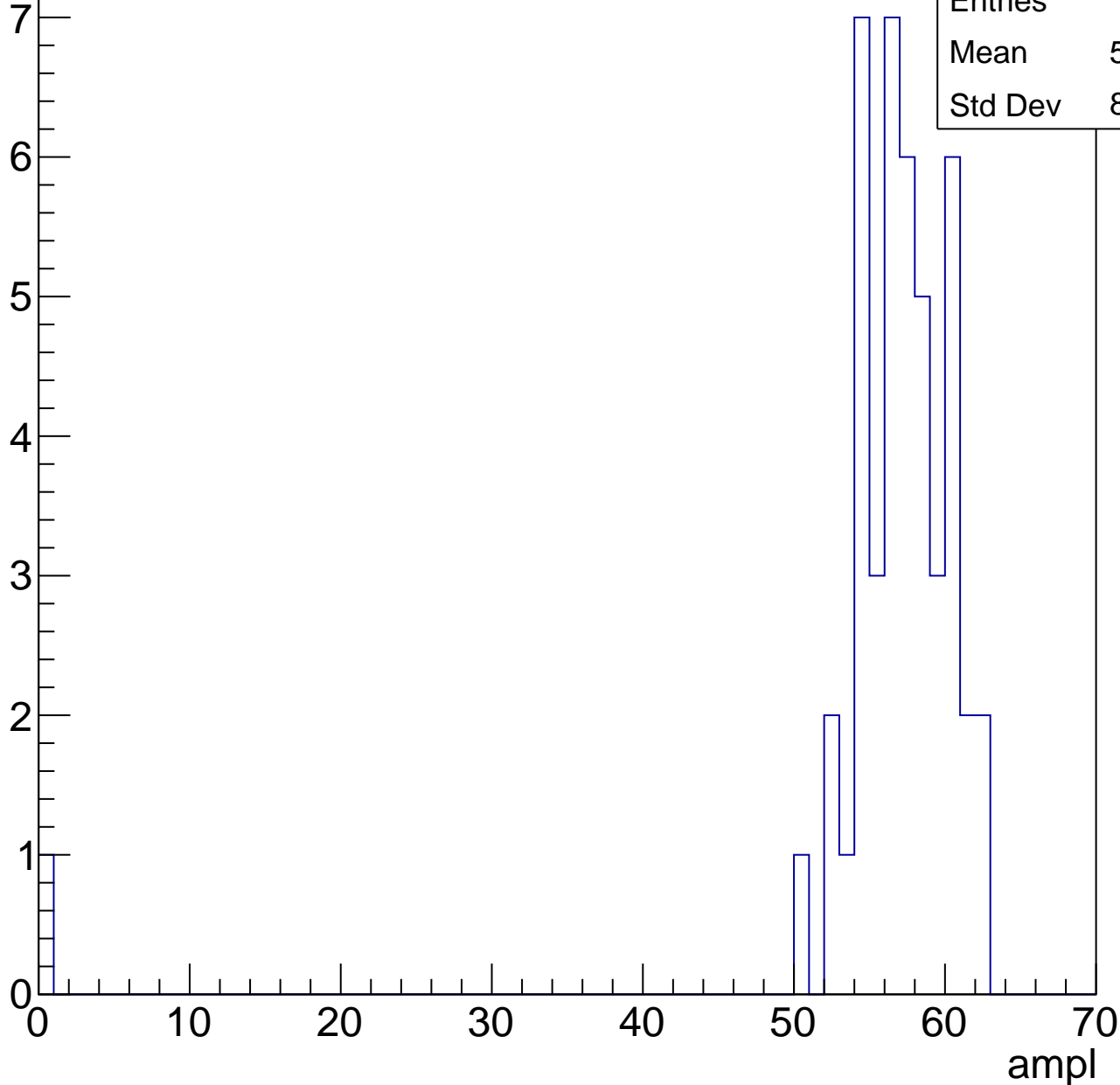


B0L102S, U20-ch35, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 55.59 |
| Std Dev | 8.732 |

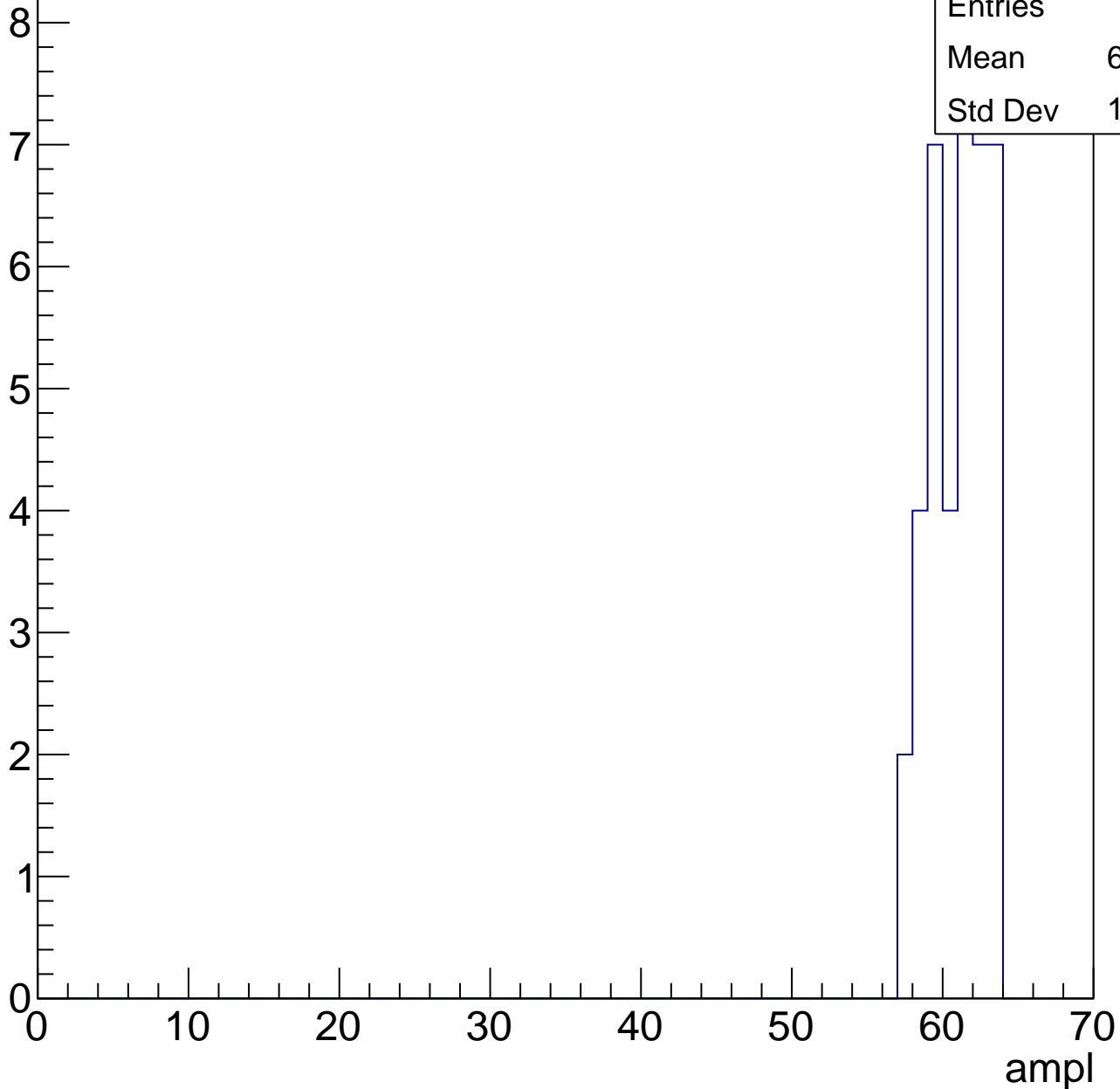


B0L102S, U20-ch35, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 60.56 |
| Std Dev | 1.809 |



B0L102S, U20-ch35, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch35, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U20-ch36, adc0

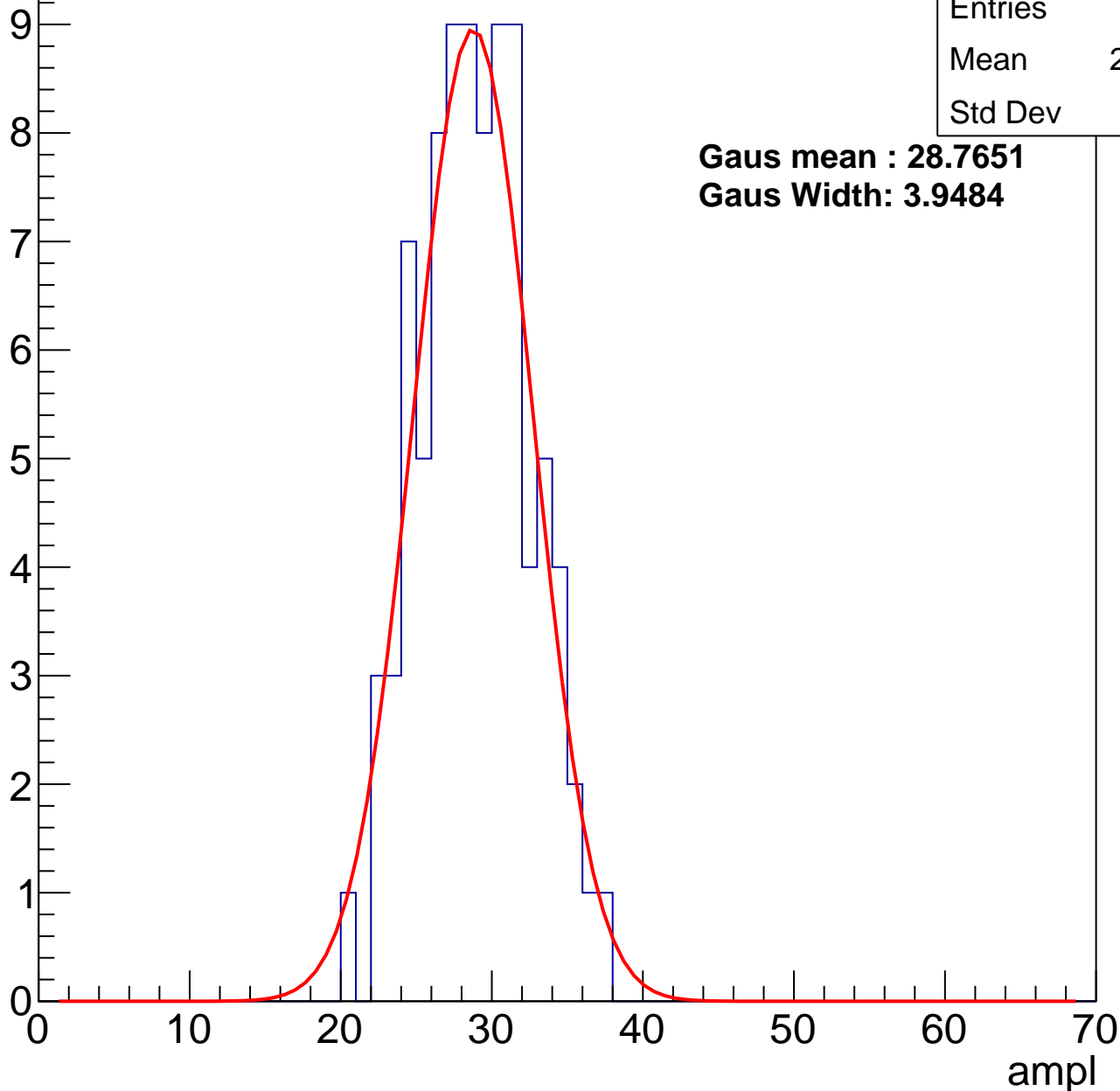
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 28.45 |
| Std Dev | 3.58 |

Gaus mean : 28.7651

Gaus Width: 3.9484



B0L102S, U20-ch36, adc1

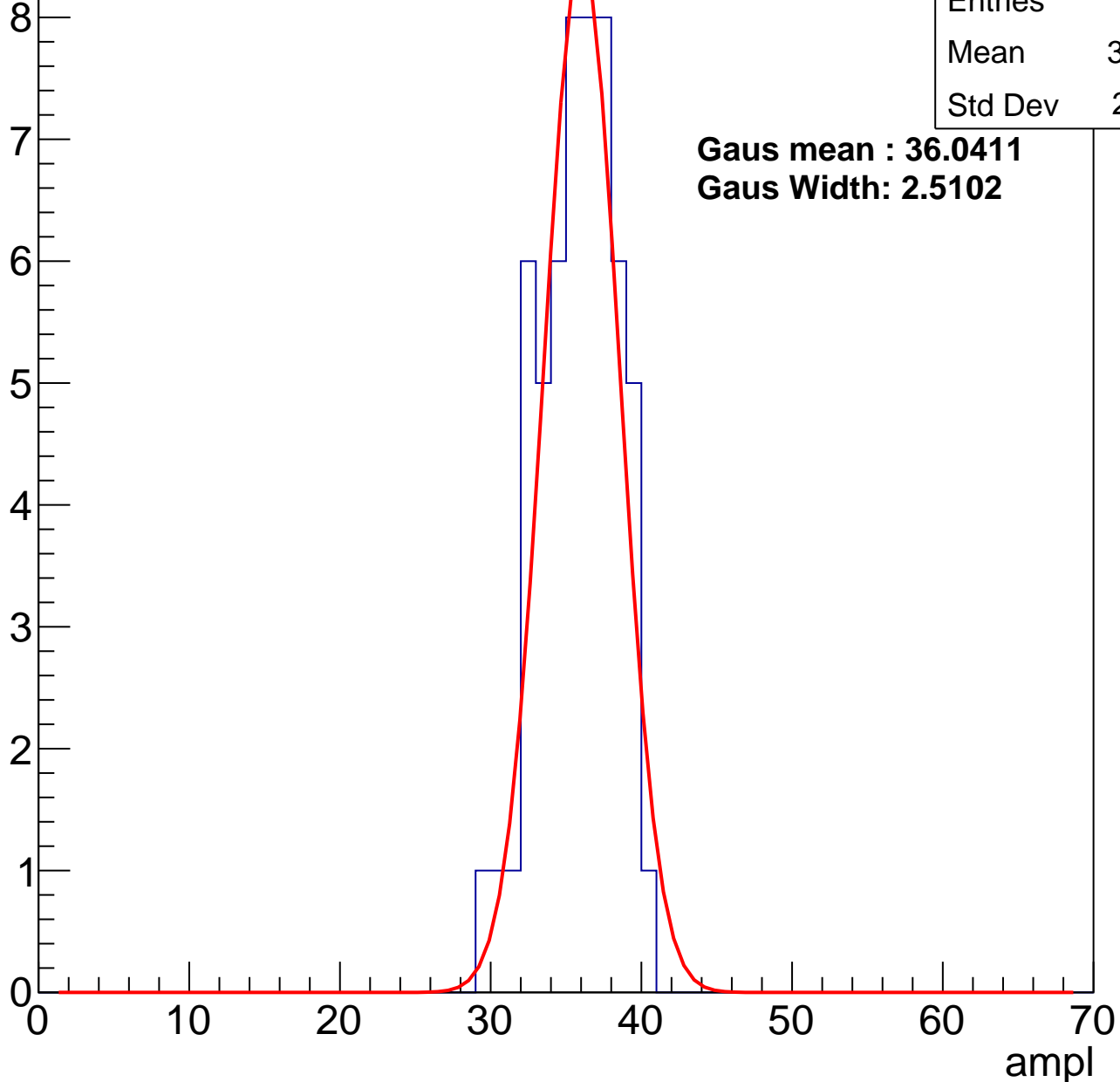
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 35.32 |
| Std Dev | 2.501 |

Gaus mean : 36.0411

Gaus Width: 2.5102



B0L102S, U20-ch36, adc2

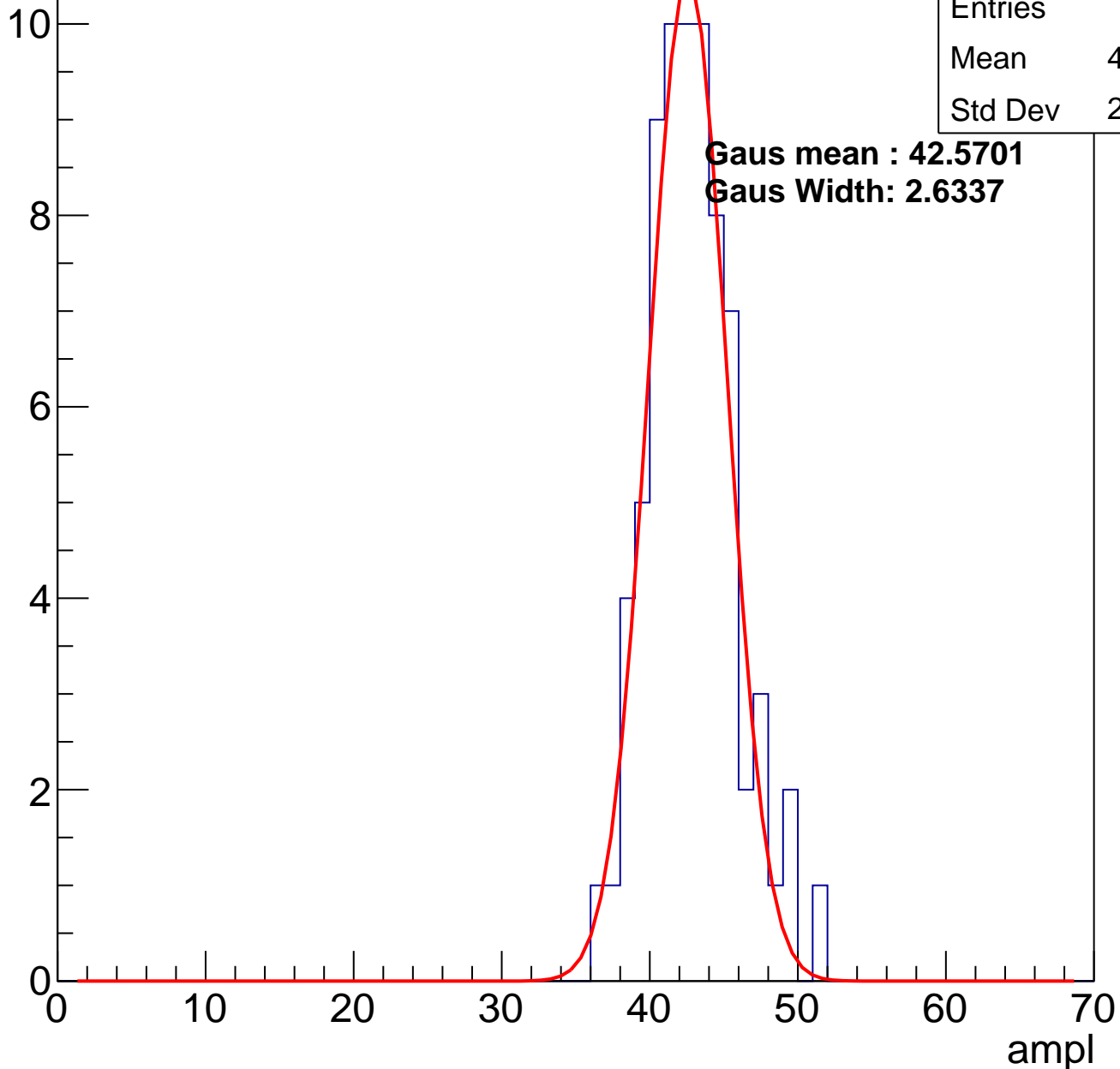
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 42.39 |
| Std Dev | 2.922 |

Gaus mean : 42.5701

Gaus Width: 2.6337

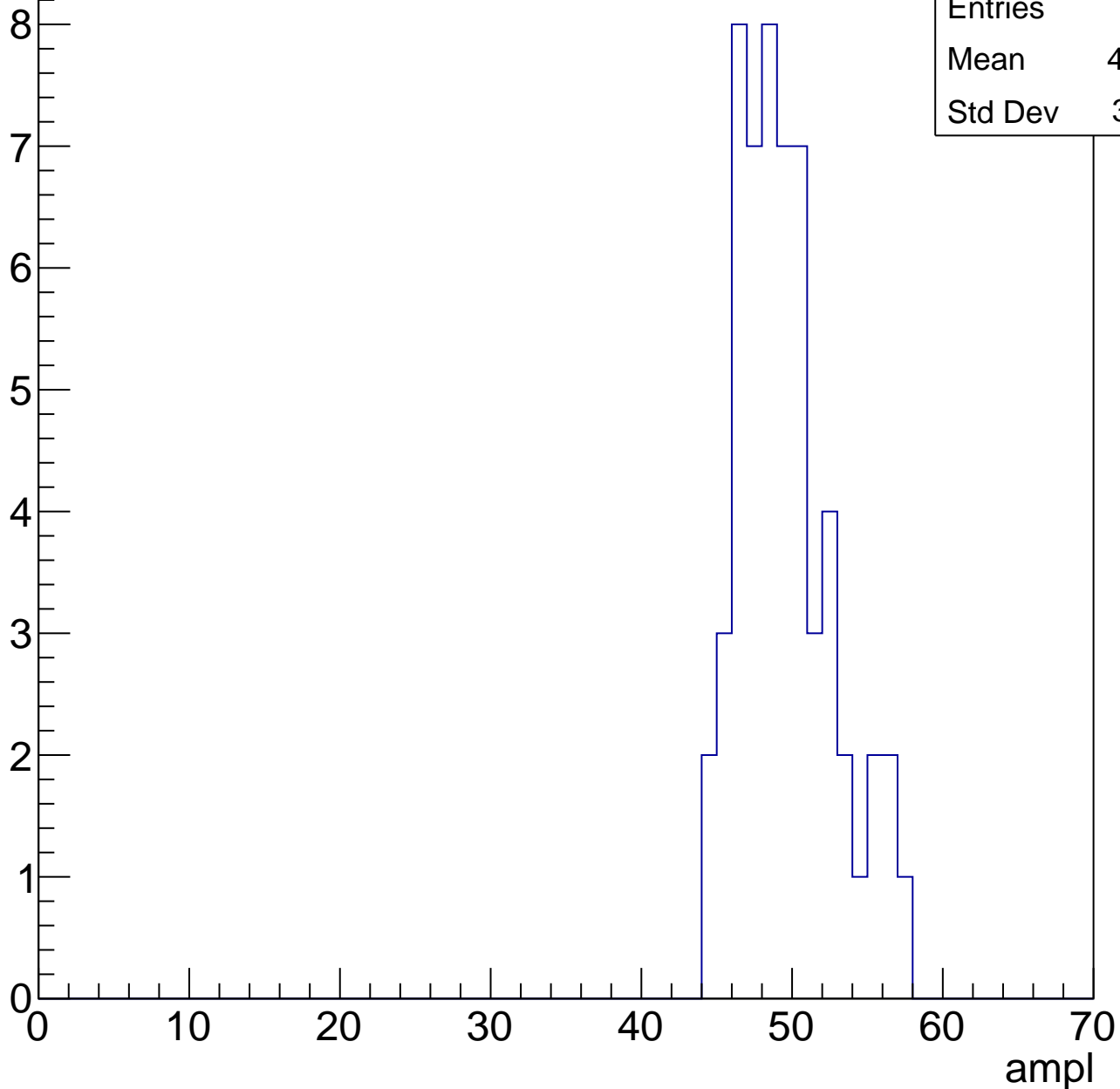
Entry



B0L102S, U20-ch36, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



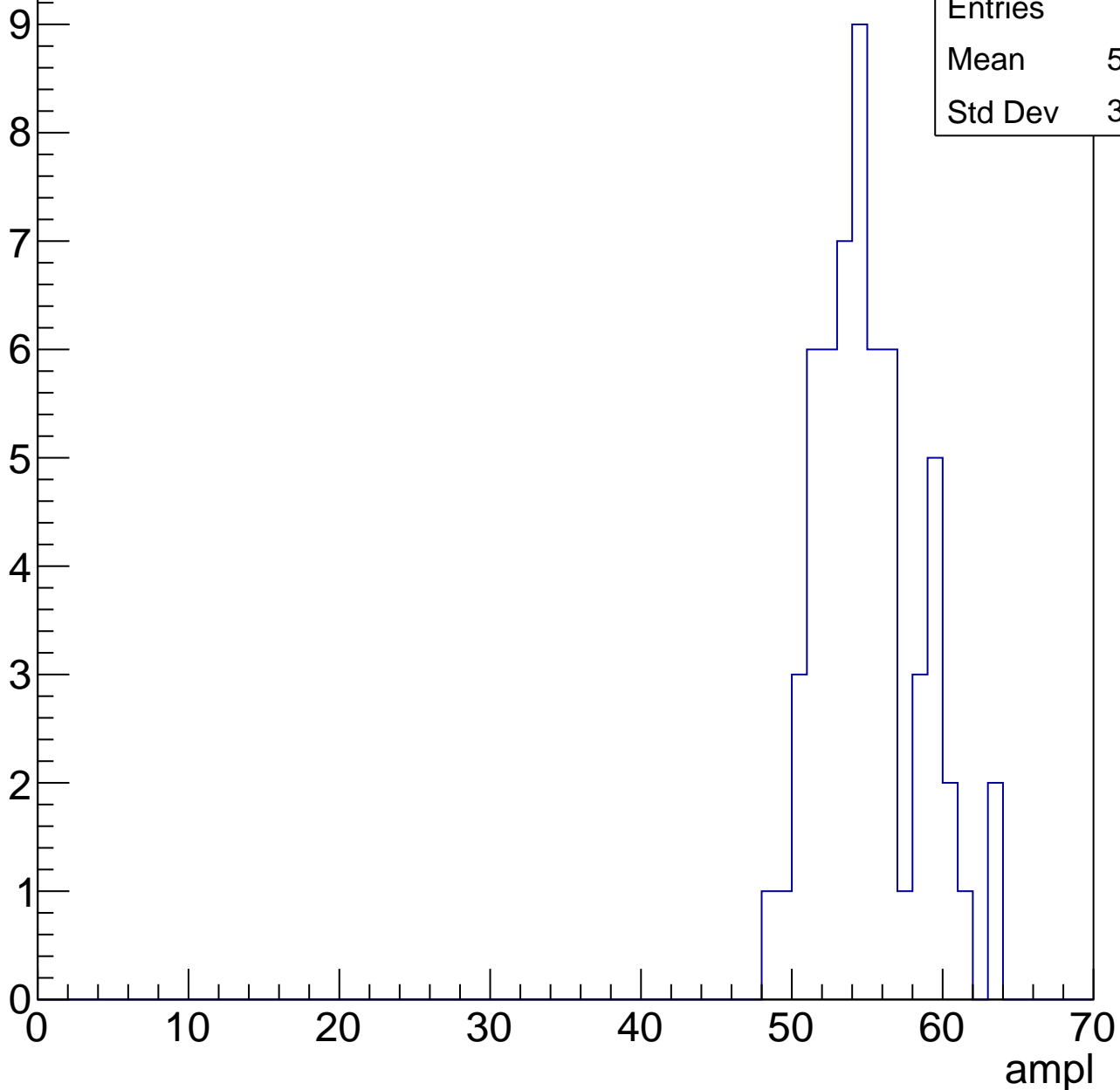
| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.07 |
| Std Dev | 3.111 |

B0L102S, U20-ch36, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.59 |
| Std Dev | 3.365 |



B0L102S, U20-ch36, adc5

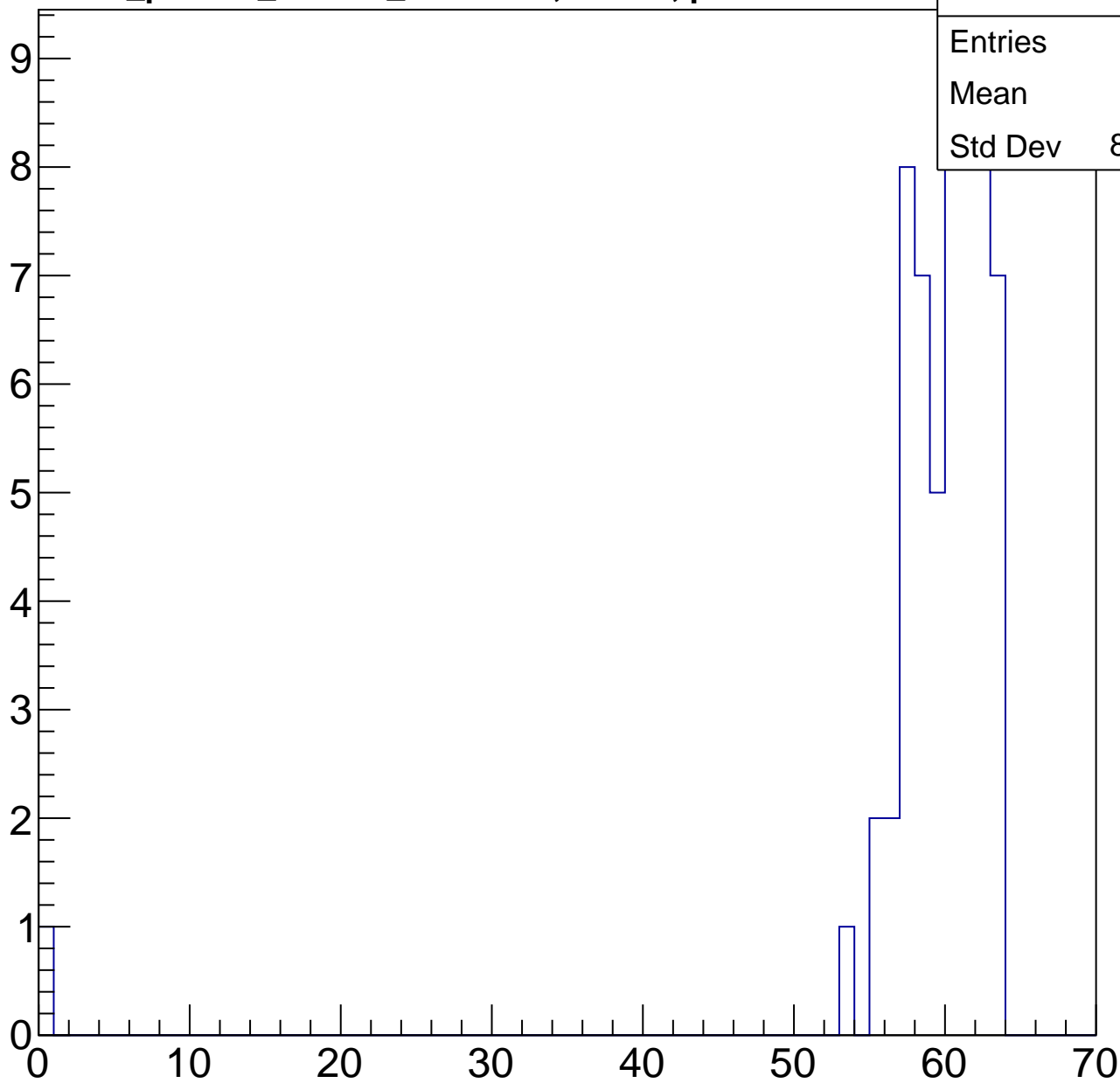
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.6 |
| Std Dev | 8.126 |

ampl



B0L102S, U20-ch36, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch36, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch37, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.27 |
| Std Dev | 3.264 |

Gaus mean : 28.9689

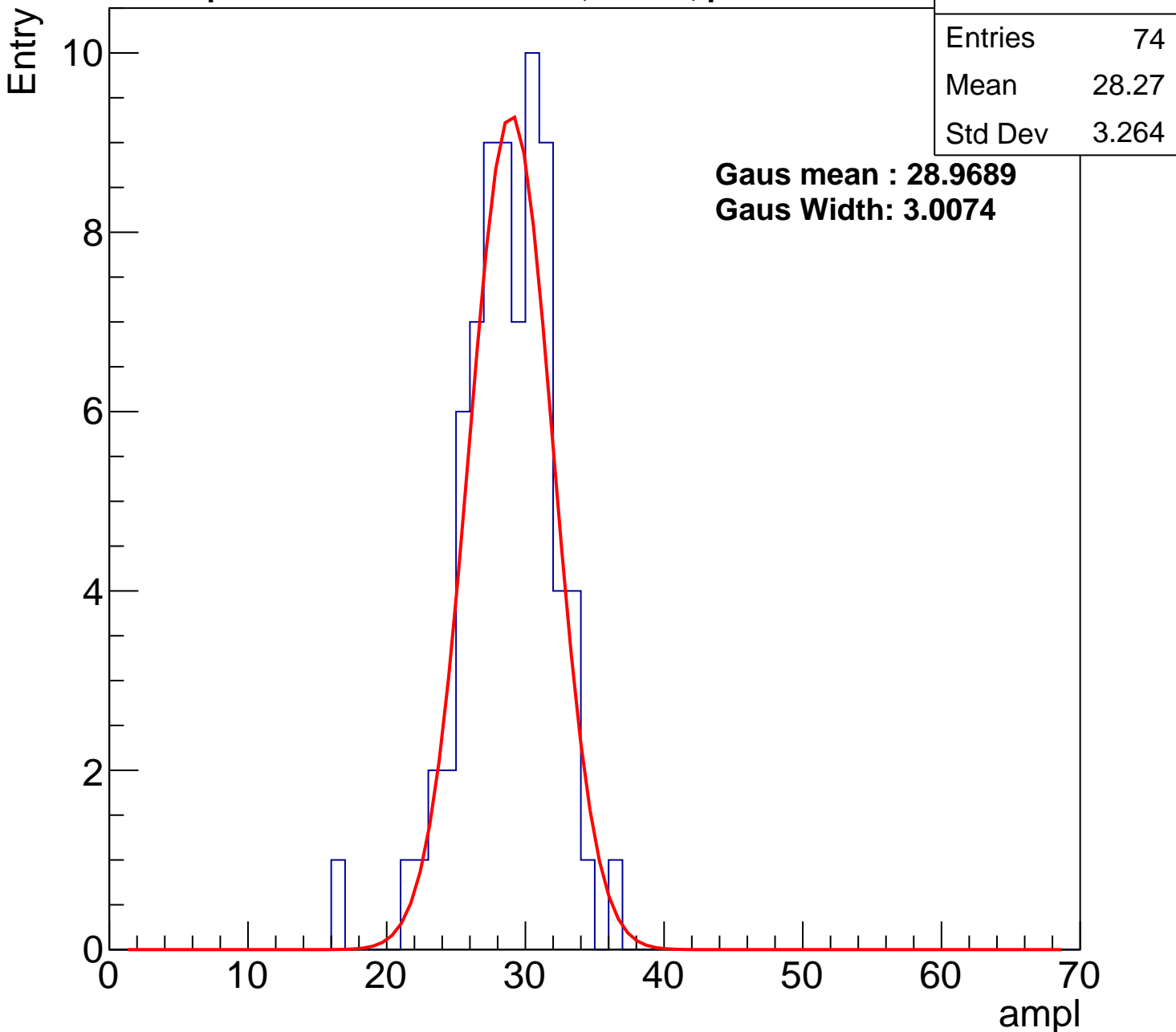
Gaus Width: 3.0074

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch37, adc1

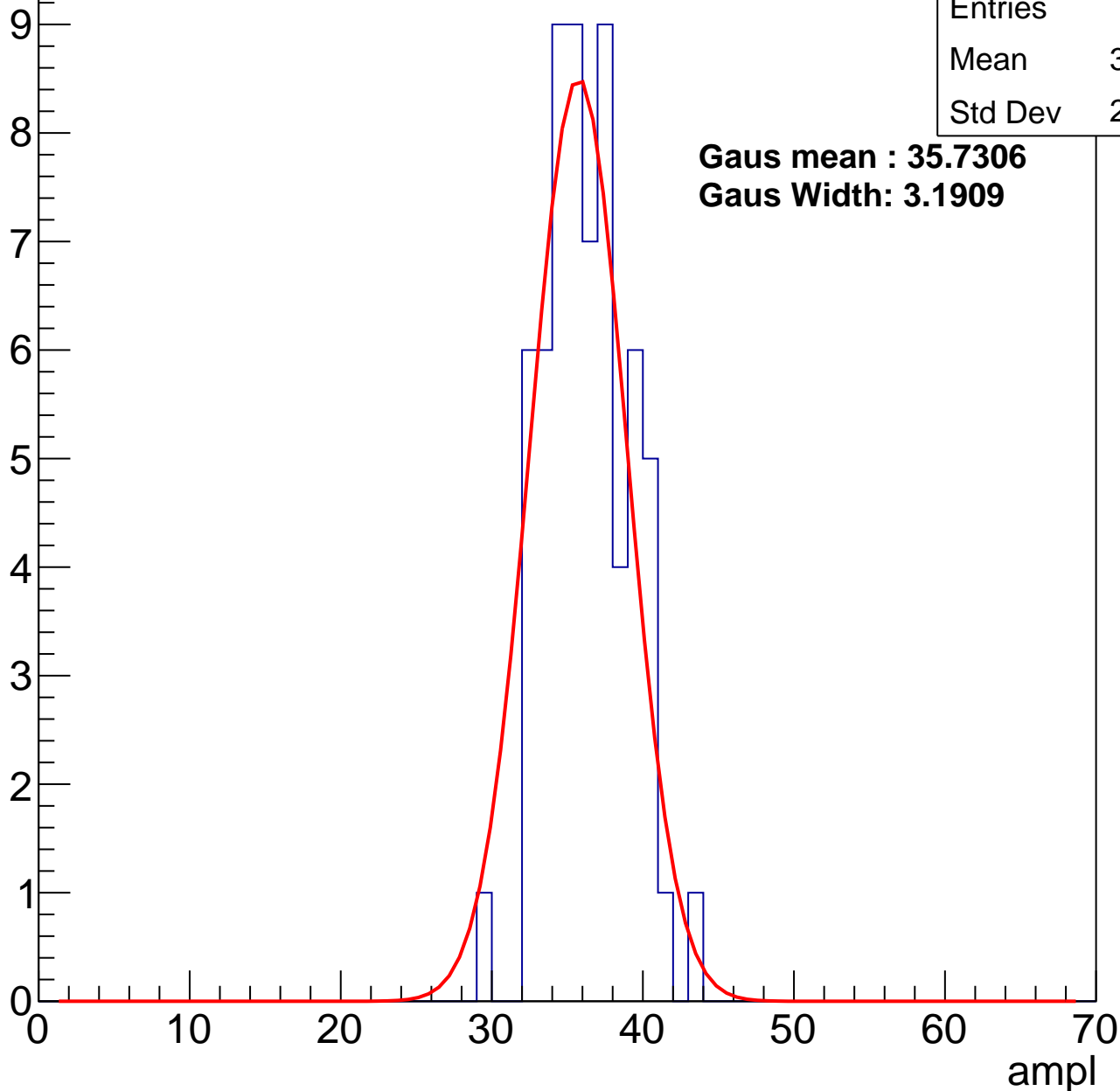
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.86 |
| Std Dev | 2.726 |

Gaus mean : 35.7306

Gaus Width: 3.1909



B0L102S, U20-ch37, adc2

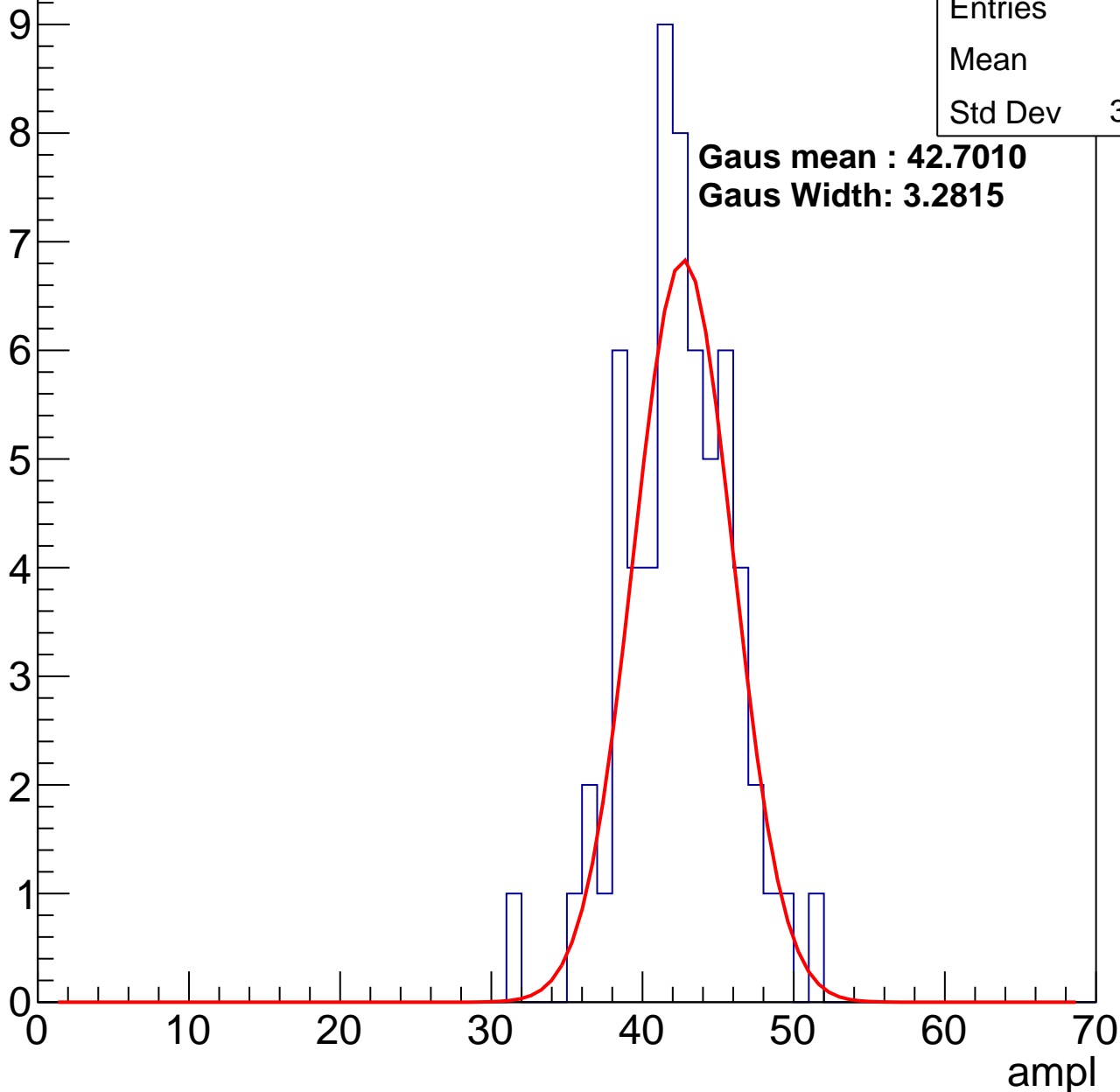
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.9 |
| Std Dev | 3.555 |

Gaus mean : 42.7010

Gaus Width: 3.2815

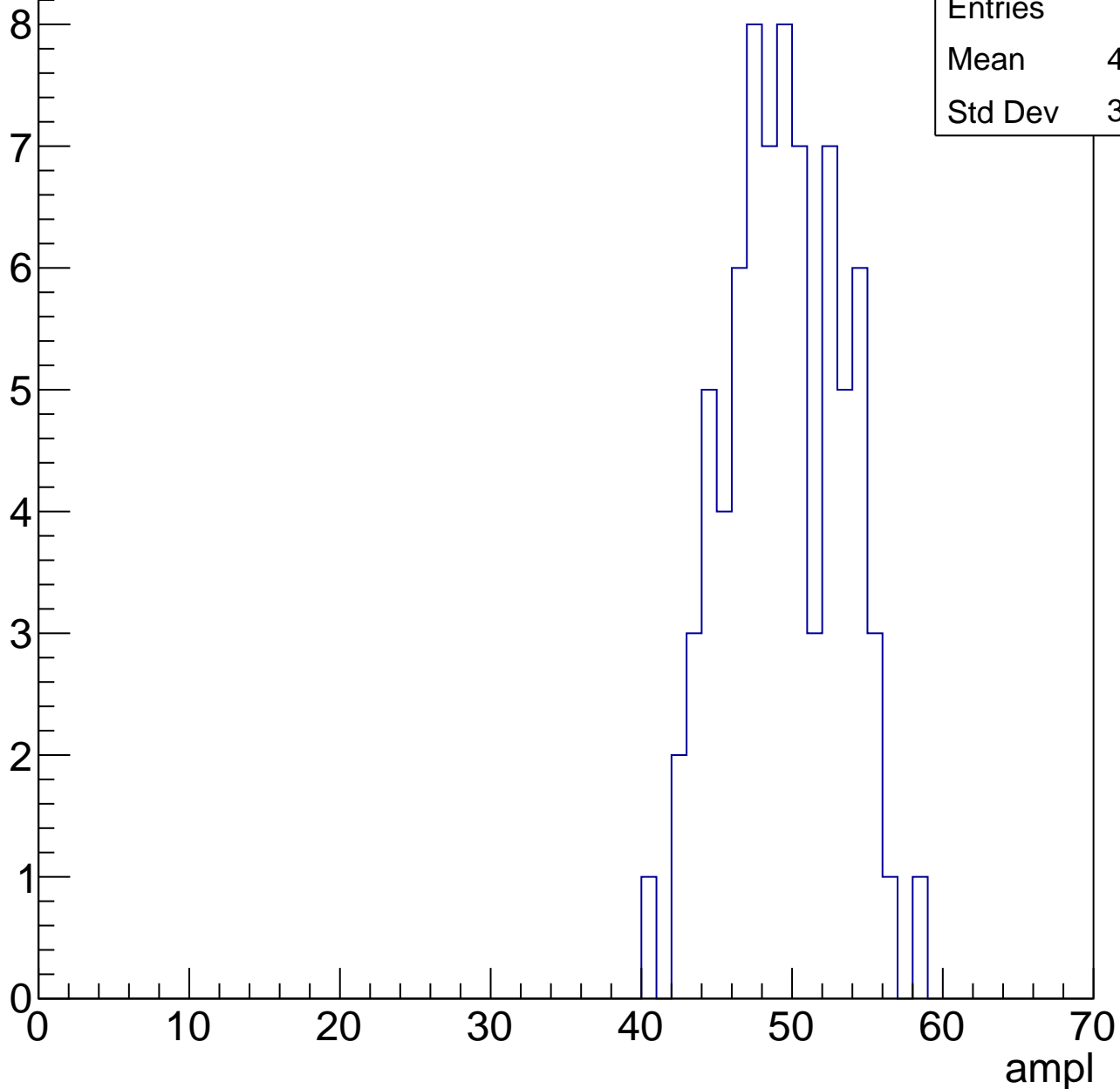


B0L102S, U20-ch37, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 48.94 |
| Std Dev | 3.812 |



B0L102S, U20-ch37, adc4

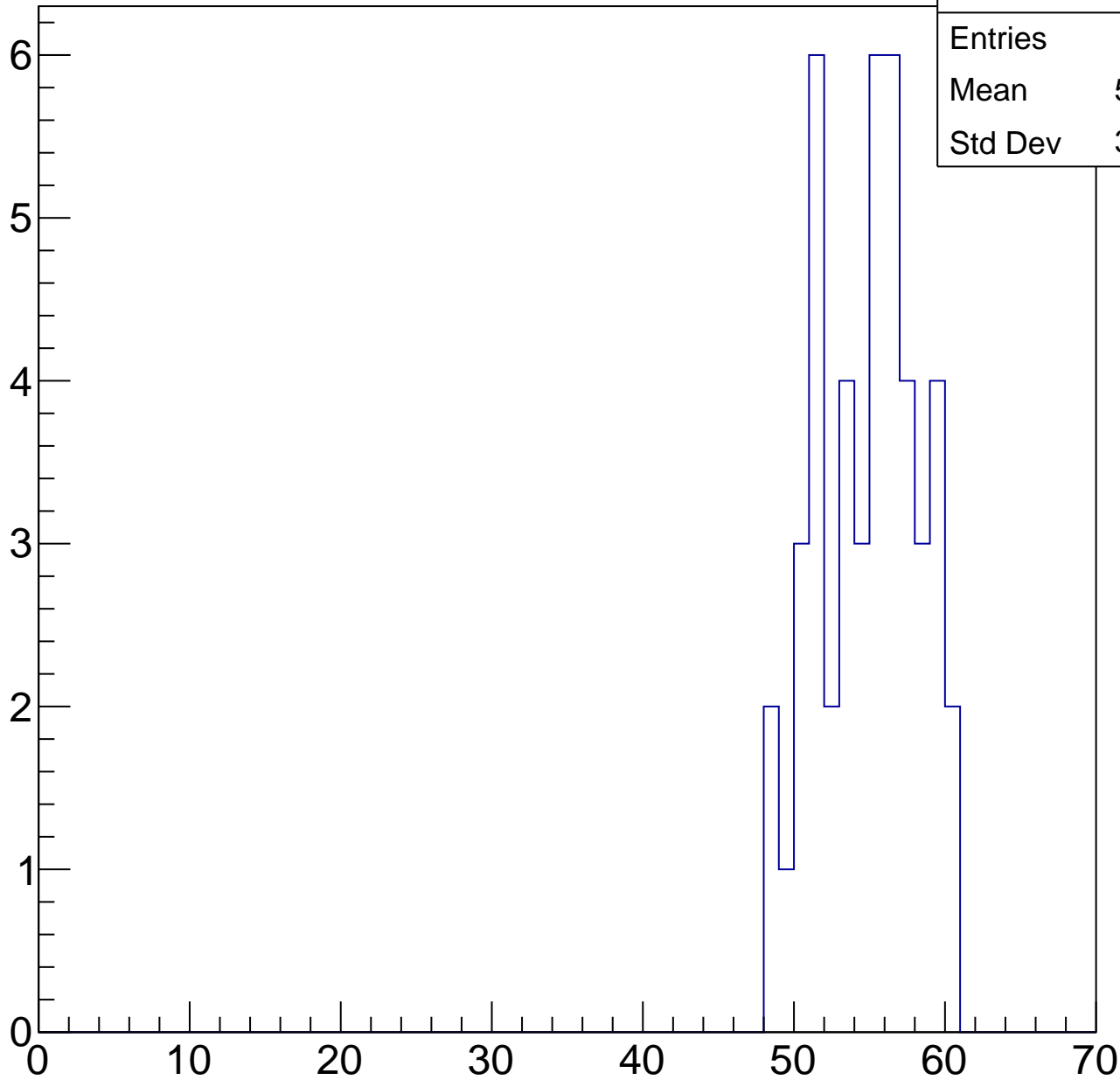
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 54.41 |
| Std Dev | 3.261 |

ampl

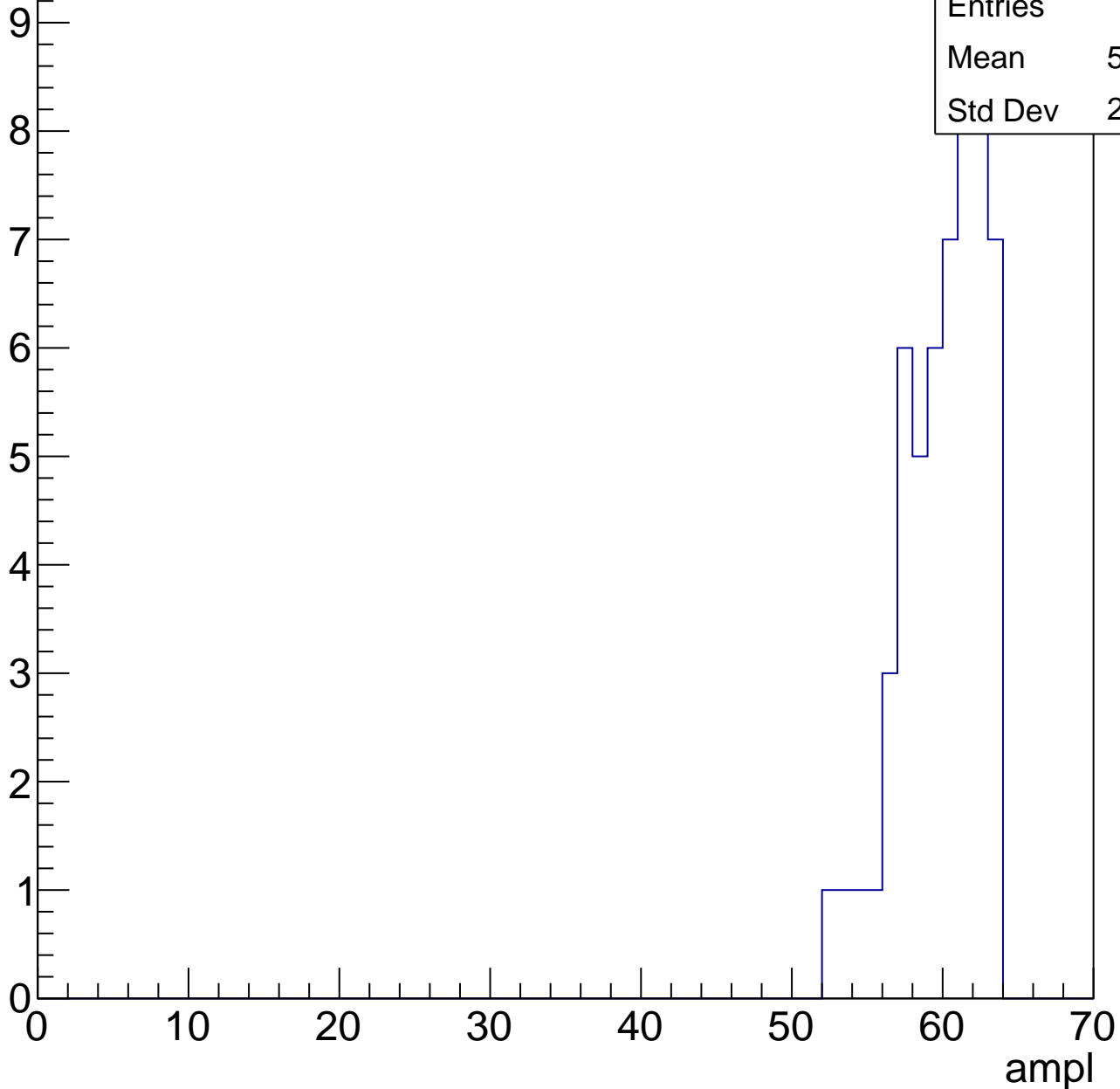


B0L102S, U20-ch37, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.57 |
| Std Dev | 2.672 |



B0L102S, U20-ch37, adc6

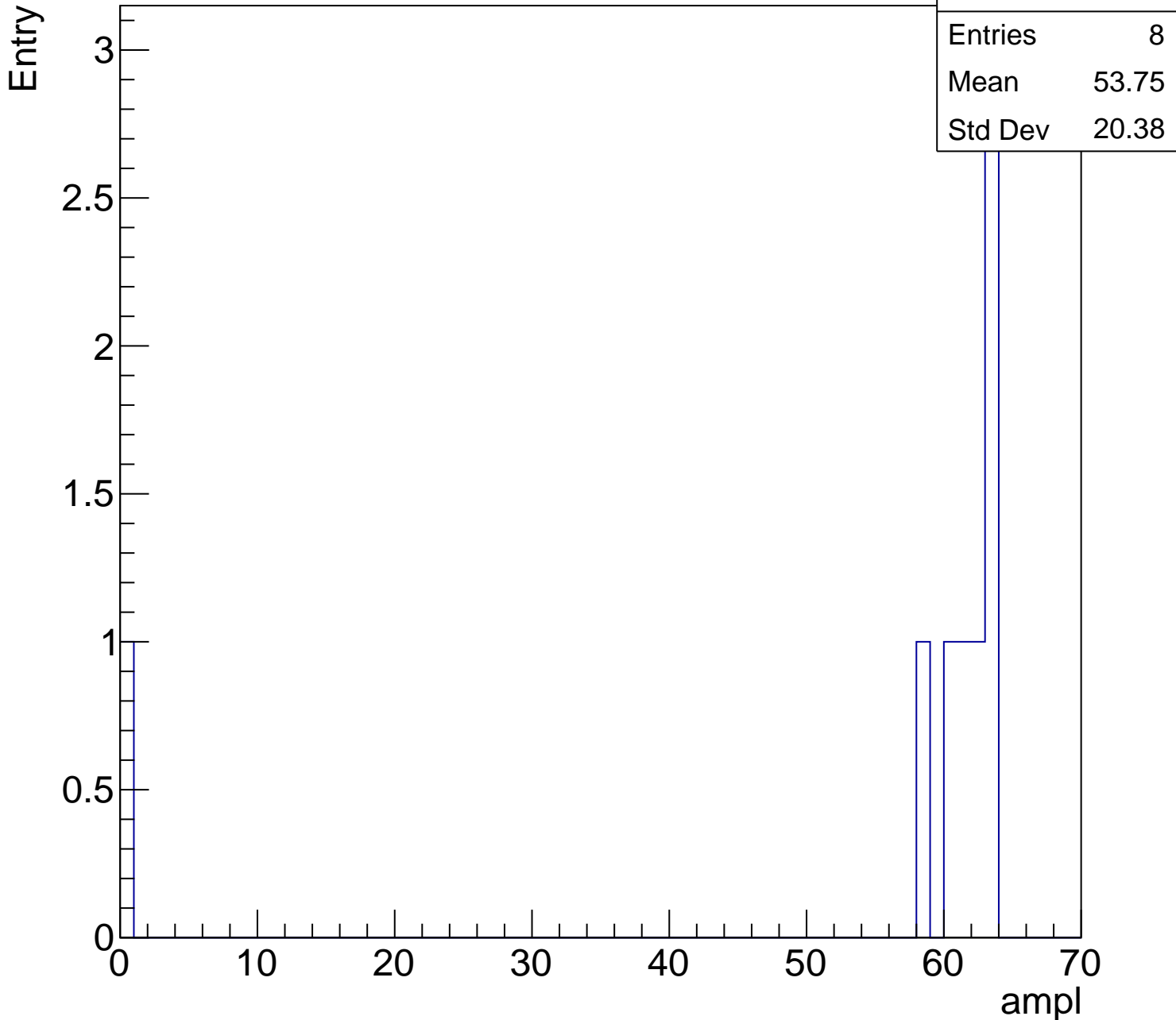
calib_packv5_042523_0143.root, FC#12, port B1

Entry

3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 53.75 |
| Std Dev | 20.38 |

ampl



B0L102S, U20-ch37, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch38, adc0

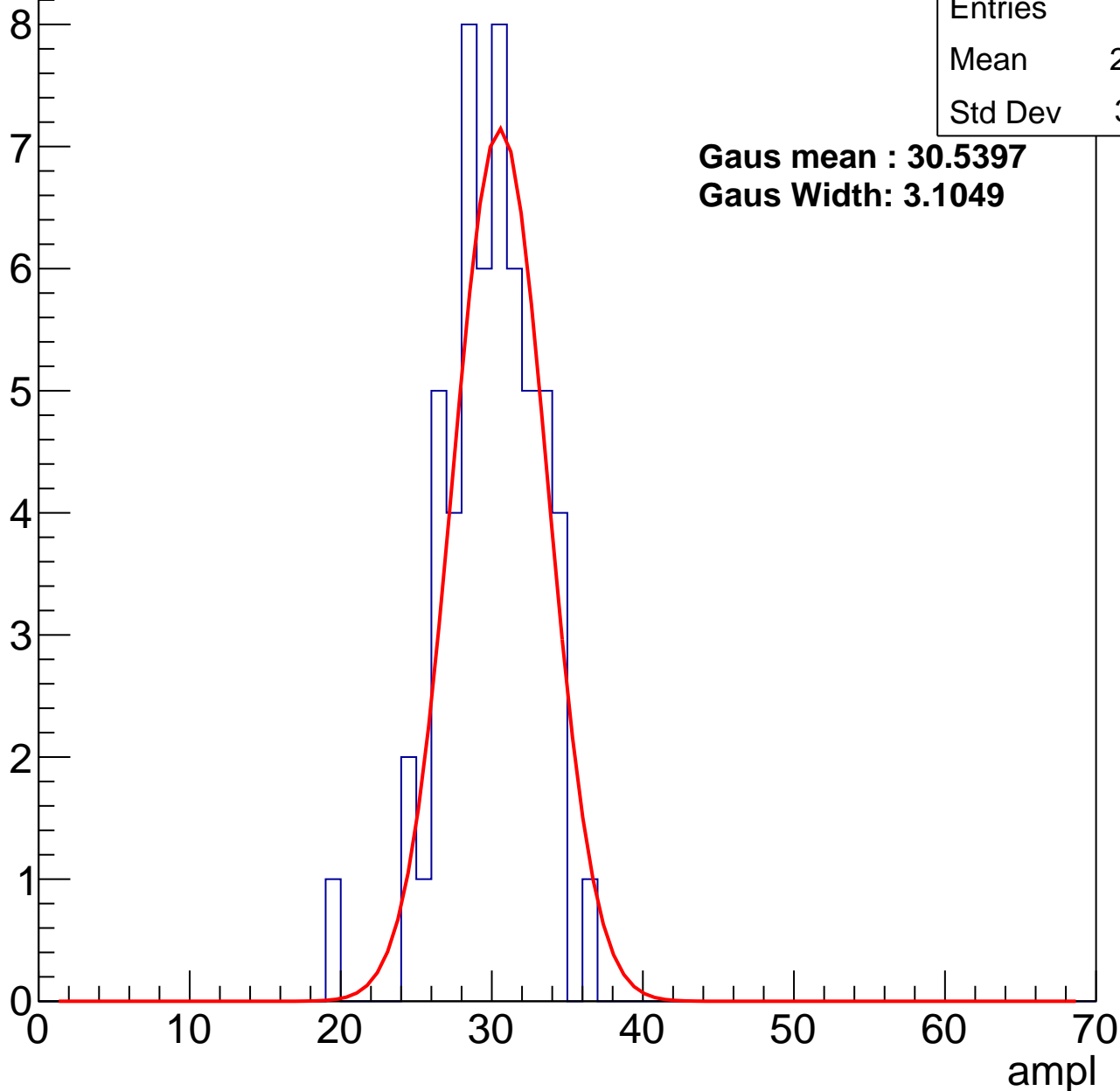
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 29.48 |
| Std Dev | 3.071 |

Gaus mean : 30.5397

Gaus Width: 3.1049



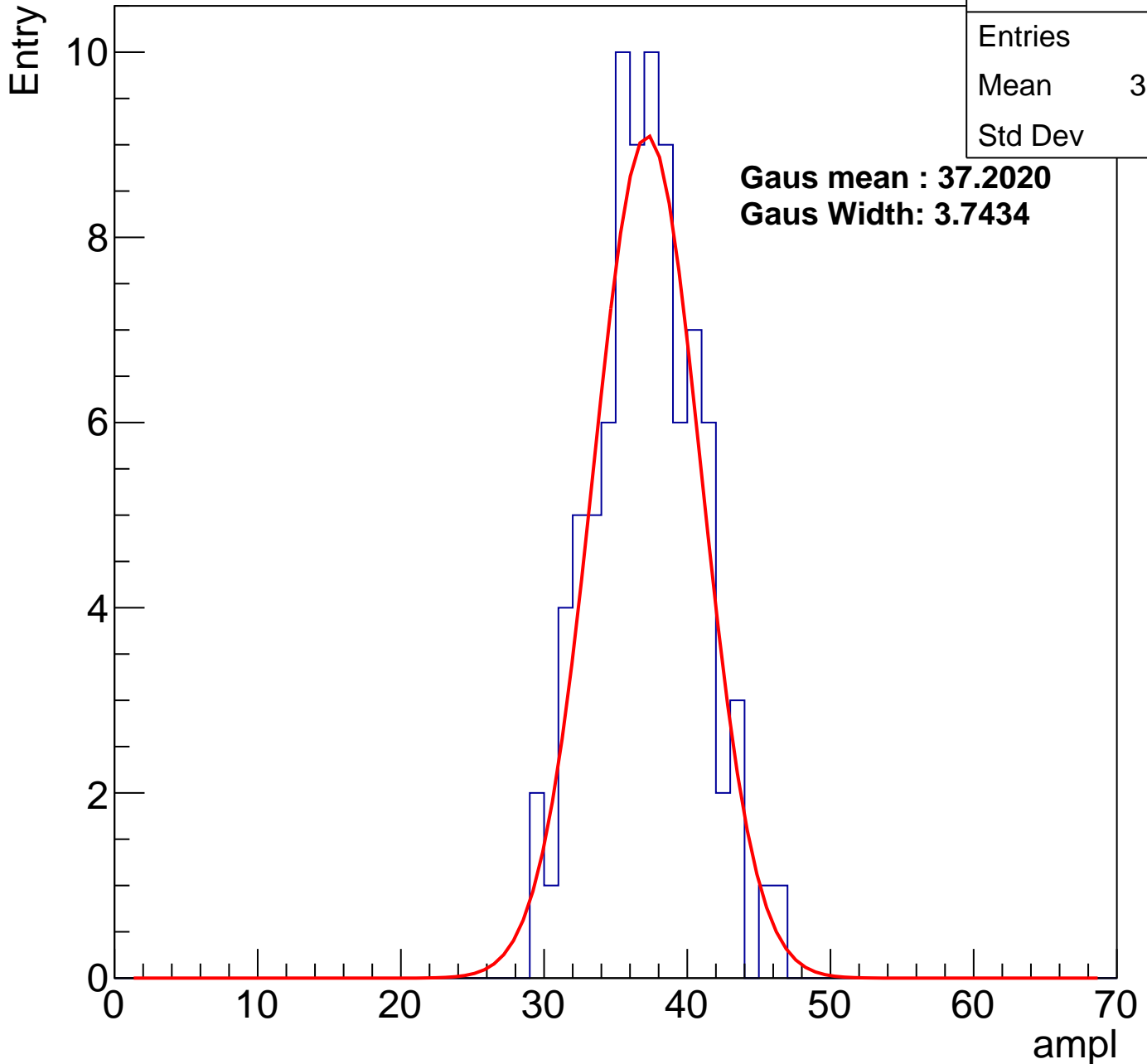
B0L102S, U20-ch38, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 36.68 |
| Std Dev | 3.58 |

Gaus mean : 37.2020

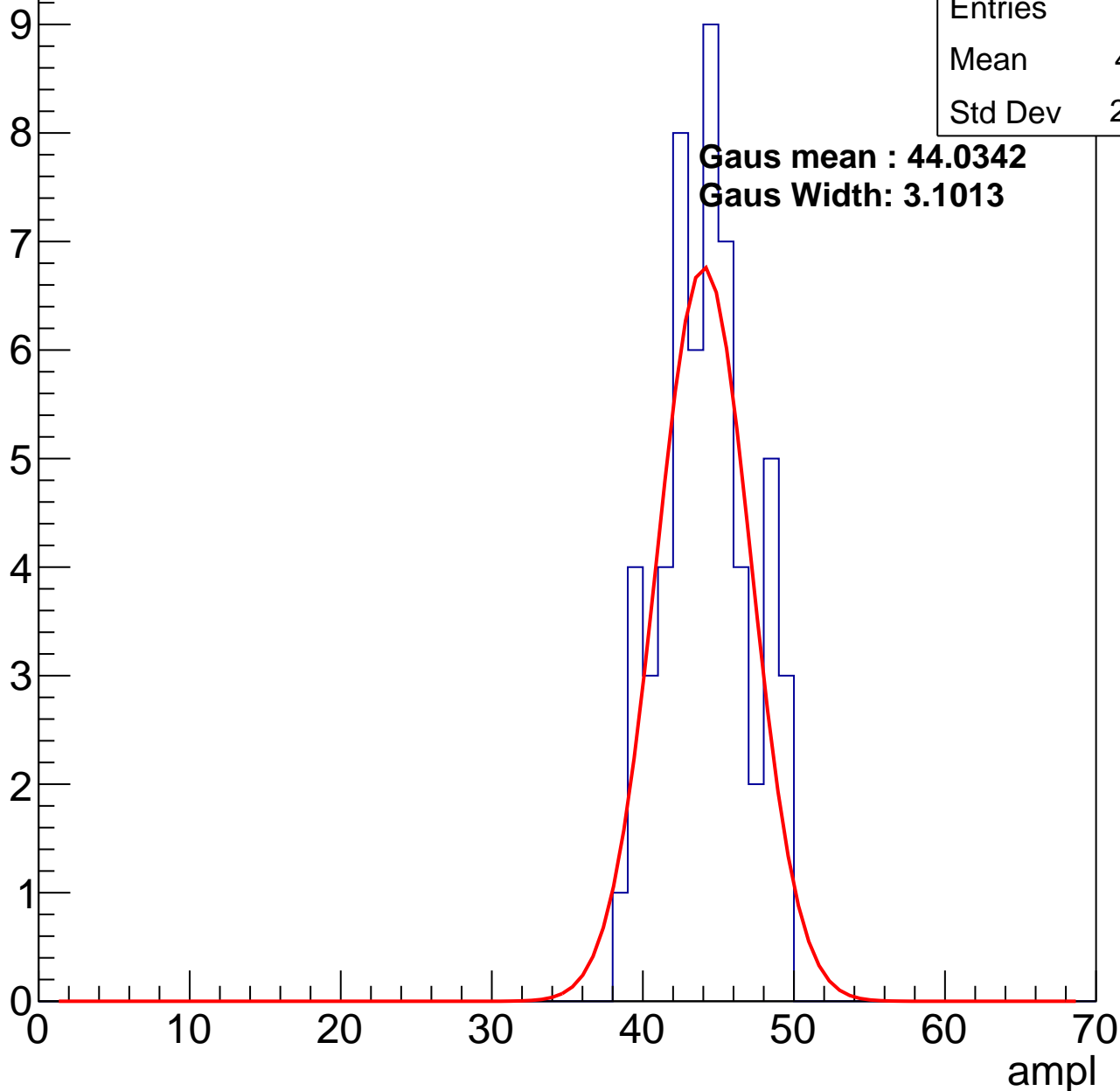
Gaus Width: 3.7434



B0L102S, U20-ch38, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch38, adc3

calib_packv5_042523_0143.root, FC#12, port B1

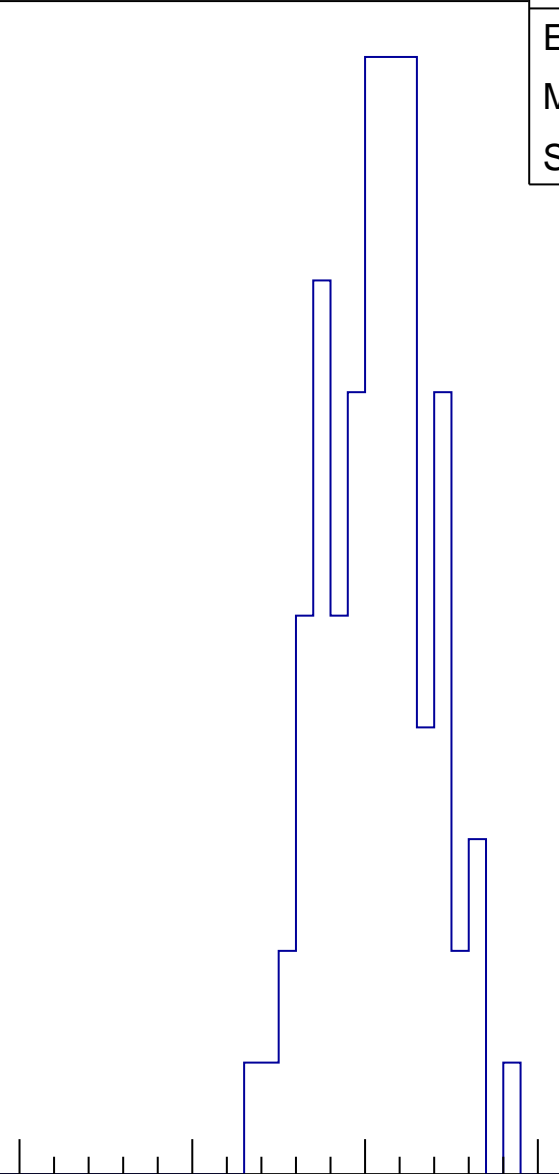
| | |
|---------|-------|
| Entries | 76 |
| Mean | 50.29 |
| Std Dev | 3.094 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U20-ch38, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

6

5

4

3

2

1

0

0

10

20

30

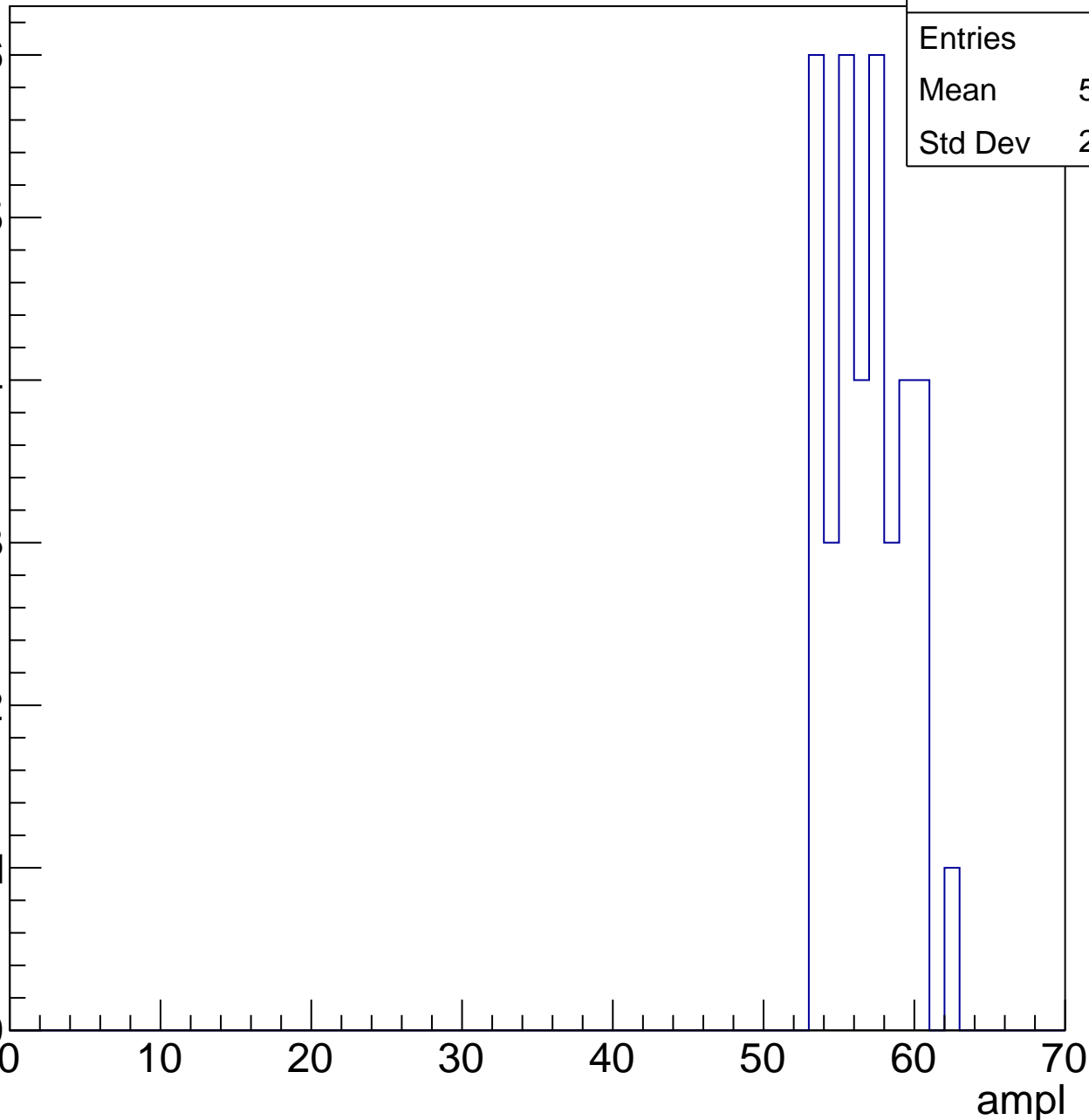
40

50

60

ampl

| | |
|---------|-------|
| Entries | 37 |
| Mean | 56.43 |
| Std Dev | 2.433 |

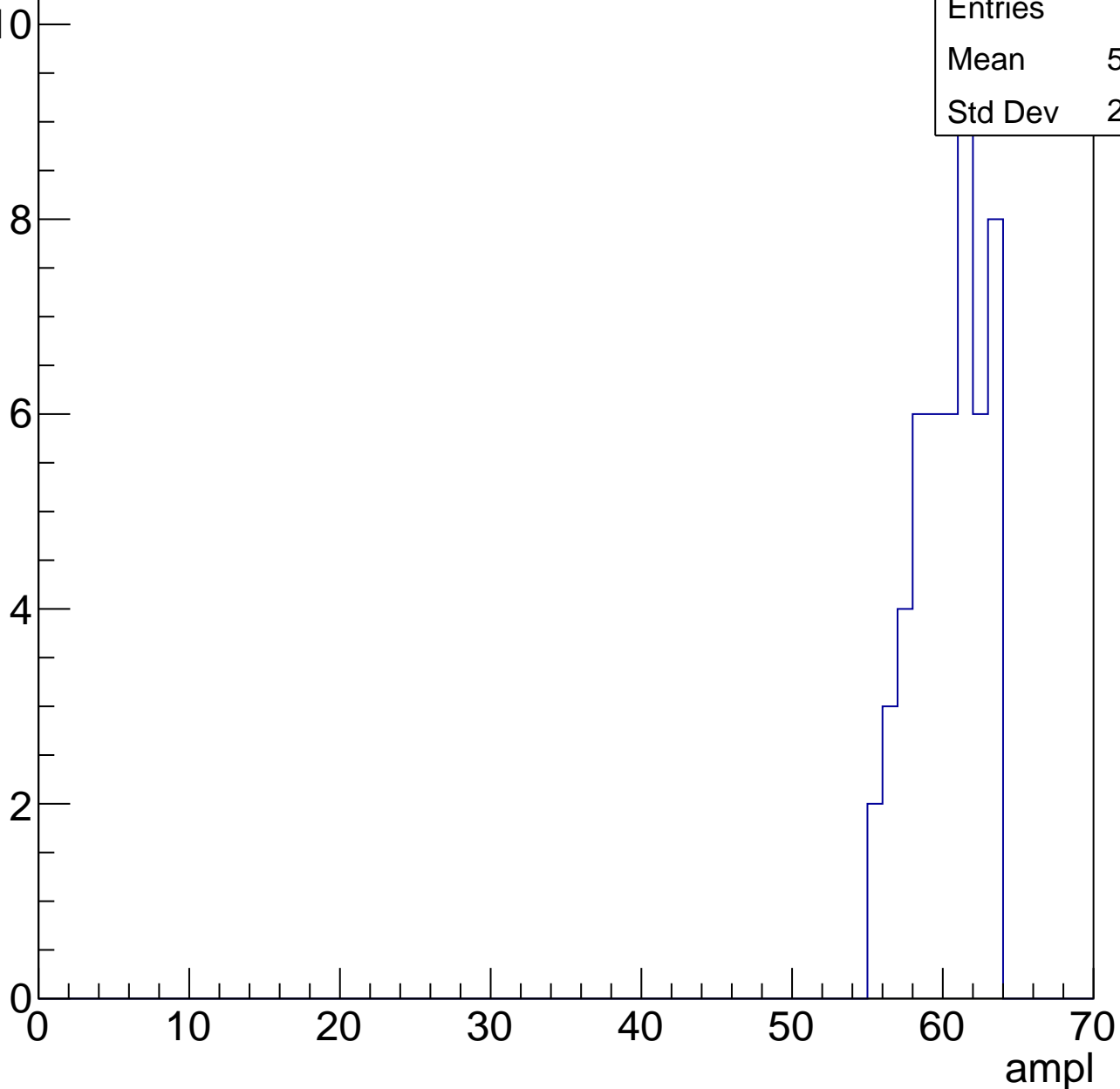


B0L102S, U20-ch38, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

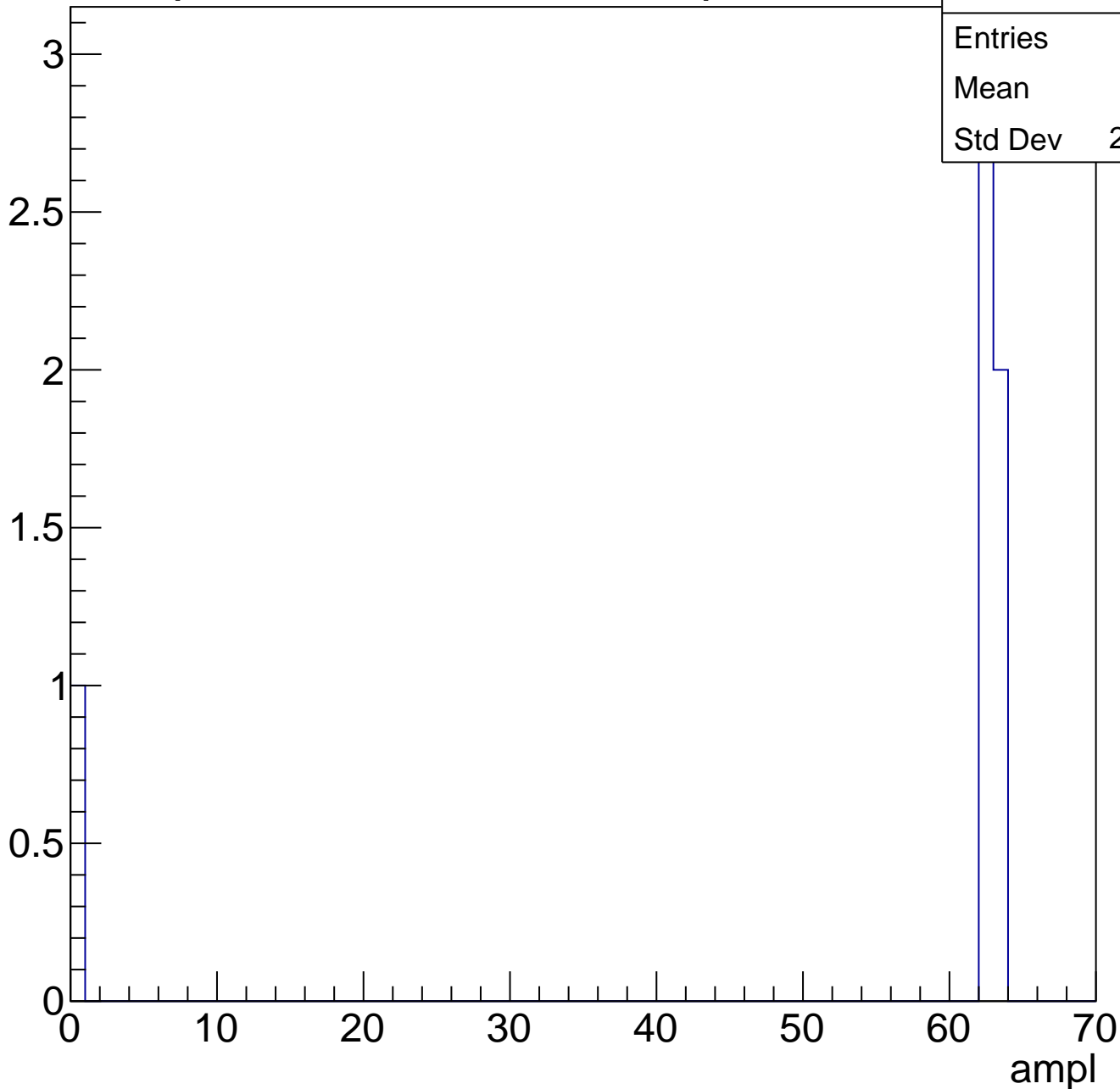
| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.88 |
| Std Dev | 2.298 |



B0L102S, U20-ch38, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch38, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch39, adc0

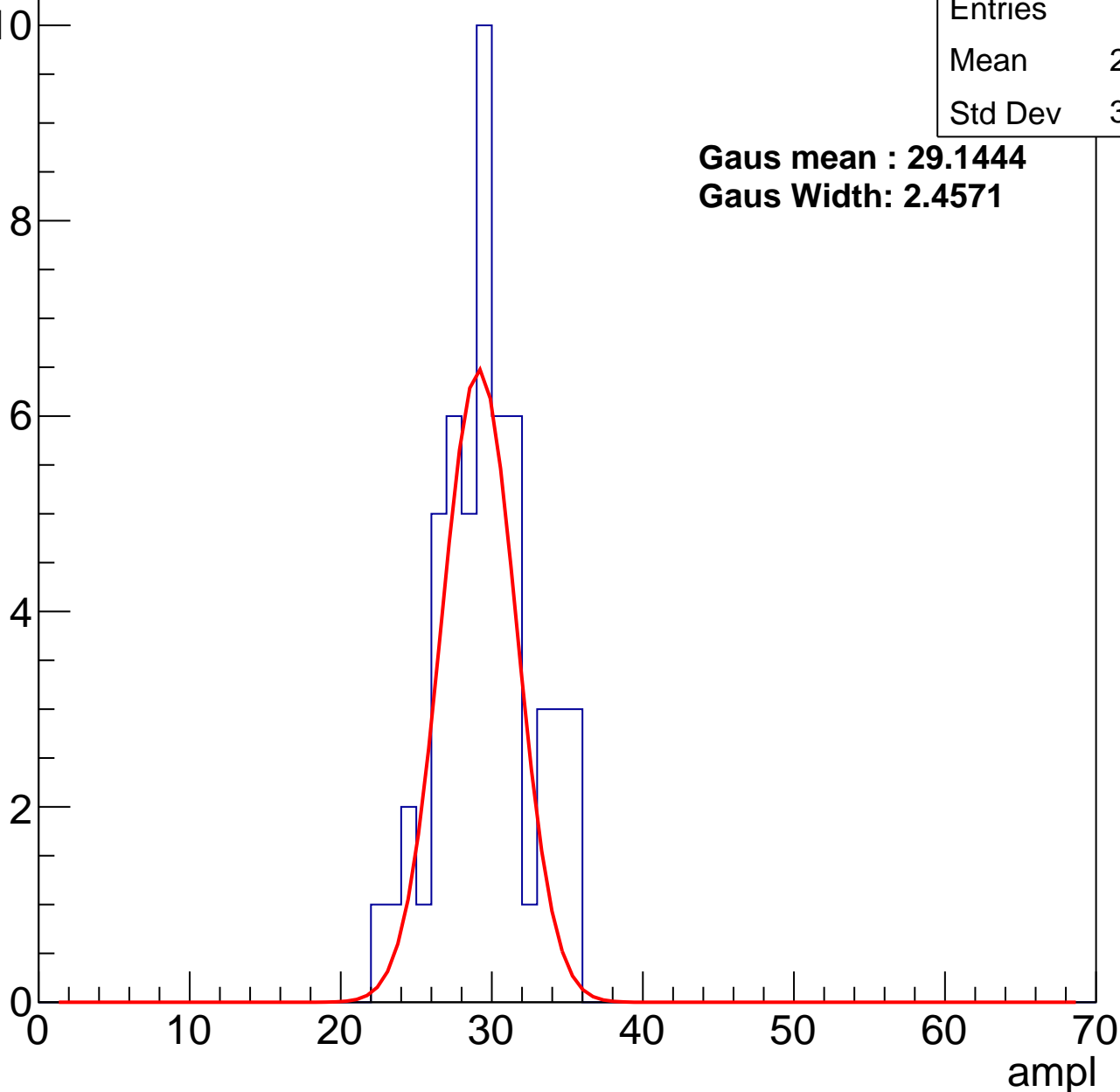
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.13 |
| Std Dev | 3.053 |

Gaus mean : 29.1444

Gaus Width: 2.4571

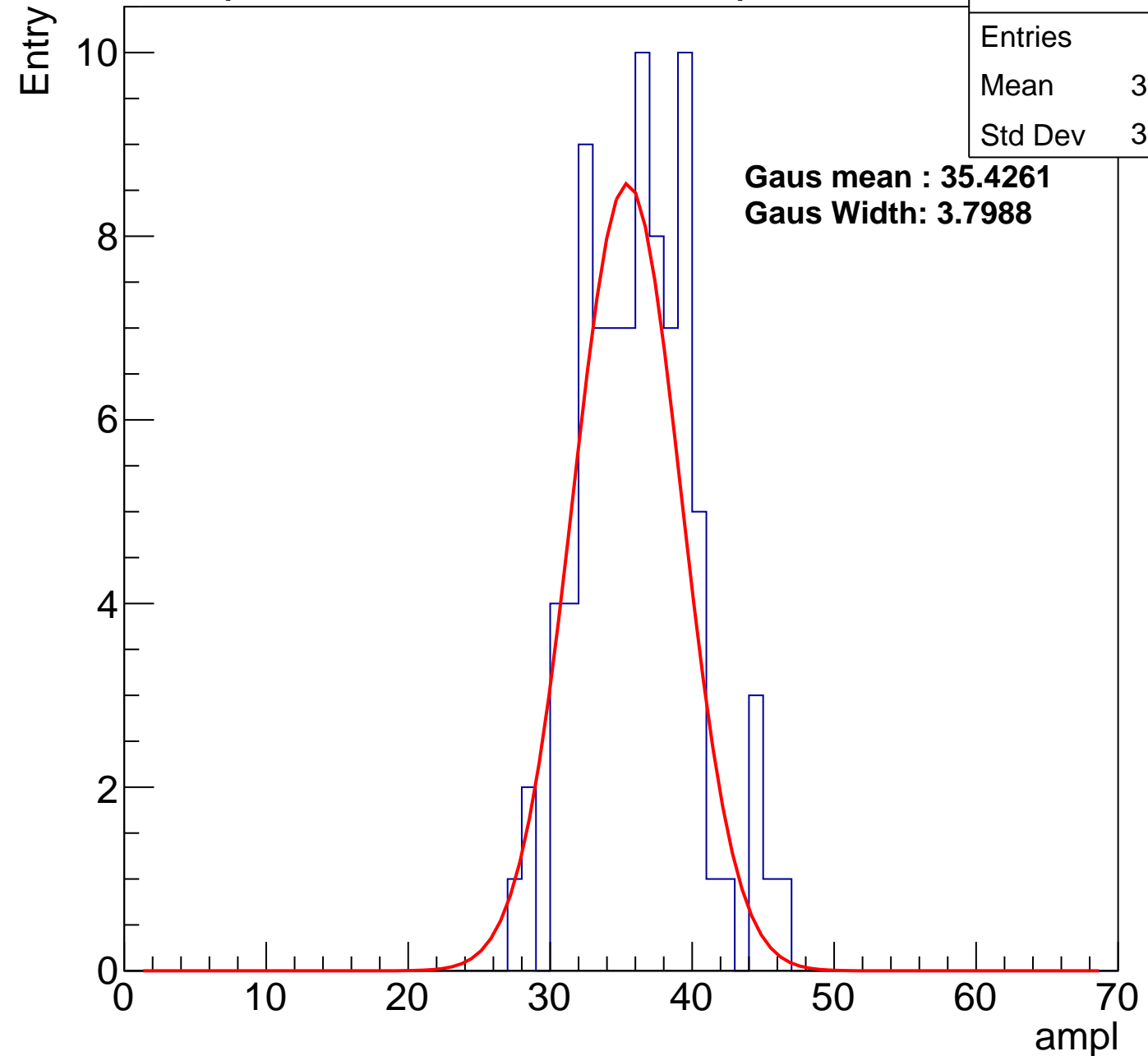


B0L102S, U20-ch39, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 88 |
| Mean | 35.76 |
| Std Dev | 3.888 |

Gaus mean : 35.4261
Gaus Width: 3.7988



B0L102S, U20-ch39, adc2

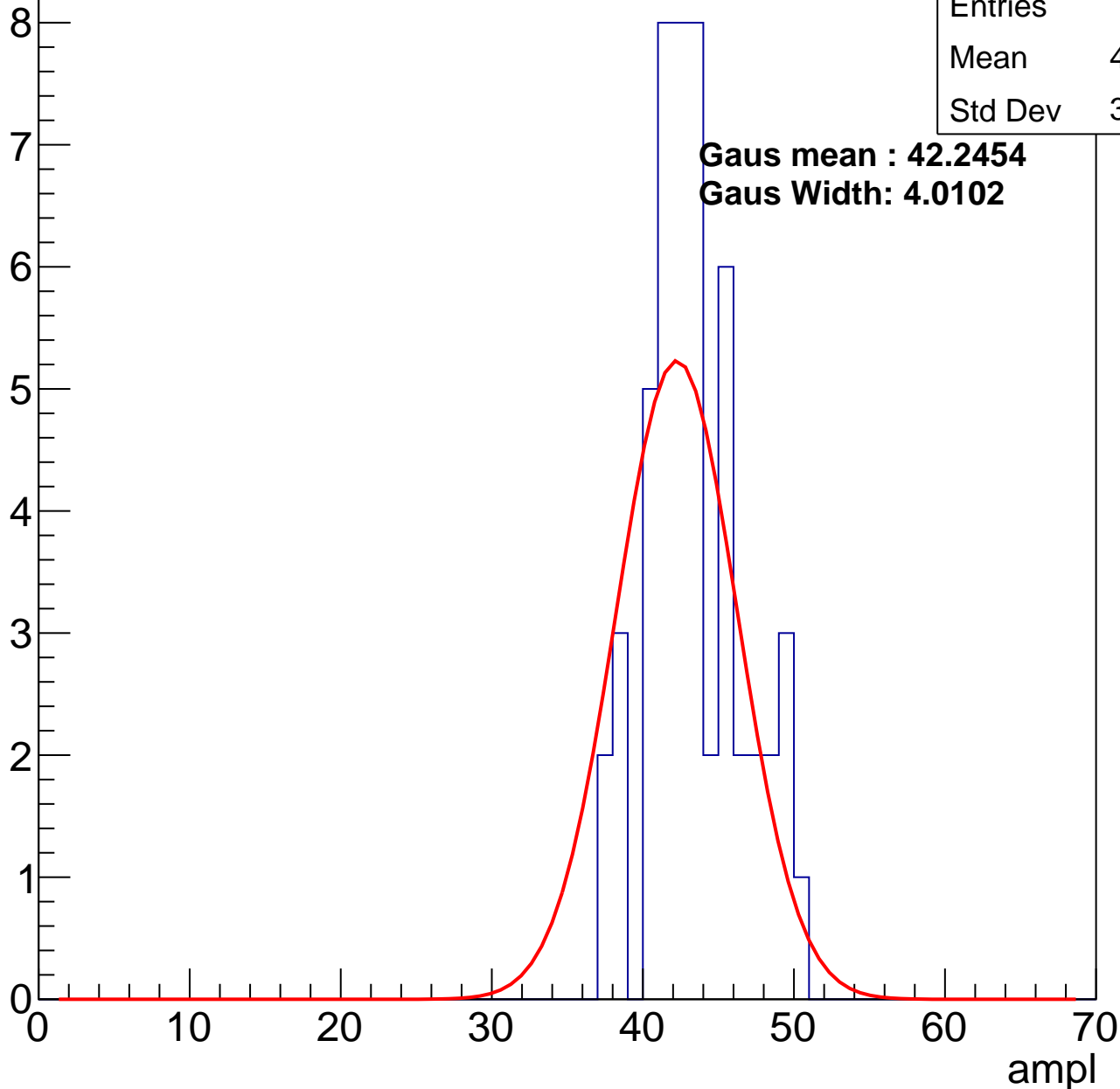
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 42.94 |
| Std Dev | 3.147 |

Gaus mean : 42.2454

Gaus Width: 4.0102

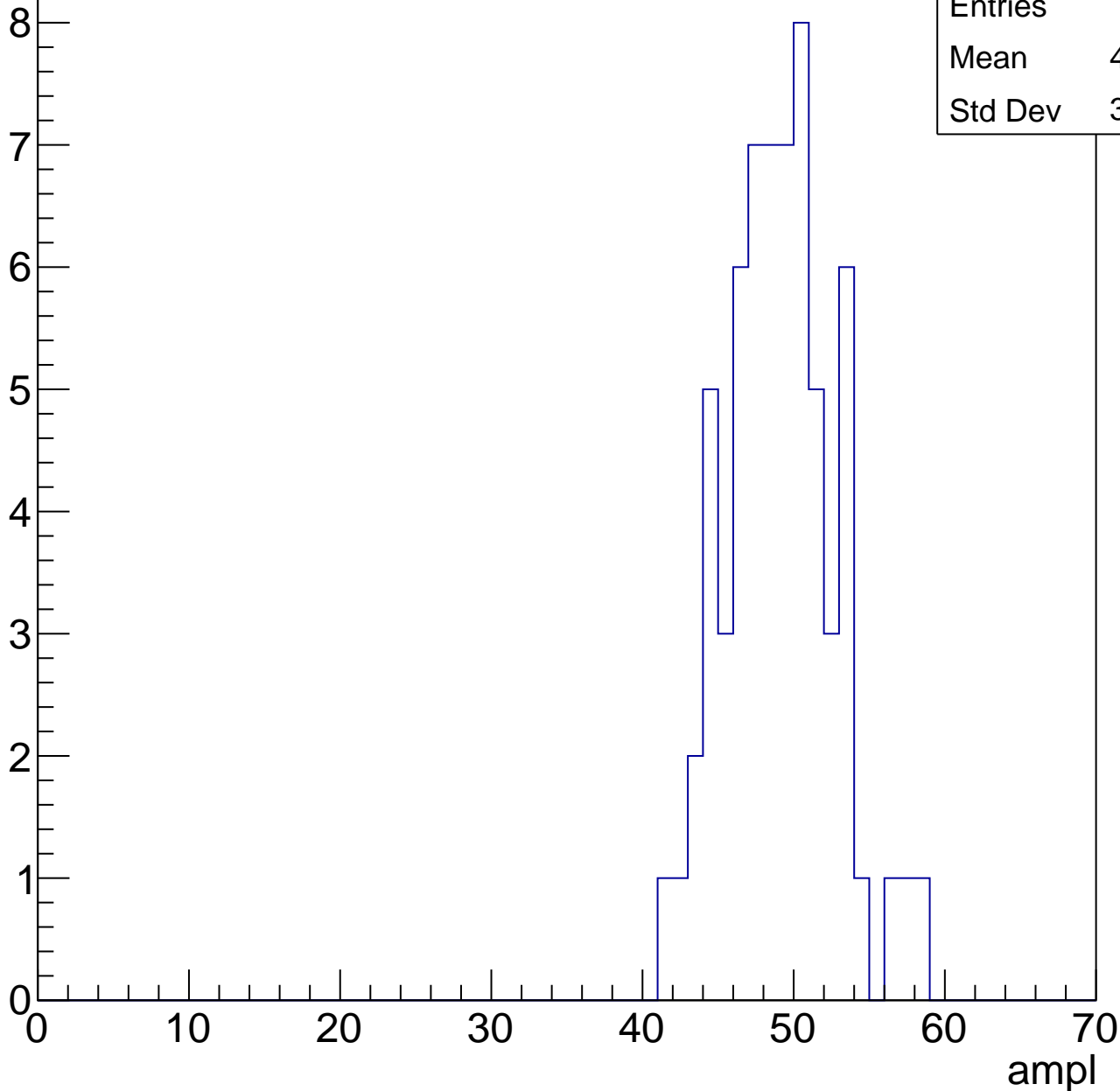


B0L102S, U20-ch39, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 48.65 |
| Std Dev | 3.528 |

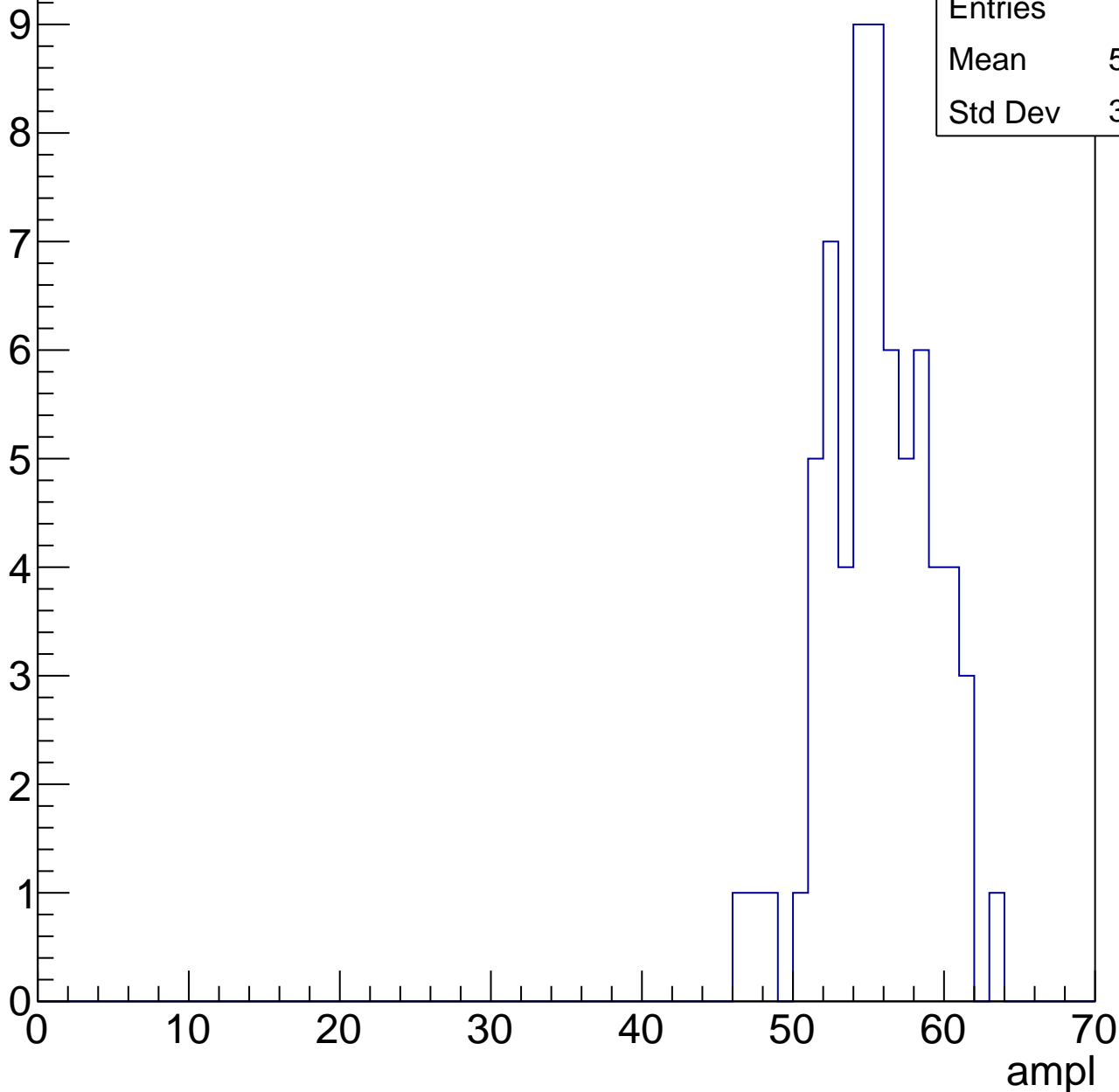


B0L102S, U20-ch39, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 55.13 |
| Std Dev | 3.459 |

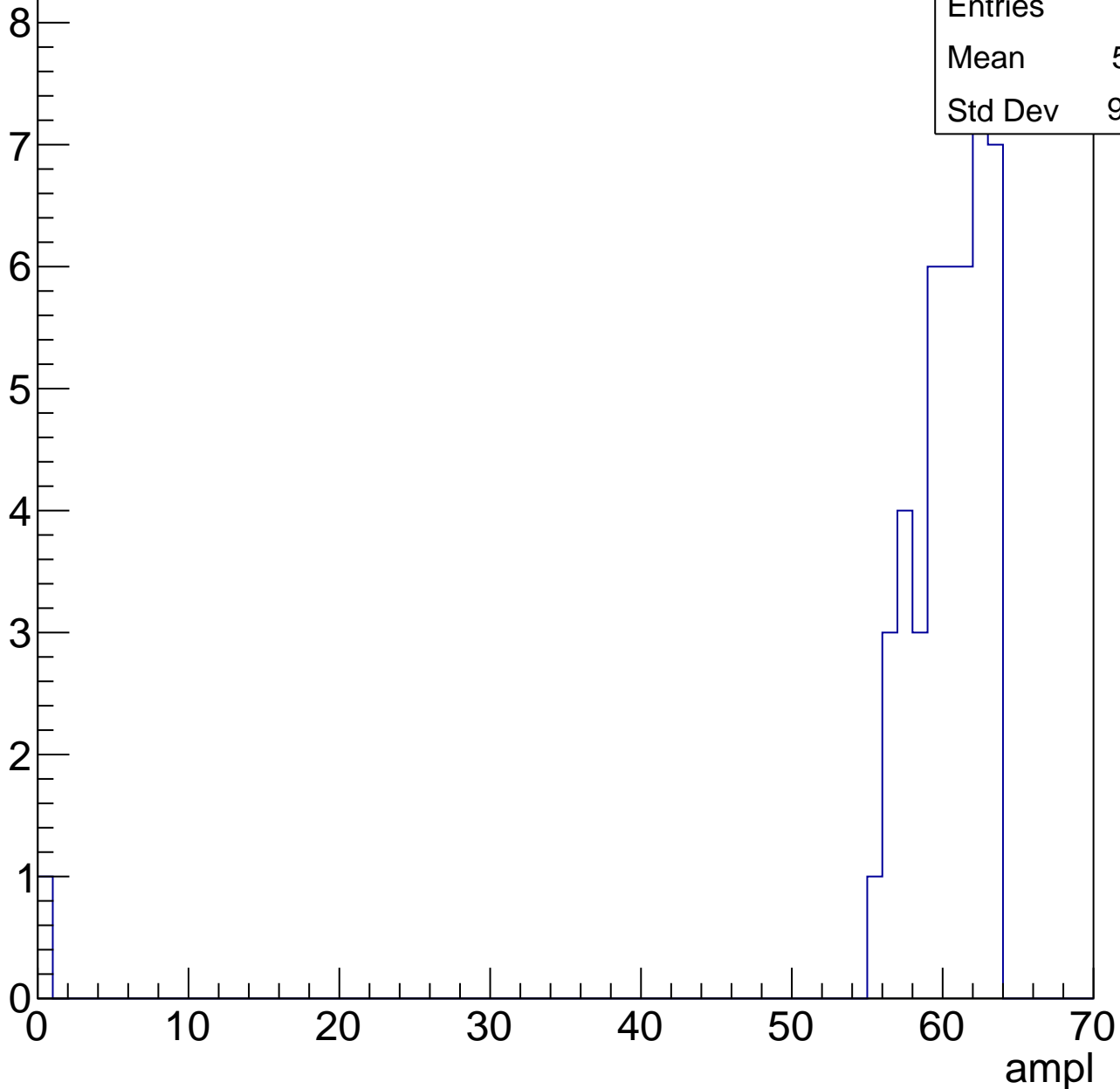


B0L102S, U20-ch39, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.71 |
| Std Dev | 9.133 |



B0L102S, U20-ch39, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62.2 |
| Std Dev | 0.7483 |

ampl

0 10 20 30 40 50 60 70

B0L102S, U20-ch39, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch40, adc0

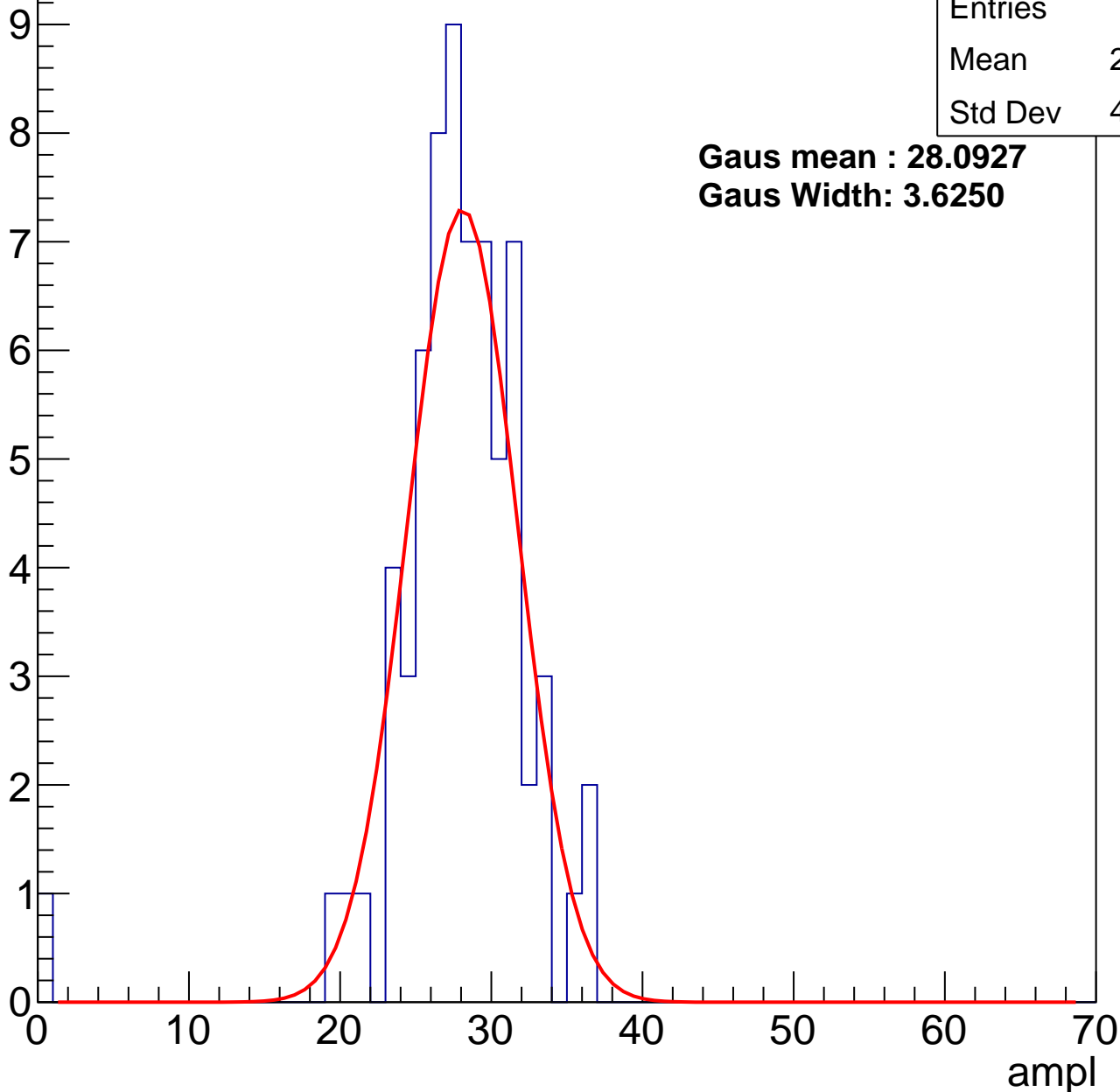
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 27.37 |
| Std Dev | 4.802 |

Gaus mean : 28.0927

Gaus Width: 3.6250



B0L102S, U20-ch40, adc1

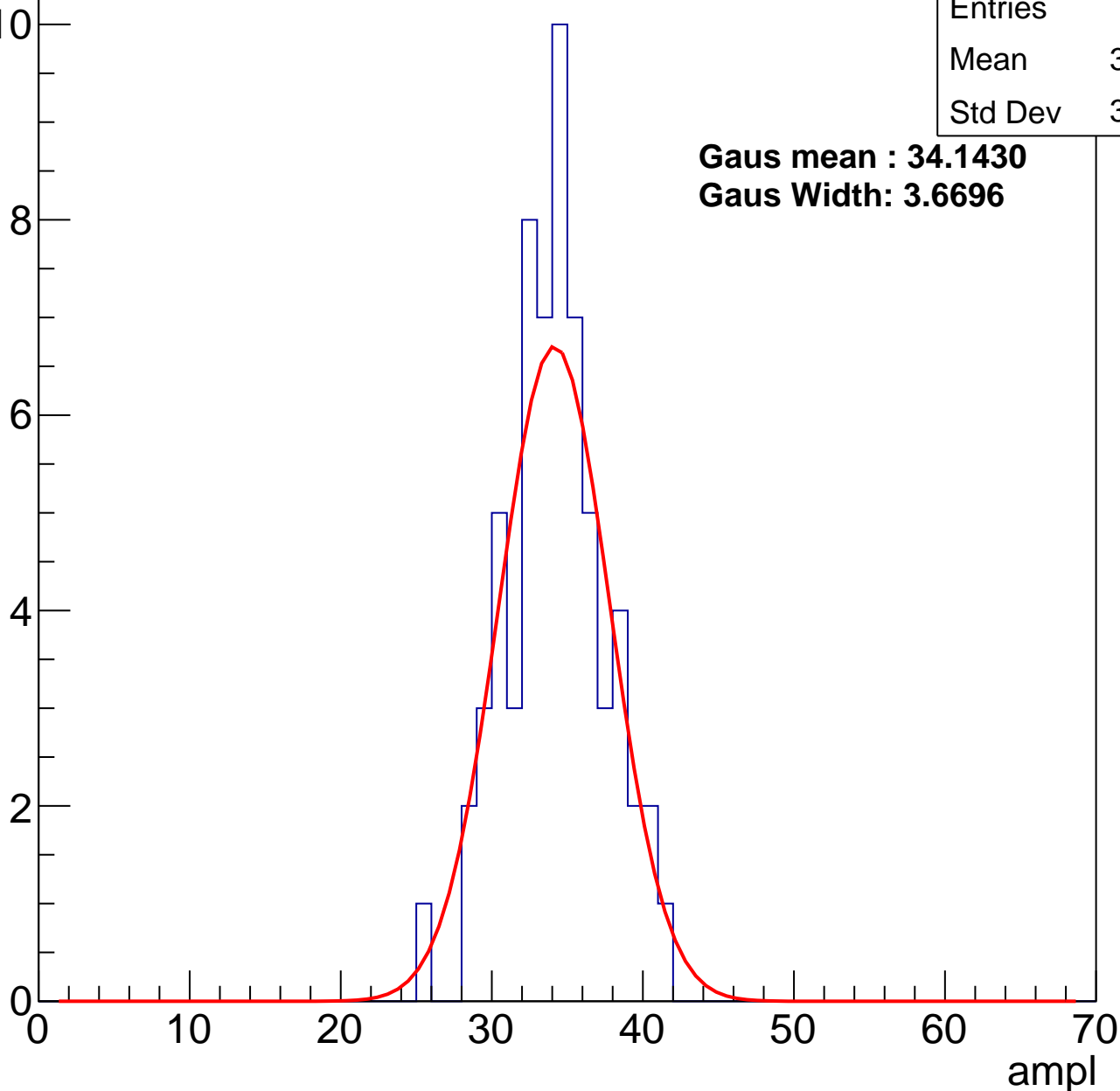
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 33.73 |
| Std Dev | 3.233 |

Gaus mean : 34.1430

Gaus Width: 3.6696



B0L102S, U20-ch40, adc2

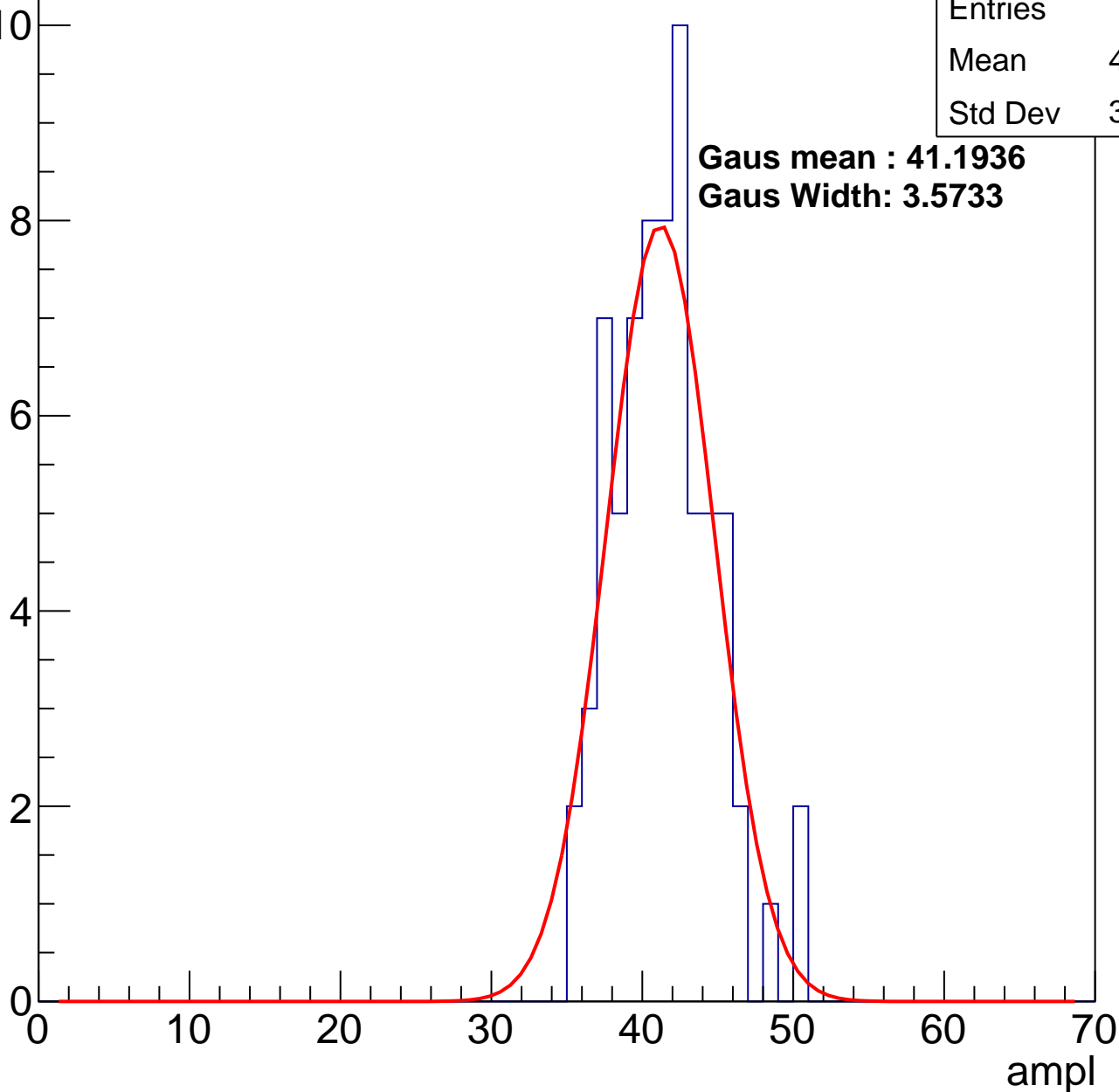
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 40.97 |
| Std Dev | 3.286 |

Gaus mean : 41.1936

Gaus Width: 3.5733

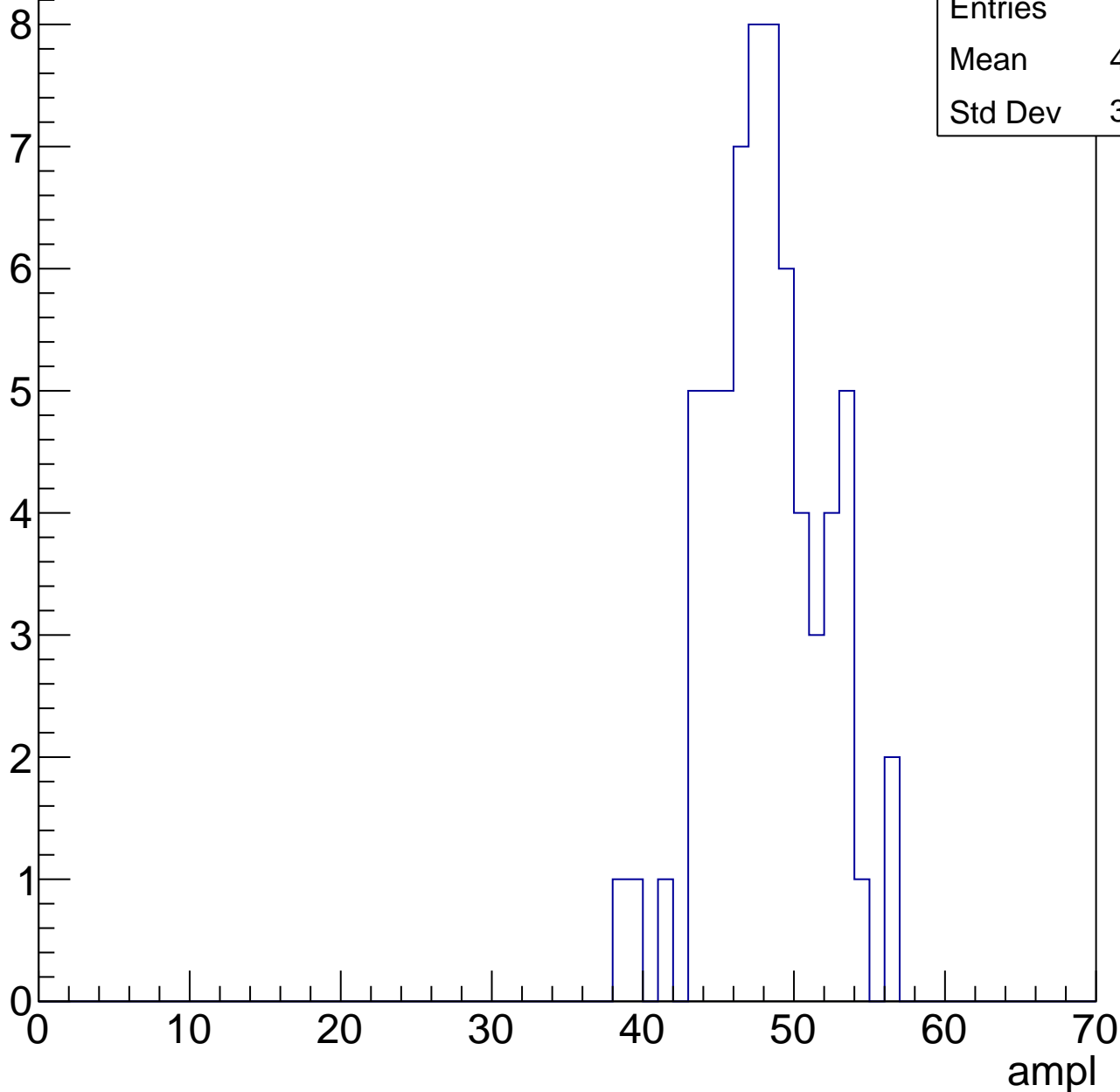


B0L102S, U20-ch40, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 47.67 |
| Std Dev | 3.715 |

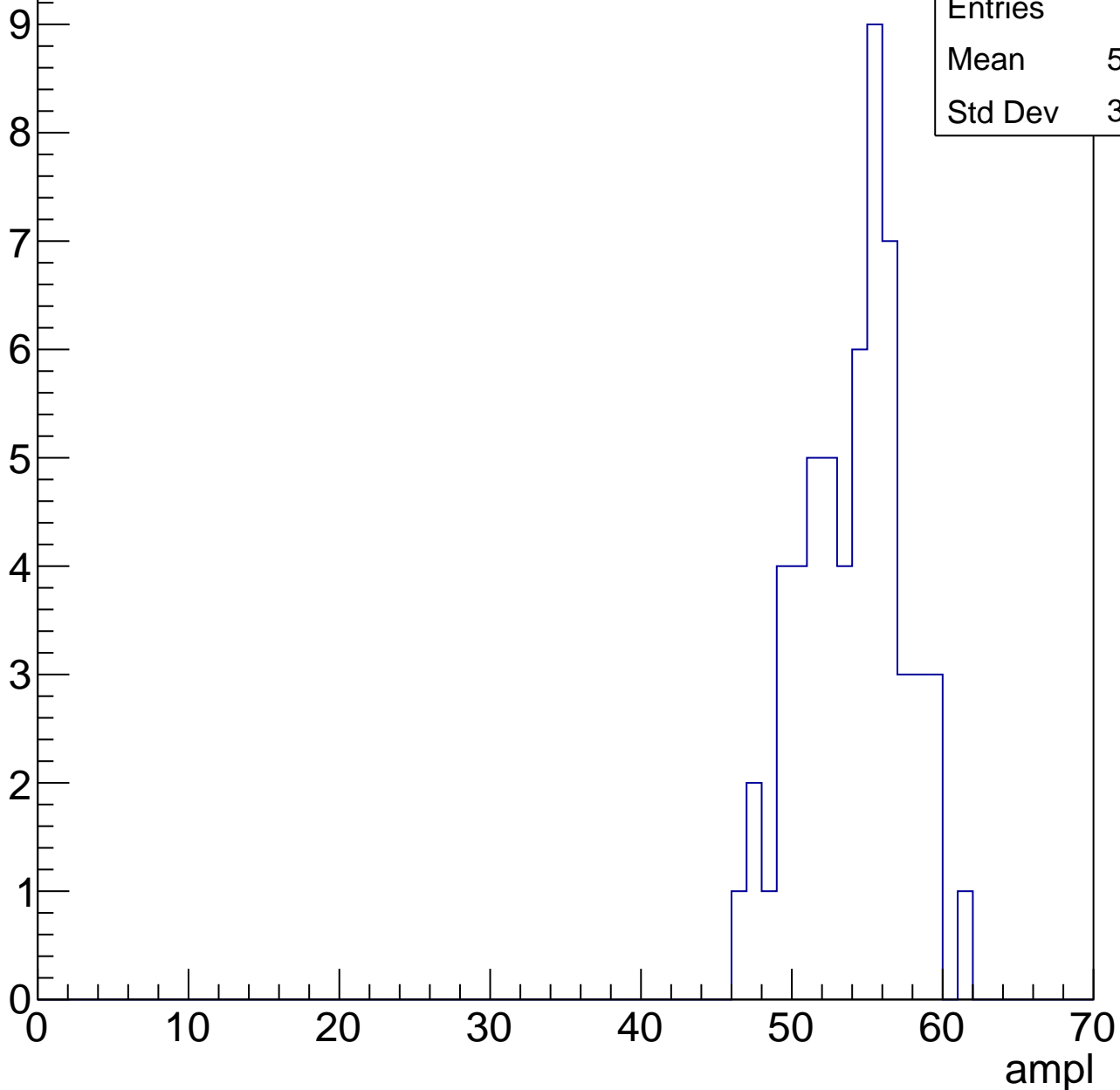


B0L102S, U20-ch40, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 53.53 |
| Std Dev | 3.359 |

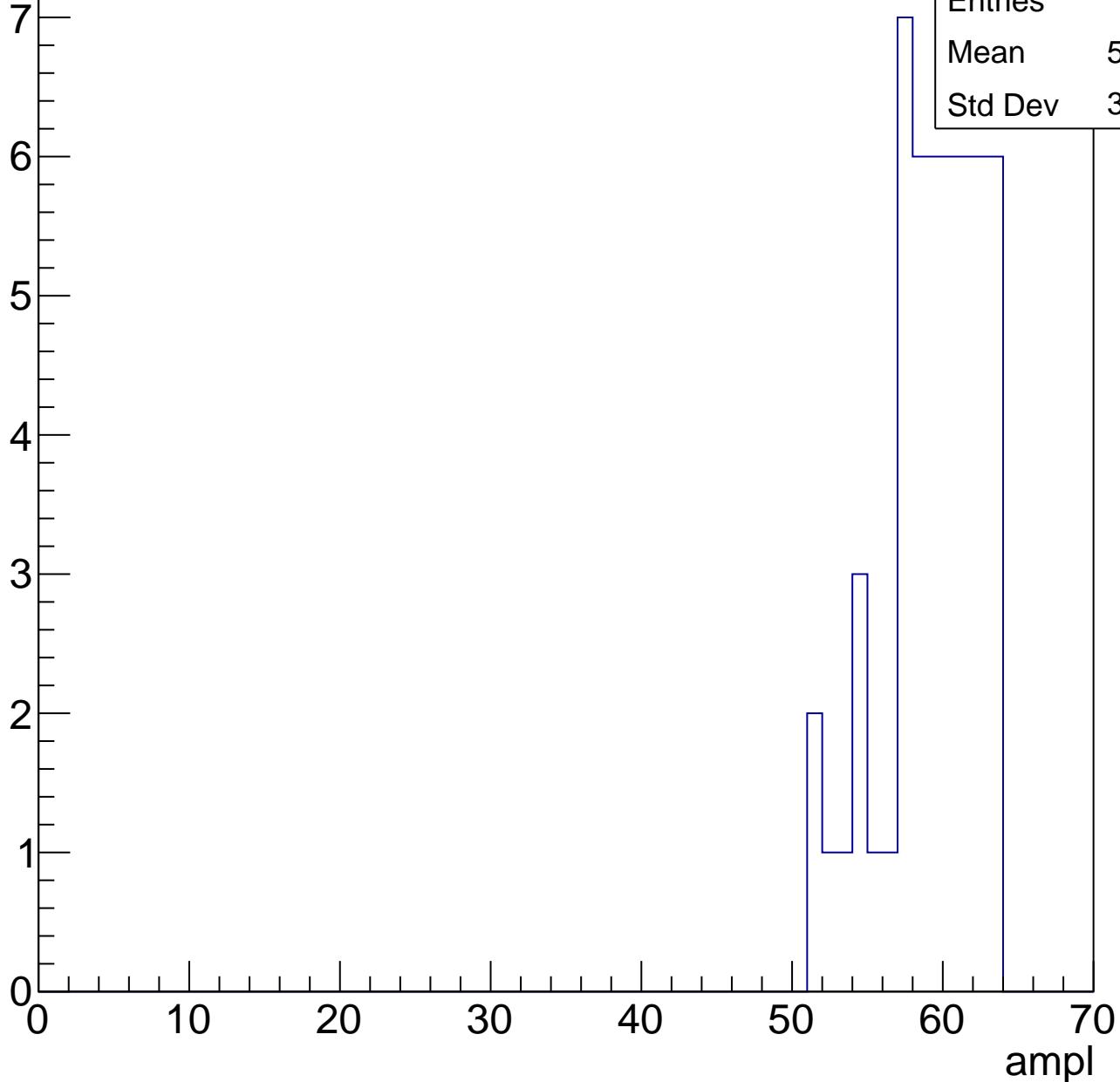


B0L102S, U20-ch40, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

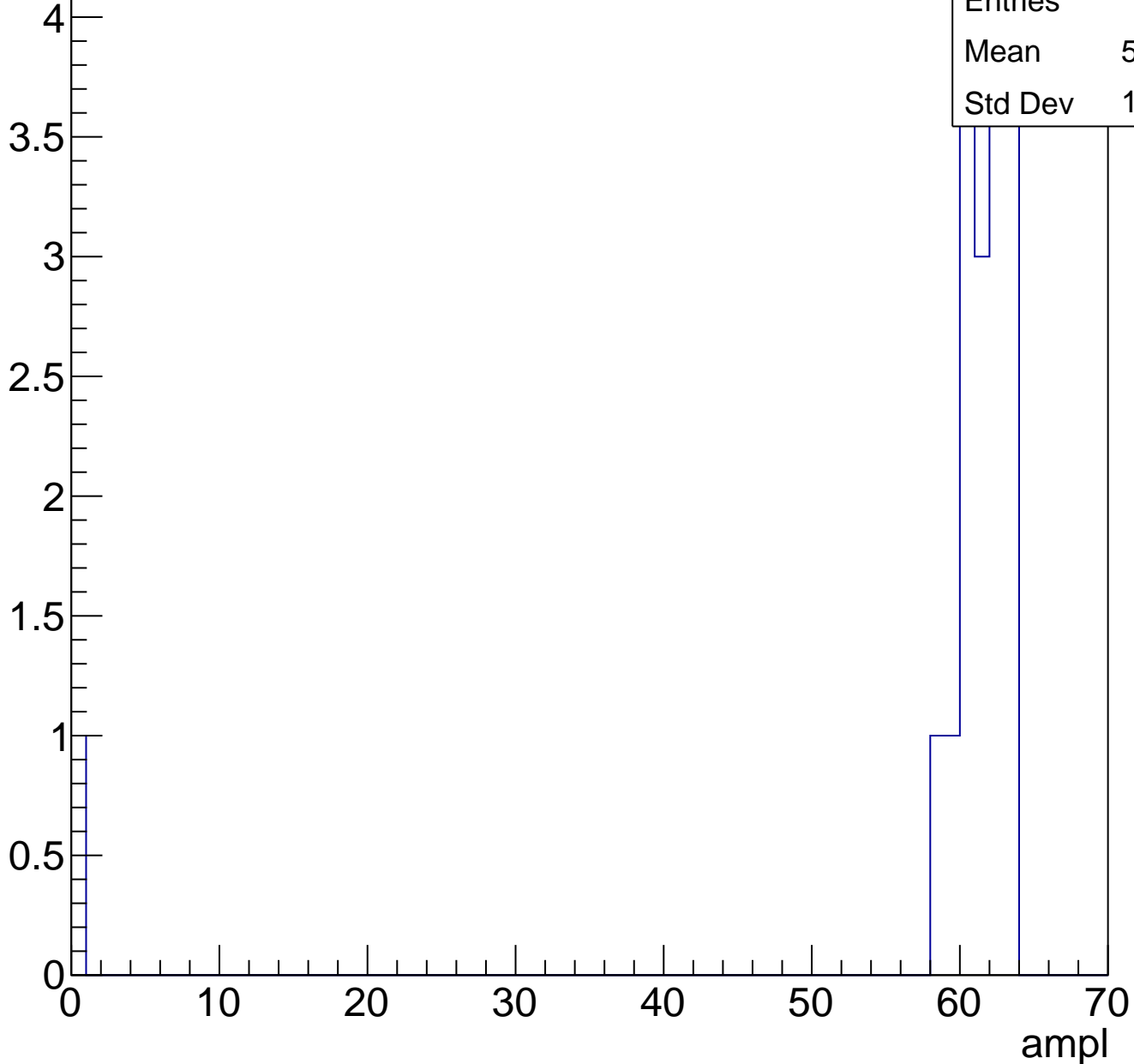
| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.79 |
| Std Dev | 3.176 |



B0L102S, U20-ch40, adc6

calib_packv5_042523_0143.root, FC#12, port B1

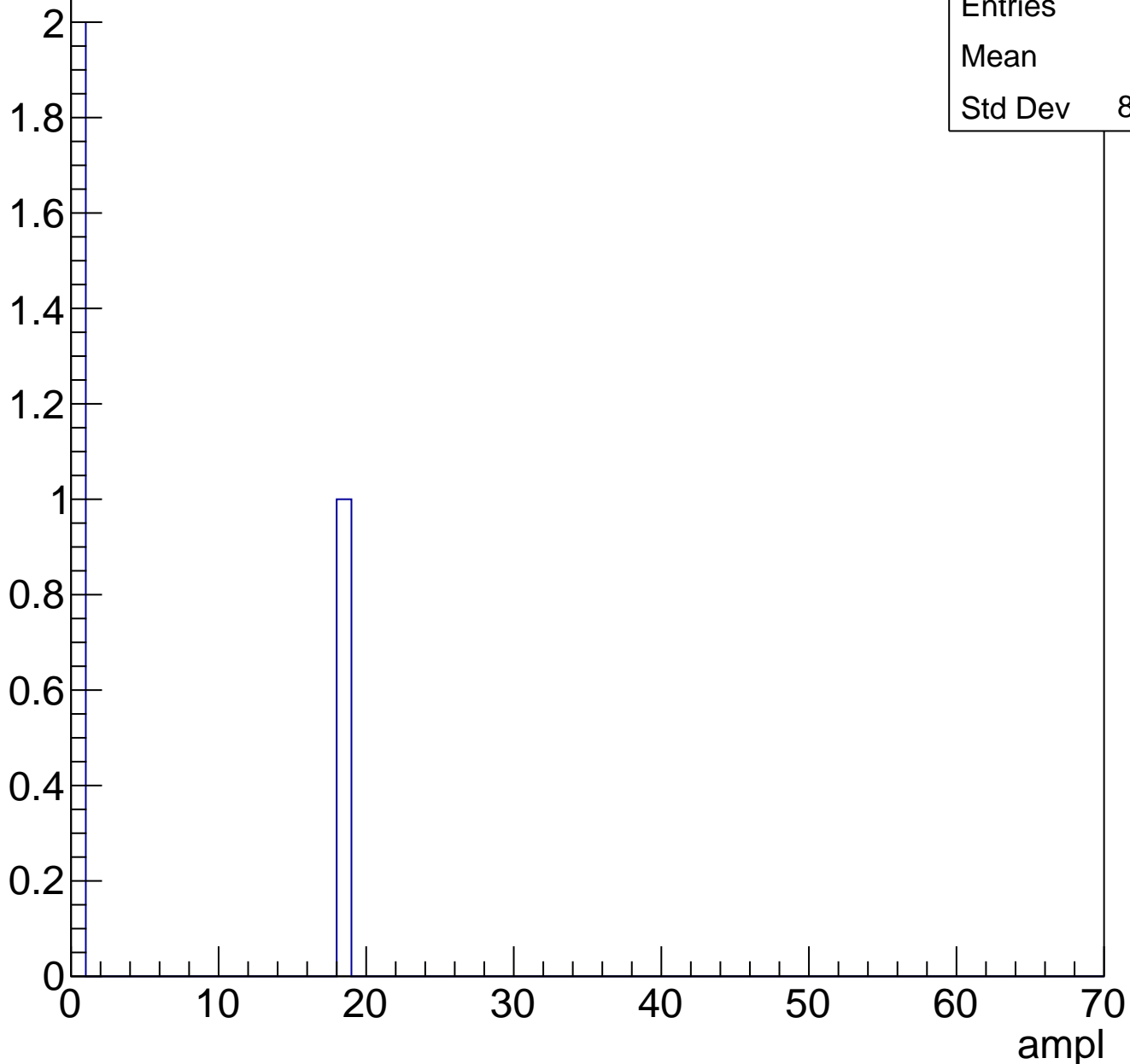
Entry



B0L102S, U20-ch40, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6 |
| Std Dev | 8.485 |

B0L102S, U20-ch41, adc0

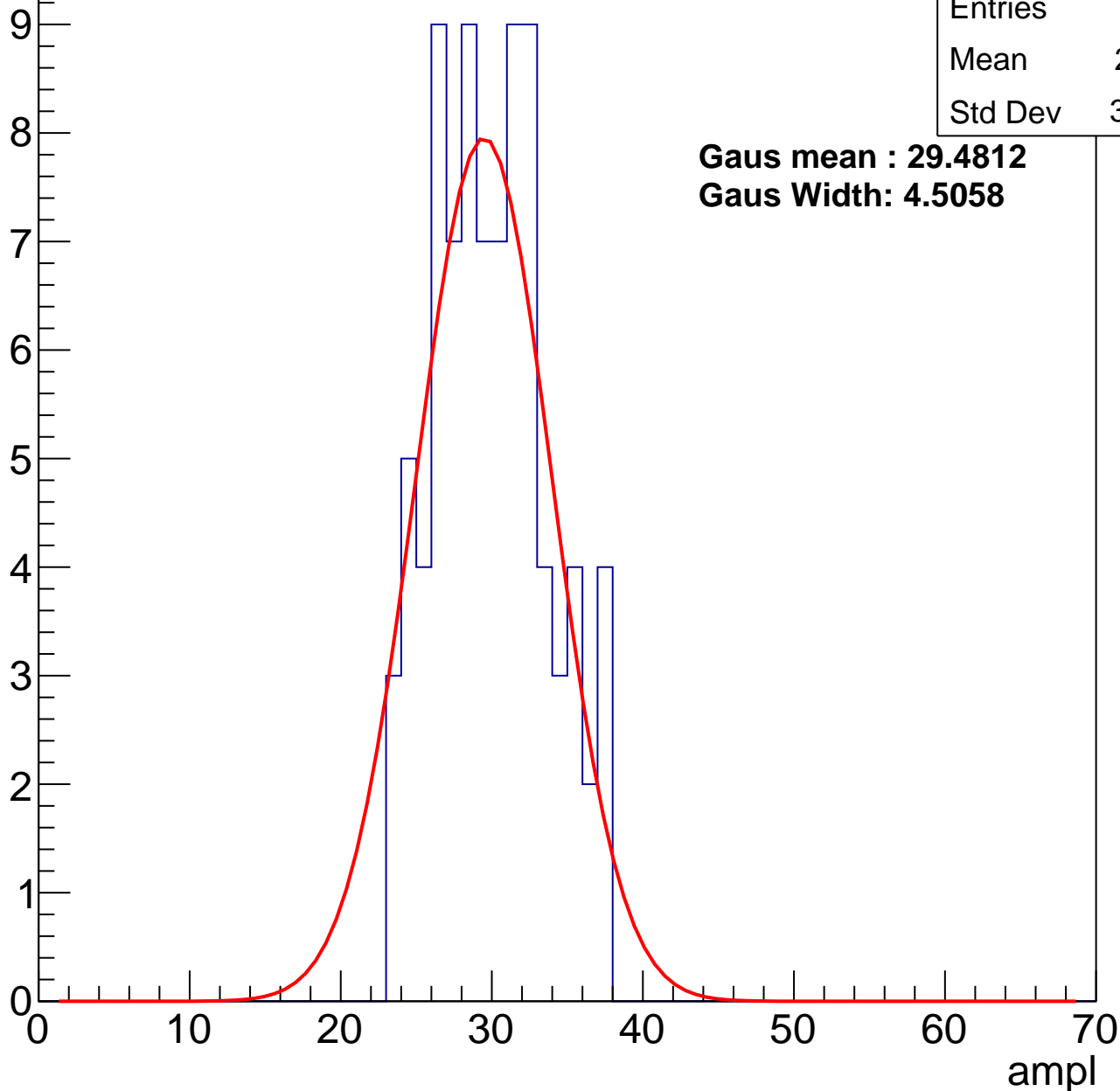
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 29.51 |
| Std Dev | 3.662 |

Gaus mean : 29.4812

Gaus Width: 4.5058



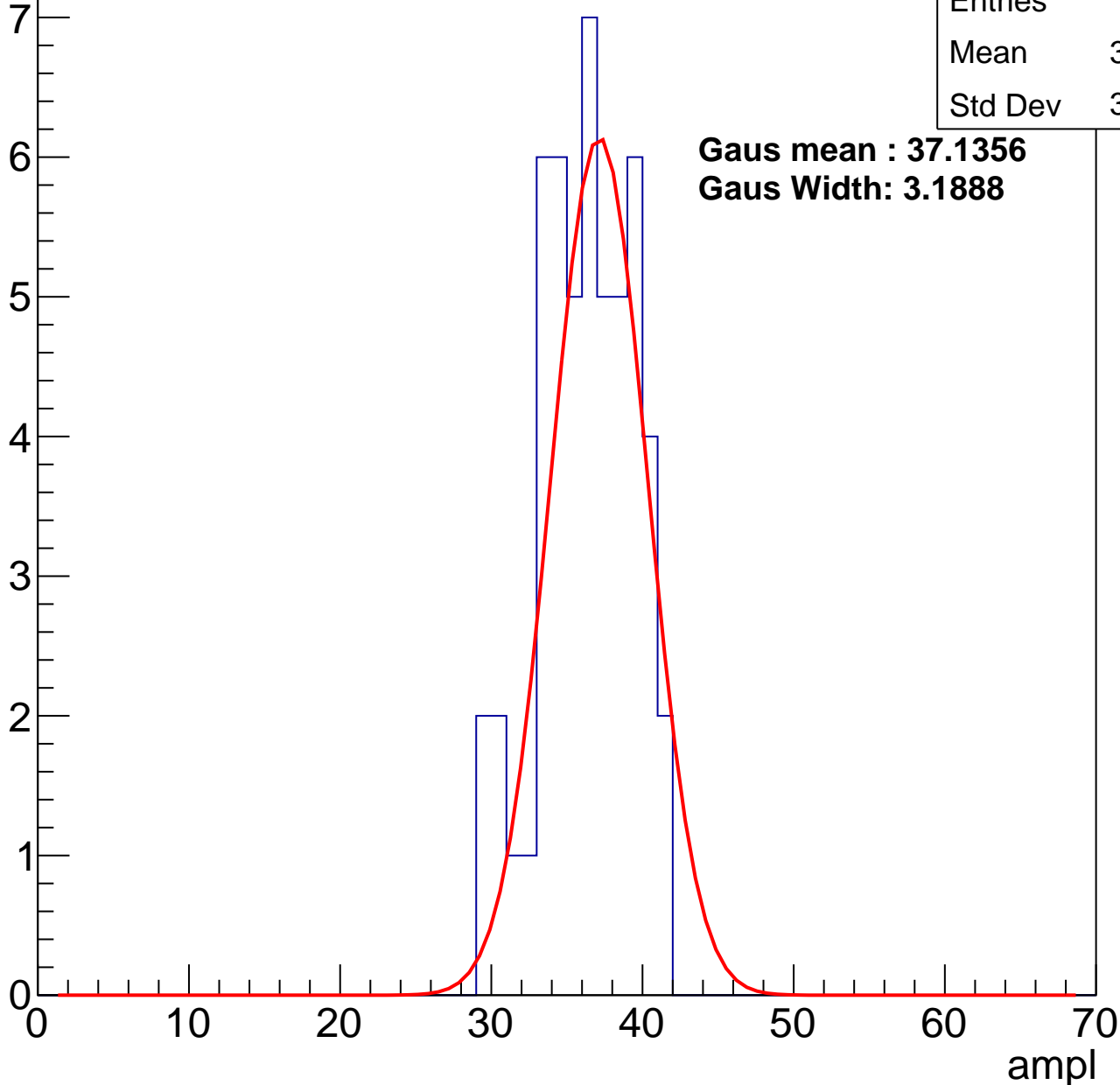
B0L102S, U20-ch41, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 35.79 |
| Std Dev | 3.053 |

Gaus mean : 37.1356
Gaus Width: 3.1888



B0L102S, U20-ch41, adc2

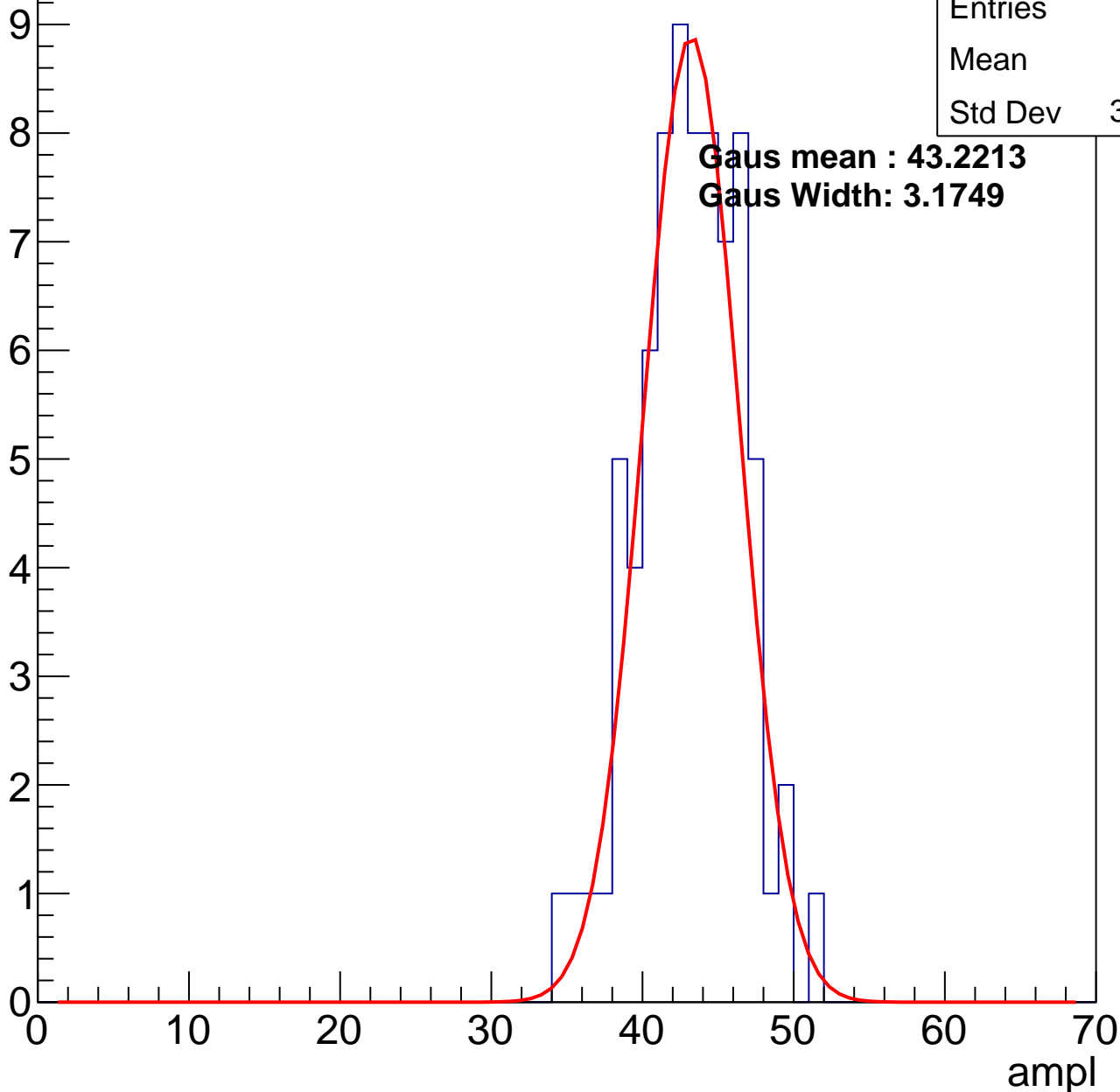
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.7 |
| Std Dev | 3.356 |

Gaus mean : 43.2213

Gaus Width: 3.1749

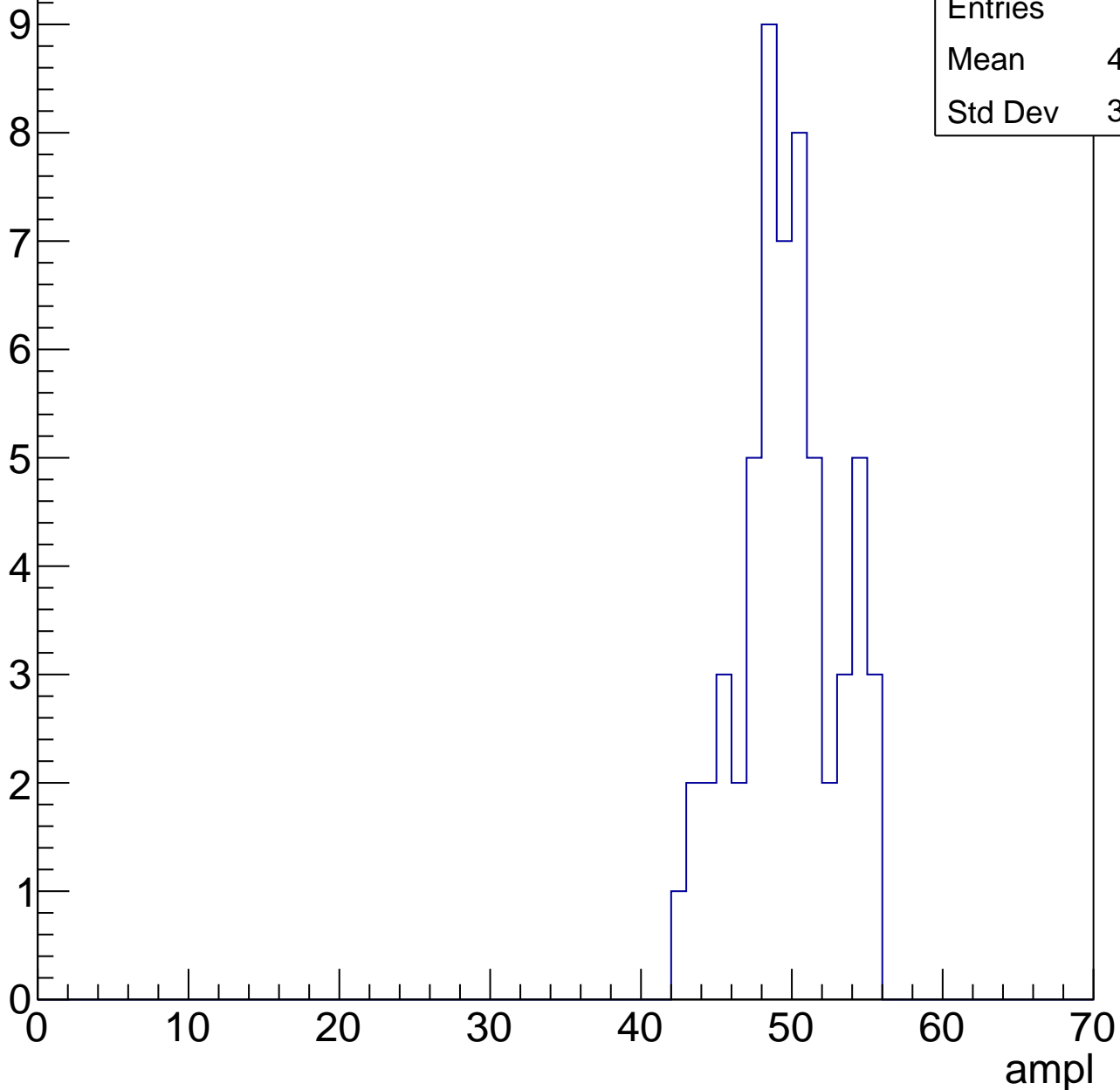


B0L102S, U20-ch41, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.23 |
| Std Dev | 3.217 |

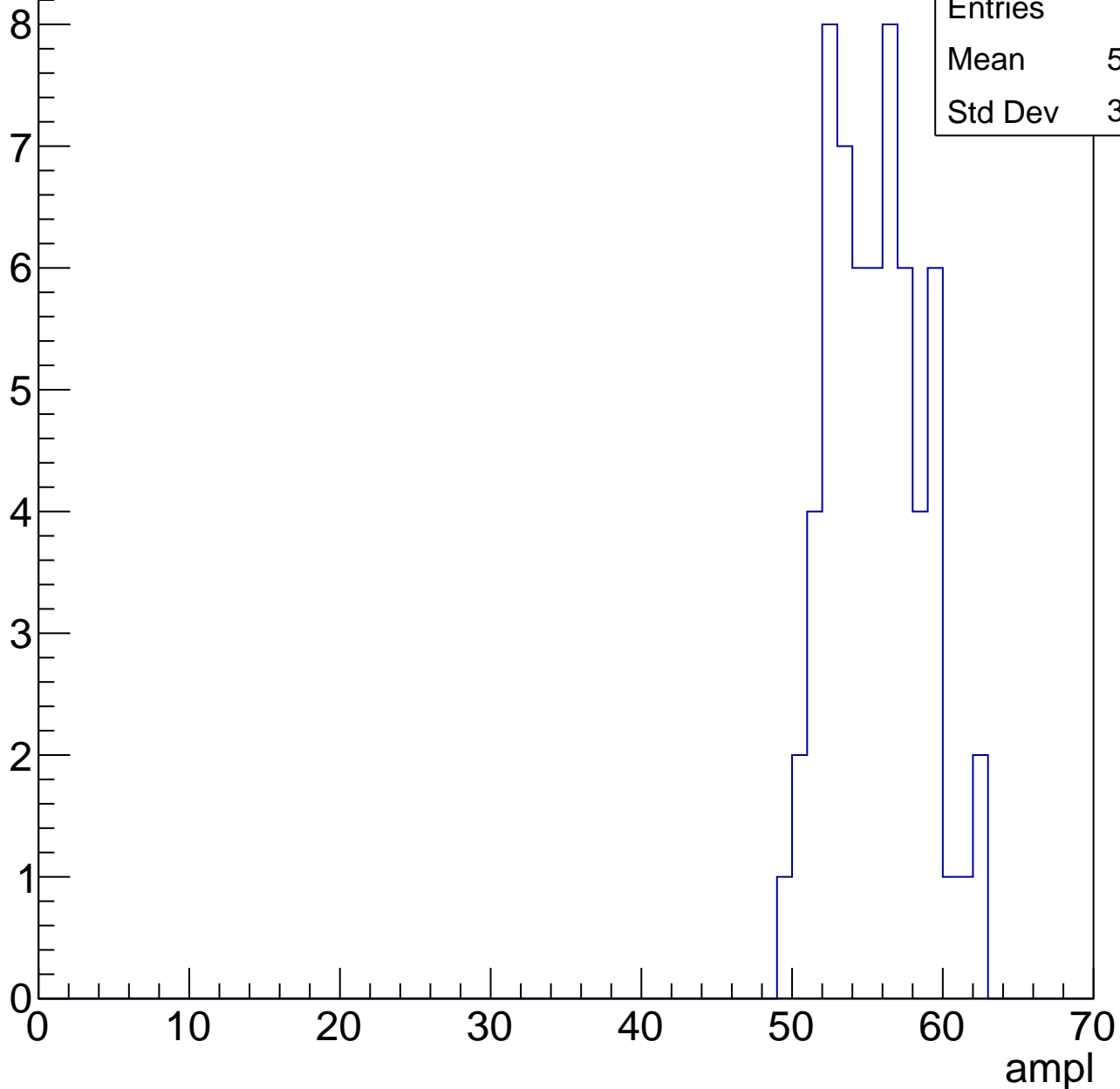


B0L102S, U20-ch41, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

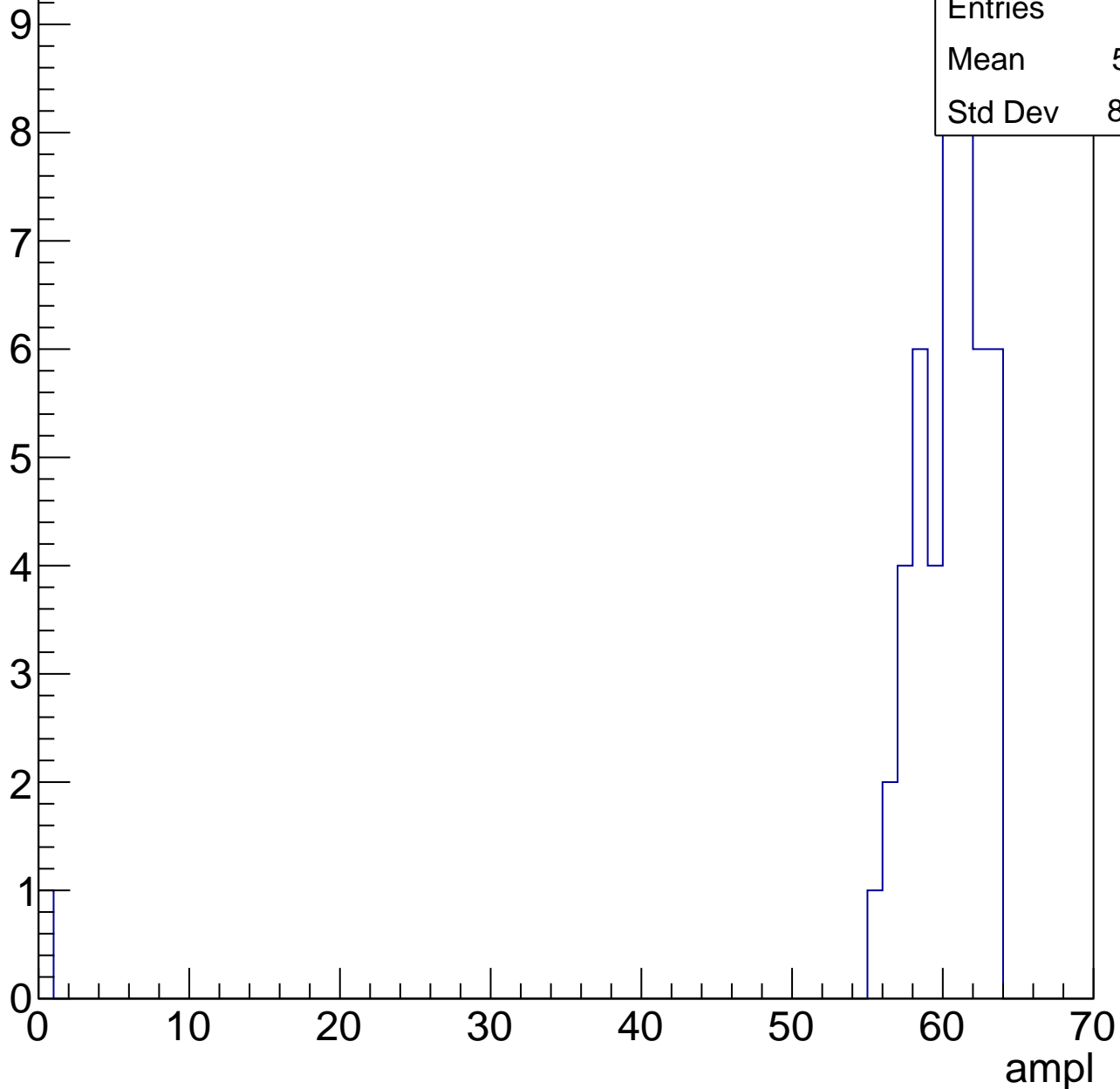
| | |
|---------|-------|
| Entries | 62 |
| Mean | 55.08 |
| Std Dev | 3.055 |



B0L102S, U20-ch41, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch41, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch41, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch42, adc0

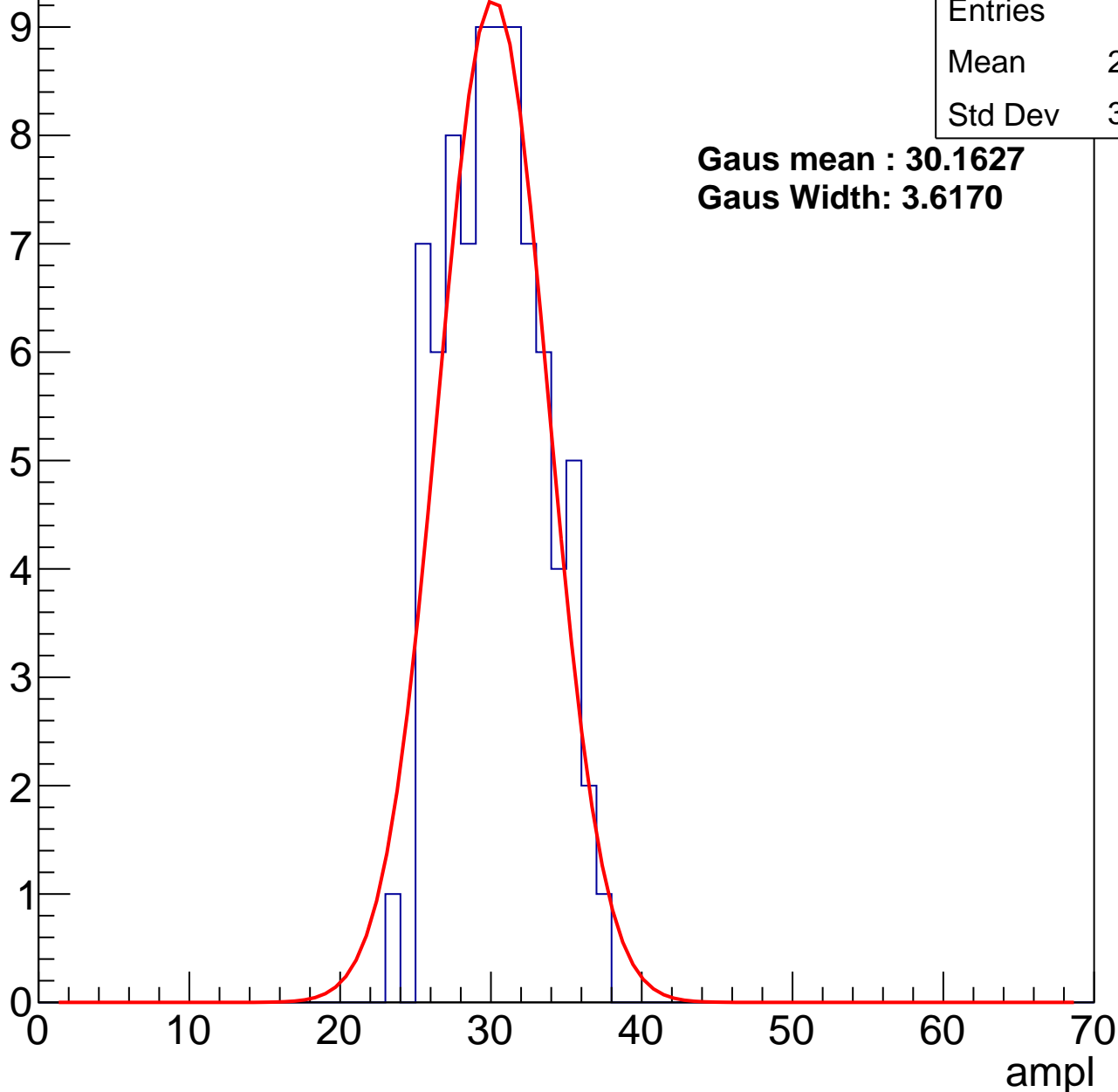
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 29.85 |
| Std Dev | 3.198 |

Gaus mean : 30.1627

Gaus Width: 3.6170



B0L102S, U20-ch42, adc1

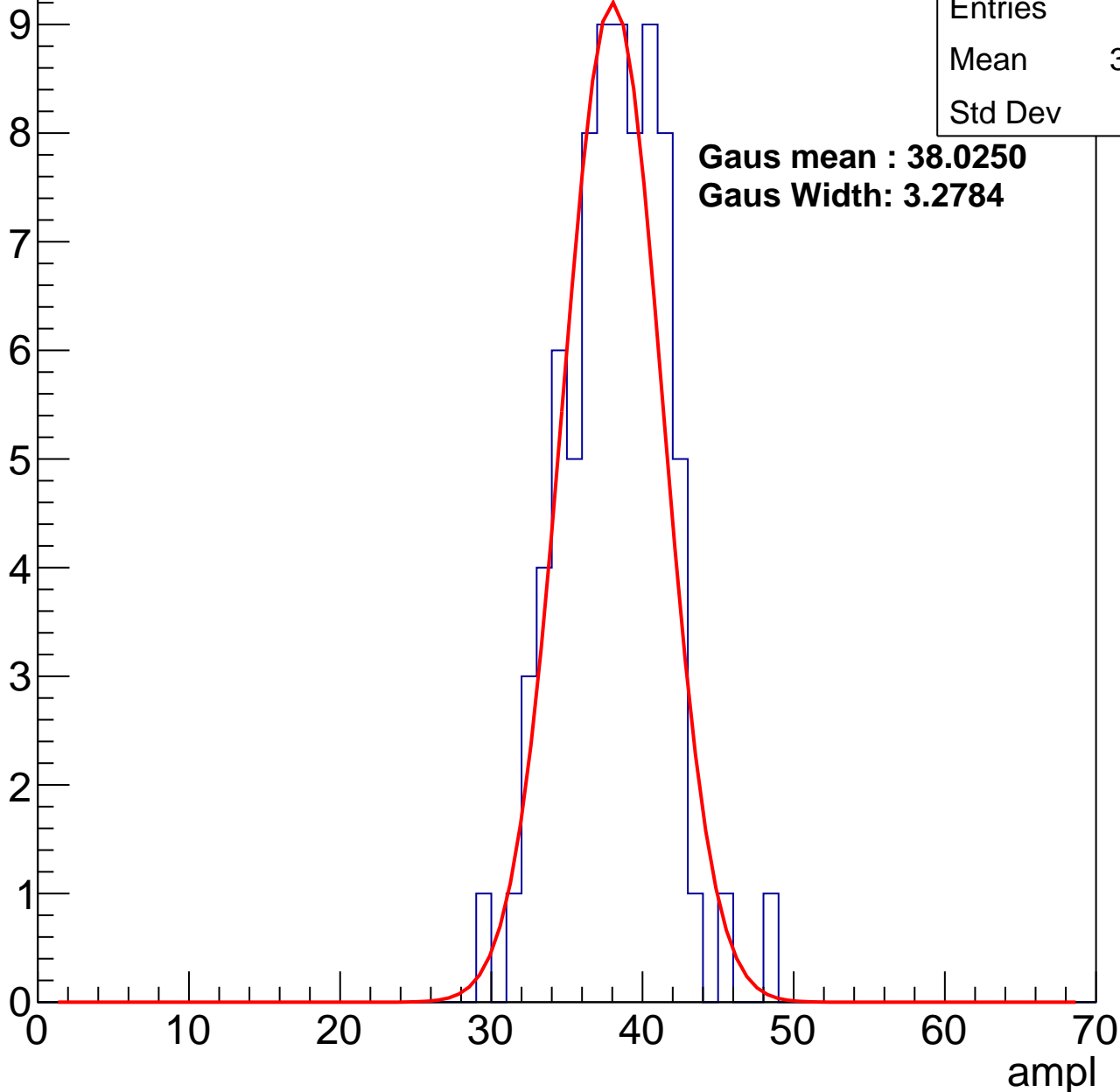
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 37.67 |
| Std Dev | 3.34 |

Gaus mean : 38.0250

Gaus Width: 3.2784



B0L102S, U20-ch42, adc2

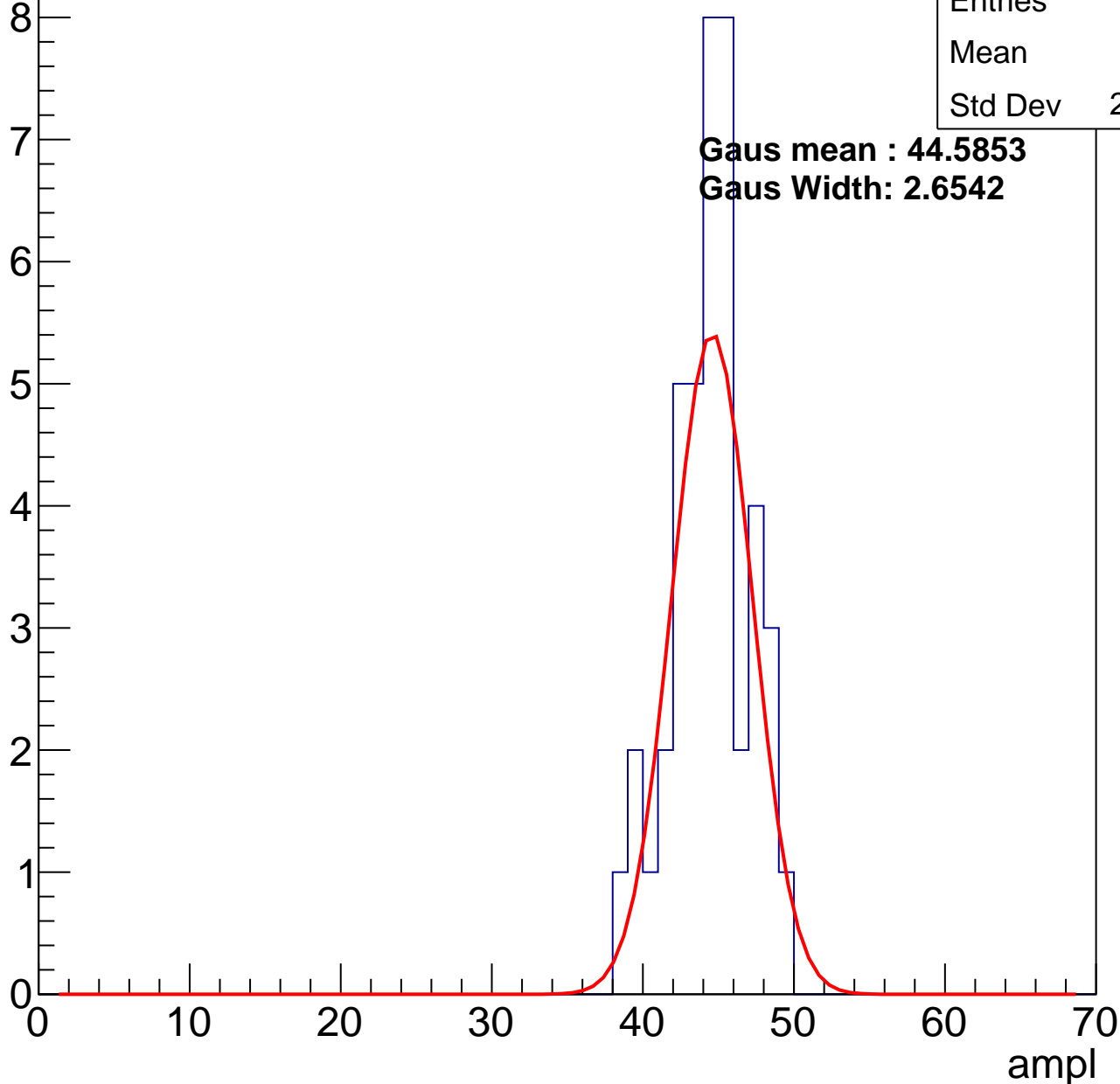
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 44 |
| Std Dev | 2.535 |

Gaus mean : 44.5853

Gaus Width: 2.6542



B0L102S, U20-ch42, adc3

calib_packv5_042523_0143.root, FC#12, port B1

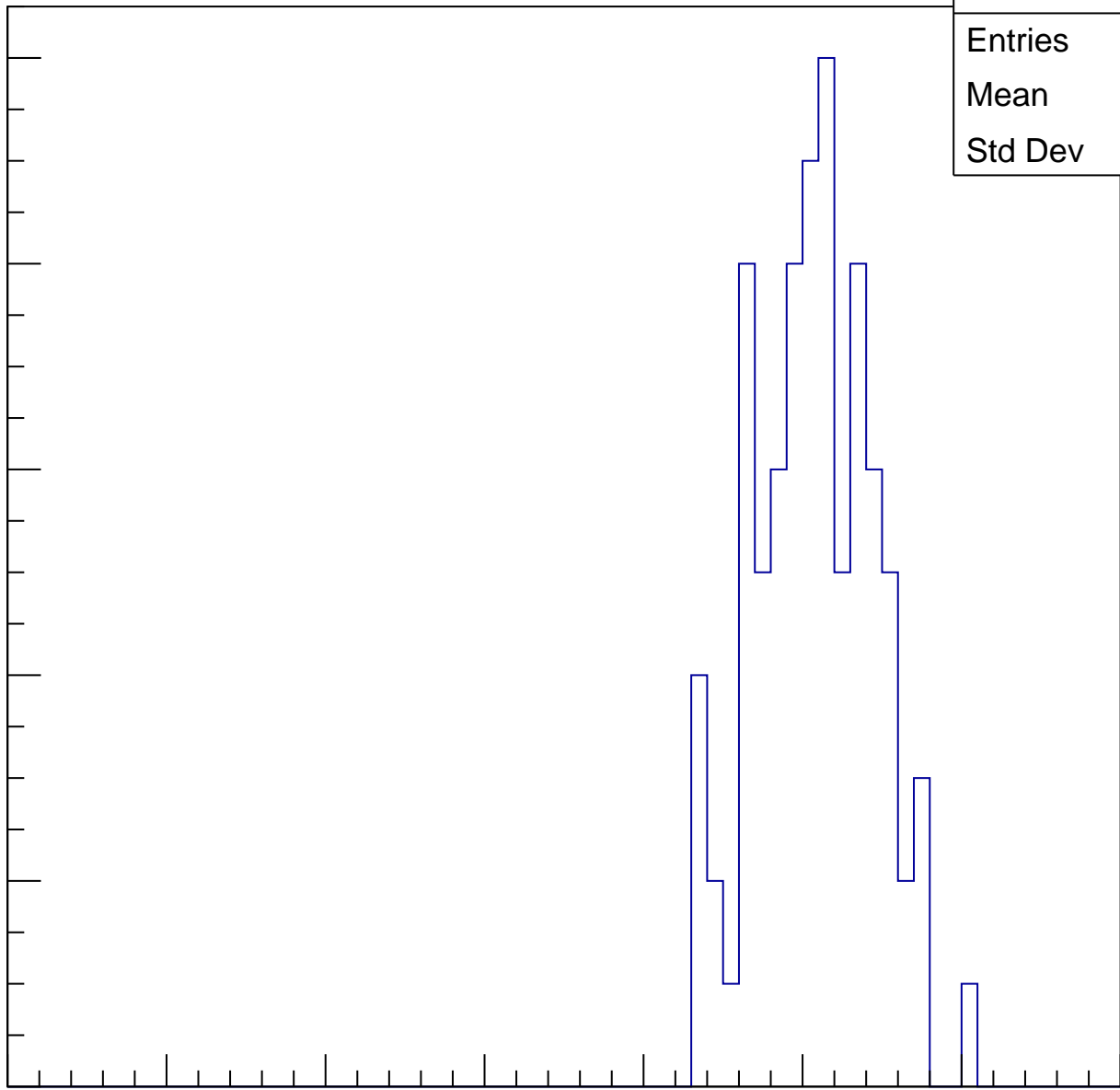
| | |
|---------|-------|
| Entries | 83 |
| Mean | 50.29 |
| Std Dev | 3.695 |

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

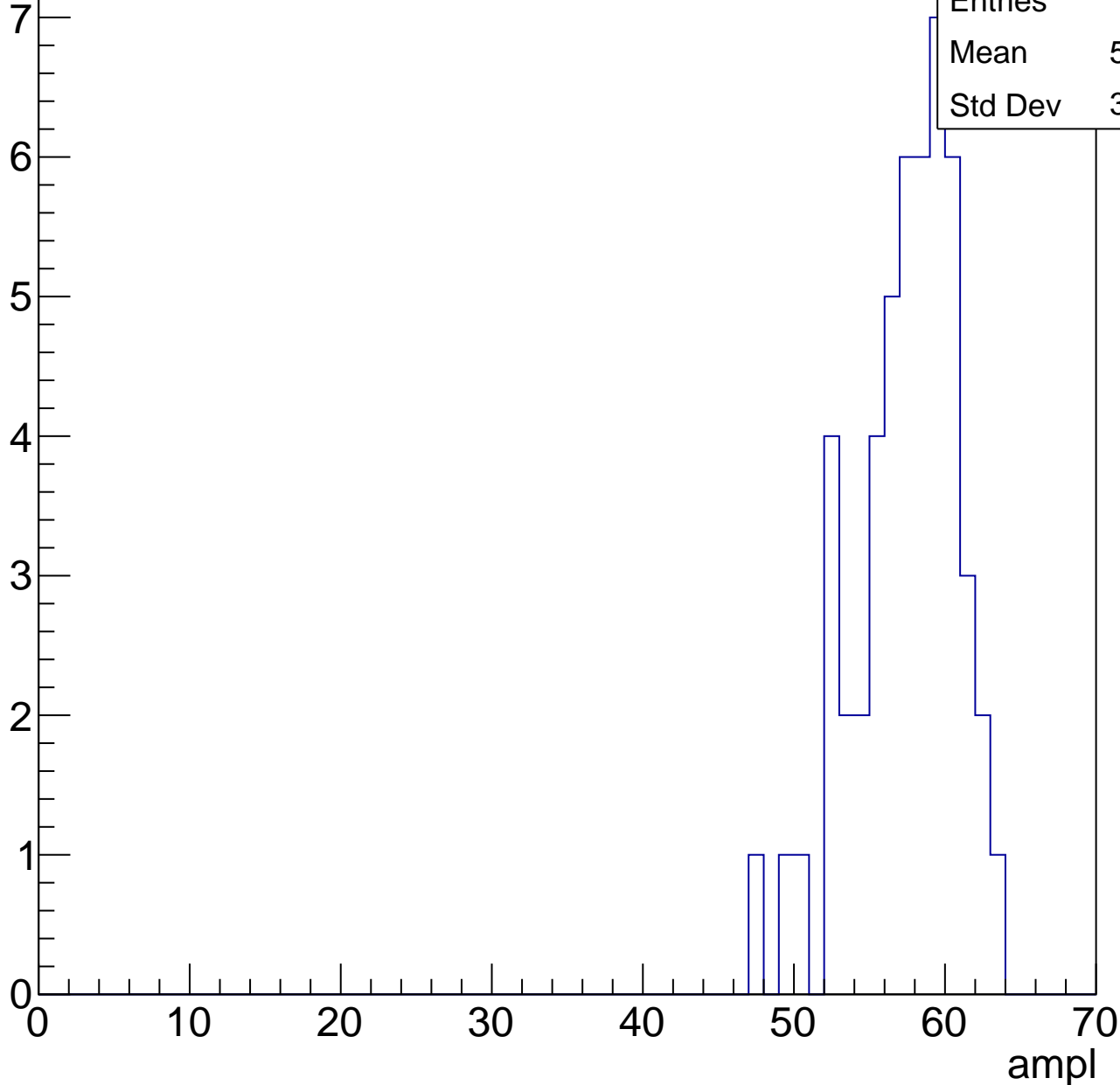


B0L102S, U20-ch42, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.88 |
| Std Dev | 3.445 |

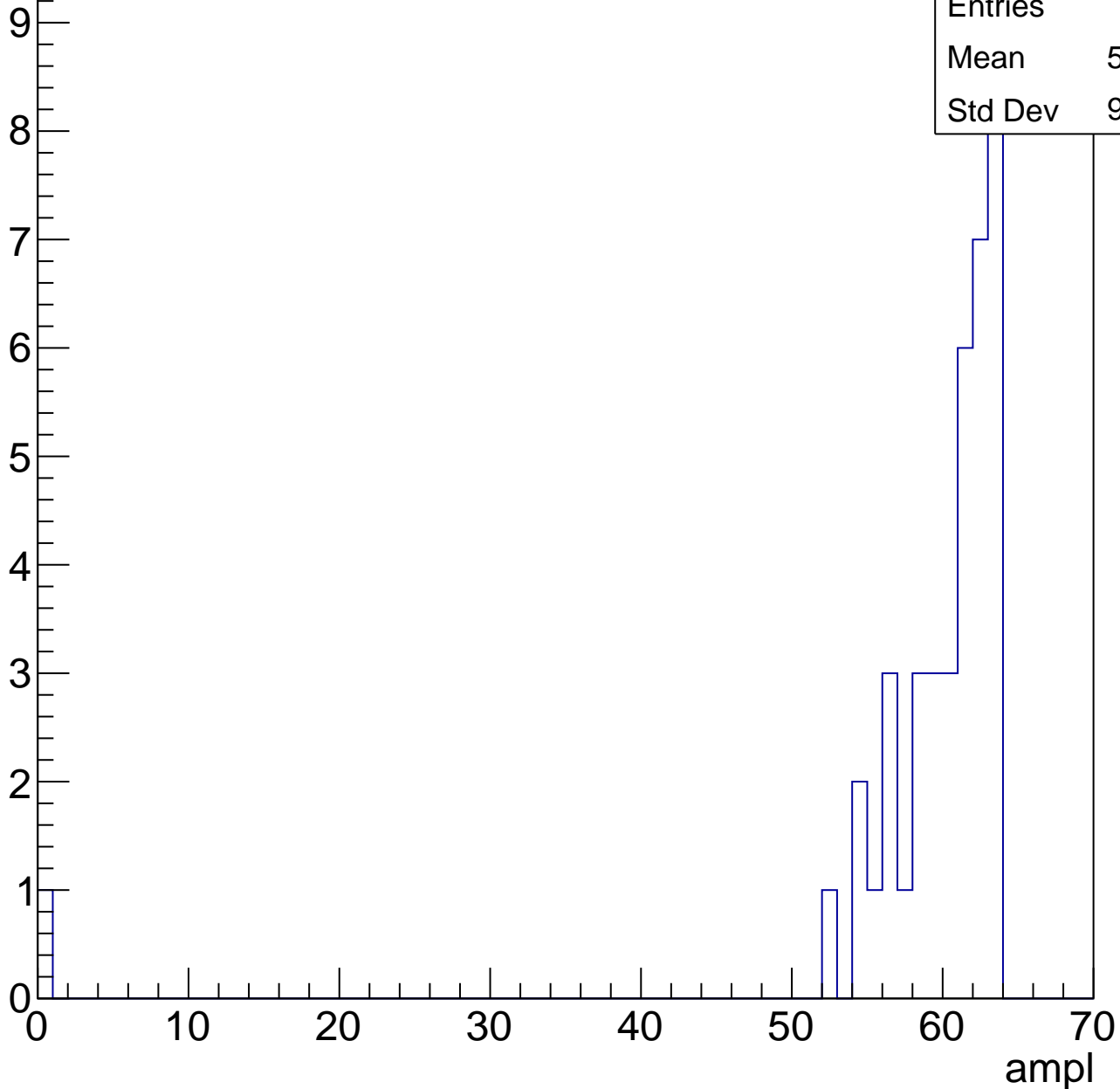


B0L102S, U20-ch42, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

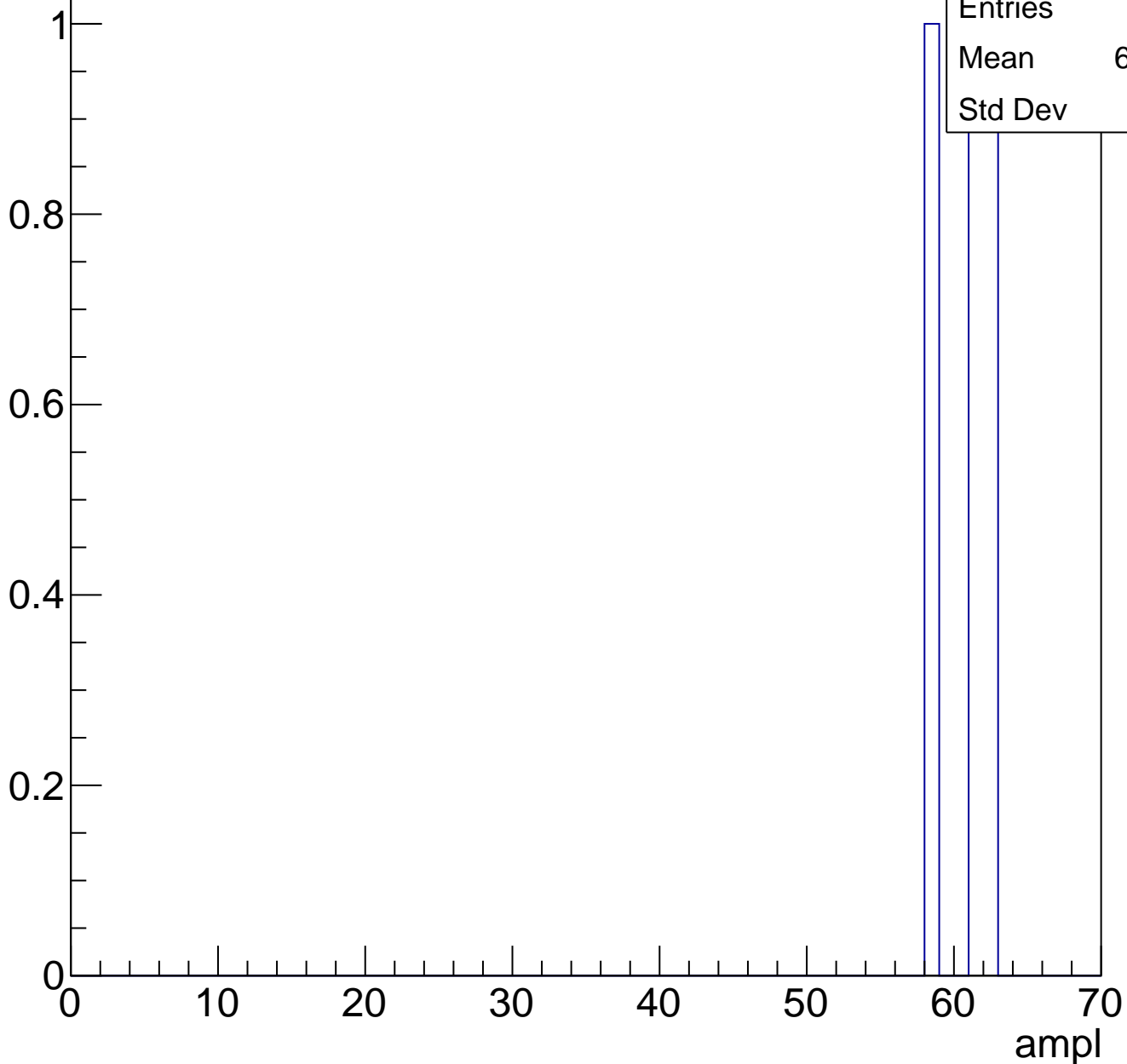
| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.45 |
| Std Dev | 9.813 |



B0L102S, U20-ch42, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch42, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch43, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 73 |
| Mean | 27.96 |
| Std Dev | 5.684 |

Gaus mean : 29.2522

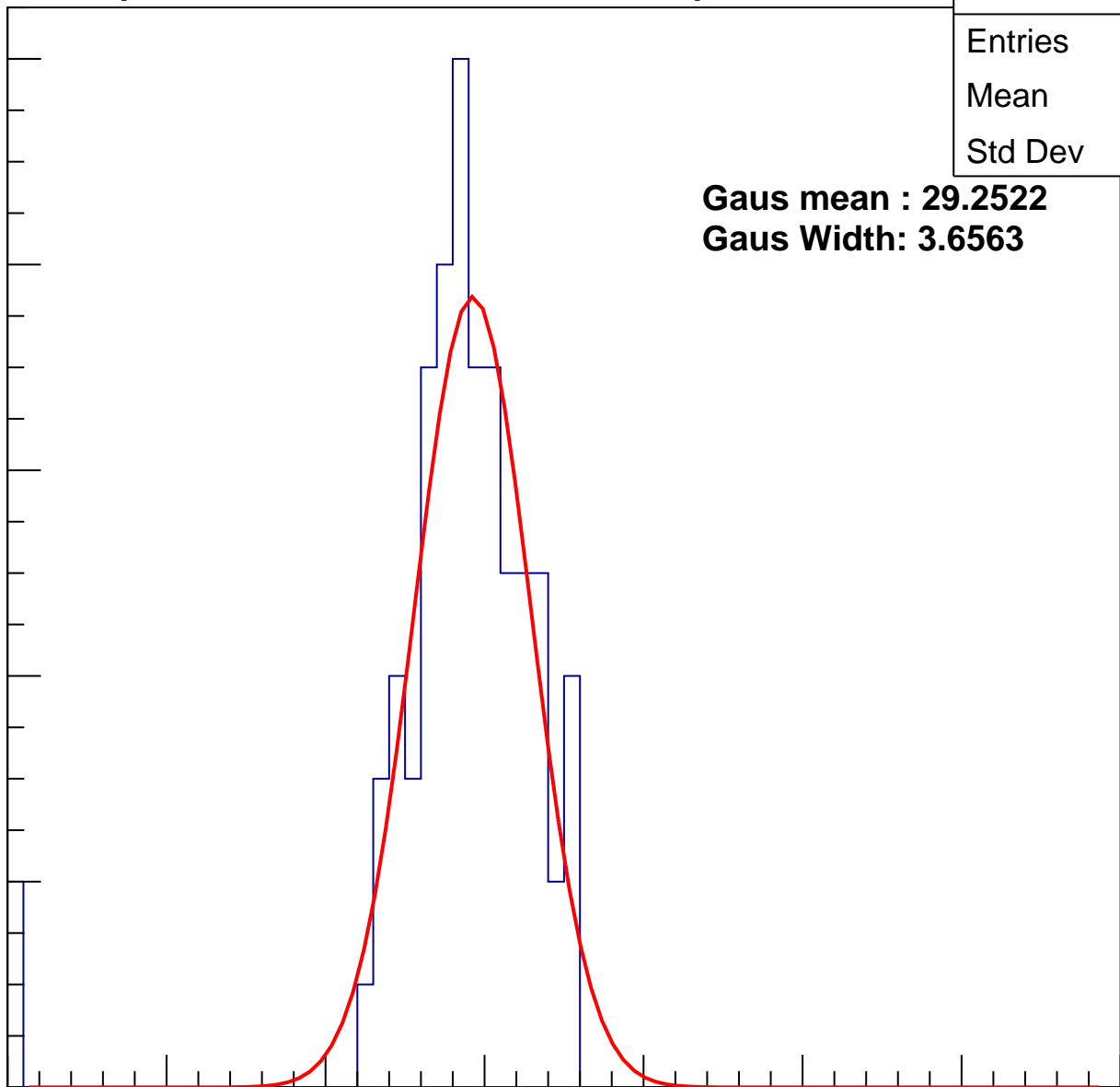
Gaus Width: 3.6563

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch43, adc1

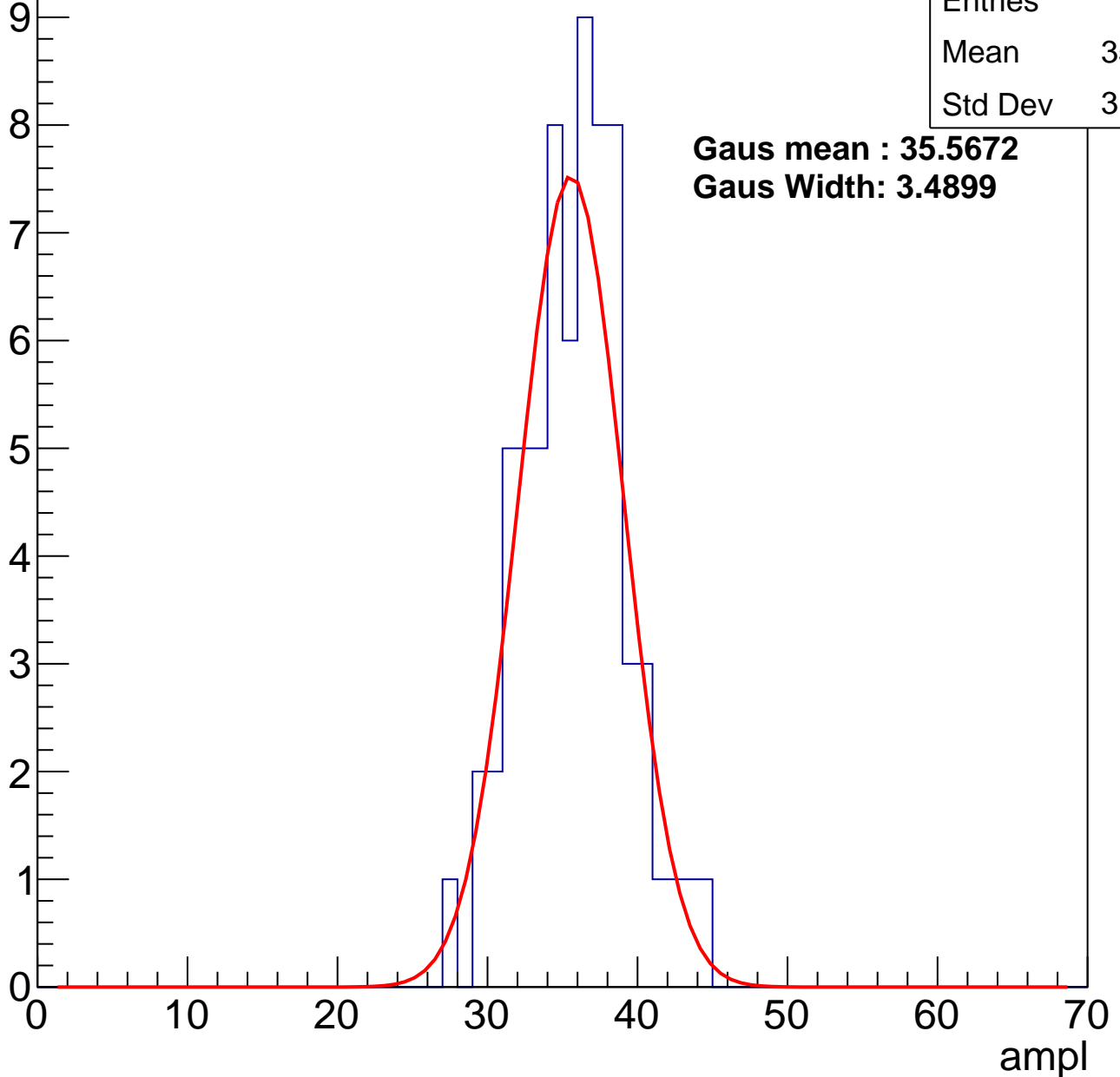
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.33 |
| Std Dev | 3.399 |

Gaus mean : 35.5672

Gaus Width: 3.4899



B0L102S, U20-ch43, adc2

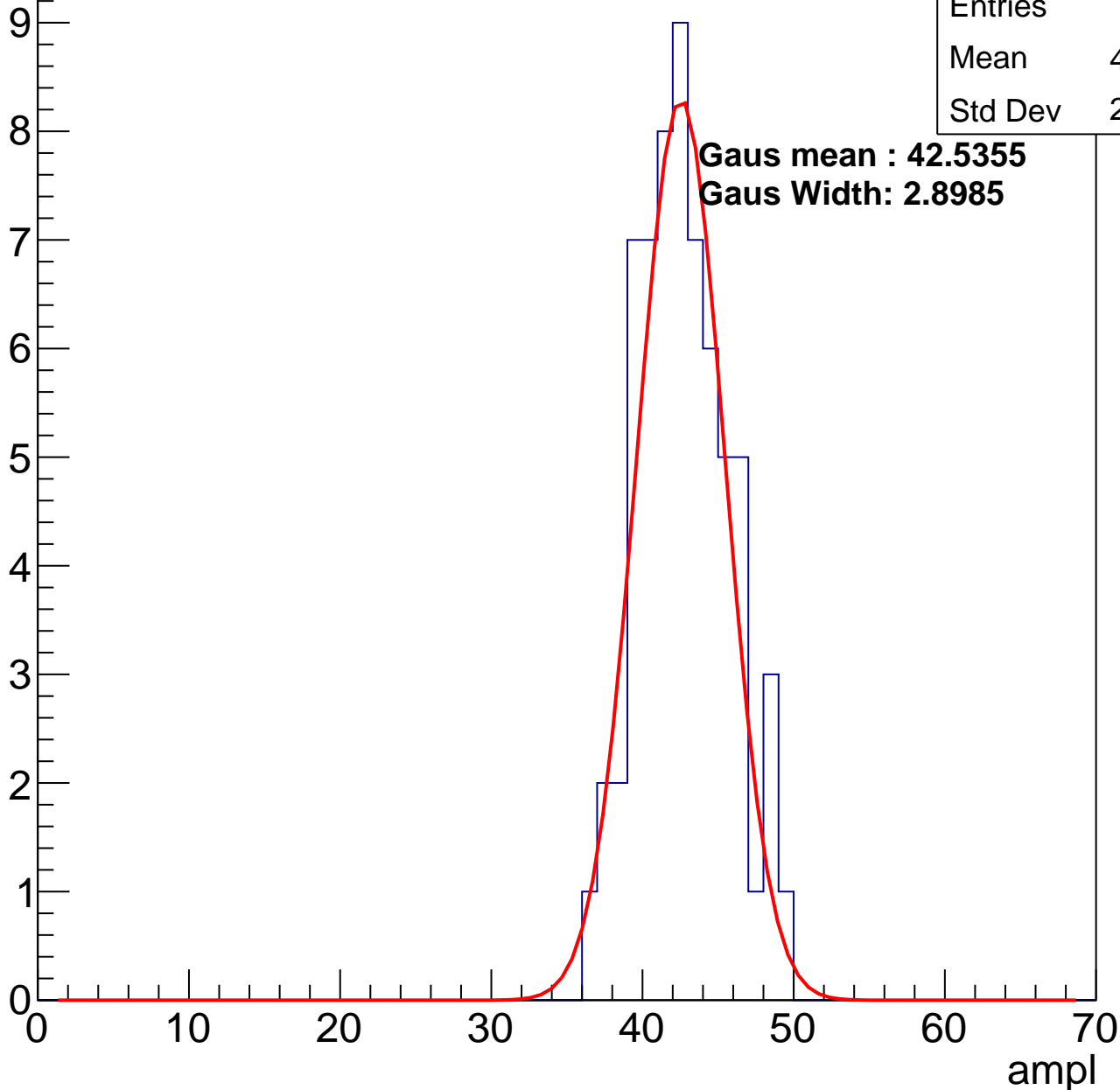
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.27 |
| Std Dev | 2.933 |

Gaus mean : 42.5355

Gaus Width: 2.8985

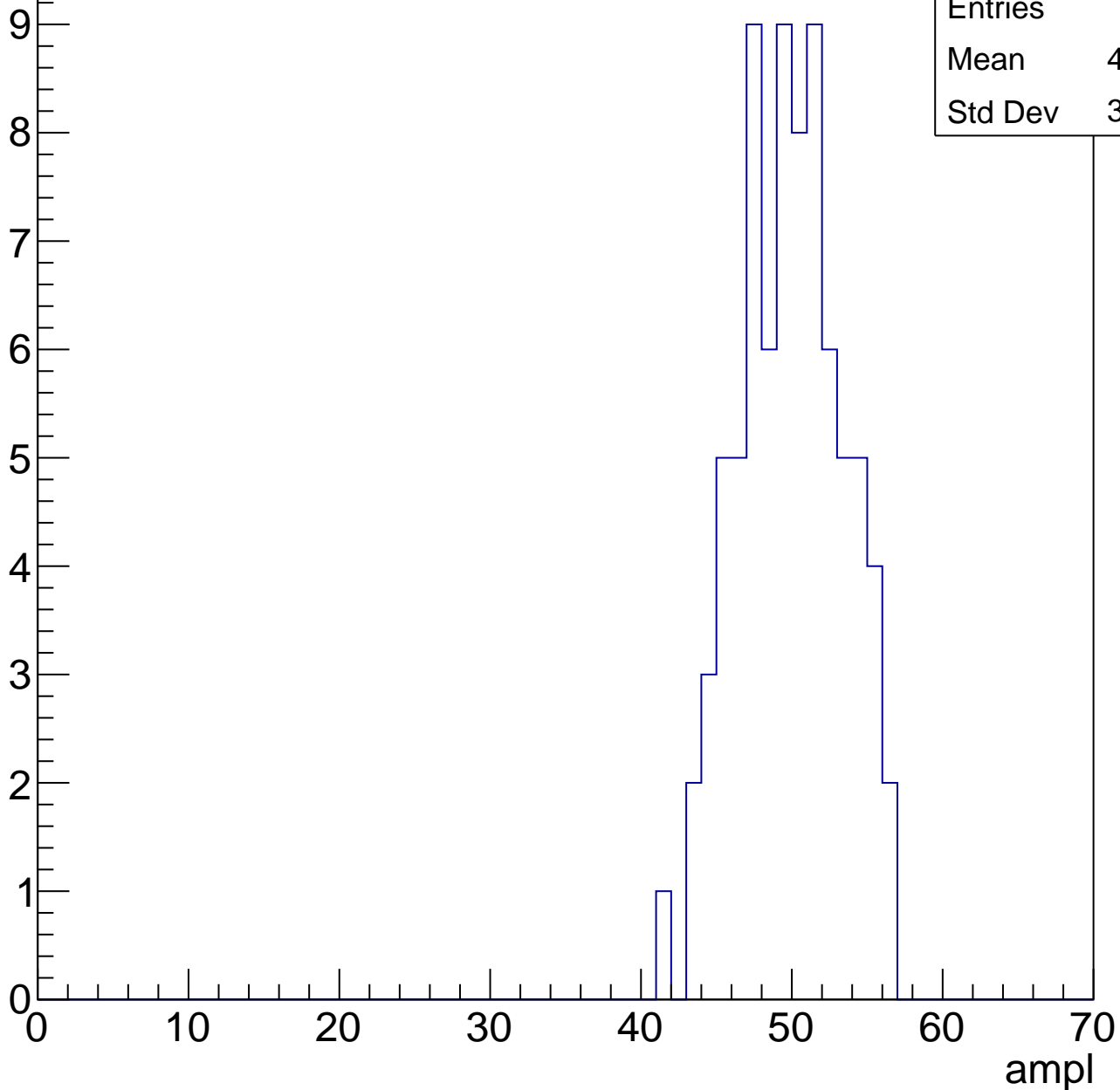


B0L102S, U20-ch43, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

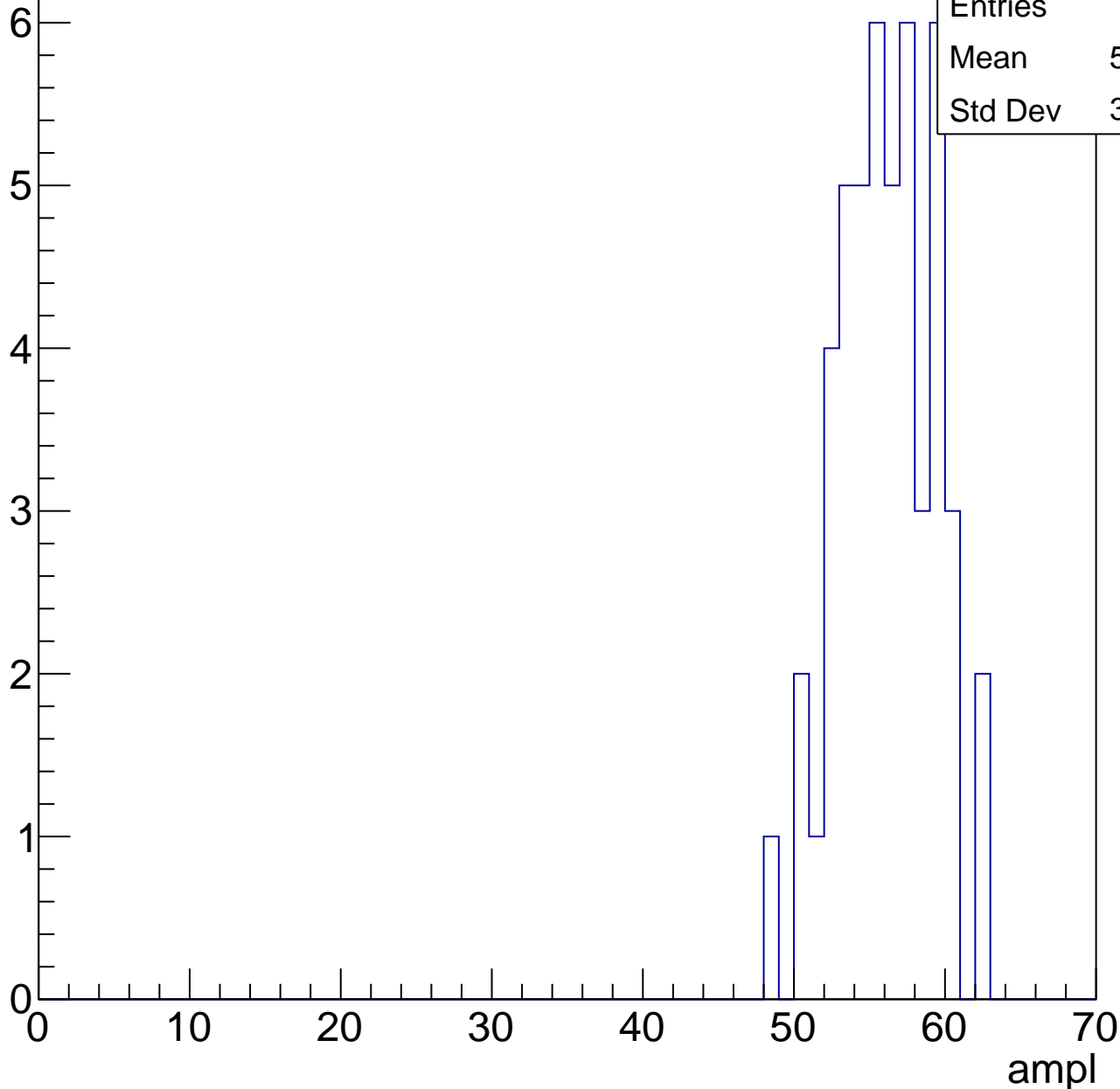
| | |
|---------|-------|
| Entries | 79 |
| Mean | 49.42 |
| Std Dev | 3.392 |



B0L102S, U20-ch43, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

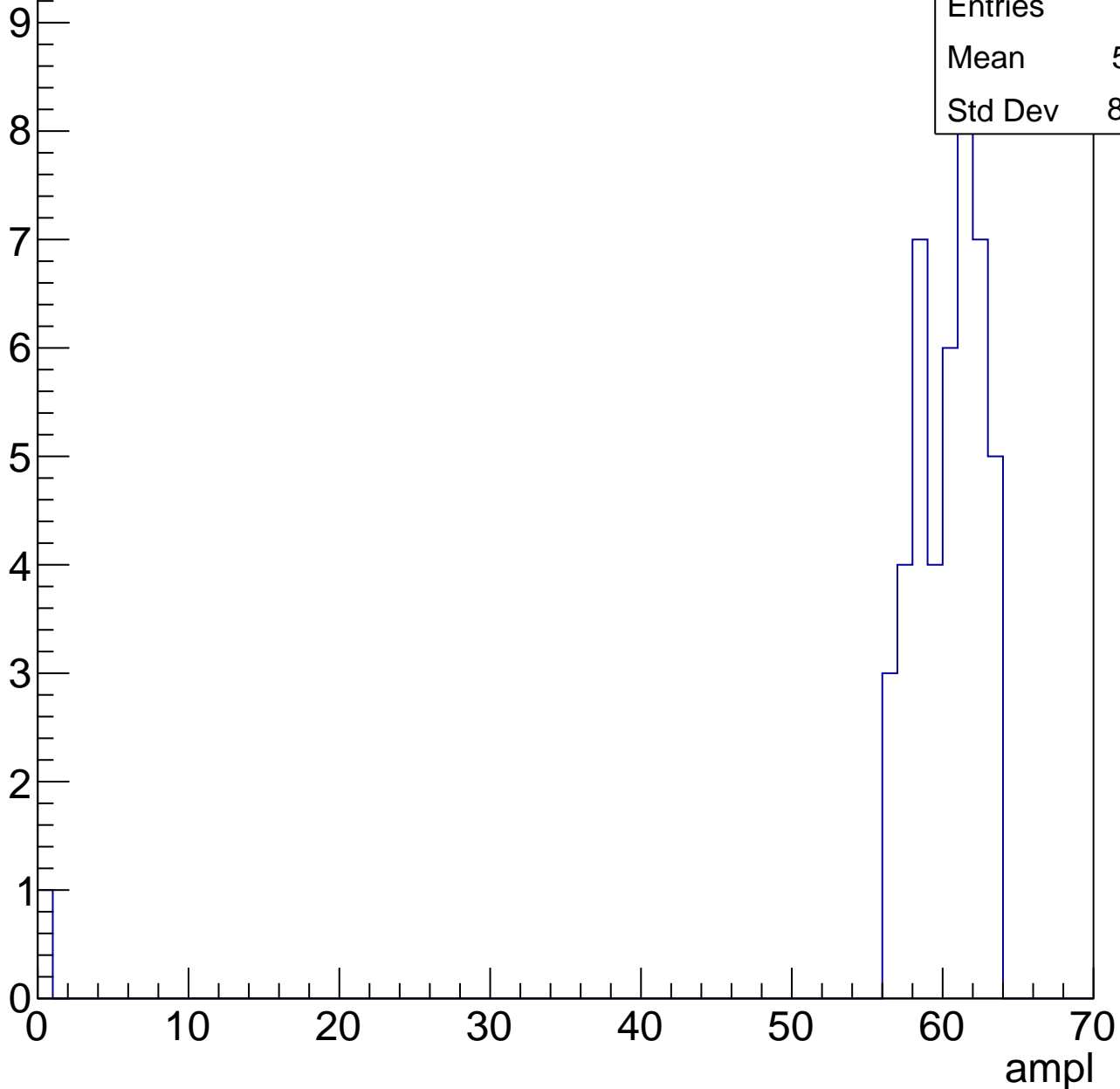


B0L102S, U20-ch43, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.61 |
| Std Dev | 8.979 |

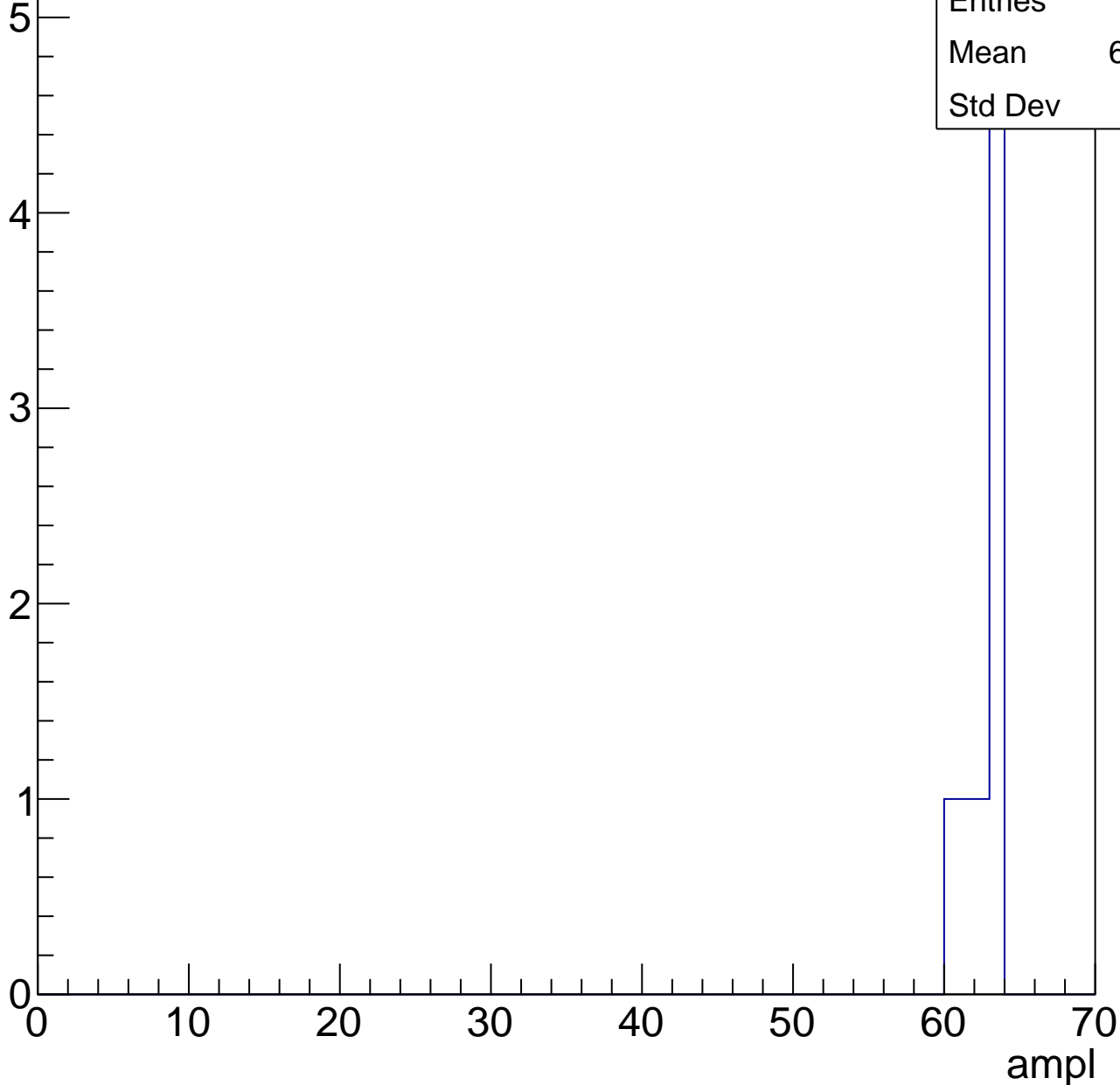


B0L102S, U20-ch43, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 62.25 |
| Std Dev | 1.09 |



B0L102S, U20-ch43, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-----|
| Entries | 2 |
| Mean | 9.5 |
| Std Dev | 9.5 |

B0L102S, U20-ch44, adc0

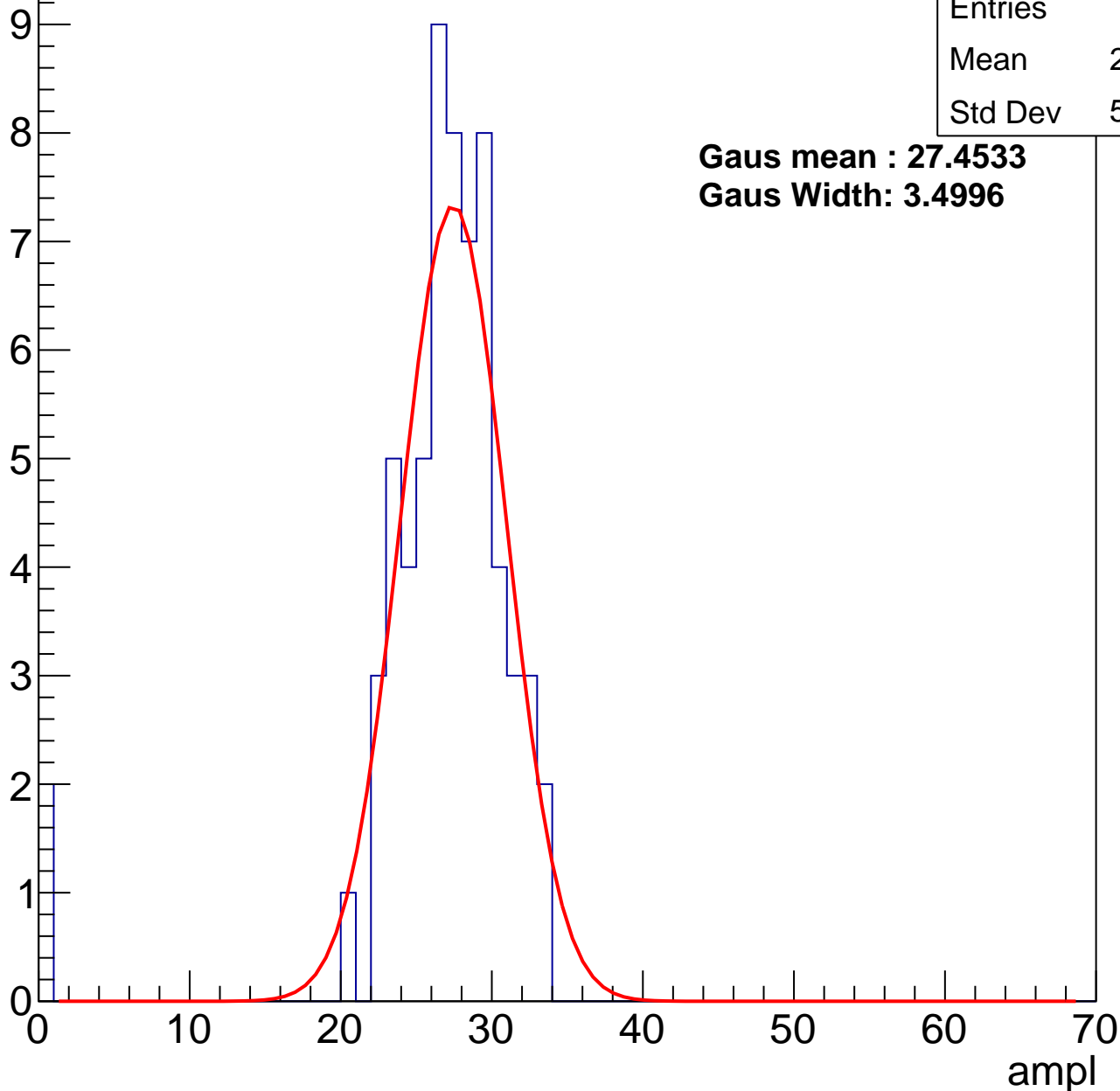
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 26.17 |
| Std Dev | 5.524 |

Gaus mean : 27.4533

Gaus Width: 3.4996



B0L102S, U20-ch44, adc1

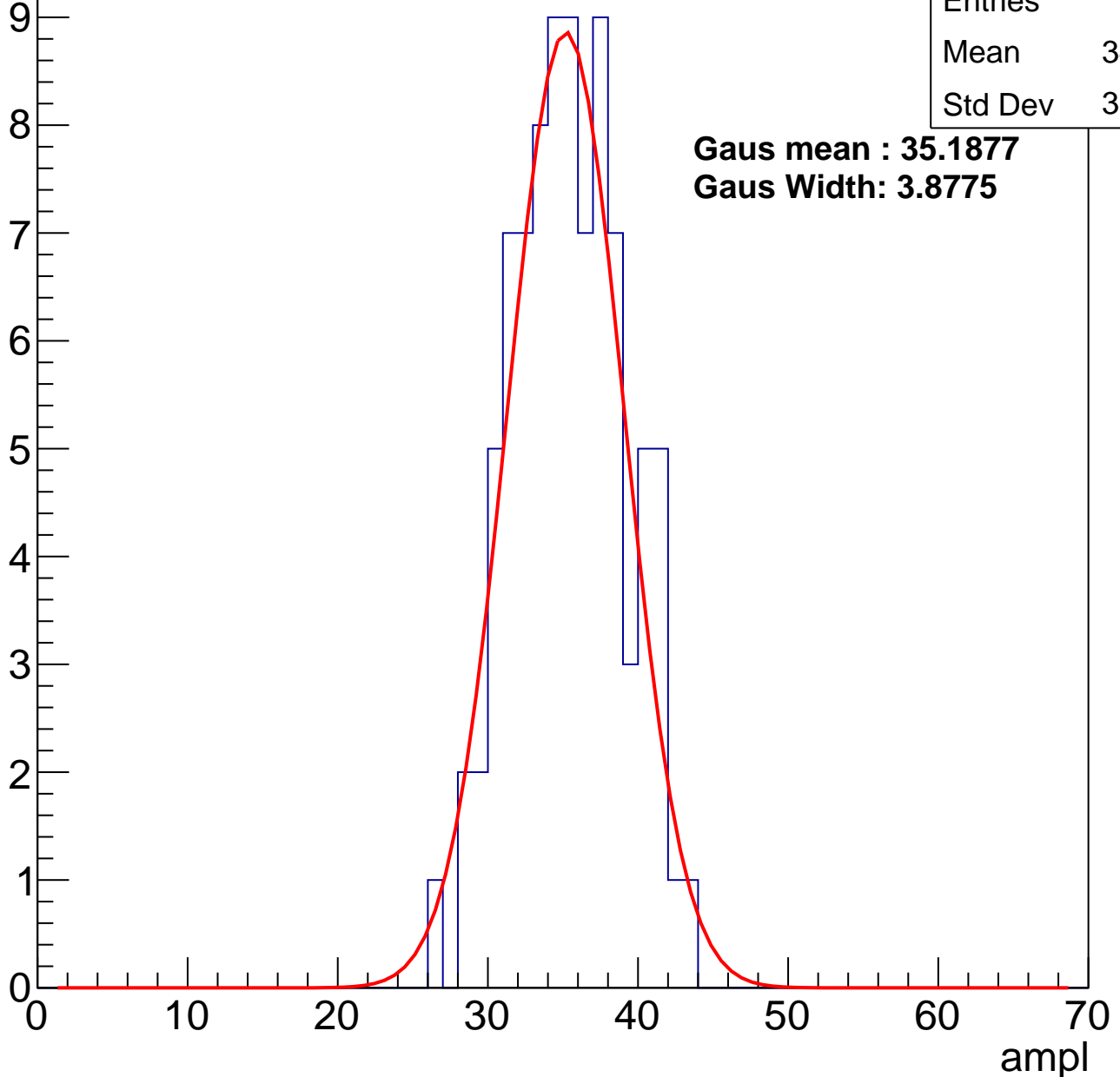
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 34.93 |
| Std Dev | 3.636 |

Gaus mean : 35.1877

Gaus Width: 3.8775



B0L102S, U20-ch44, adc2

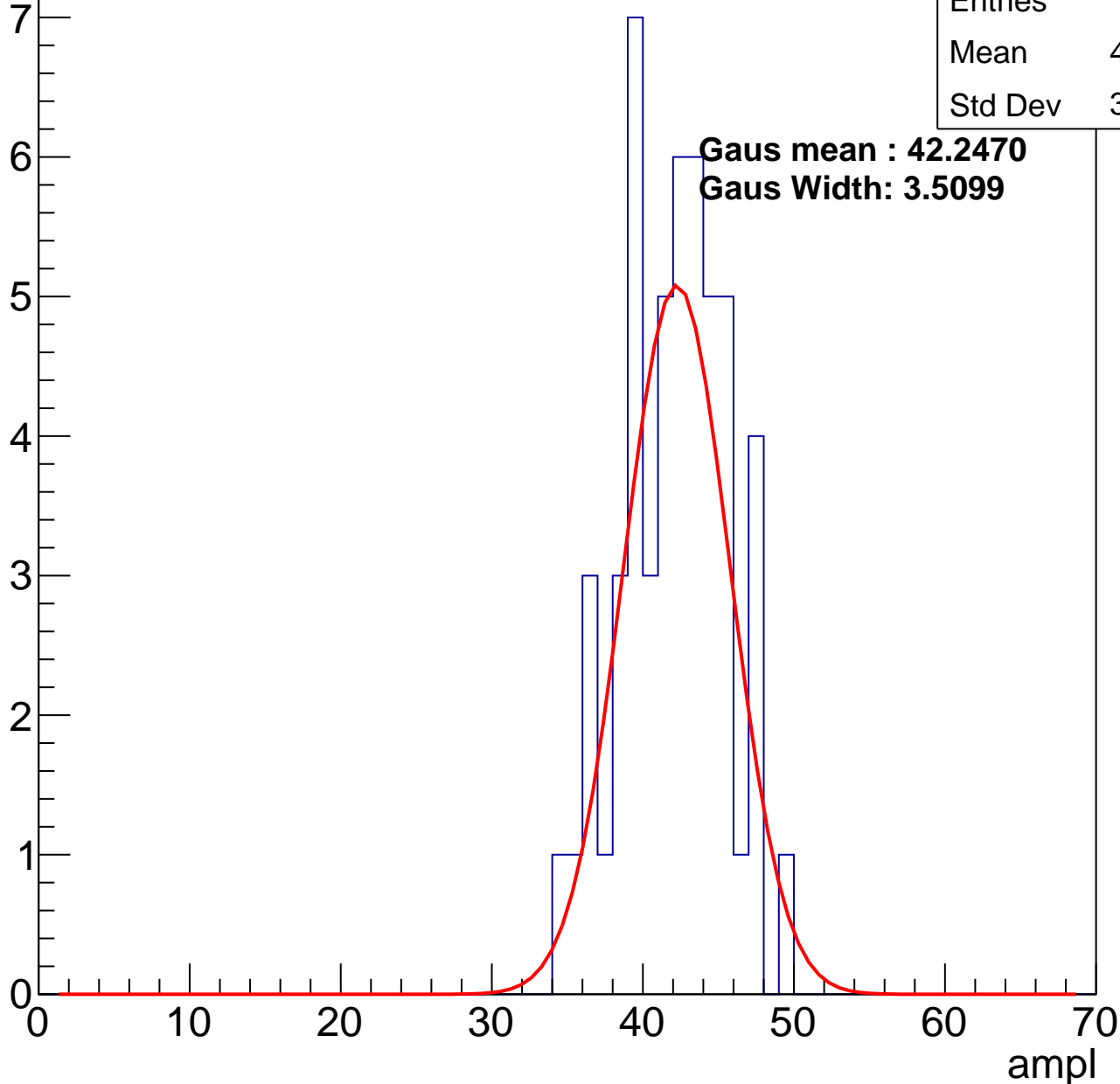
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 41.62 |
| Std Dev | 3.403 |

Gaus mean : 42.2470

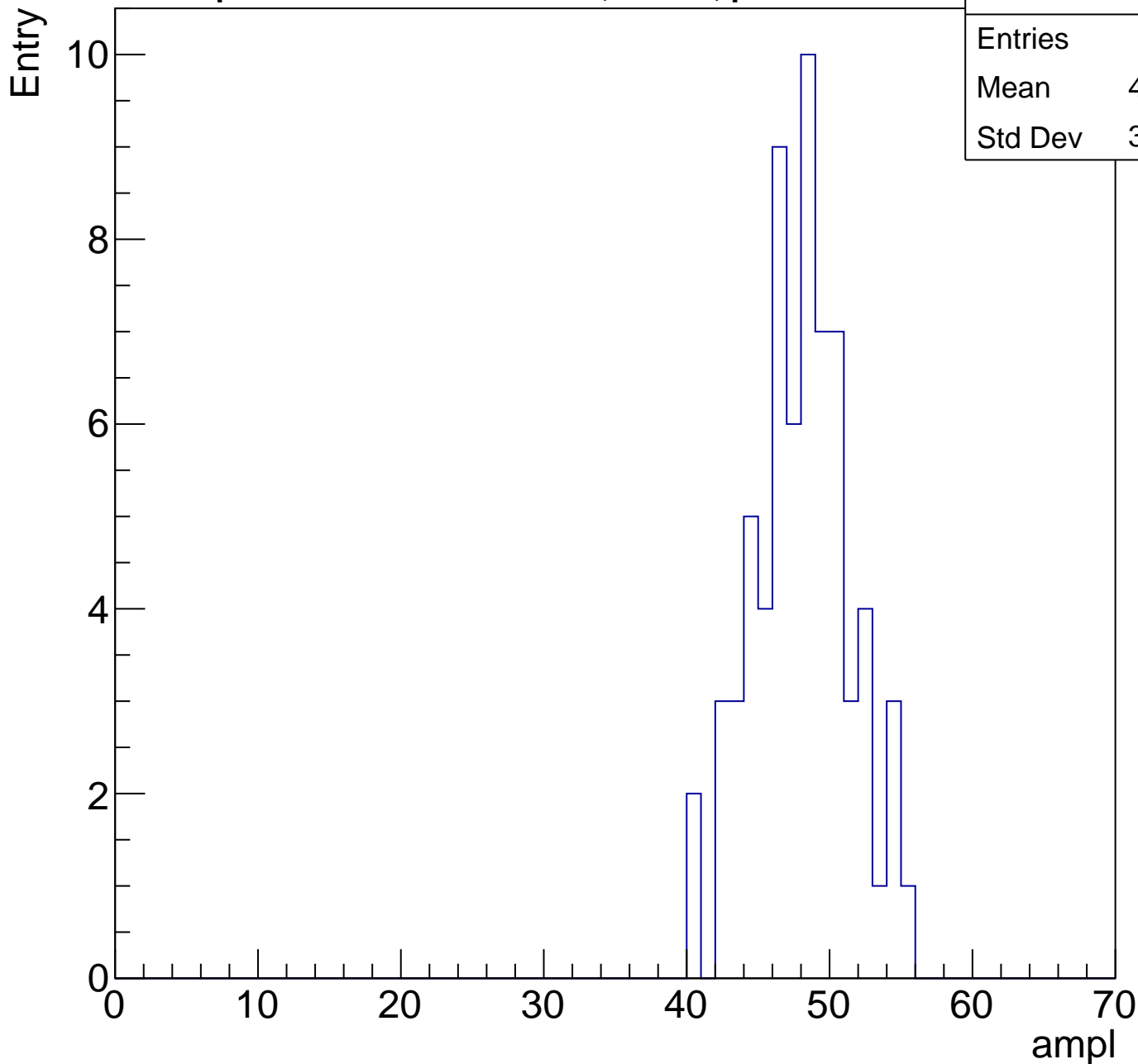
Gaus Width: 3.5099



B0L102S, U20-ch44, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 68 |
| Mean | 47.57 |
| Std Dev | 3.353 |

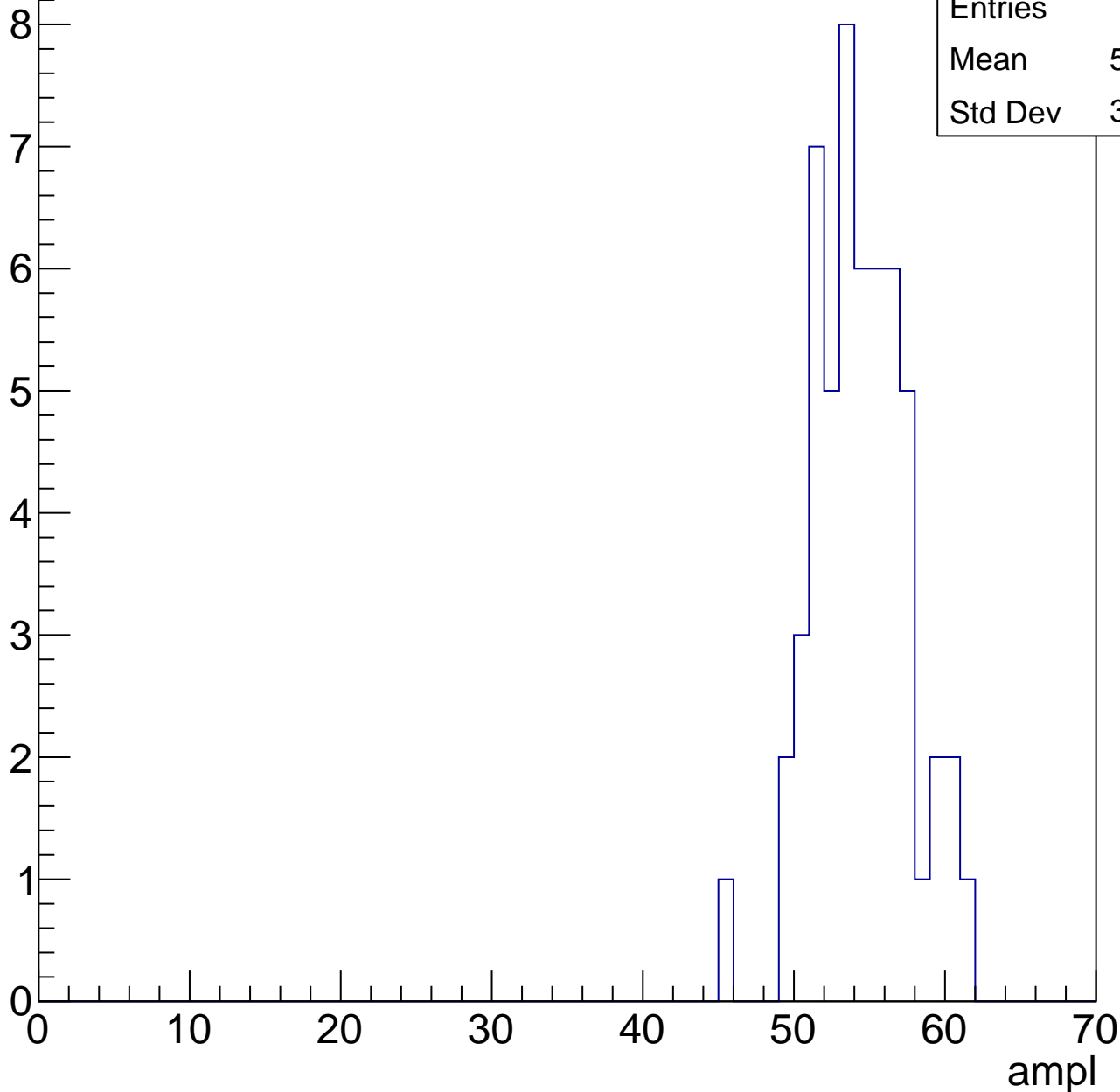


B0L102S, U20-ch44, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 53.93 |
| Std Dev | 3.109 |

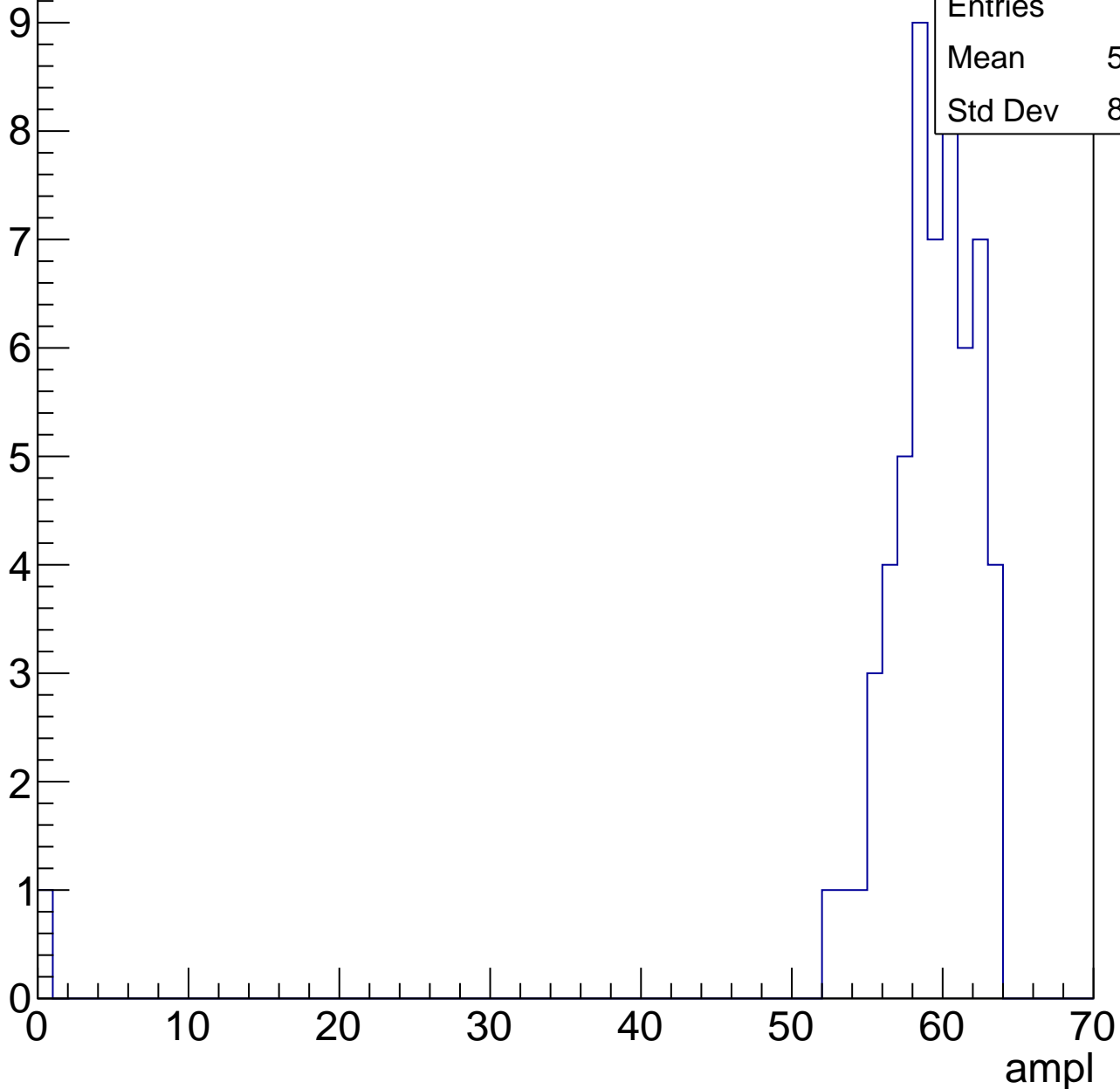


B0L102S, U20-ch44, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.89 |
| Std Dev | 8.158 |

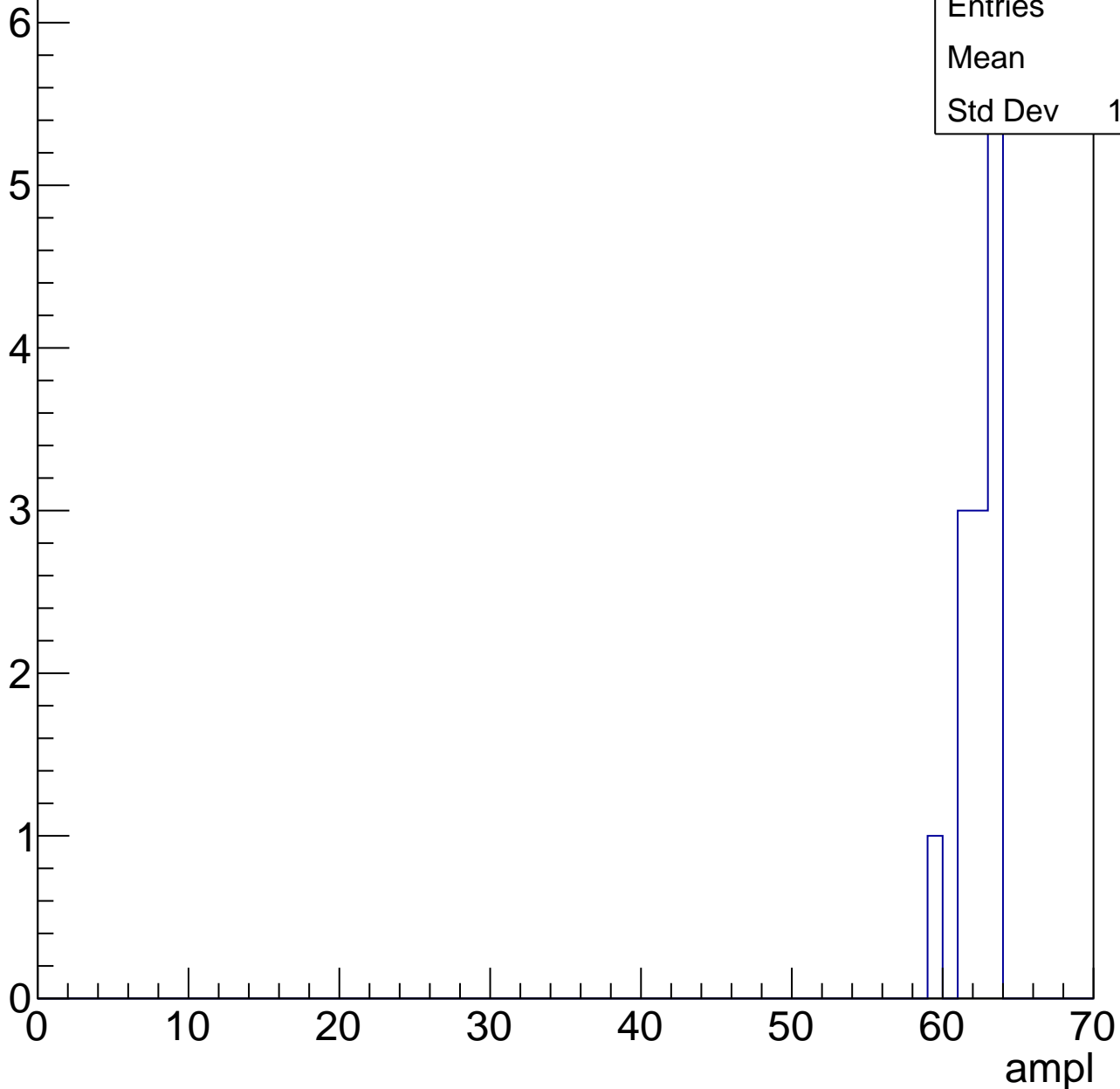


B0L102S, U20-ch44, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 62 |
| Std Dev | 1.177 |



B0L102S, U20-ch44, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch45, adc0

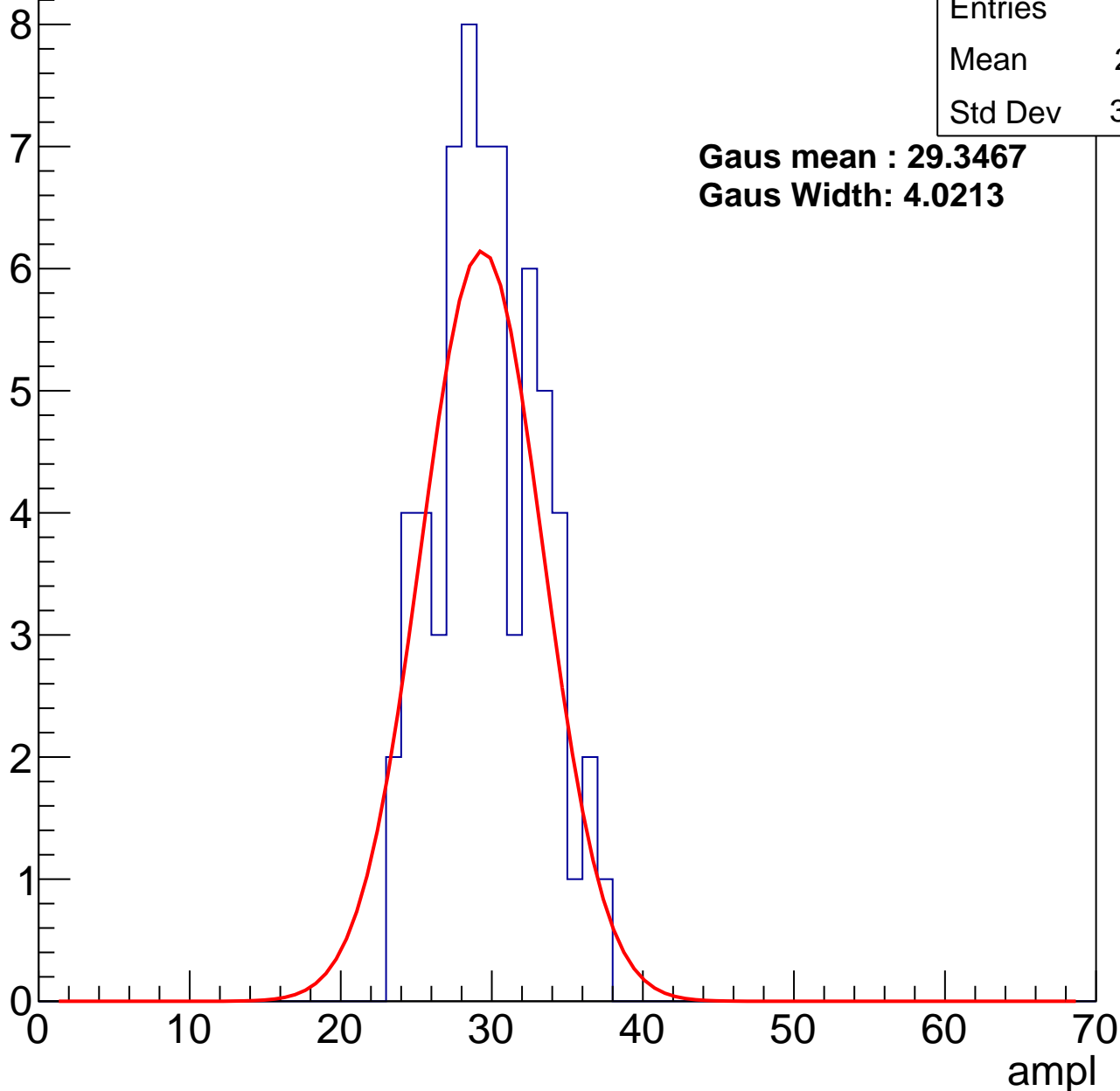
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.31 |
| Std Dev | 3.409 |

Gaus mean : 29.3467

Gaus Width: 4.0213



B0L102S, U20-ch45, adc1

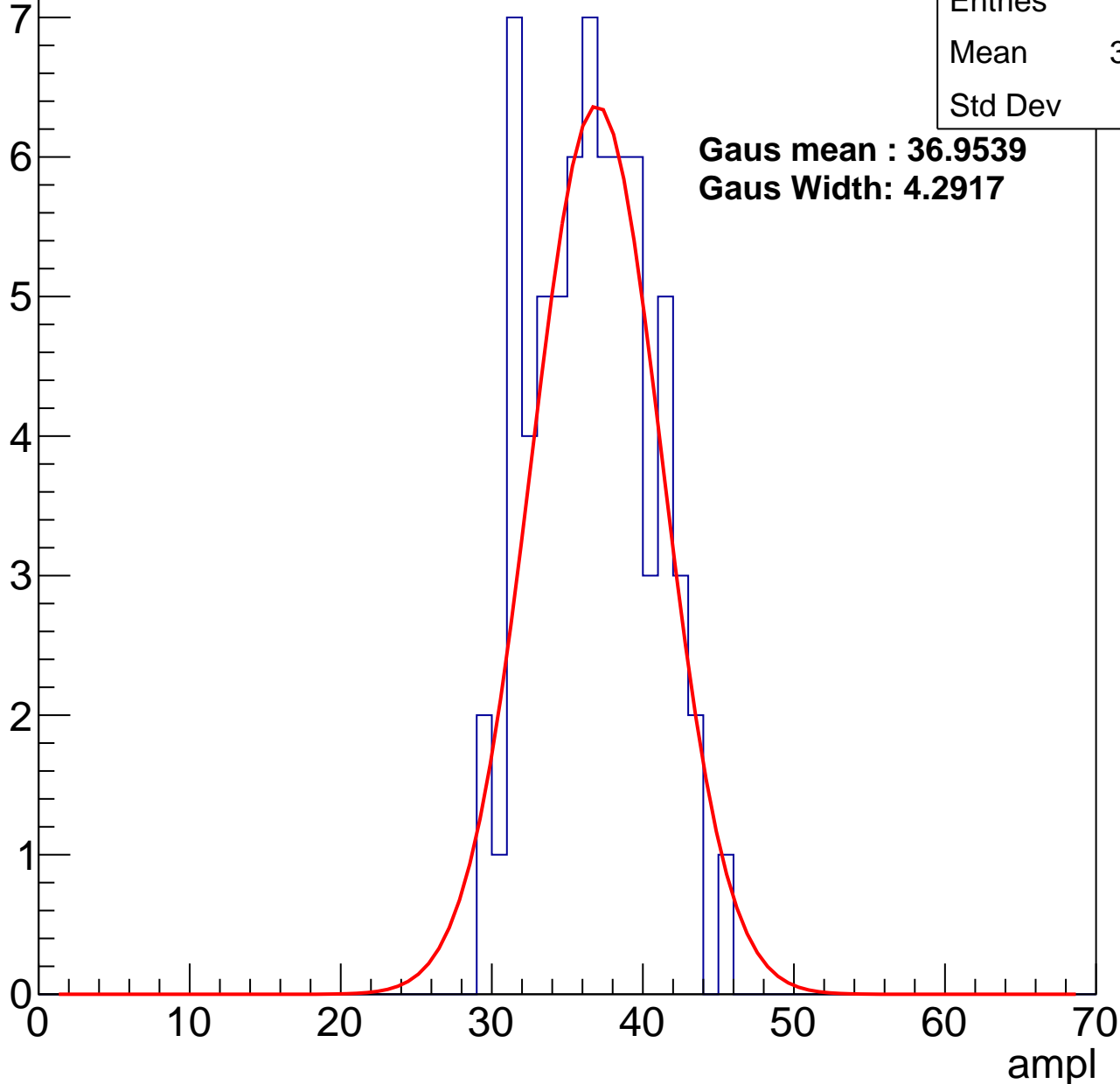
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.17 |
| Std Dev | 3.78 |

Gaus mean : 36.9539

Gaus Width: 4.2917



B0L102S, U20-ch45, adc2

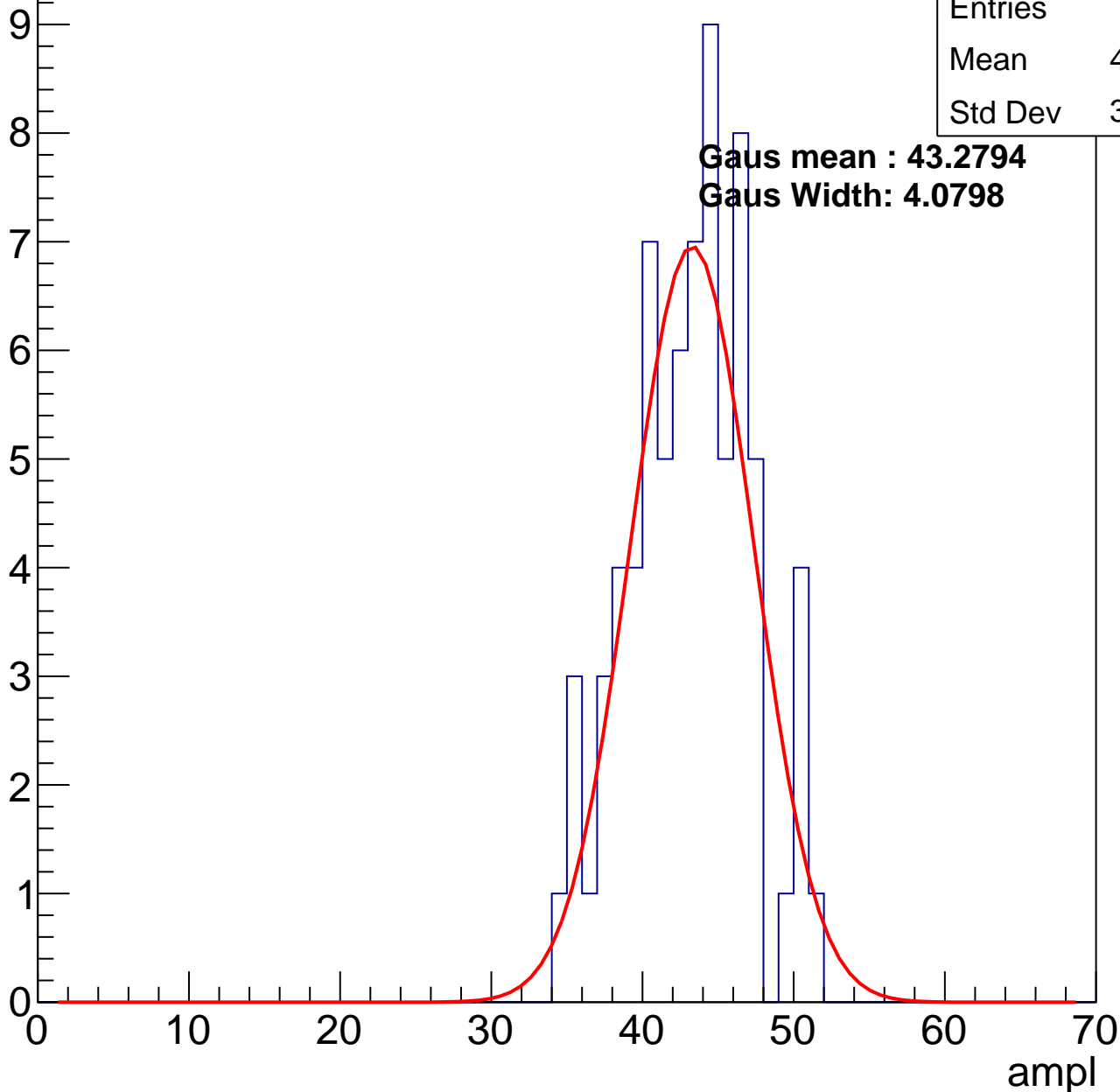
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 42.65 |
| Std Dev | 3.944 |

Gaus mean : 43.2794

Gaus Width: 4.0798

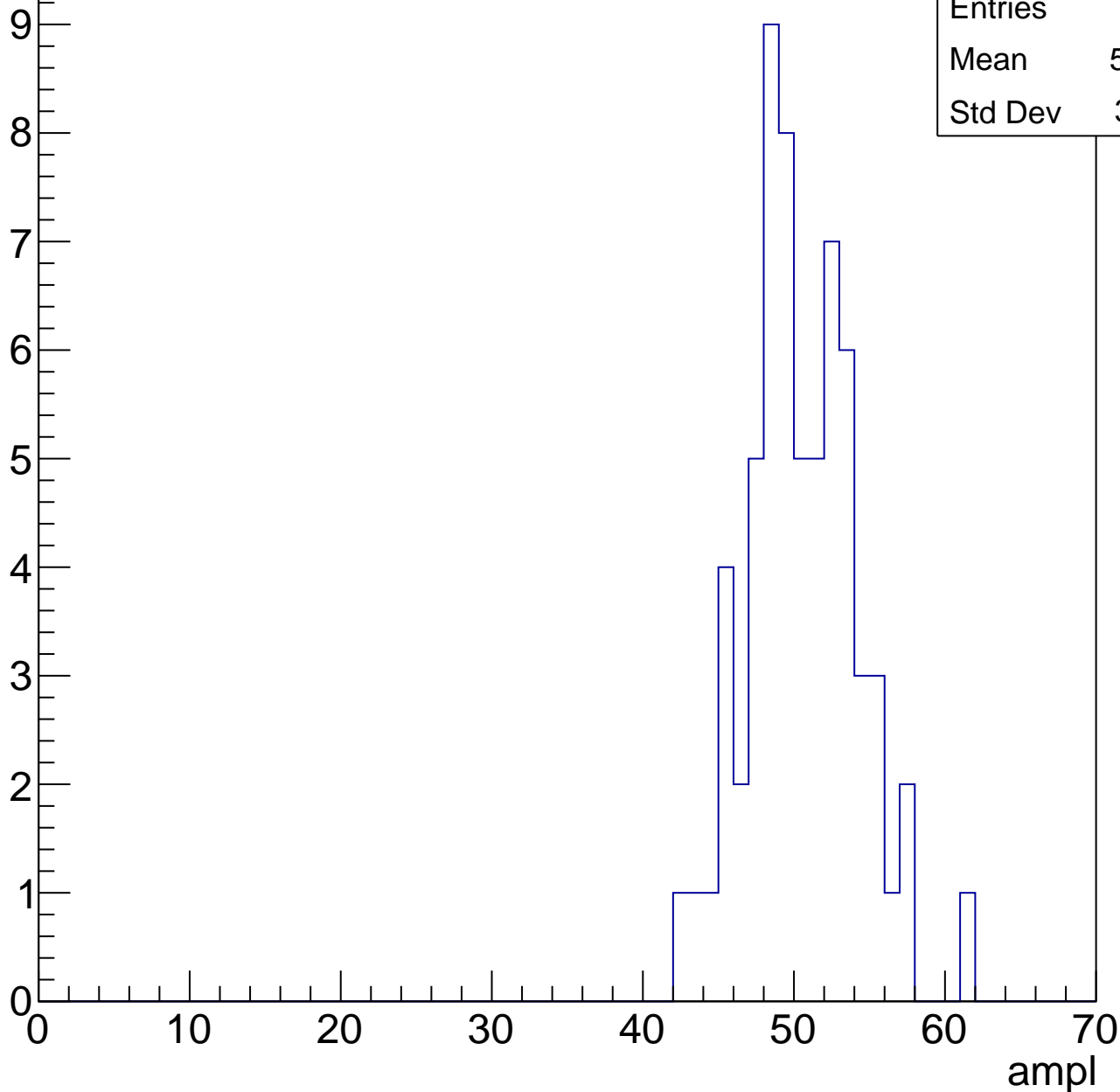


B0L102S, U20-ch45, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.08 |
| Std Dev | 3.611 |

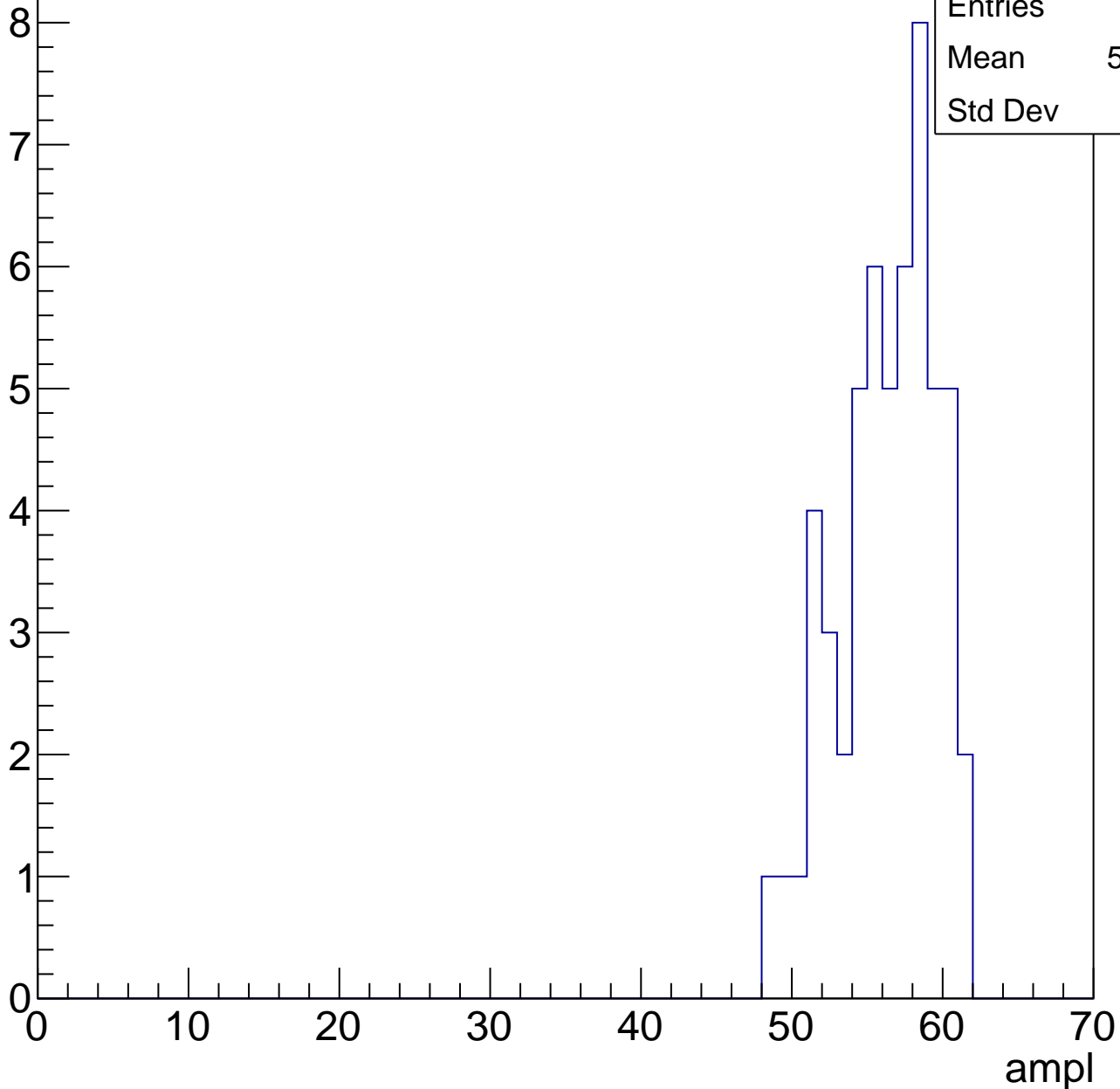


B0L102S, U20-ch45, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 55.85 |
| Std Dev | 3.2 |

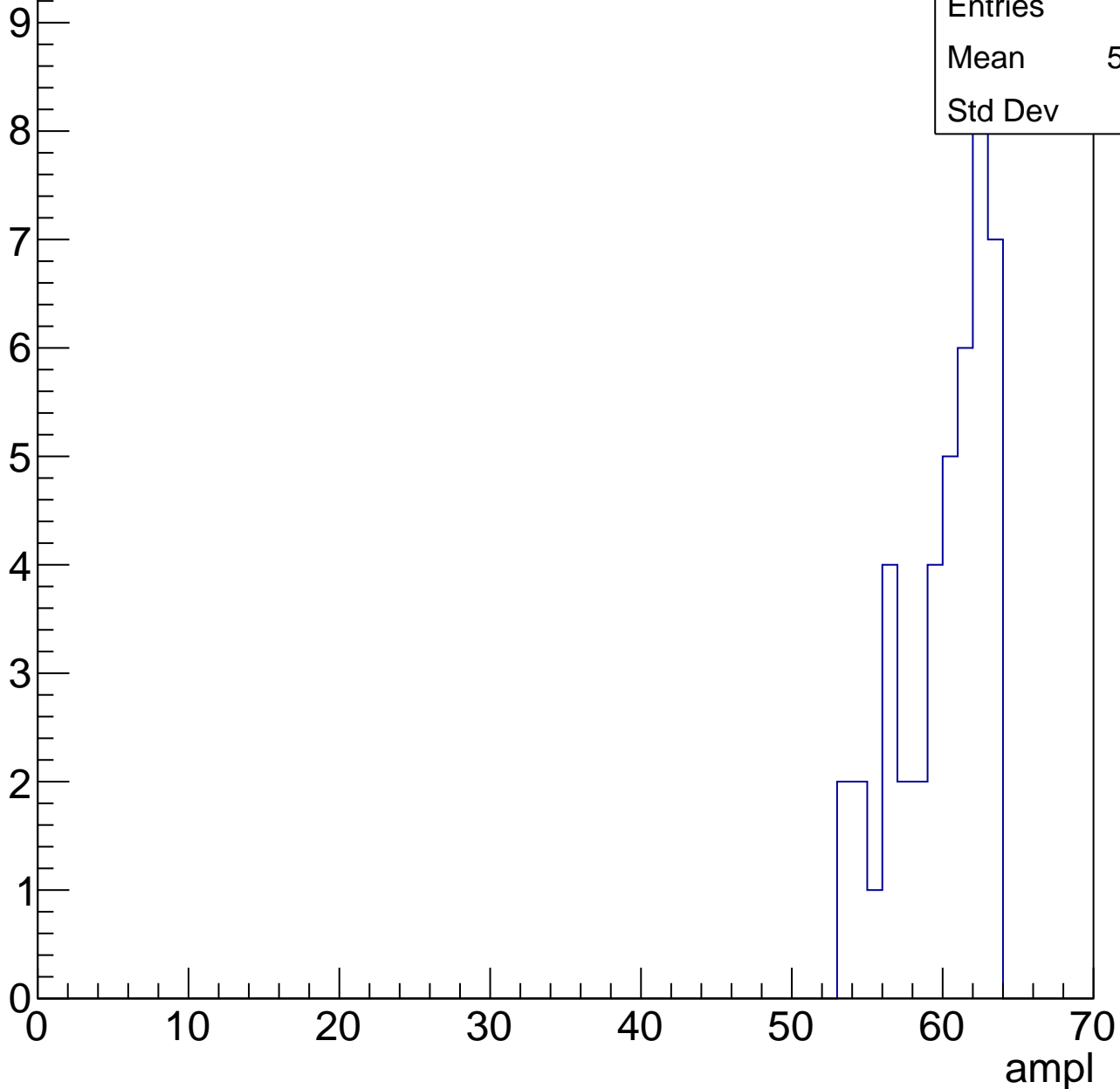


B0L102S, U20-ch45, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

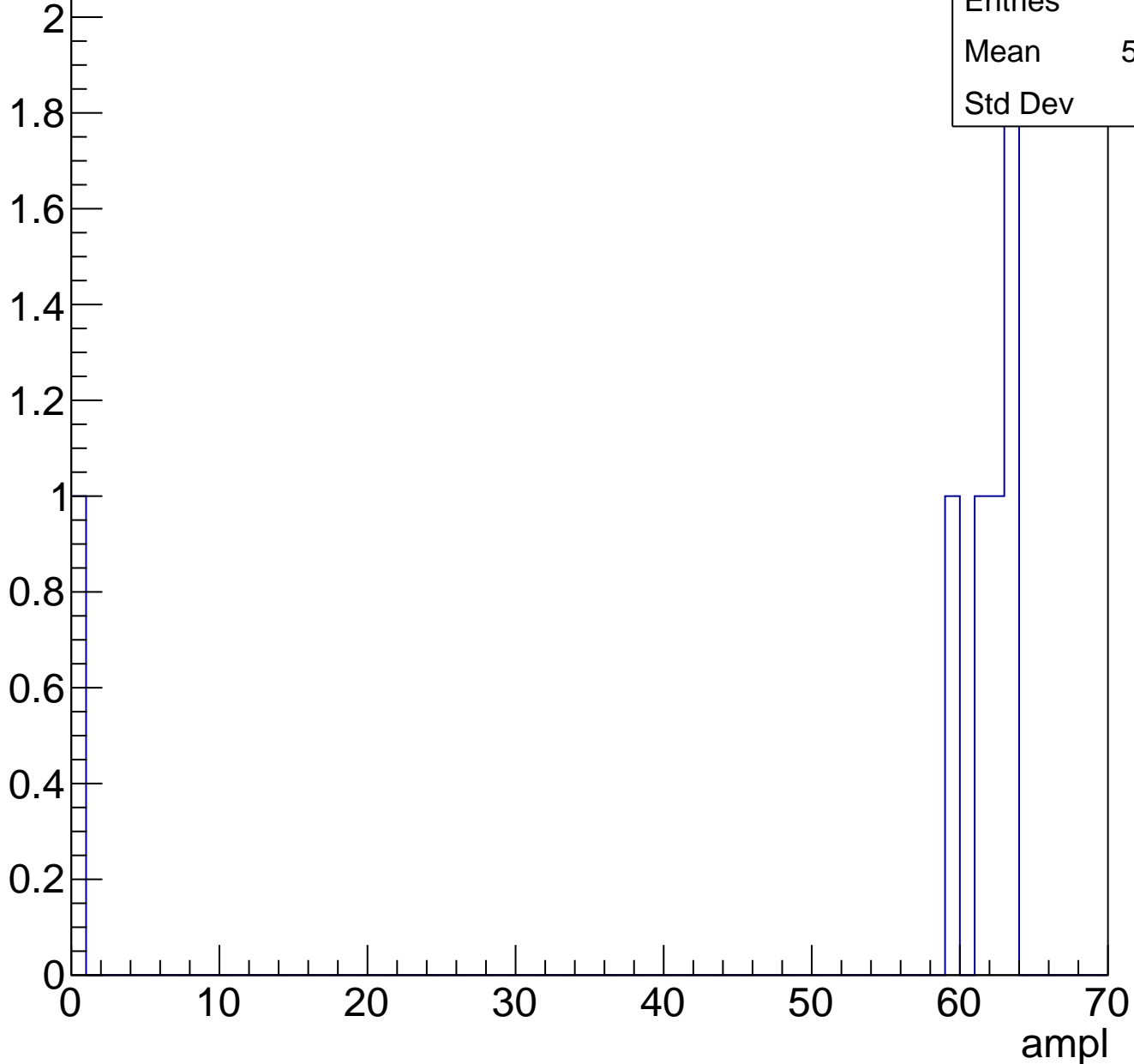
| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.64 |
| Std Dev | 2.97 |



B0L102S, U20-ch45, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch45, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B0L102S, U20-ch46, adc0

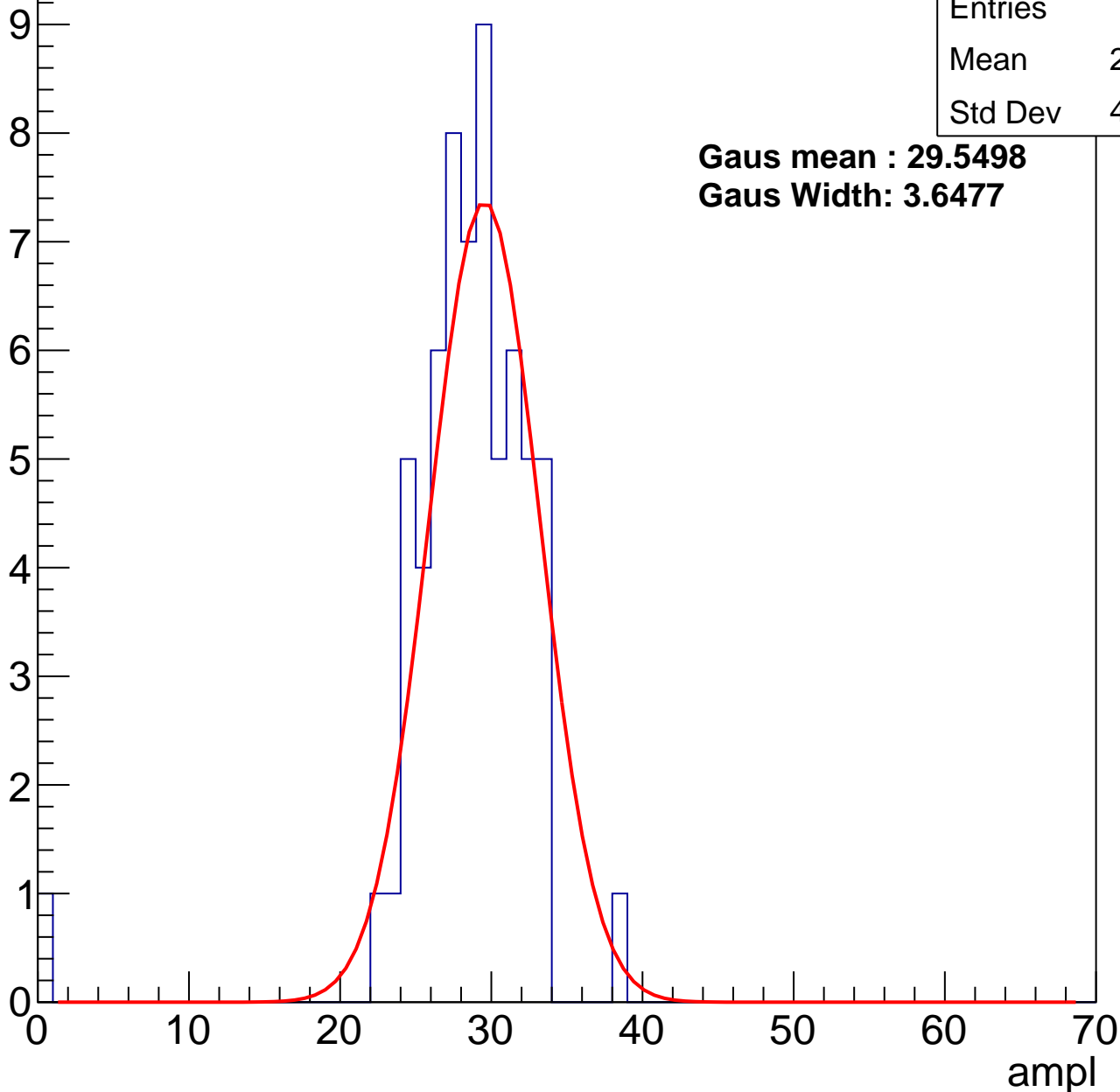
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 28.02 |
| Std Dev | 4.645 |

Gaus mean : 29.5498

Gaus Width: 3.6477



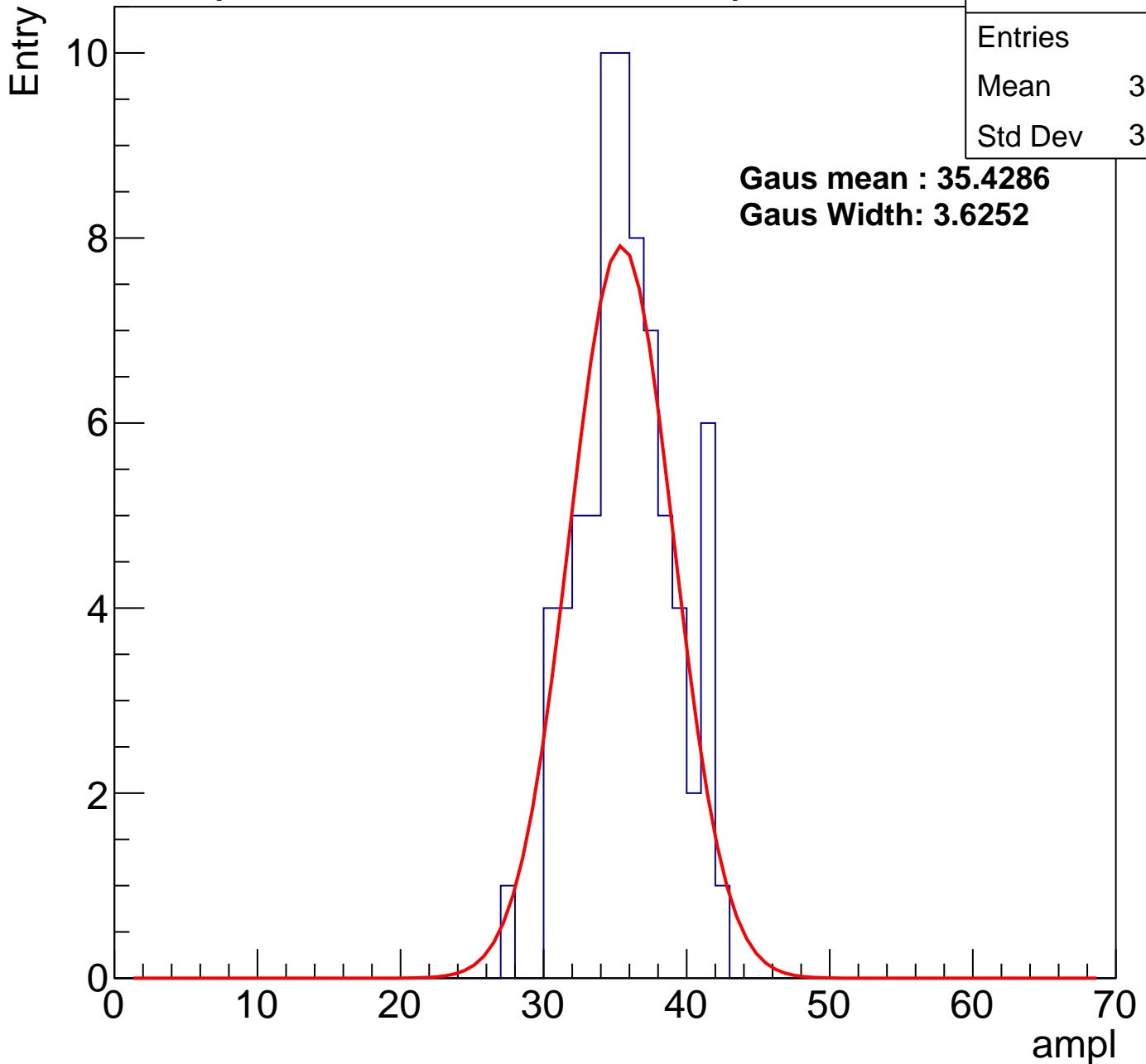
B0L102S, U20-ch46, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.38 |
| Std Dev | 3.234 |

Gaus mean : 35.4286

Gaus Width: 3.6252



B0L102S, U20-ch46, adc2

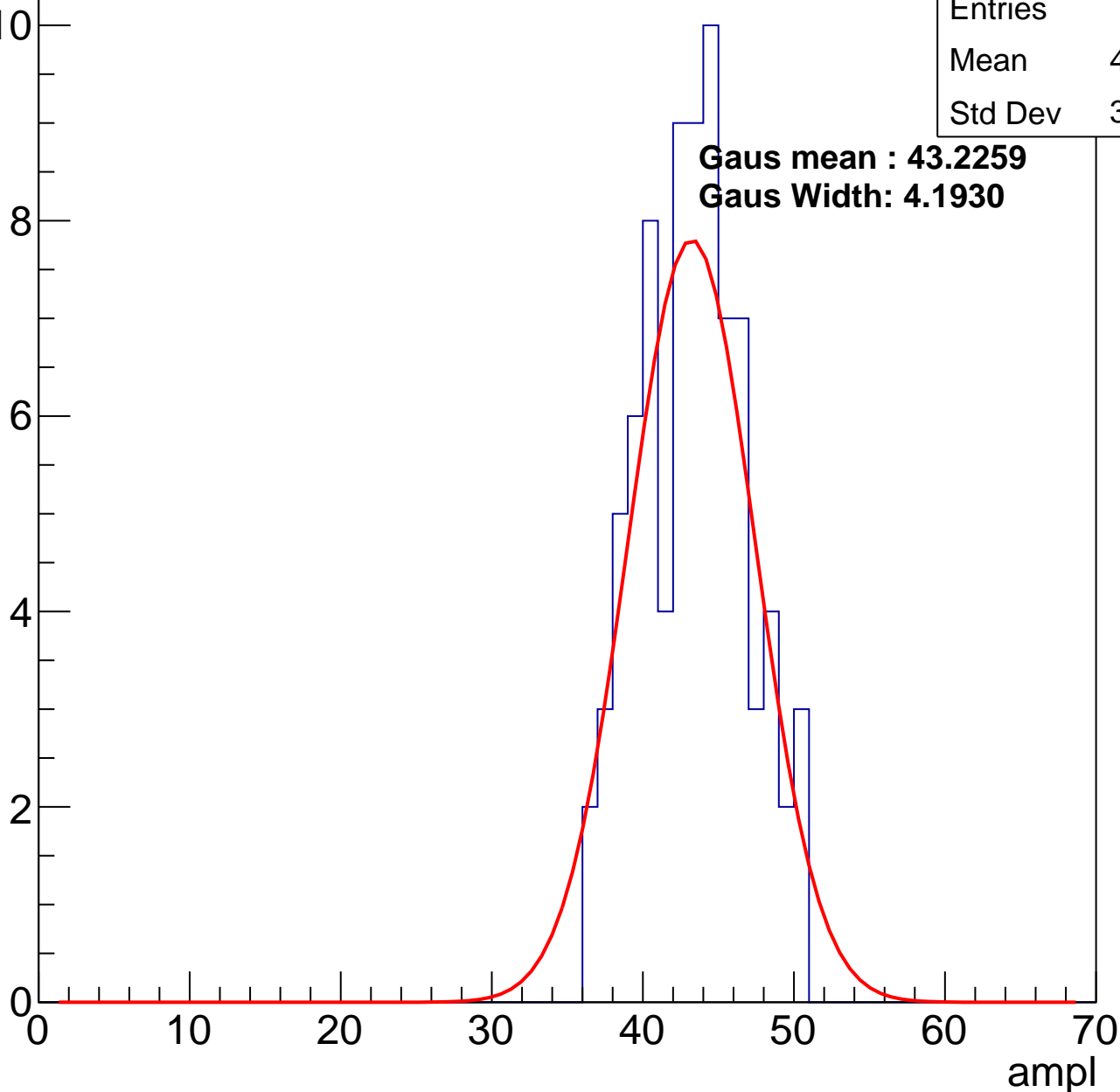
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 42.85 |
| Std Dev | 3.475 |

Gaus mean : 43.2259

Gaus Width: 4.1930

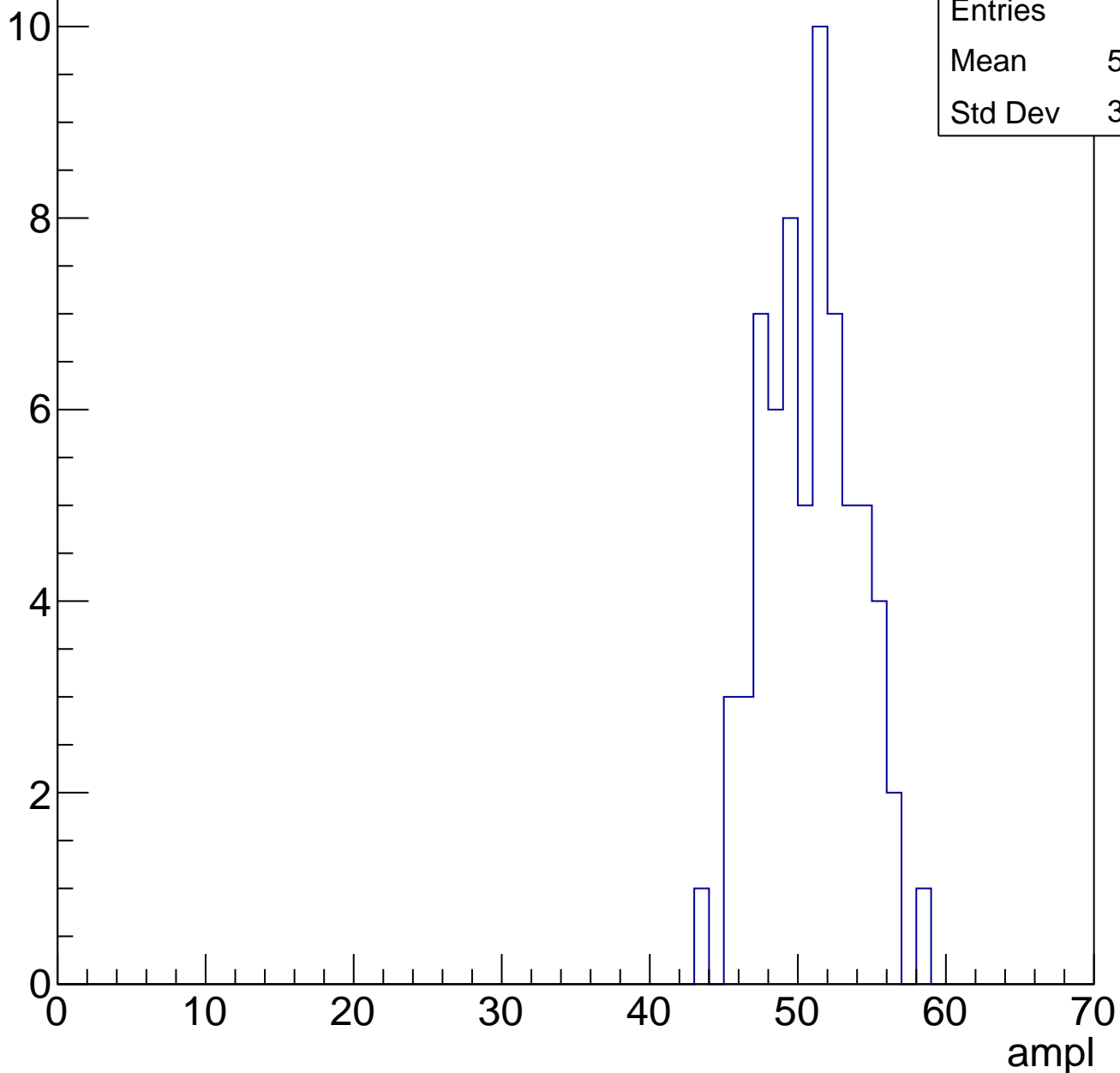


B0L102S, U20-ch46, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50.36 |
| Std Dev | 3.132 |



B0L102S, U20-ch46, adc4

calib_packv5_042523_0143.root, FC#12, port B1

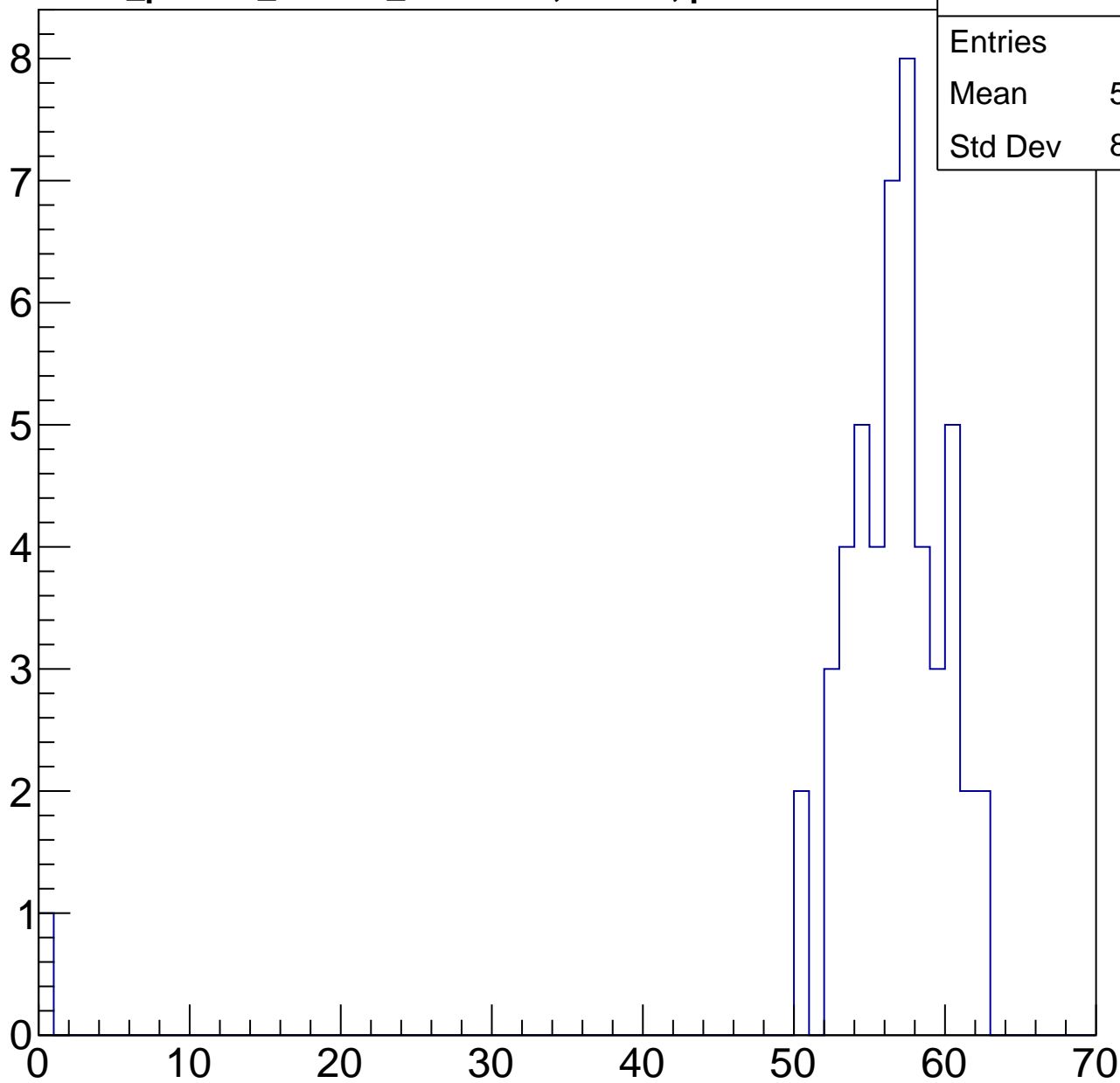
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 50 |
| Mean | 55.22 |
| Std Dev | 8.413 |

ampl

0 10 20 30 40 50 60 70

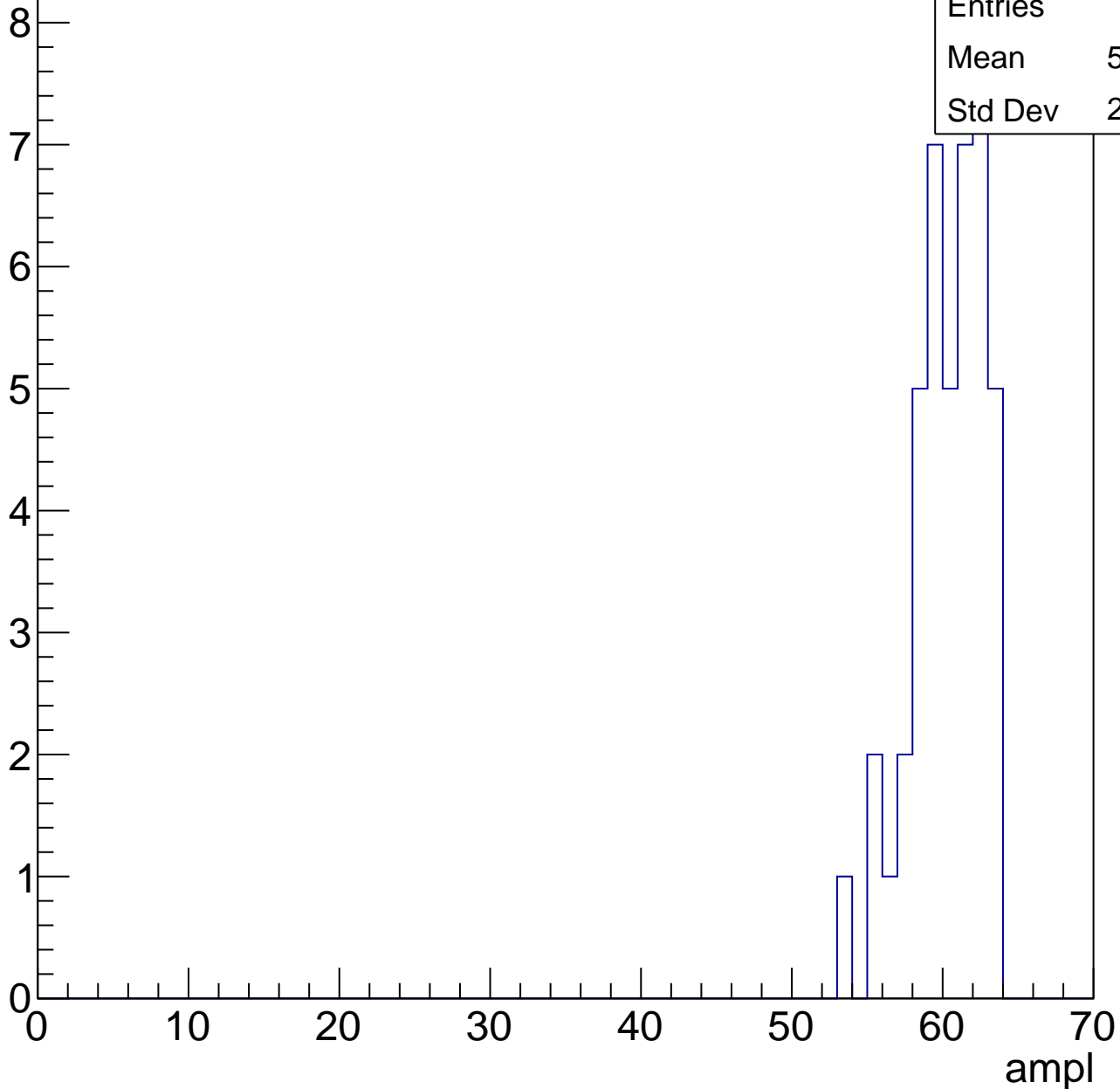


B0L102S, U20-ch46, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.86 |
| Std Dev | 2.378 |

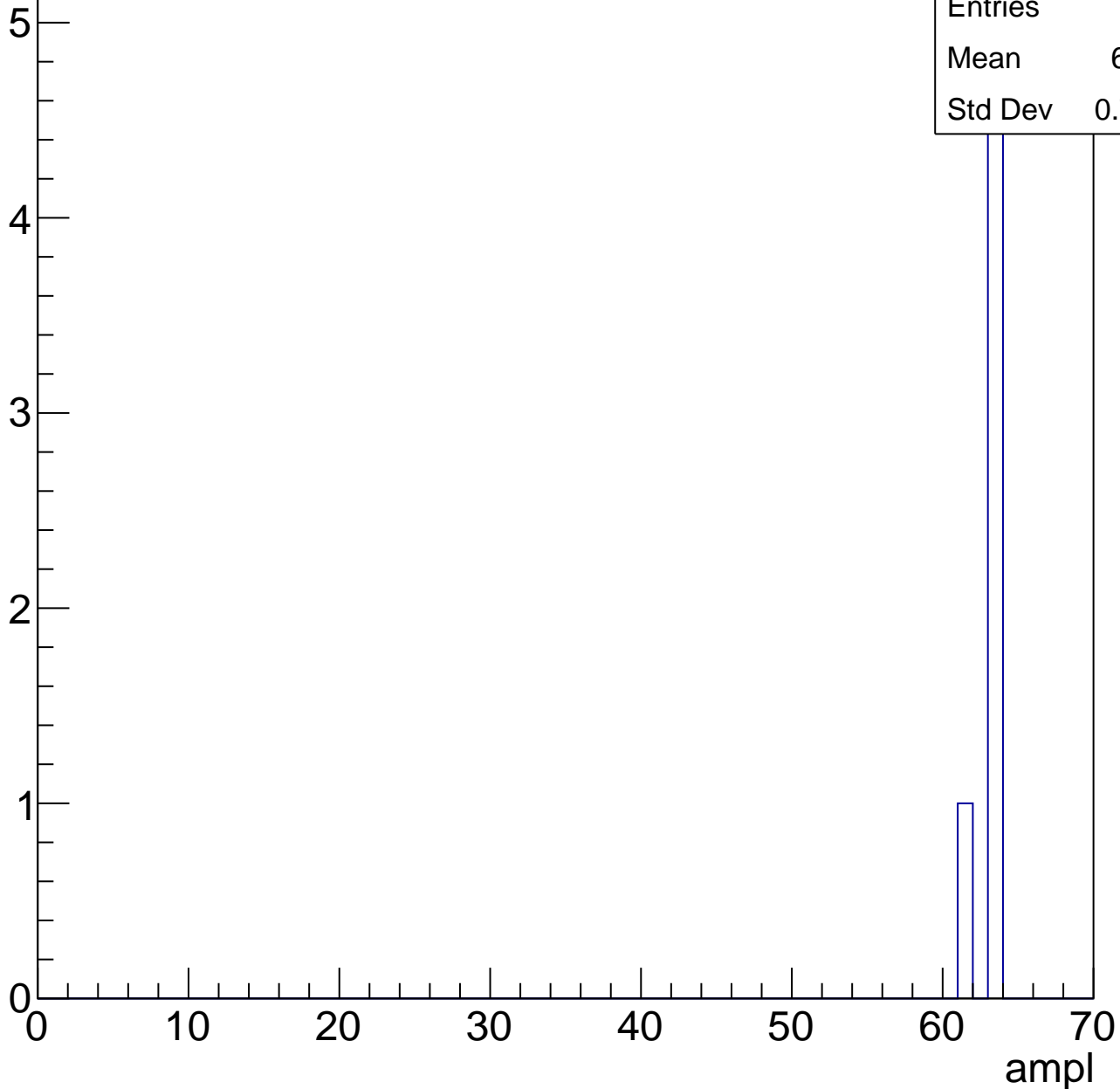


B0L102S, U20-ch46, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|--------|
| Entries | 6 |
| Mean | 62.67 |
| Std Dev | 0.7454 |



B0L102S, U20-ch46, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch47, adc0

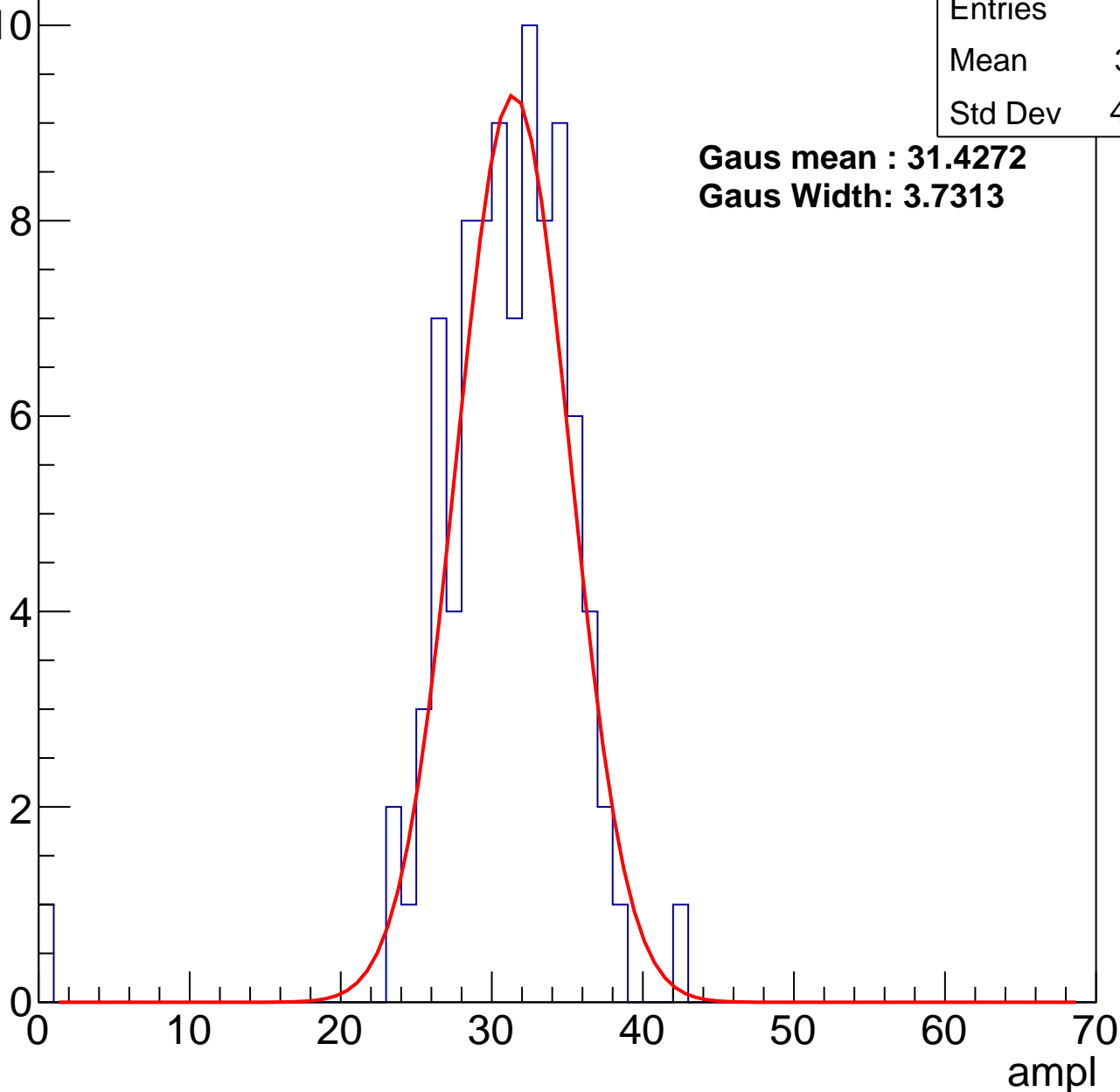
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 91 |
| Mean | 30.51 |
| Std Dev | 4.846 |

Gaus mean : 31.4272

Gaus Width: 3.7313



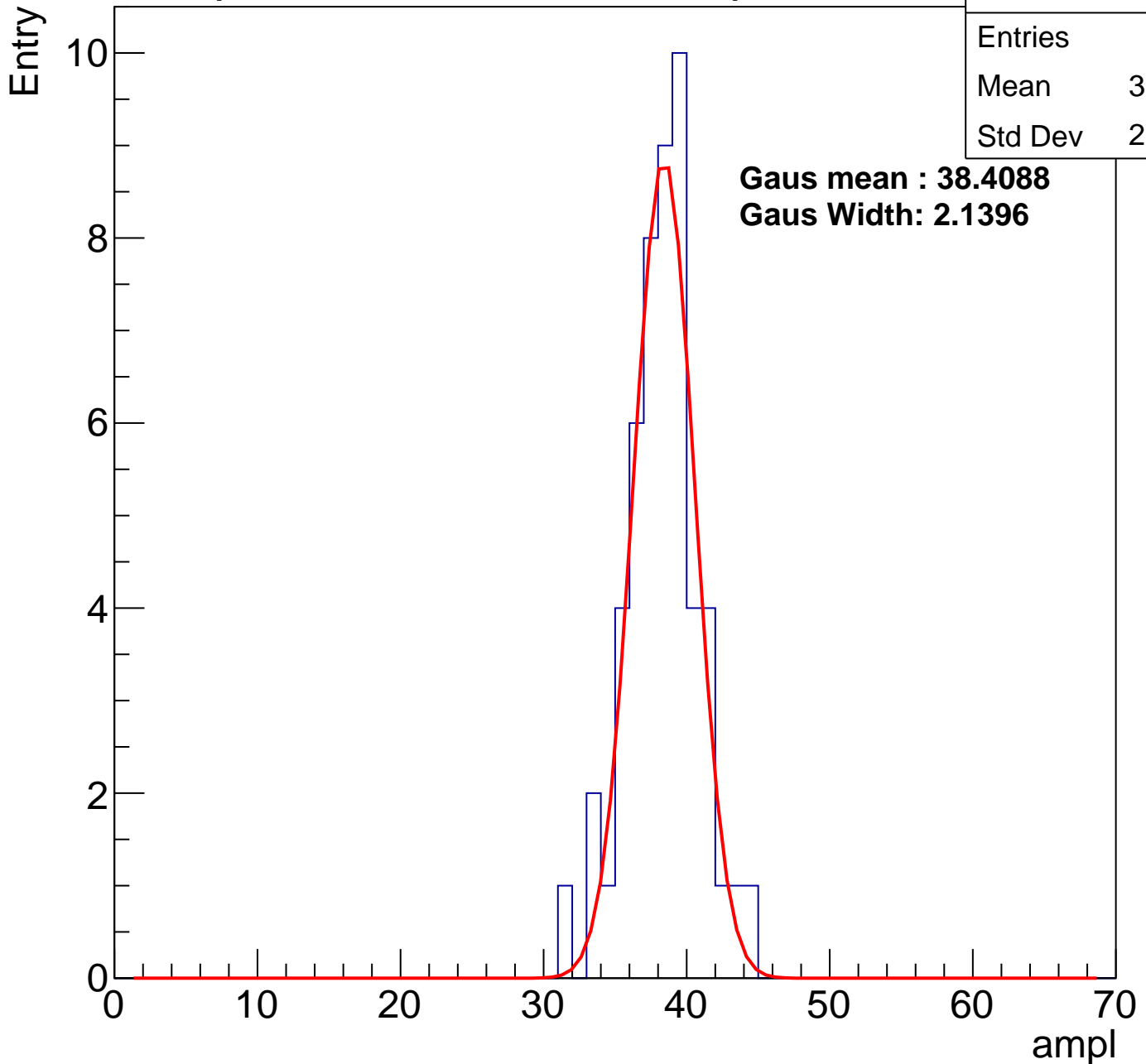
B0L102S, U20-ch47, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 52 |
| Mean | 37.85 |
| Std Dev | 2.484 |

Gaus mean : 38.4088

Gaus Width: 2.1396



B0L102S, U20-ch47, adc2

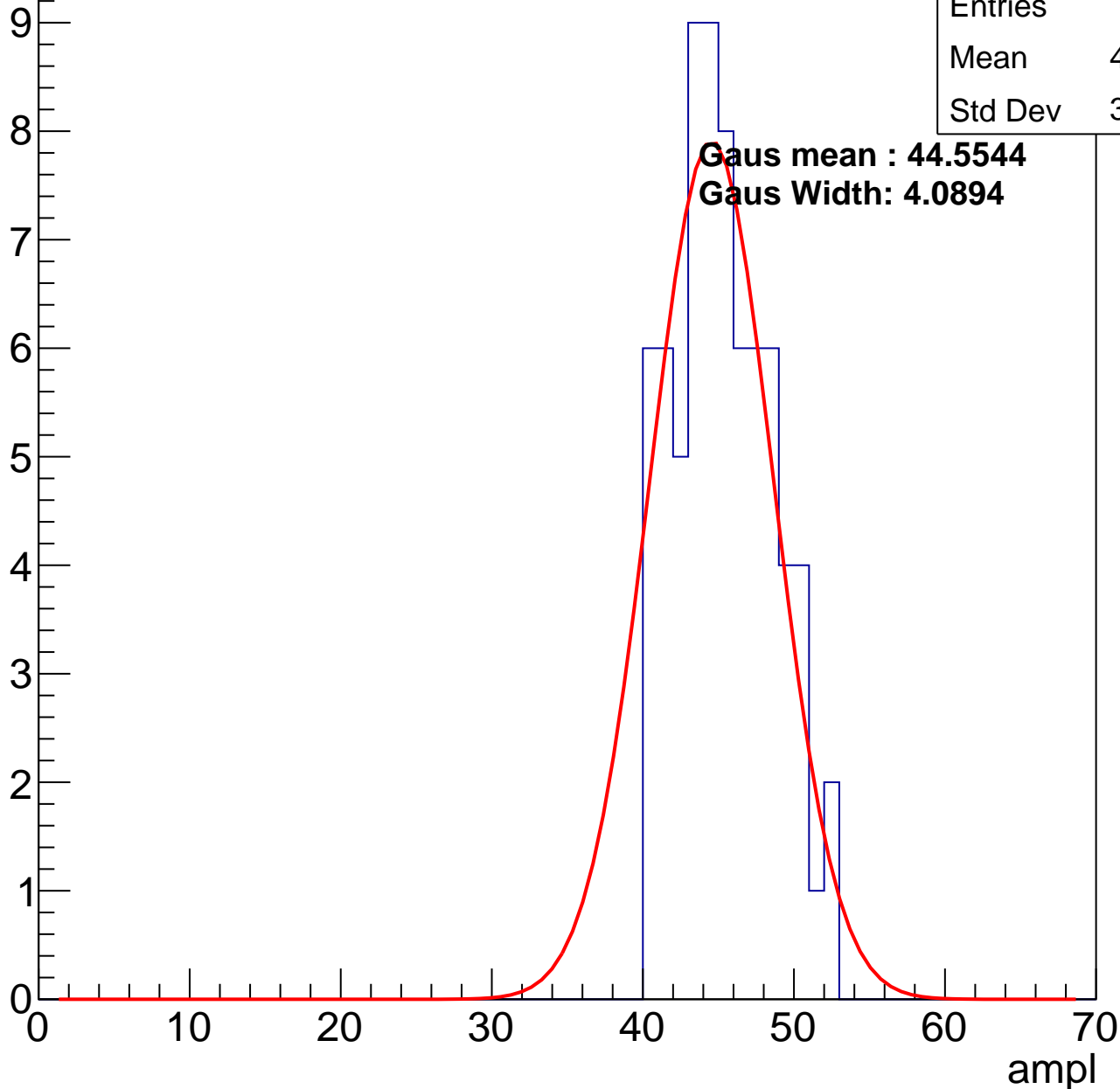
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 44.94 |
| Std Dev | 3.157 |

Gaus mean : 44.5544

Gaus Width: 4.0894

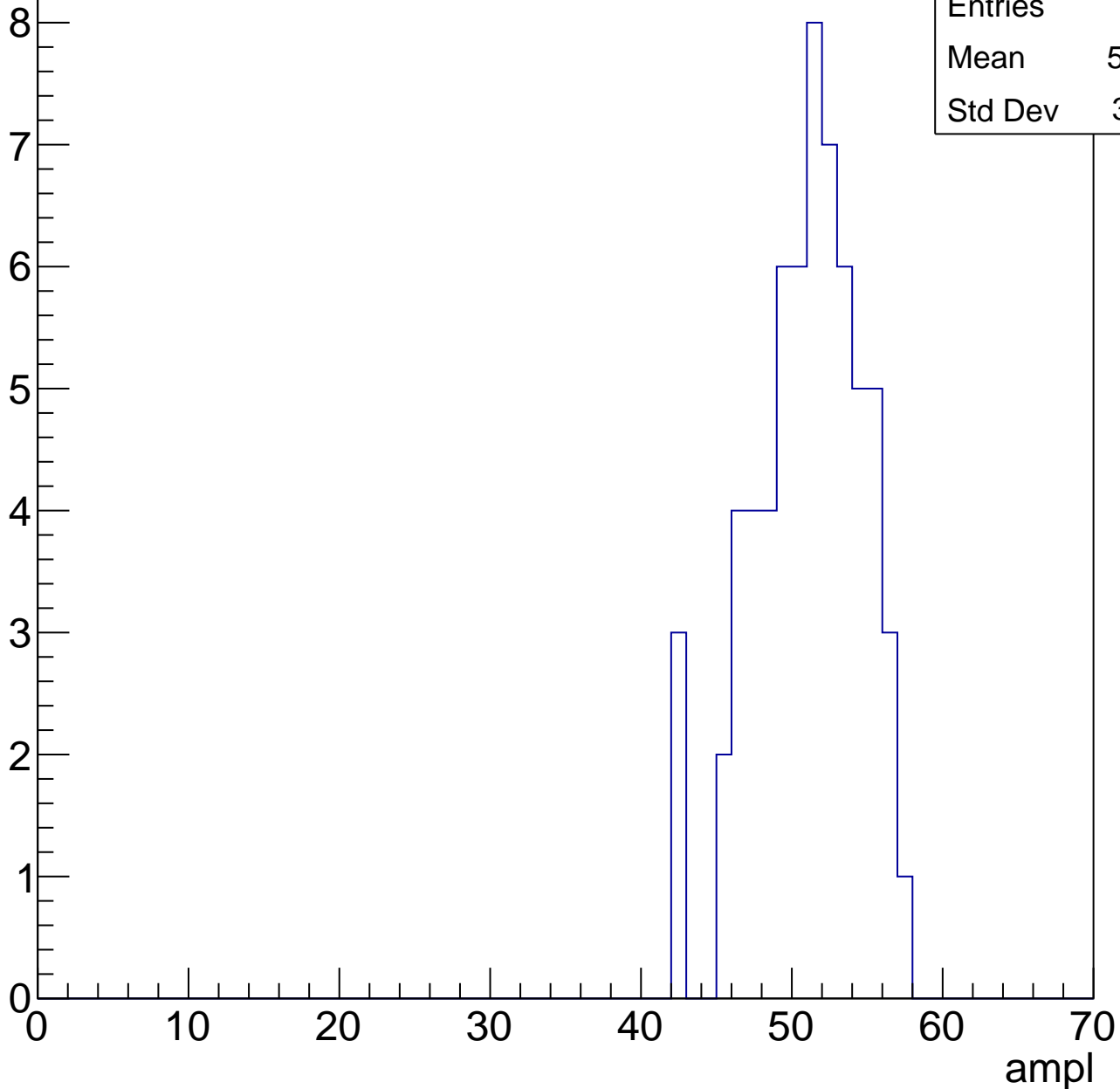


B0L102S, U20-ch47, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

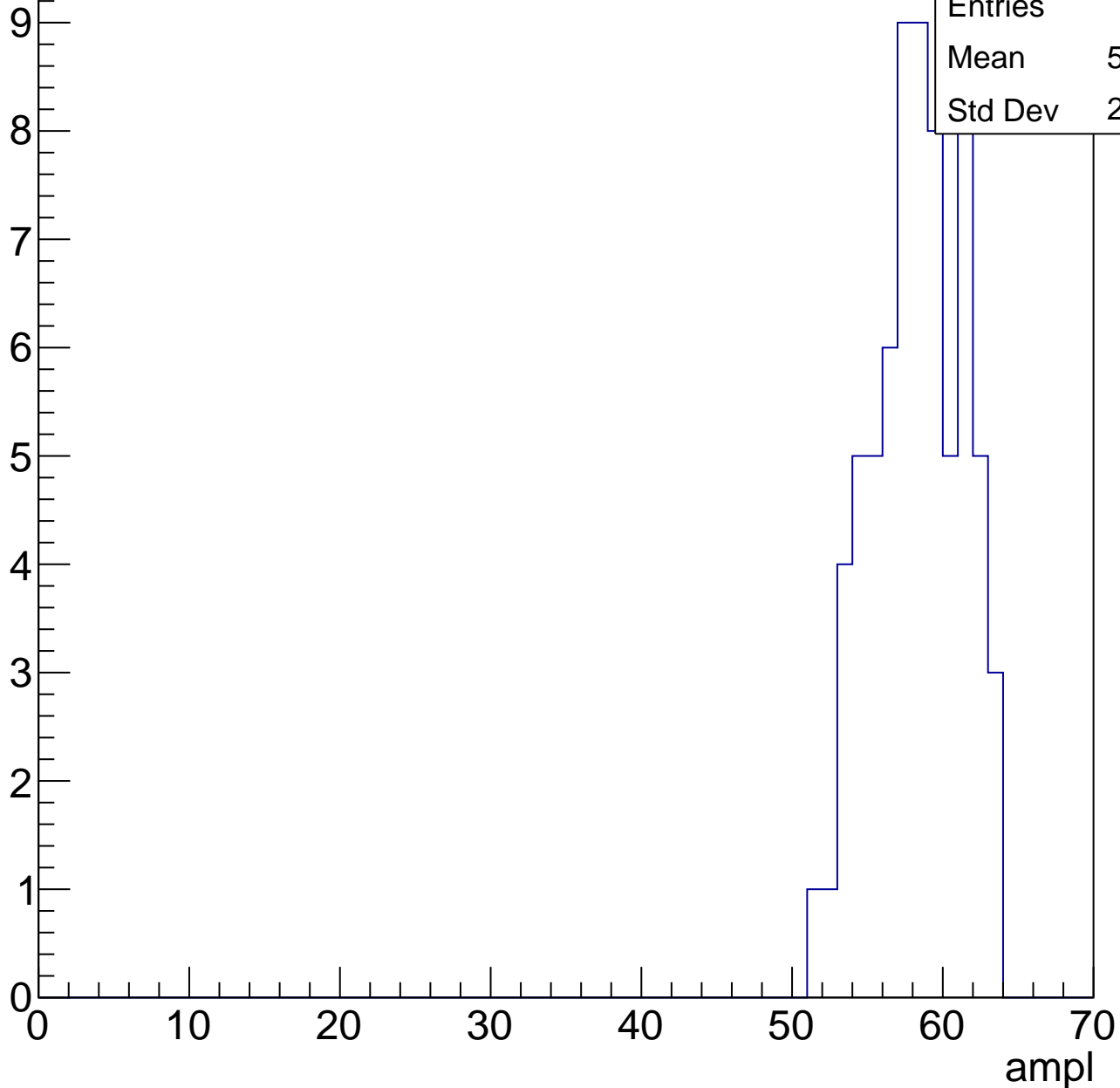
| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.53 |
| Std Dev | 3.531 |



B0L102S, U20-ch47, adc4

calib_packv5_042523_0143.root, FC#12, port B1

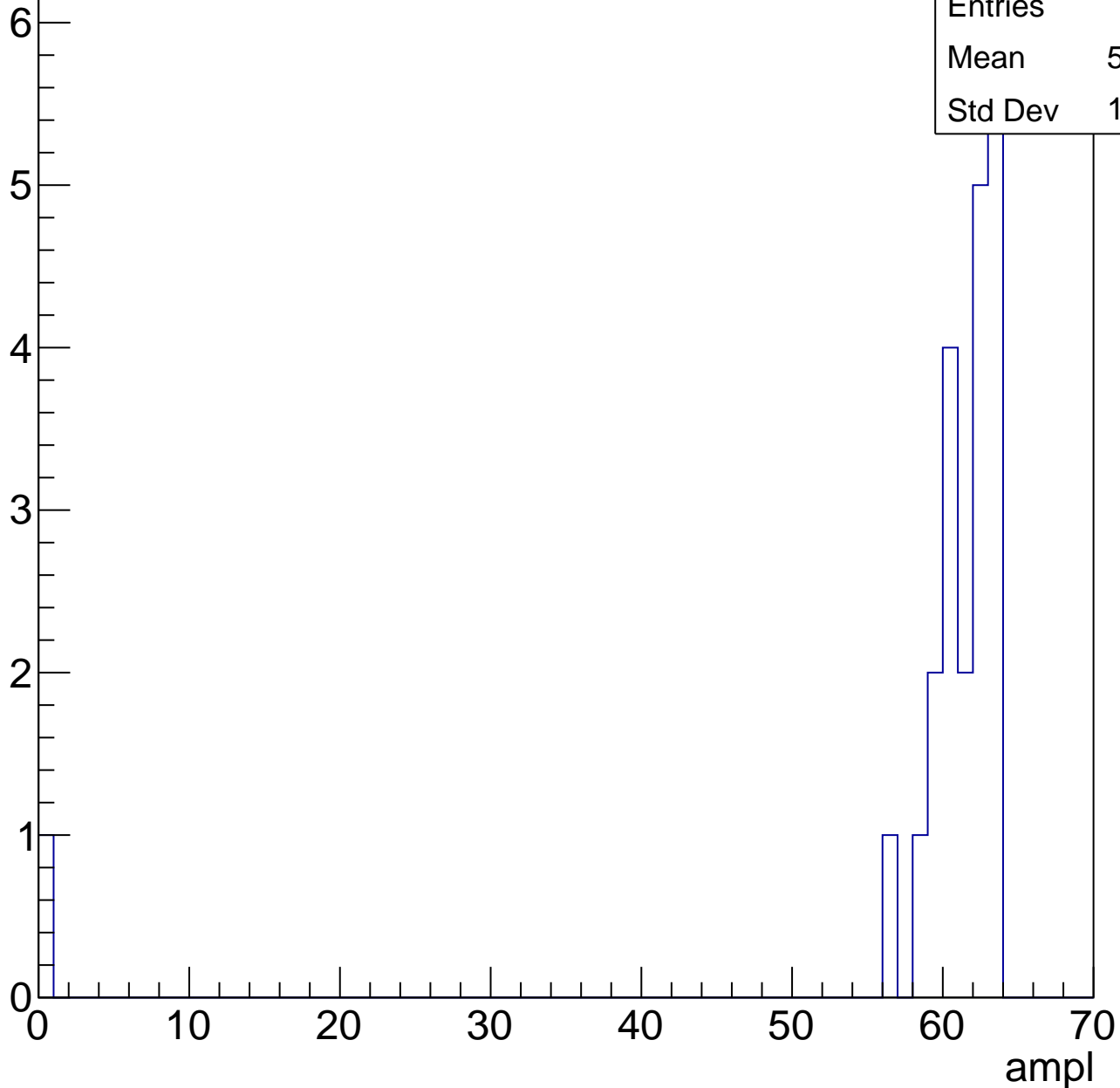
Entry



B0L102S, U20-ch47, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch47, adc6

calib_packv5_042523_0143.root, FC#12, port B1

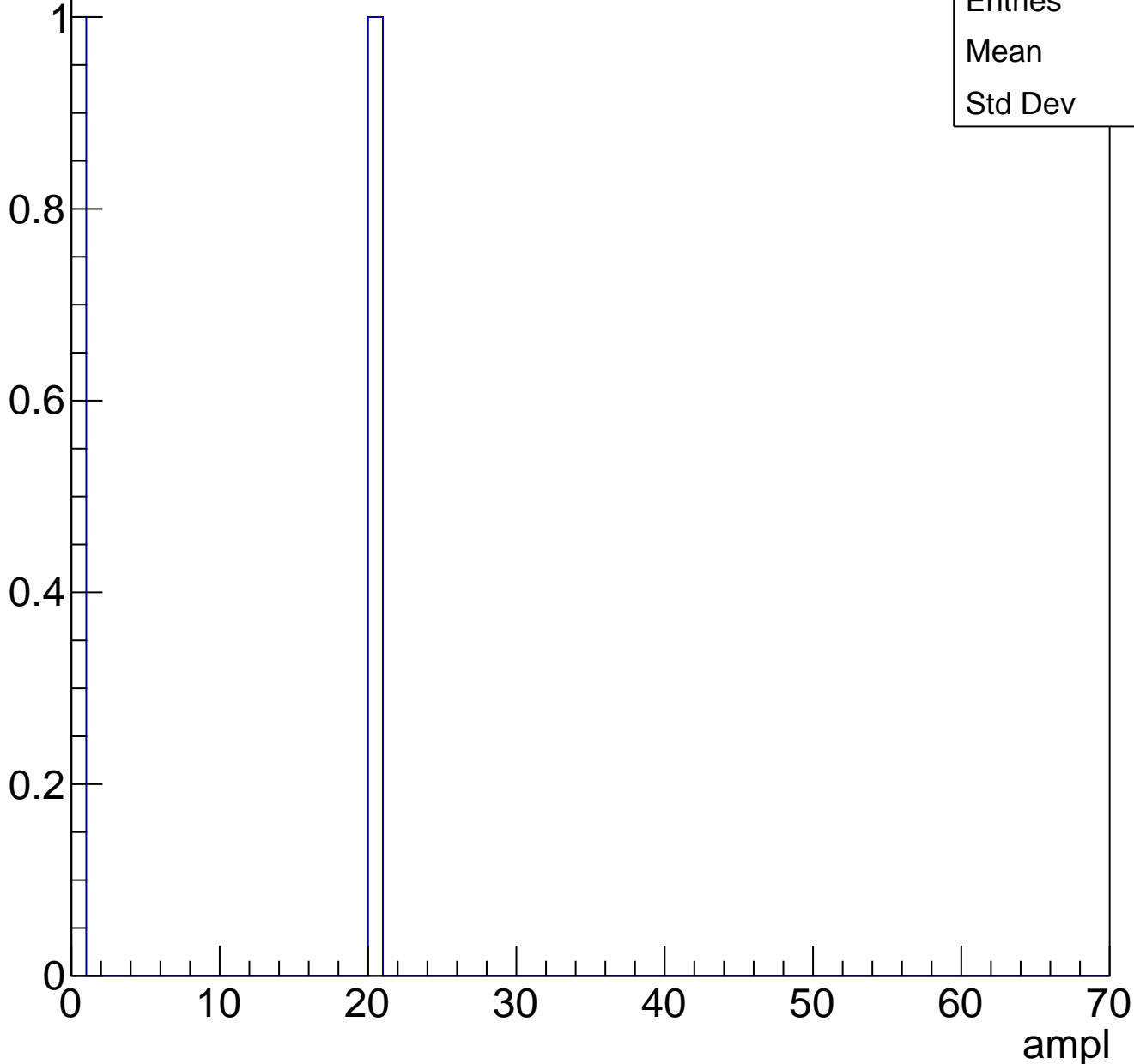
Entry



B0L102S, U20-ch47, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 10 |
| Std Dev | 10 |

B0L102S, U20-ch48, adc0

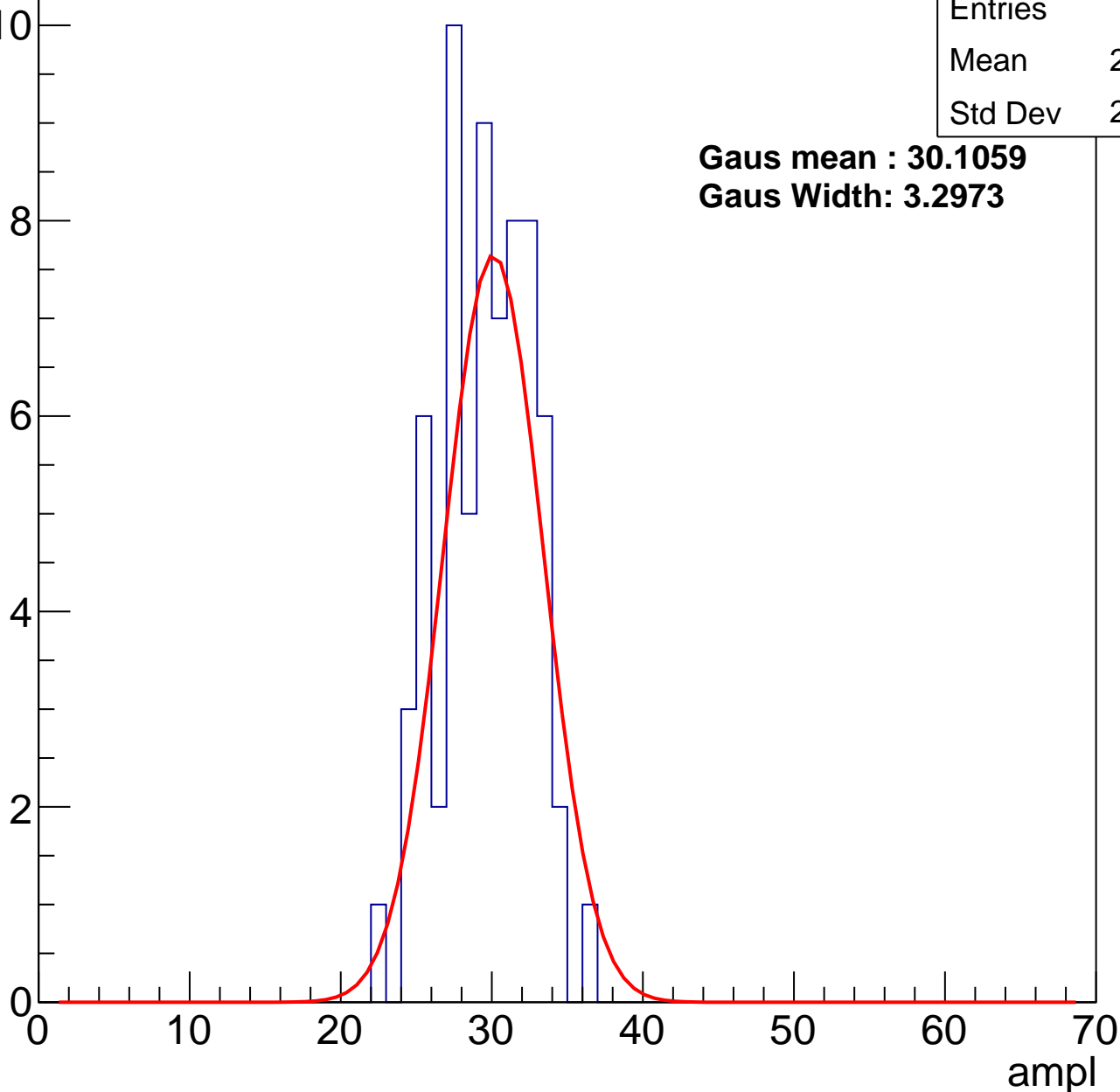
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.16 |
| Std Dev | 2.939 |

Gaus mean : 30.1059

Gaus Width: 3.2973



B0L102S, U20-ch48, adc1

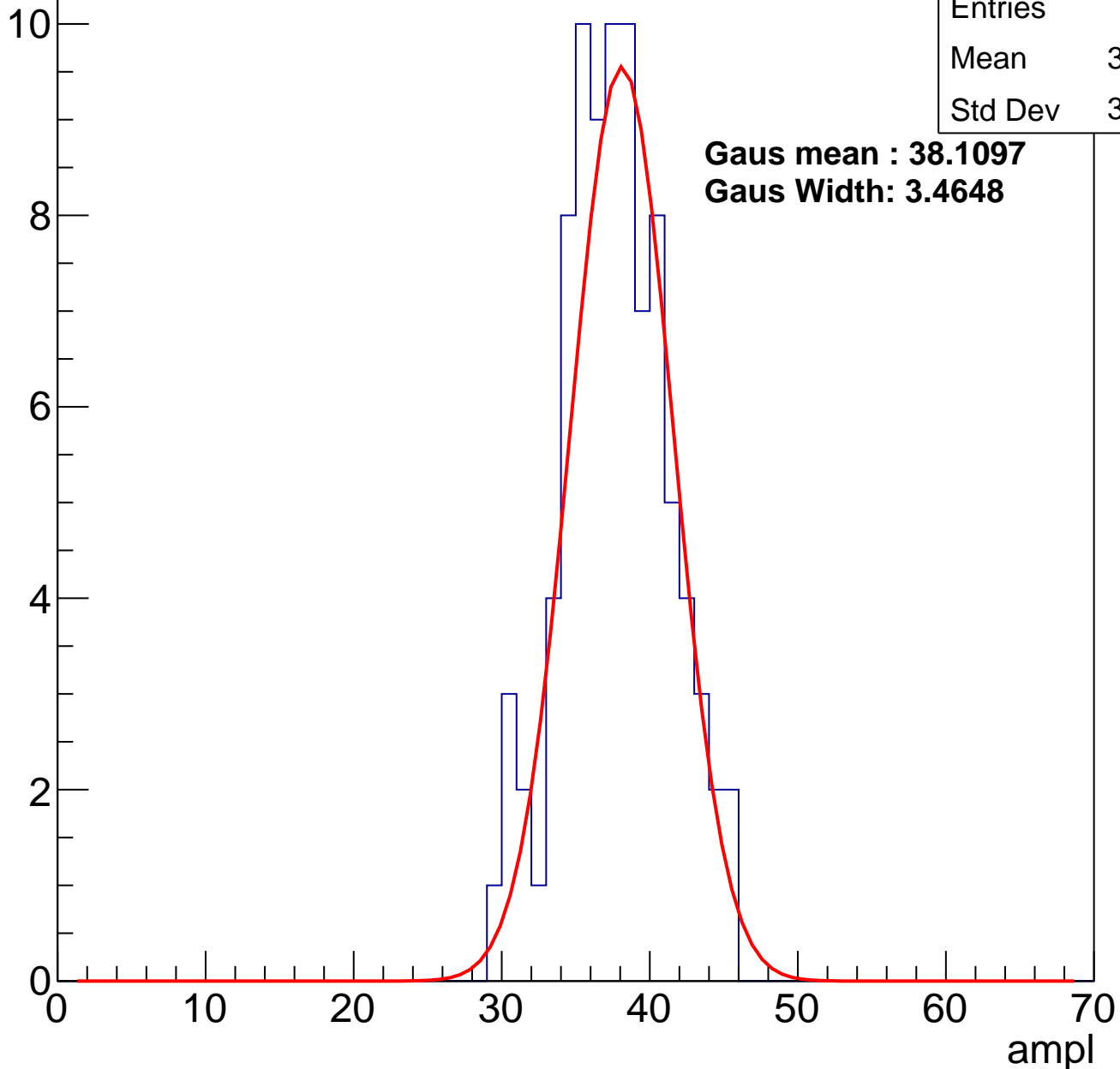
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 89 |
| Mean | 37.24 |
| Std Dev | 3.535 |

Gaus mean : 38.1097

Gaus Width: 3.4648

Entry



B0L102S, U20-ch48, adc2

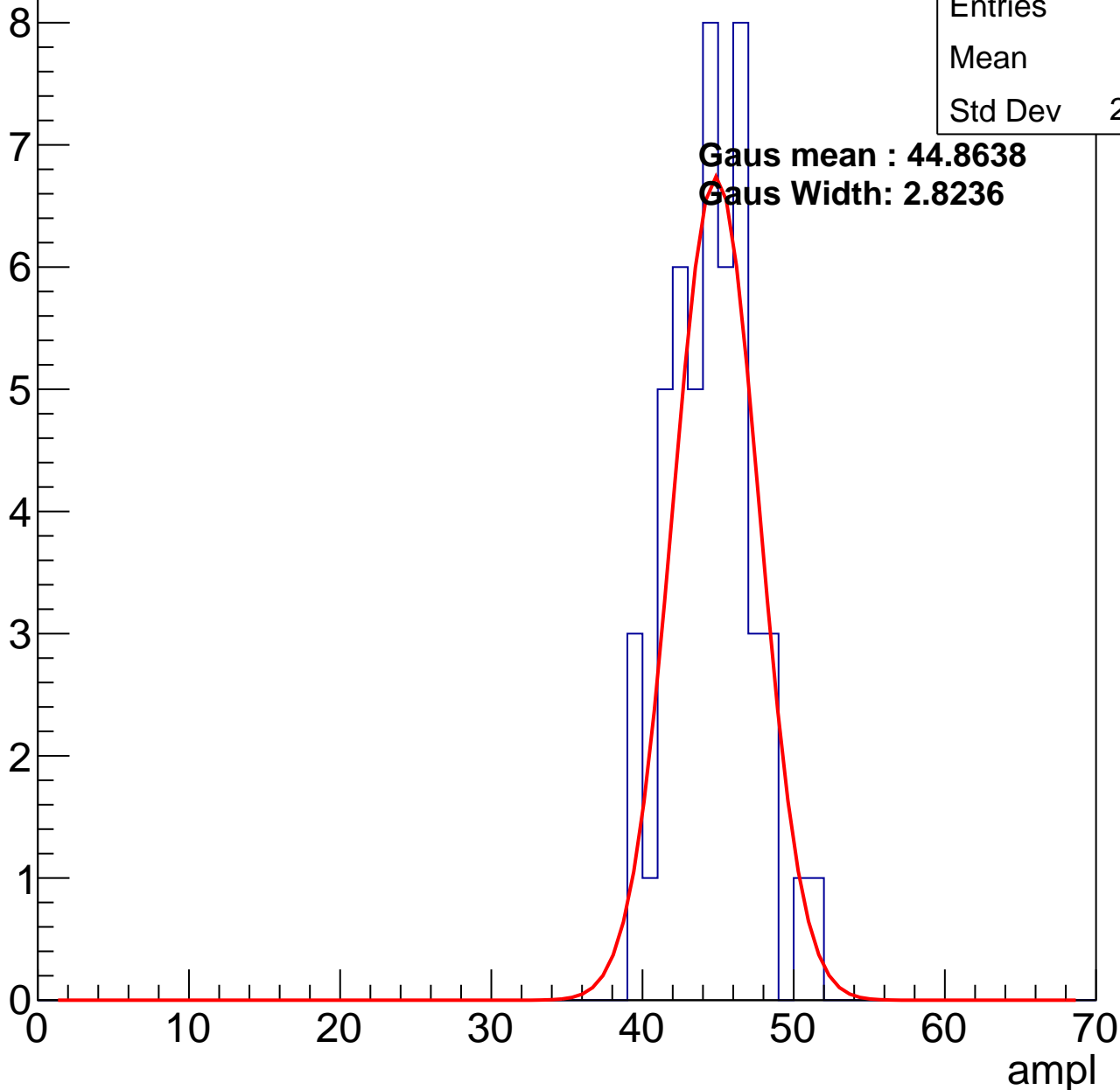
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 44.1 |
| Std Dev | 2.693 |

Gaus mean : 44.8638

Gaus Width: 2.8236

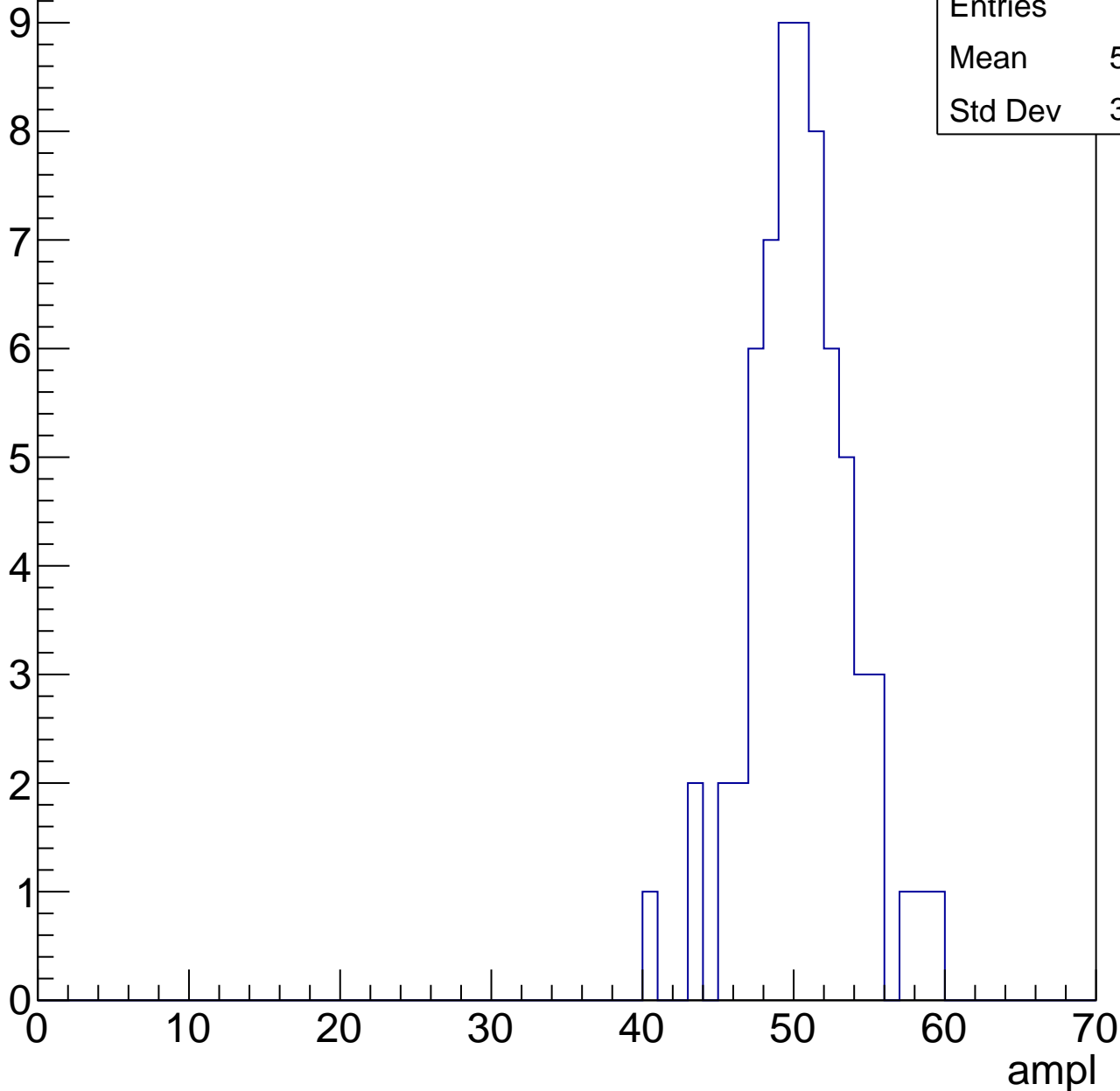


B0L102S, U20-ch48, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 50.05 |
| Std Dev | 3.404 |

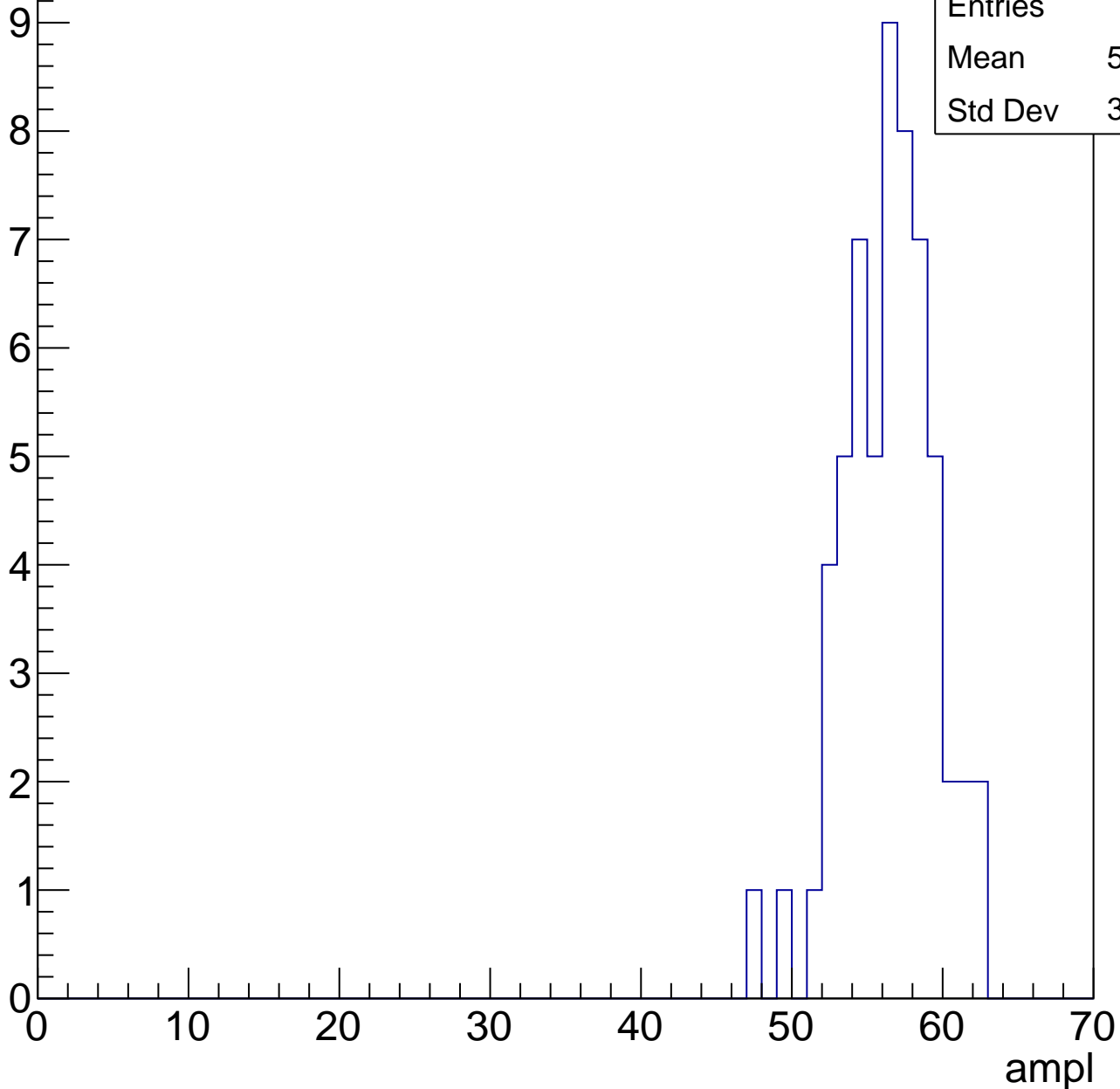


B0L102S, U20-ch48, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

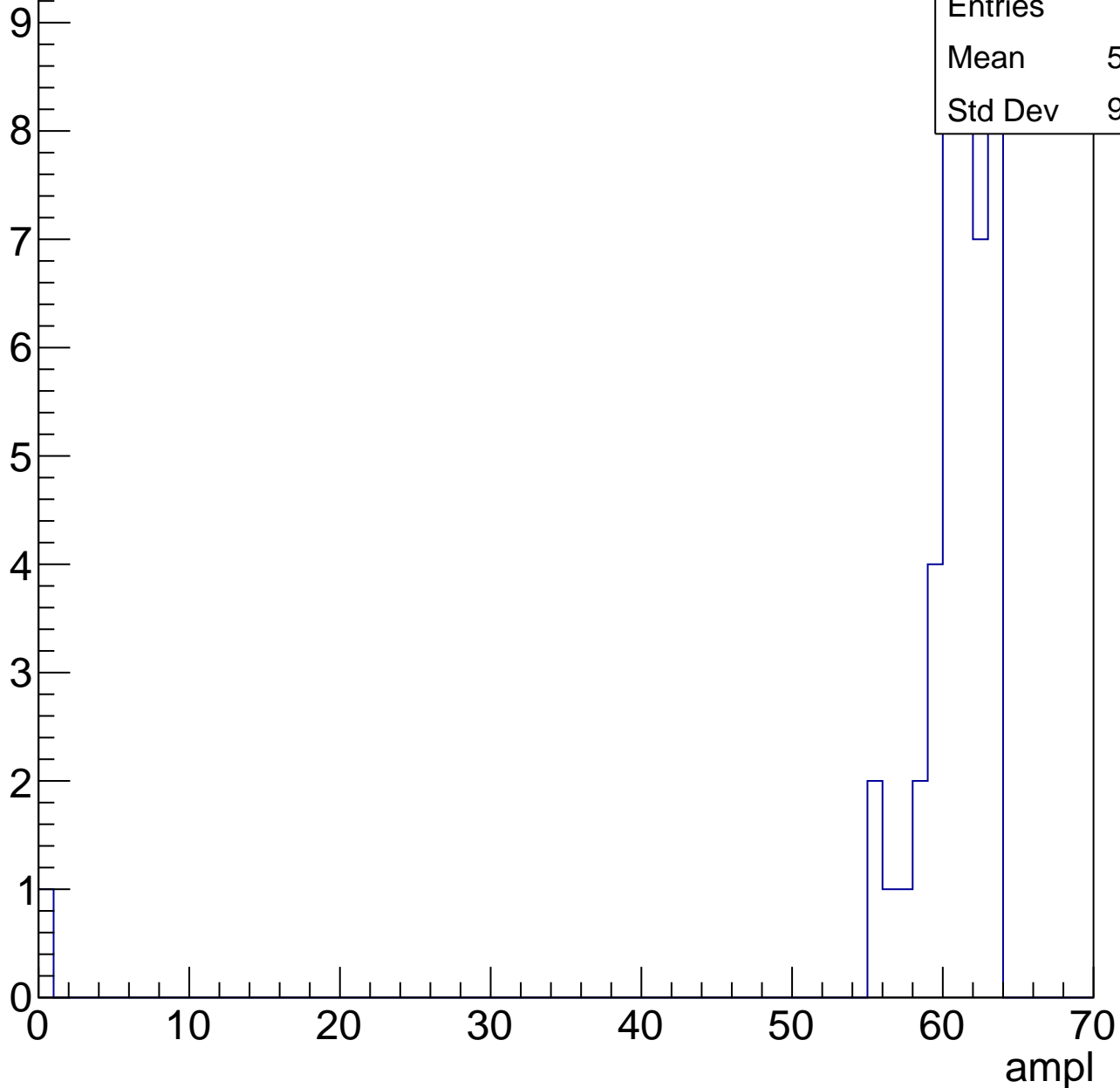
| | |
|---------|-------|
| Entries | 59 |
| Mean | 55.93 |
| Std Dev | 3.002 |



B0L102S, U20-ch48, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.16 |
| Std Dev | 9.368 |

B0L102S, U20-ch48, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch48, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch49, adc0

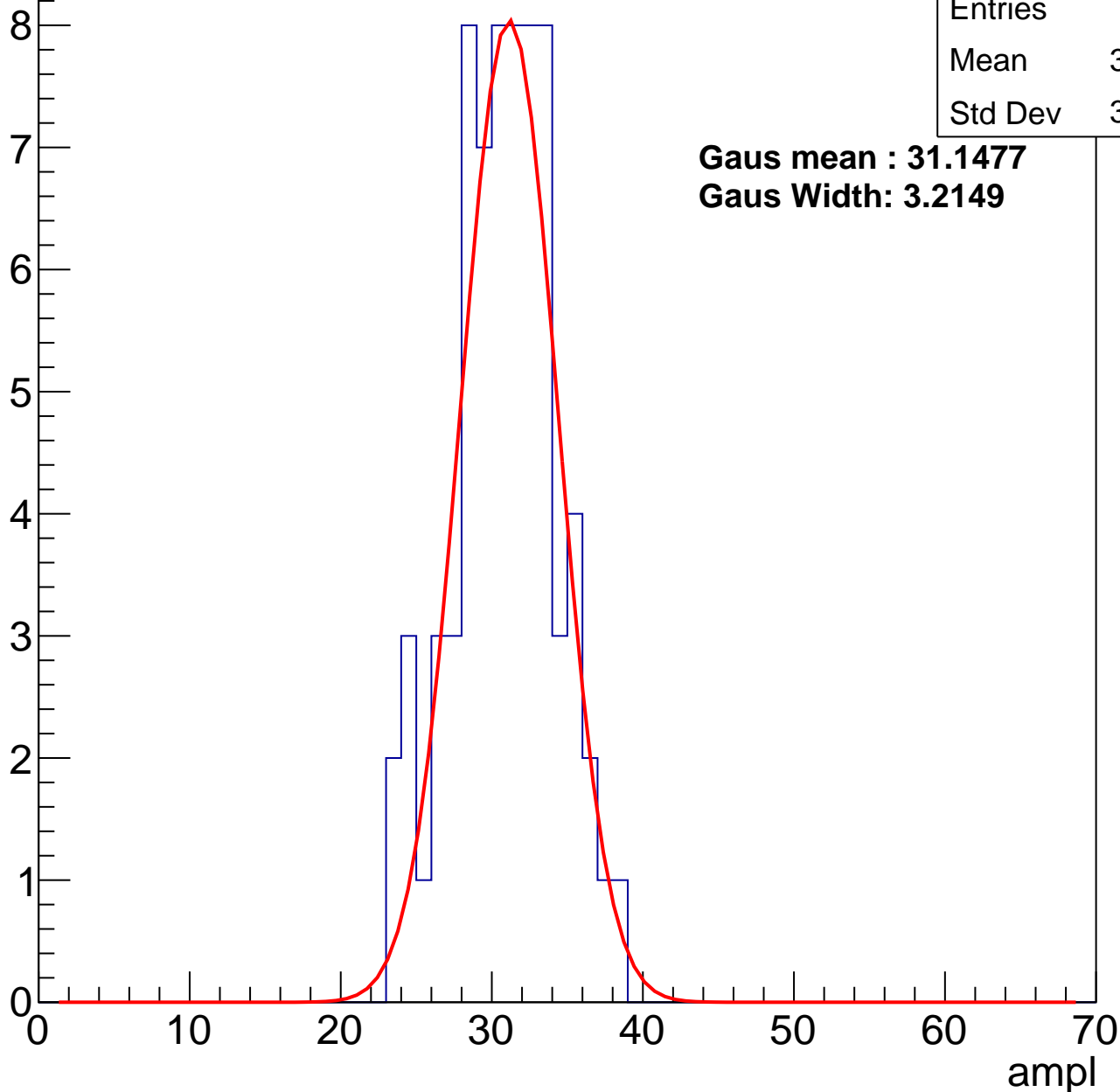
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 30.37 |
| Std Dev | 3.339 |

Gaus mean : 31.1477

Gaus Width: 3.2149



B0L102S, U20-ch49, adc1

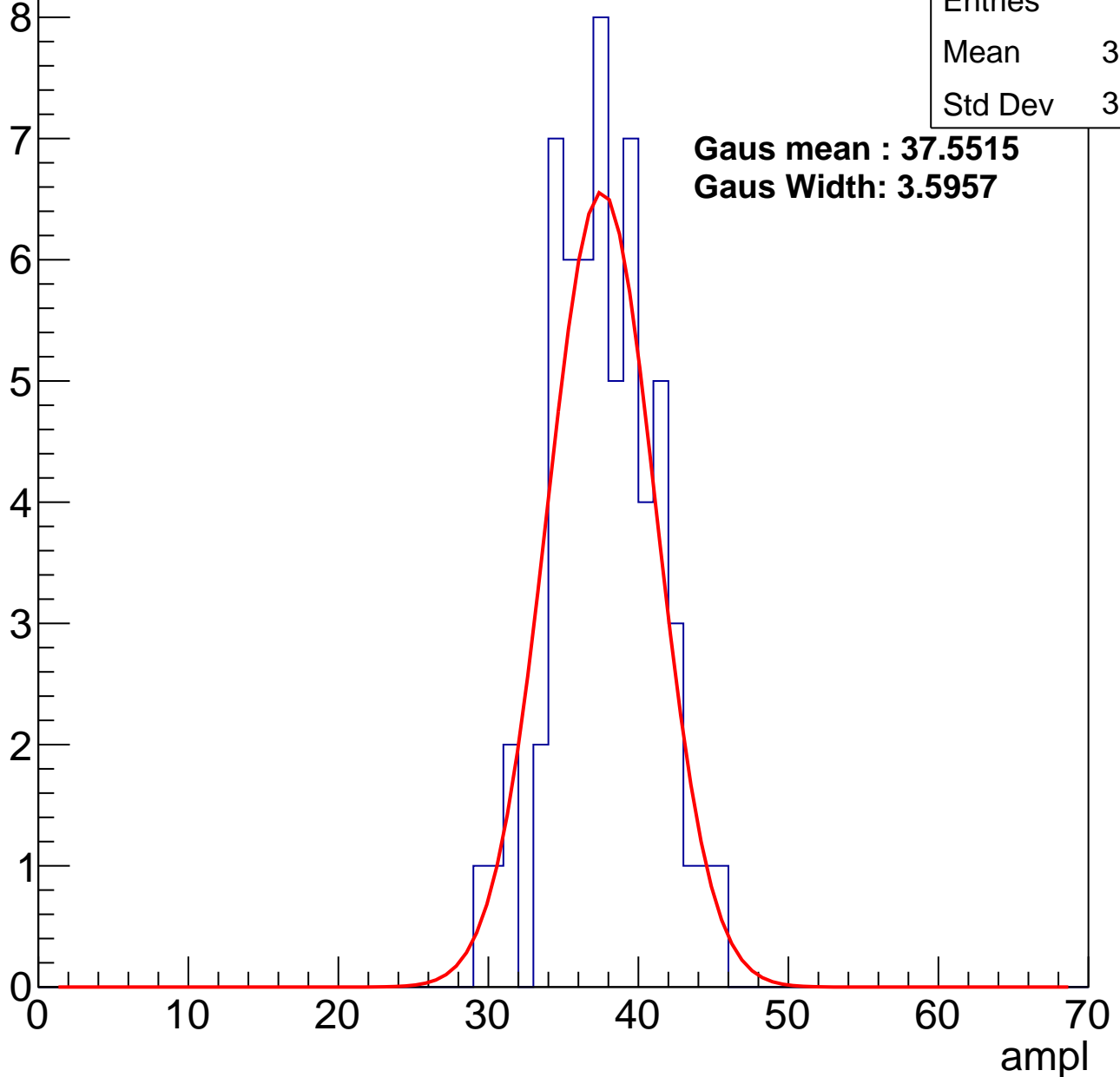
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 37.22 |
| Std Dev | 3.367 |

Gaus mean : 37.5515

Gaus Width: 3.5957



B0L102S, U20-ch49, adc2

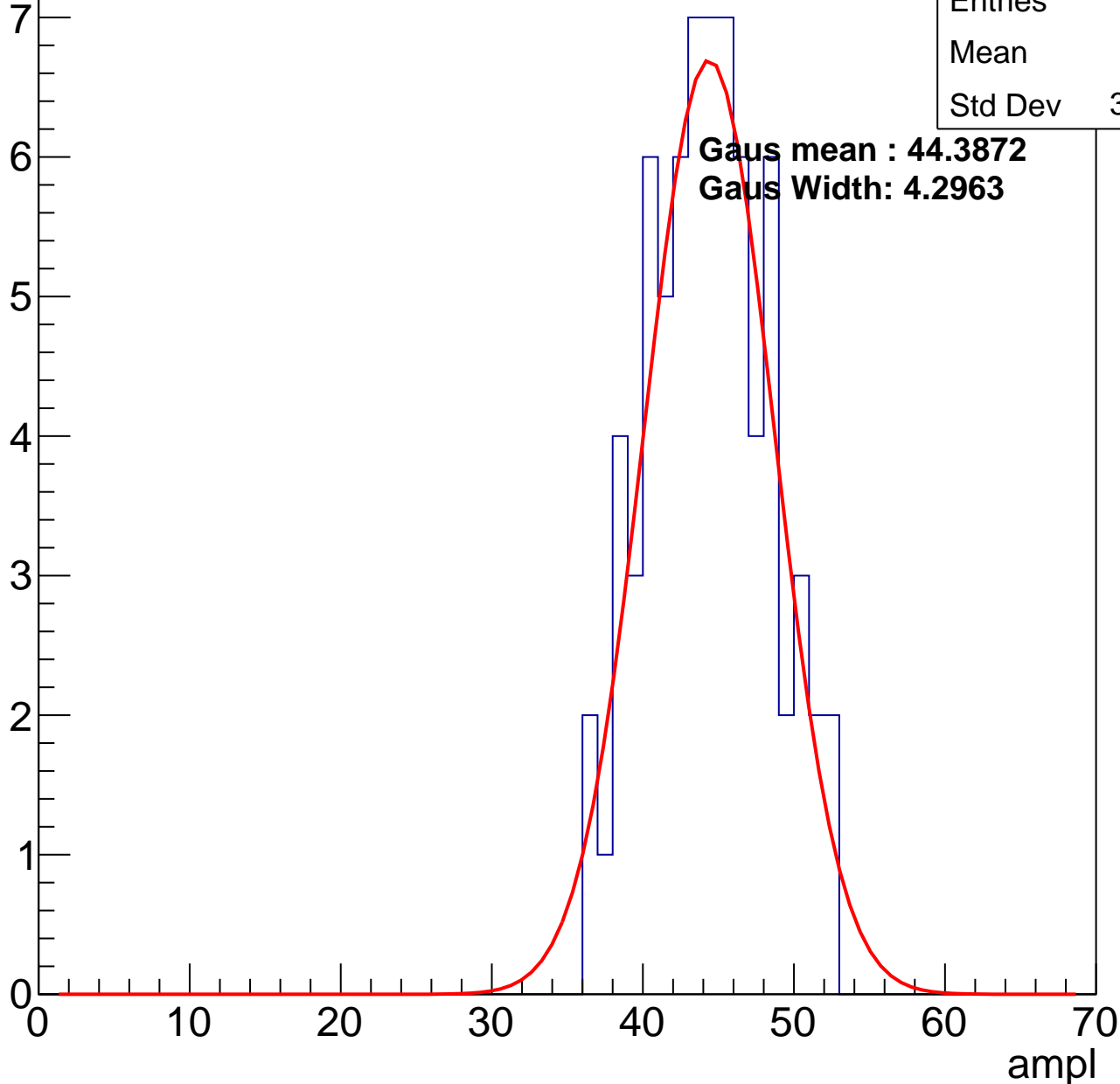
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.9 |
| Std Dev | 3.907 |

Gaus mean : 44.3872

Gaus Width: 4.2963

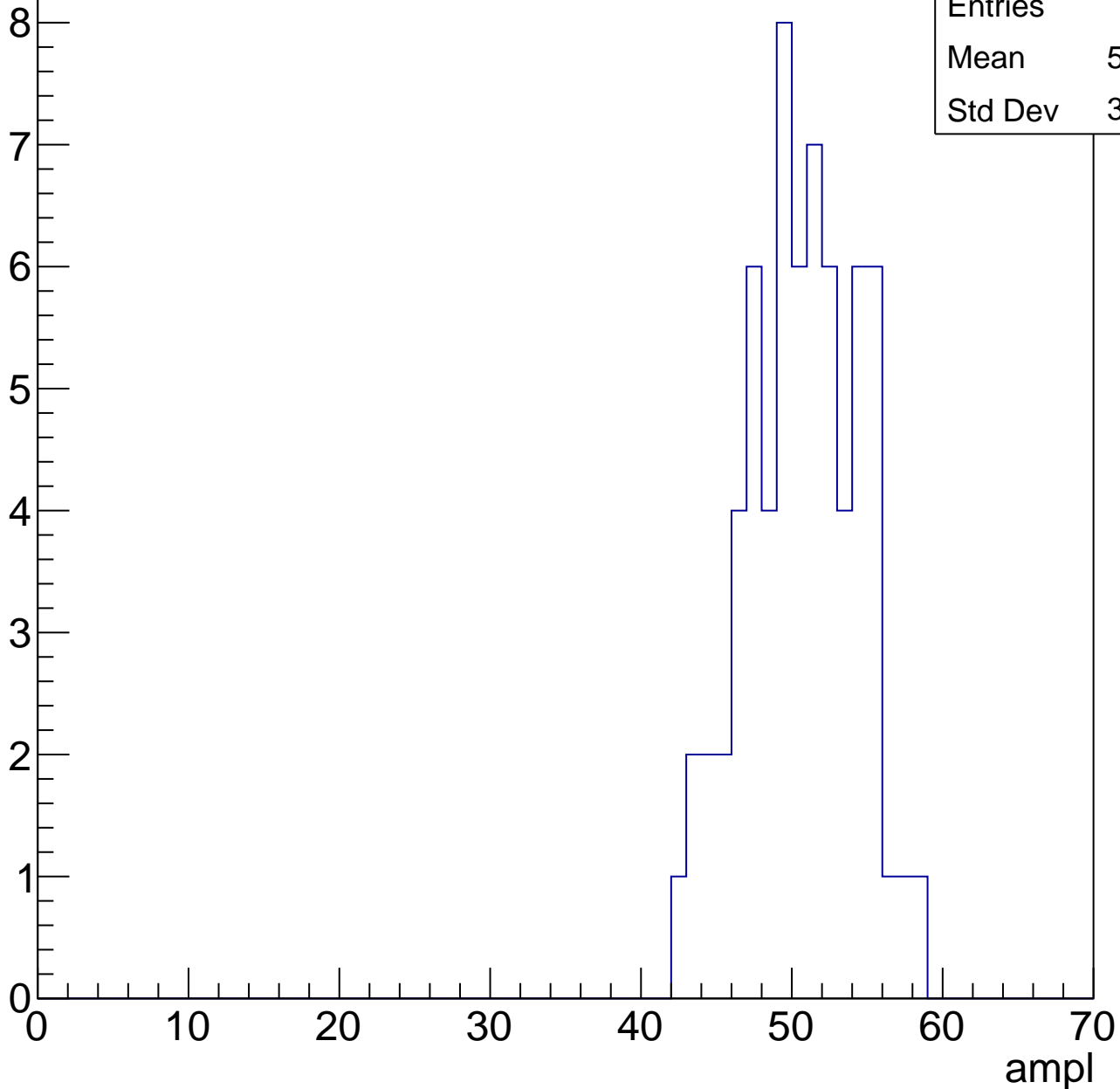


B0L102S, U20-ch49, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50.18 |
| Std Dev | 3.636 |

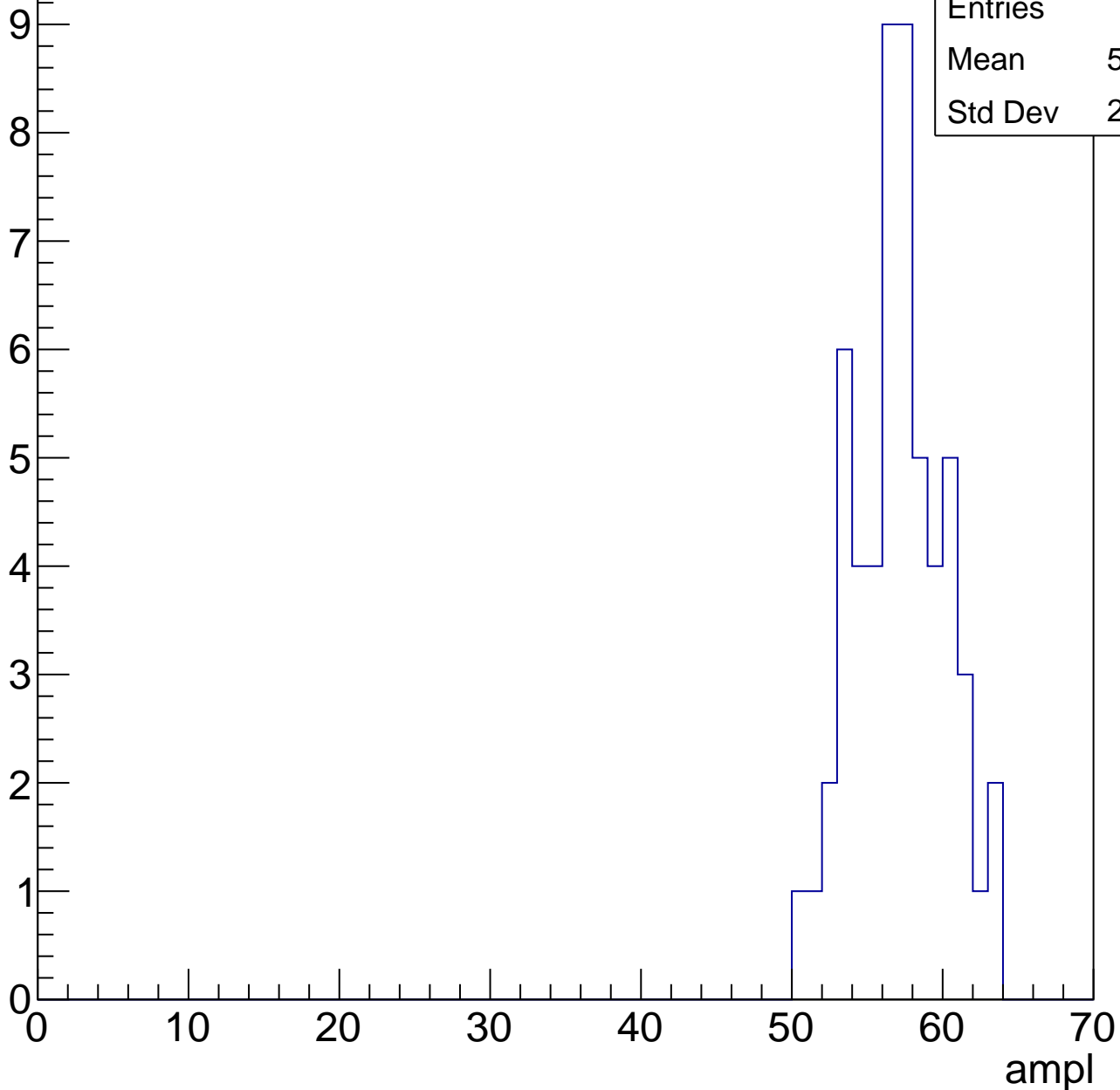


B0L102S, U20-ch49, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 56.66 |
| Std Dev | 2.978 |

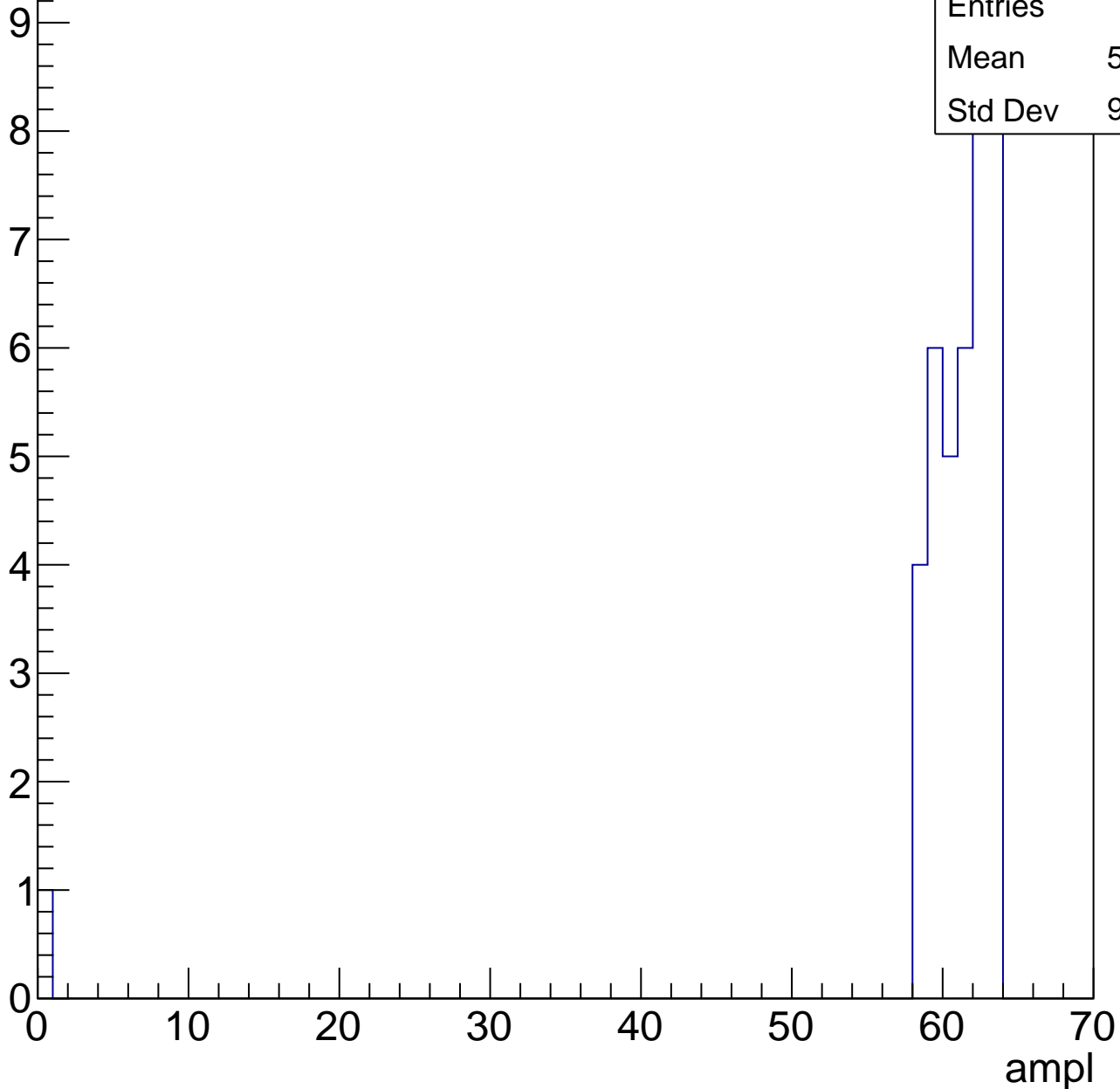


B0L102S, U20-ch49, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 59.33 |
| Std Dev | 9.765 |



B0L102S, U20-ch49, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch49, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch50, adc0

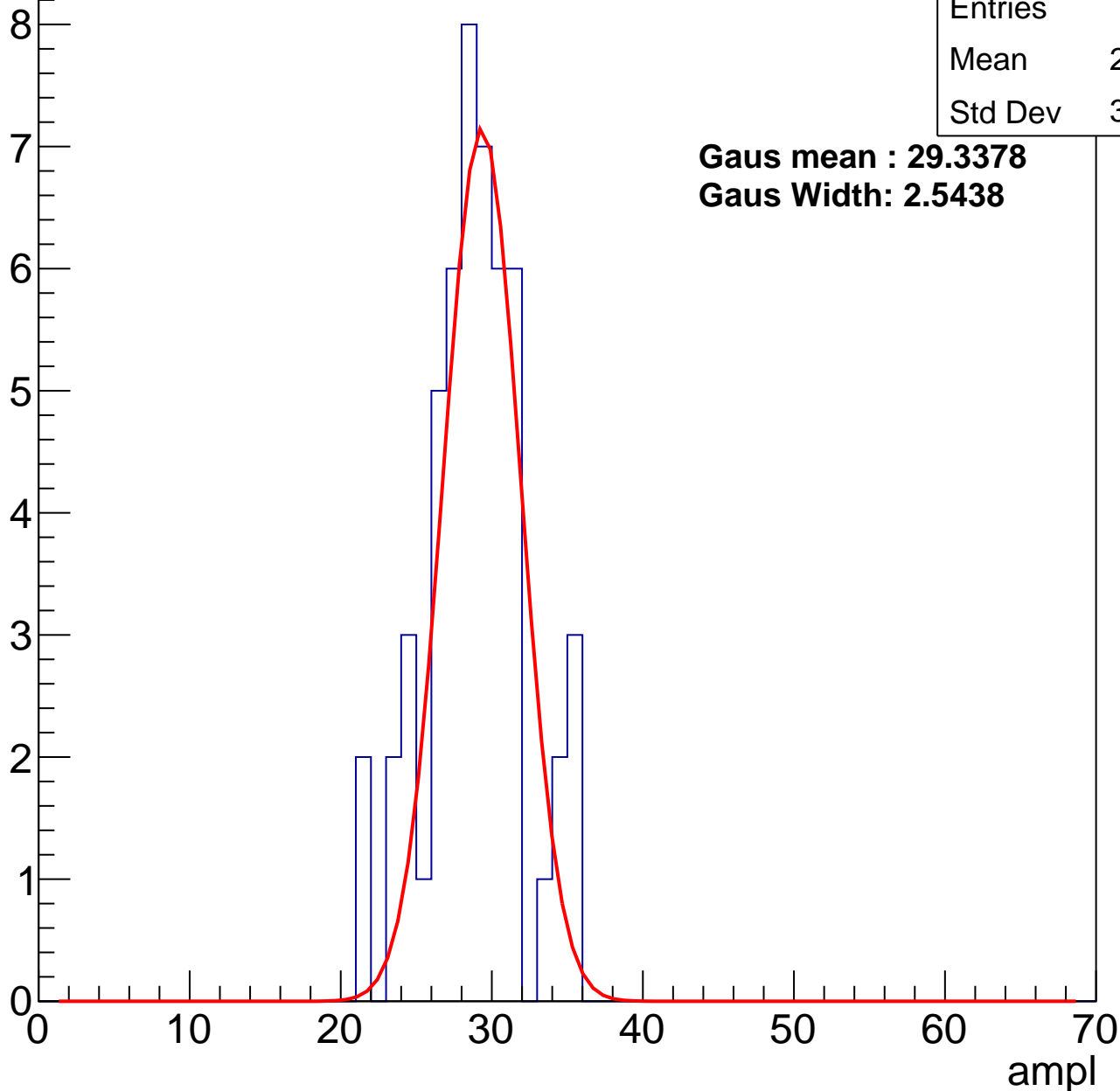
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 28.38 |
| Std Dev | 3.259 |

Gaus mean : 29.3378

Gaus Width: 2.5438



B0L102S, U20-ch50, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 34.62 |
| Std Dev | 3.093 |

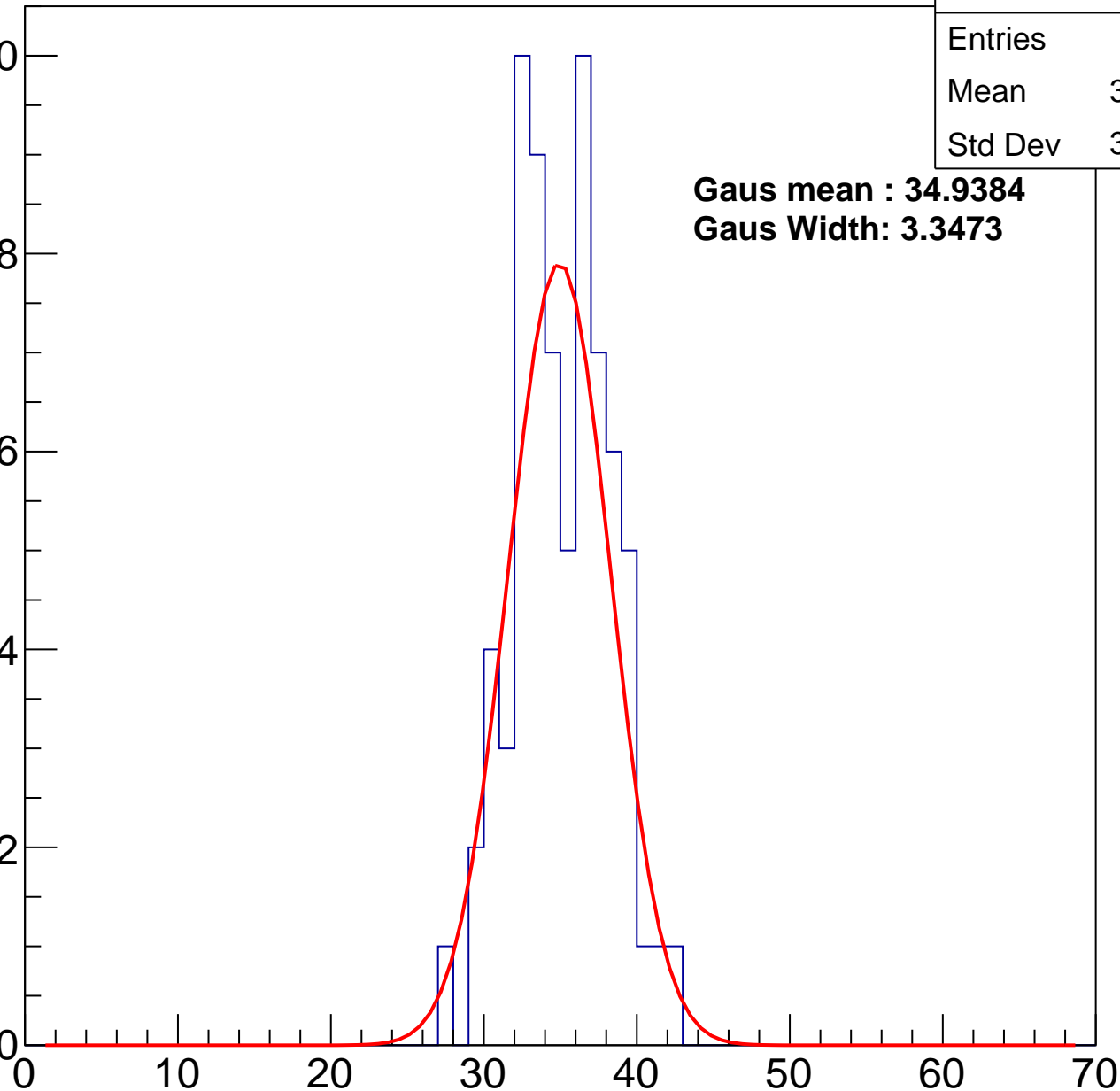
Gaus mean : 34.9384

Gaus Width: 3.3473

Entry

10
8
6
4
2
0

ampl



B0L102S, U20-ch50, adc2

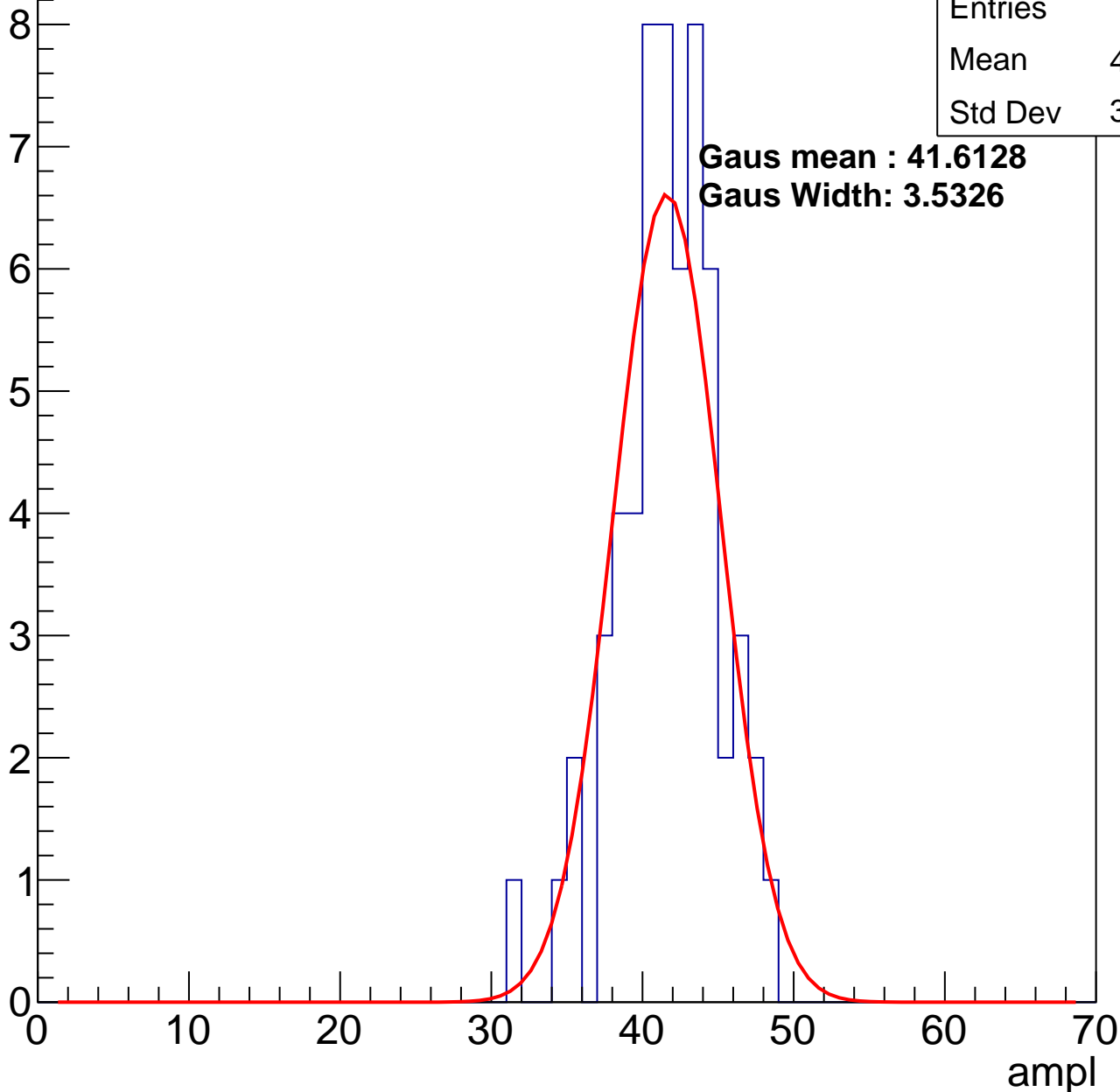
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 41.22 |
| Std Dev | 3.309 |

Gaus mean : 41.6128

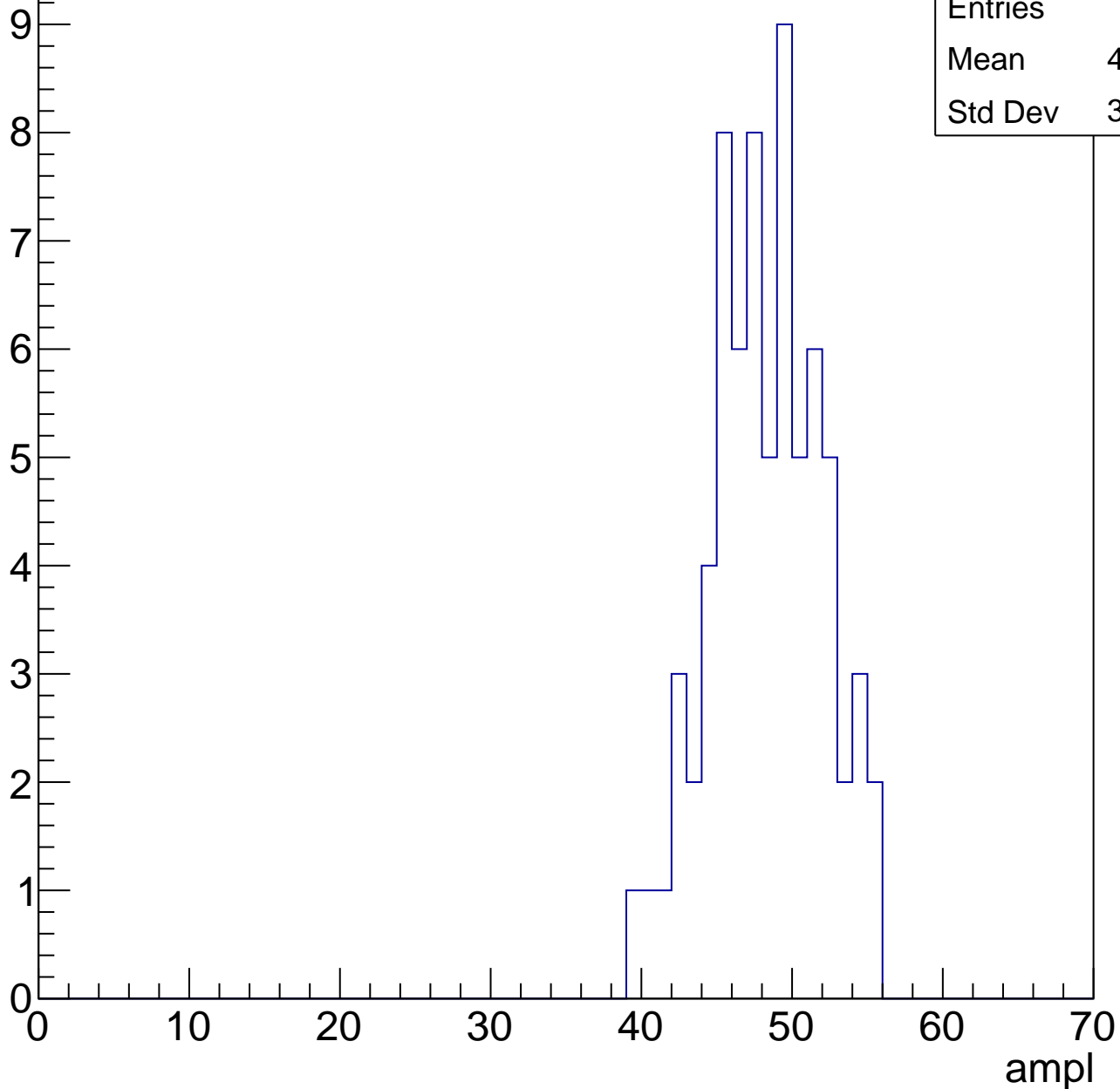
Gaus Width: 3.5326



B0L102S, U20-ch50, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

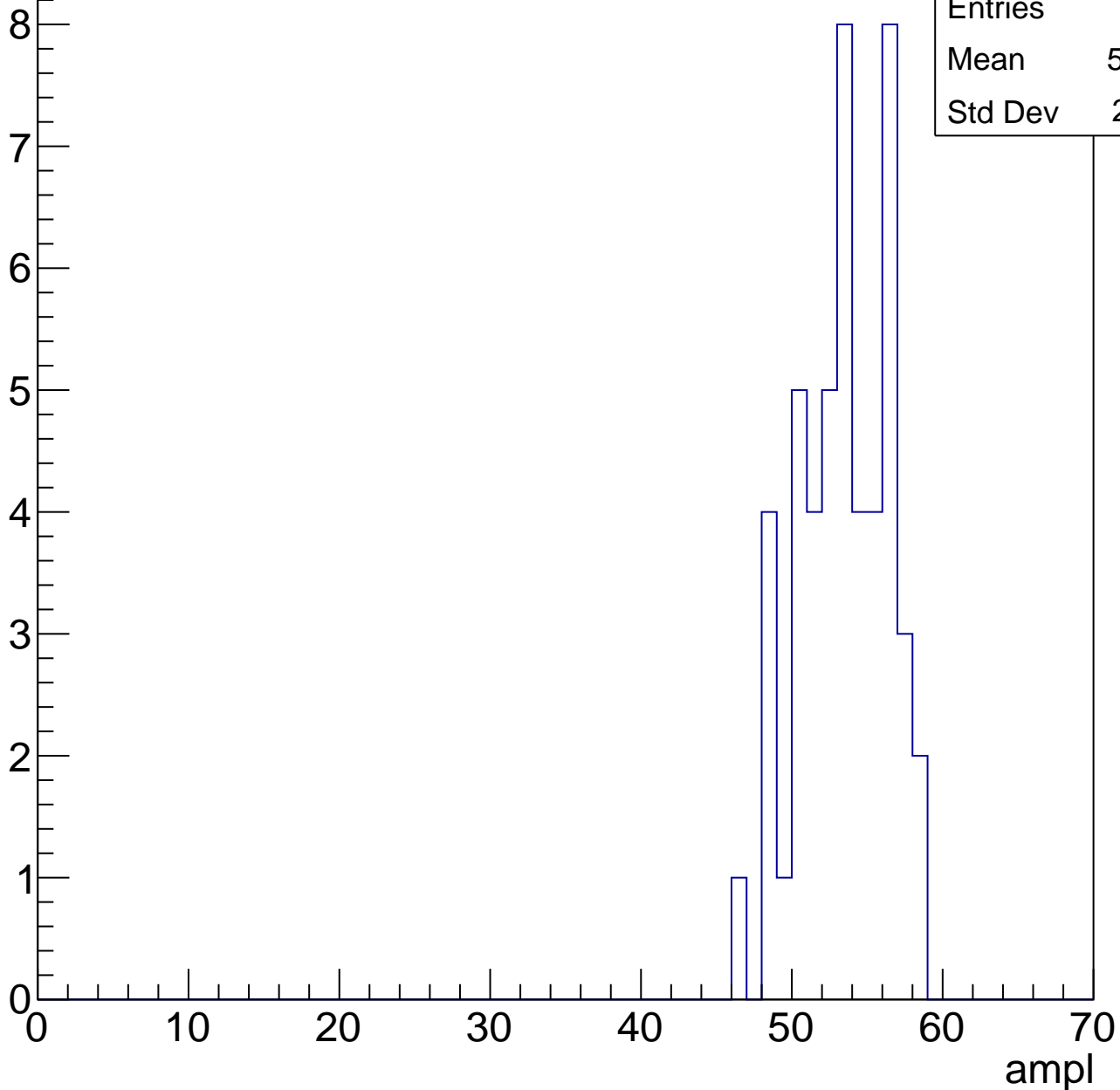


B0L102S, U20-ch50, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 52.98 |
| Std Dev | 2.931 |

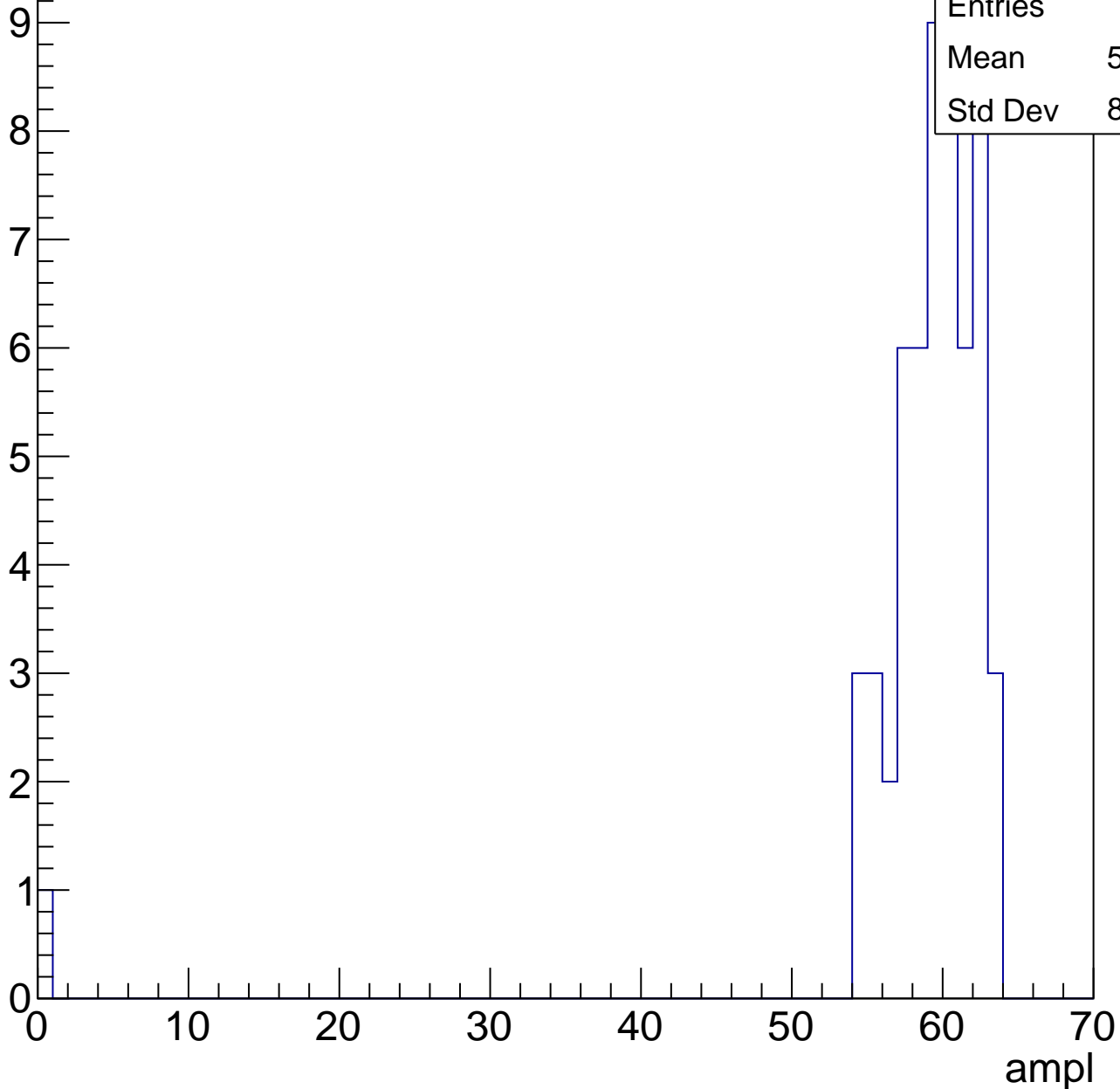


B0L102S, U20-ch50, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.02 |
| Std Dev | 8.258 |



B0L102S, U20-ch50, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

| | |
|---------|-------|
| Entries | 19 |
| Mean | 60.79 |
| Std Dev | 2.353 |

ampl

10

20

30

40

50

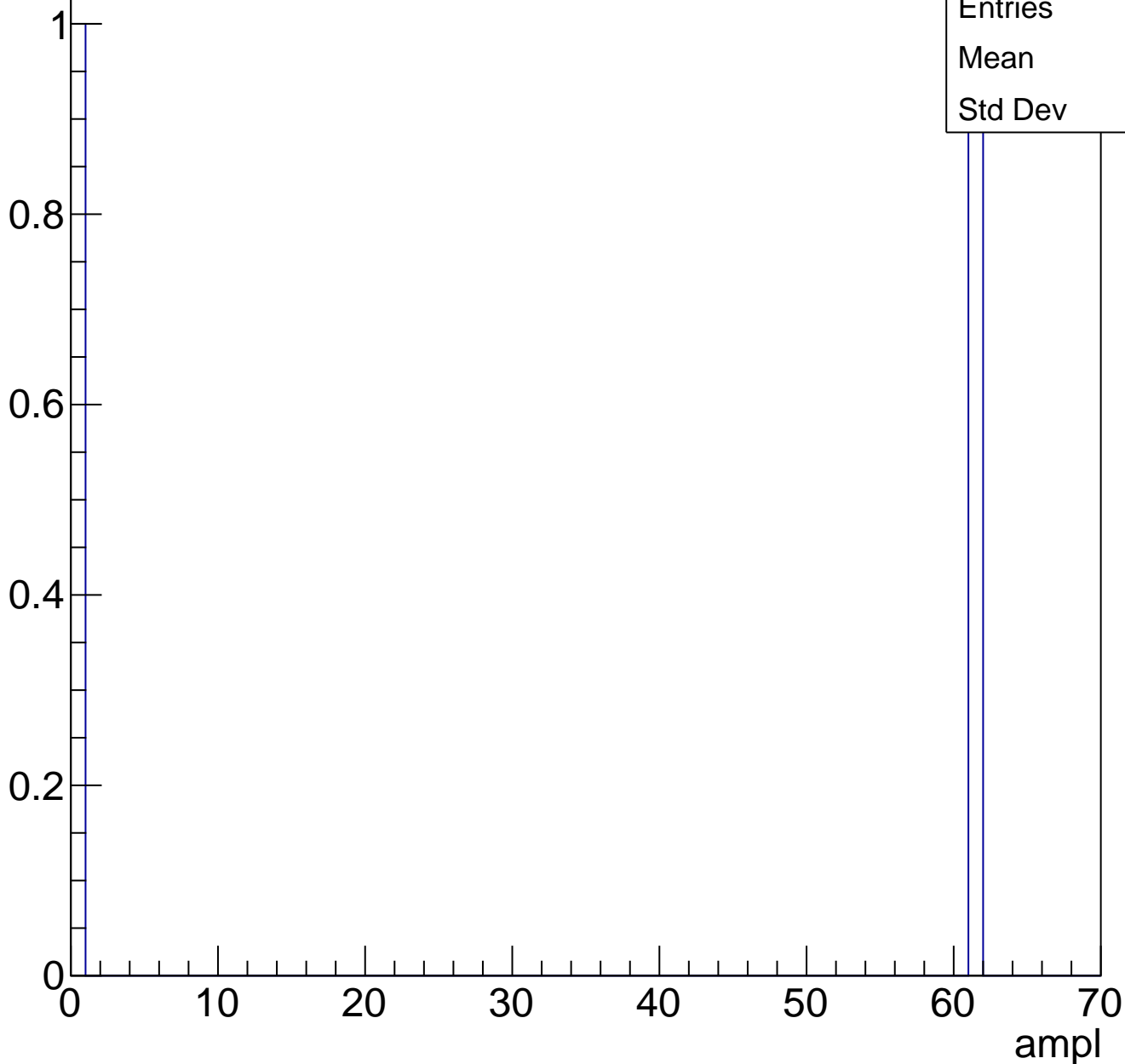
60

70

B0L102S, U20-ch50, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch51, adc0

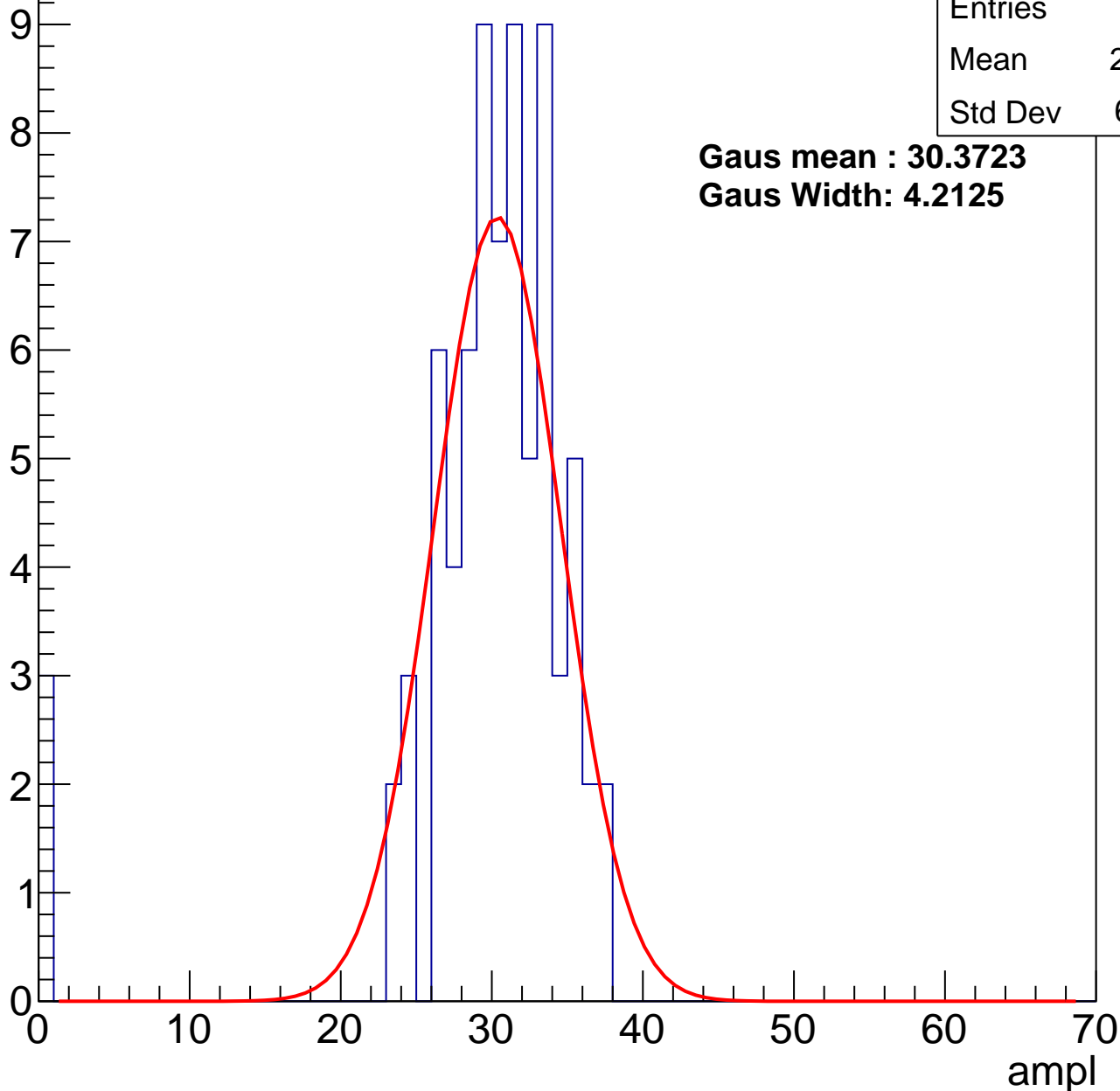
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.07 |
| Std Dev | 6.791 |

Gaus mean : 30.3723

Gaus Width: 4.2125



B0L102S, U20-ch51, adc1

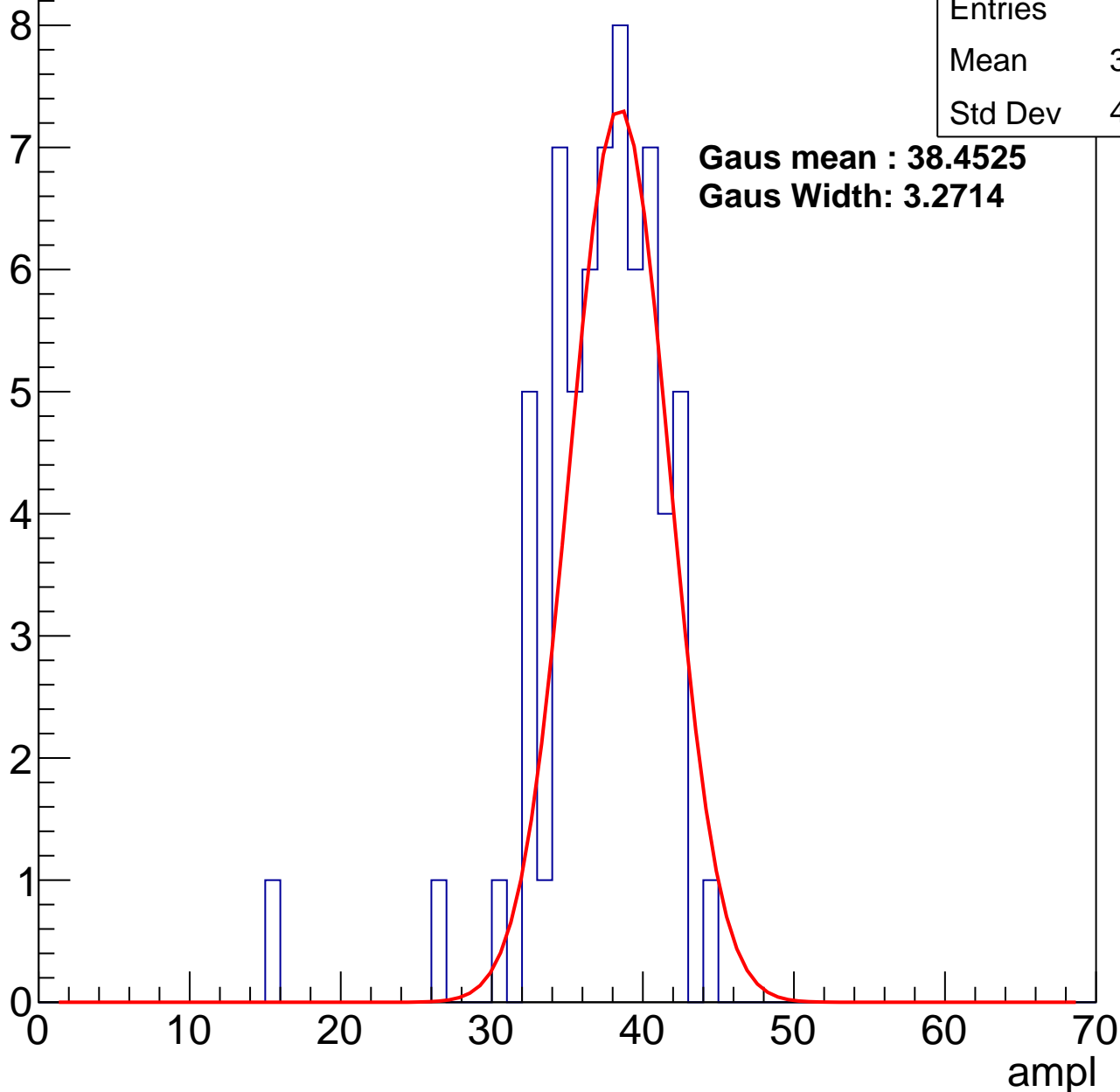
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.74 |
| Std Dev | 4.319 |

Gaus mean : 38.4525

Gaus Width: 3.2714



B0L102S, U20-ch51, adc2

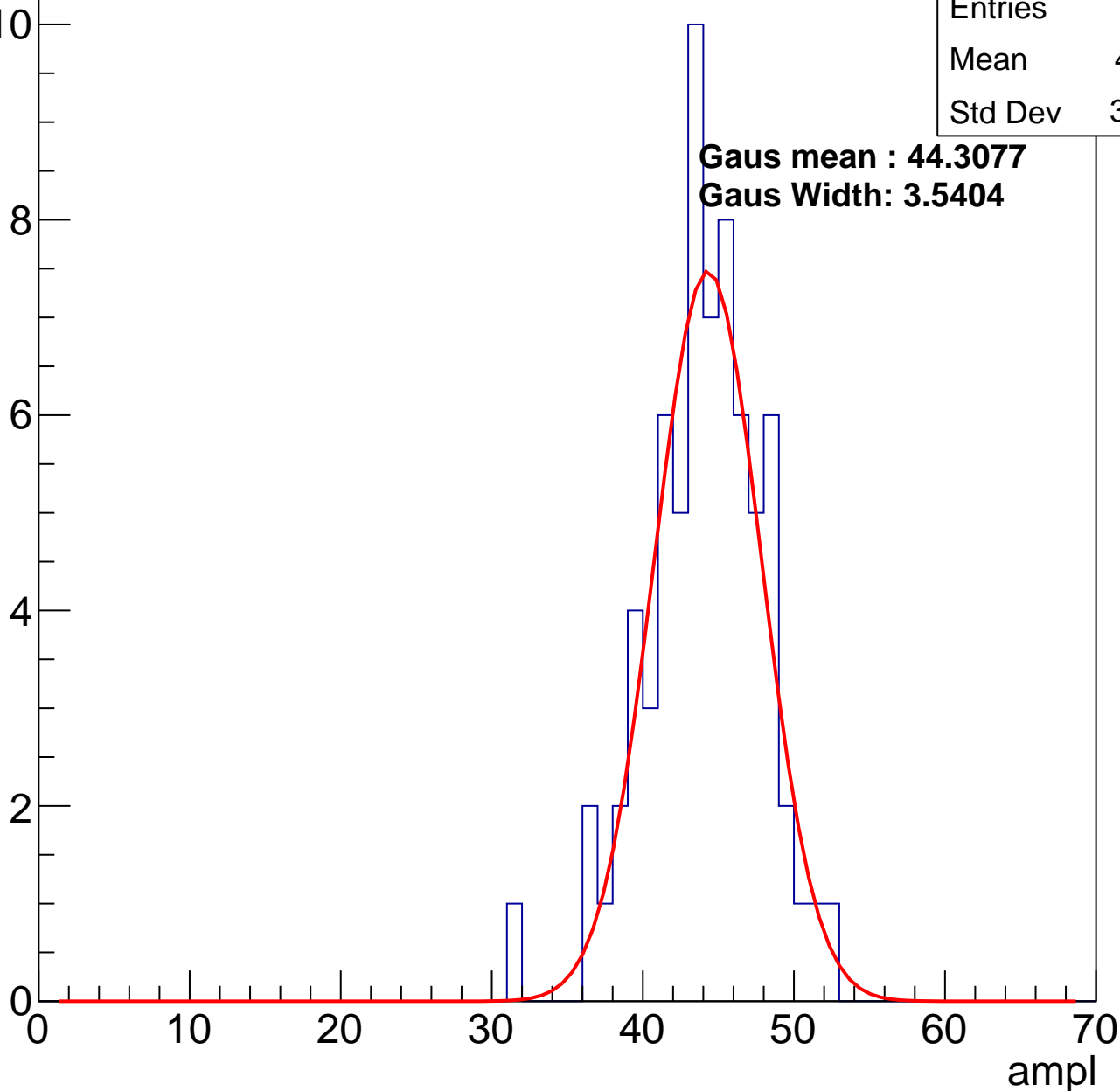
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 43.61 |
| Std Dev | 3.773 |

Gaus mean : 44.3077

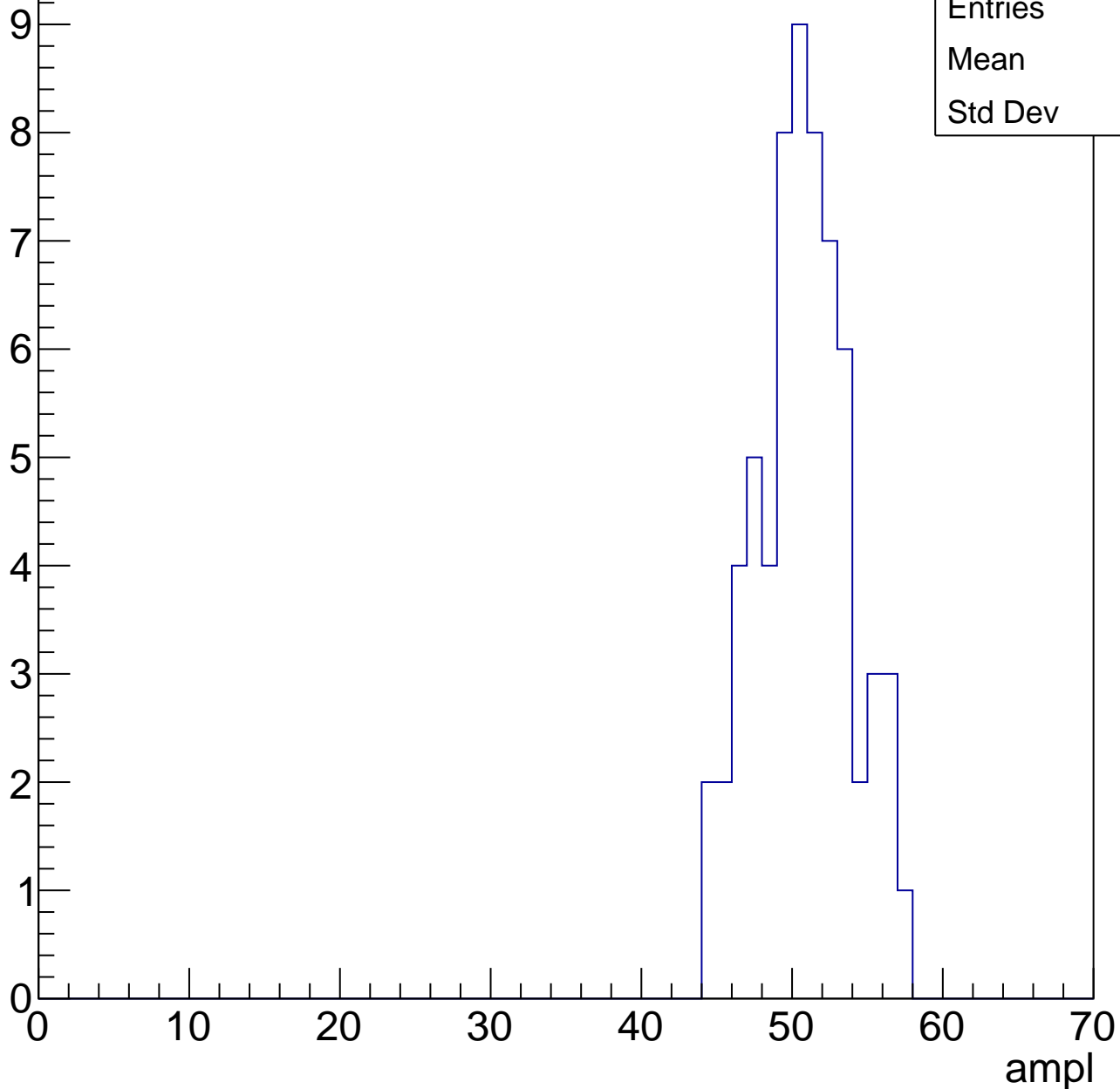
Gaus Width: 3.5404



B0L102S, U20-ch51, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

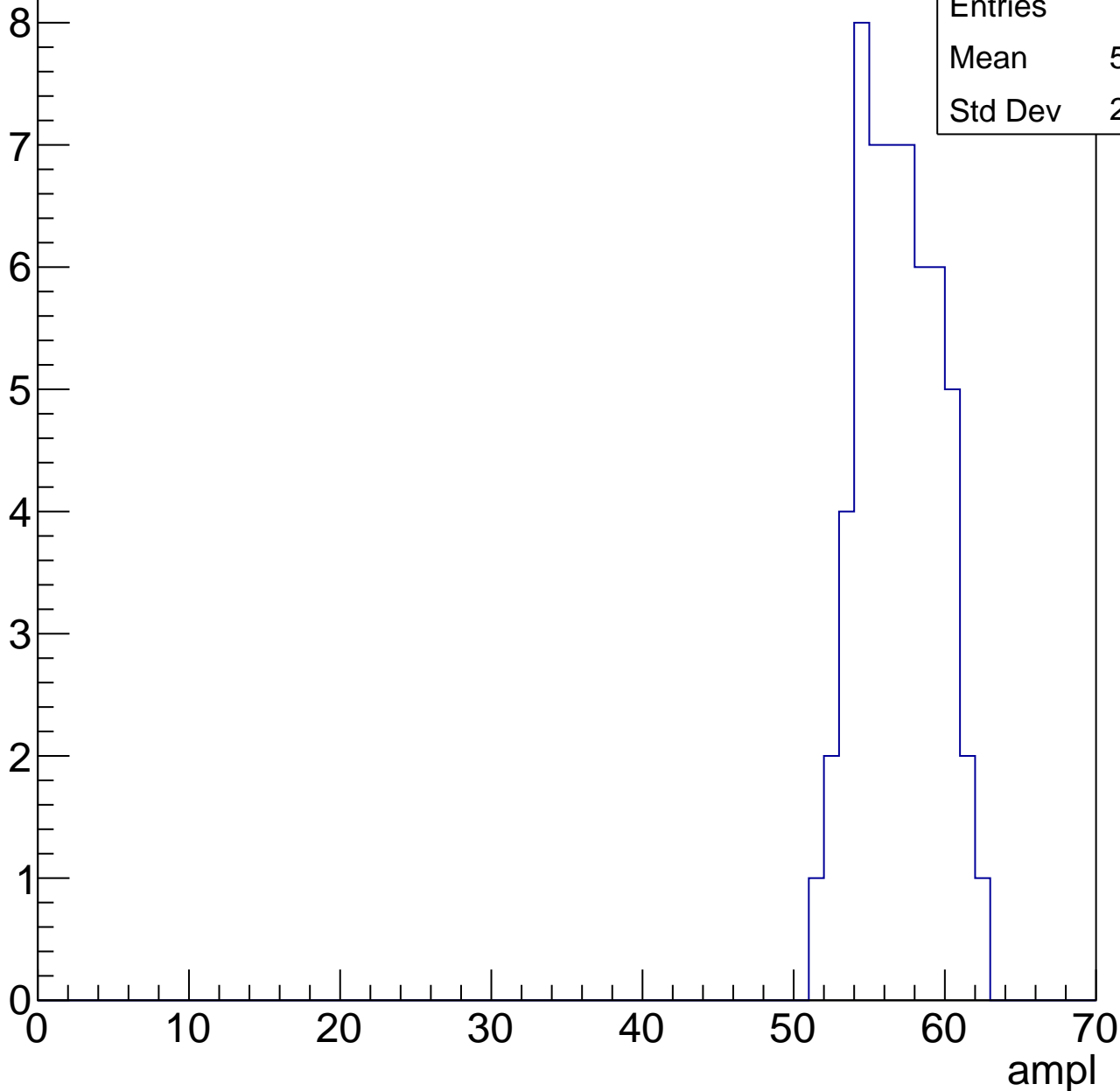


B0L102S, U20-ch51, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

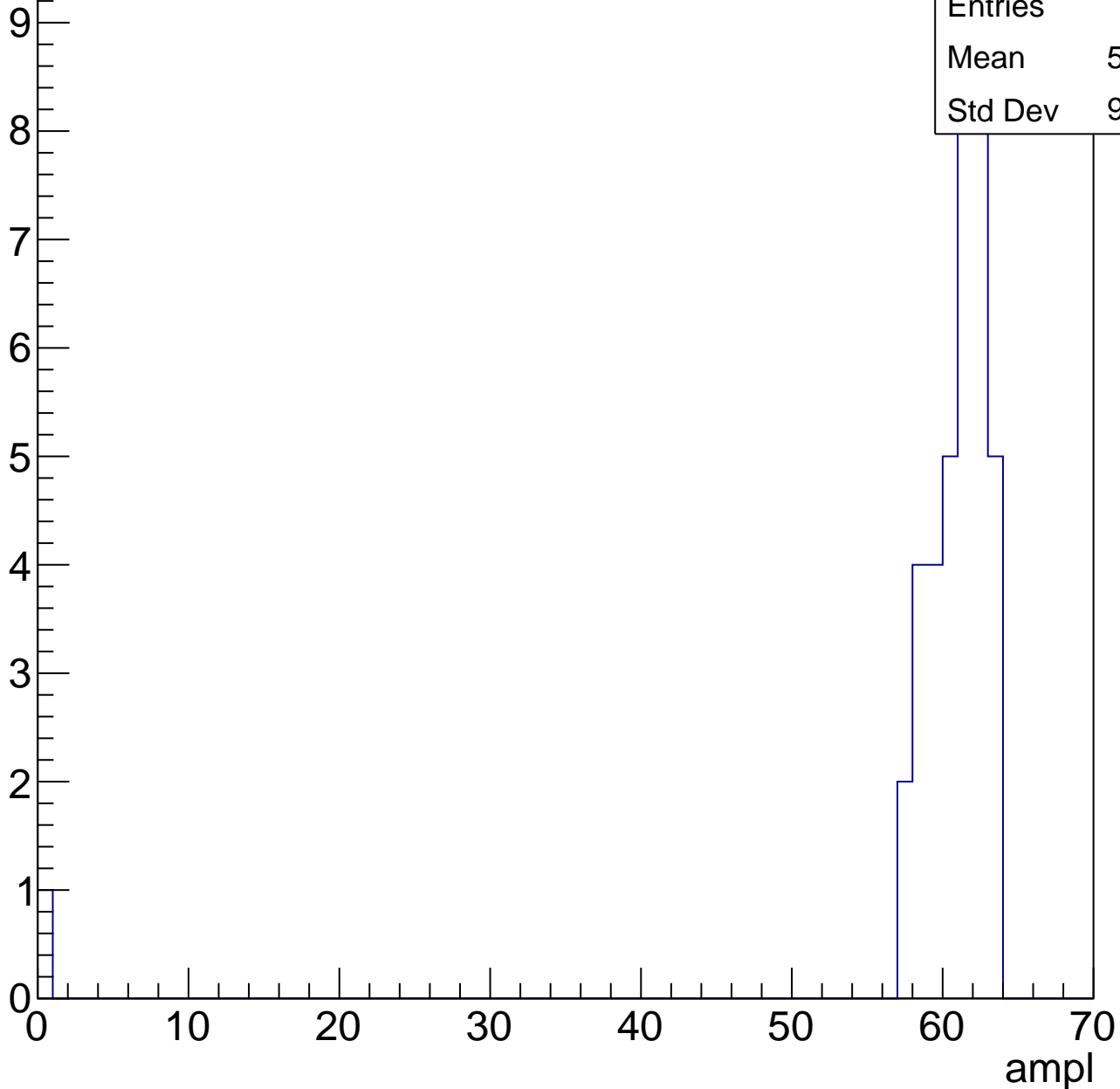
| | |
|---------|-------|
| Entries | 56 |
| Mean | 56.45 |
| Std Dev | 2.577 |



B0L102S, U20-ch51, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch51, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch51, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch52, adc0

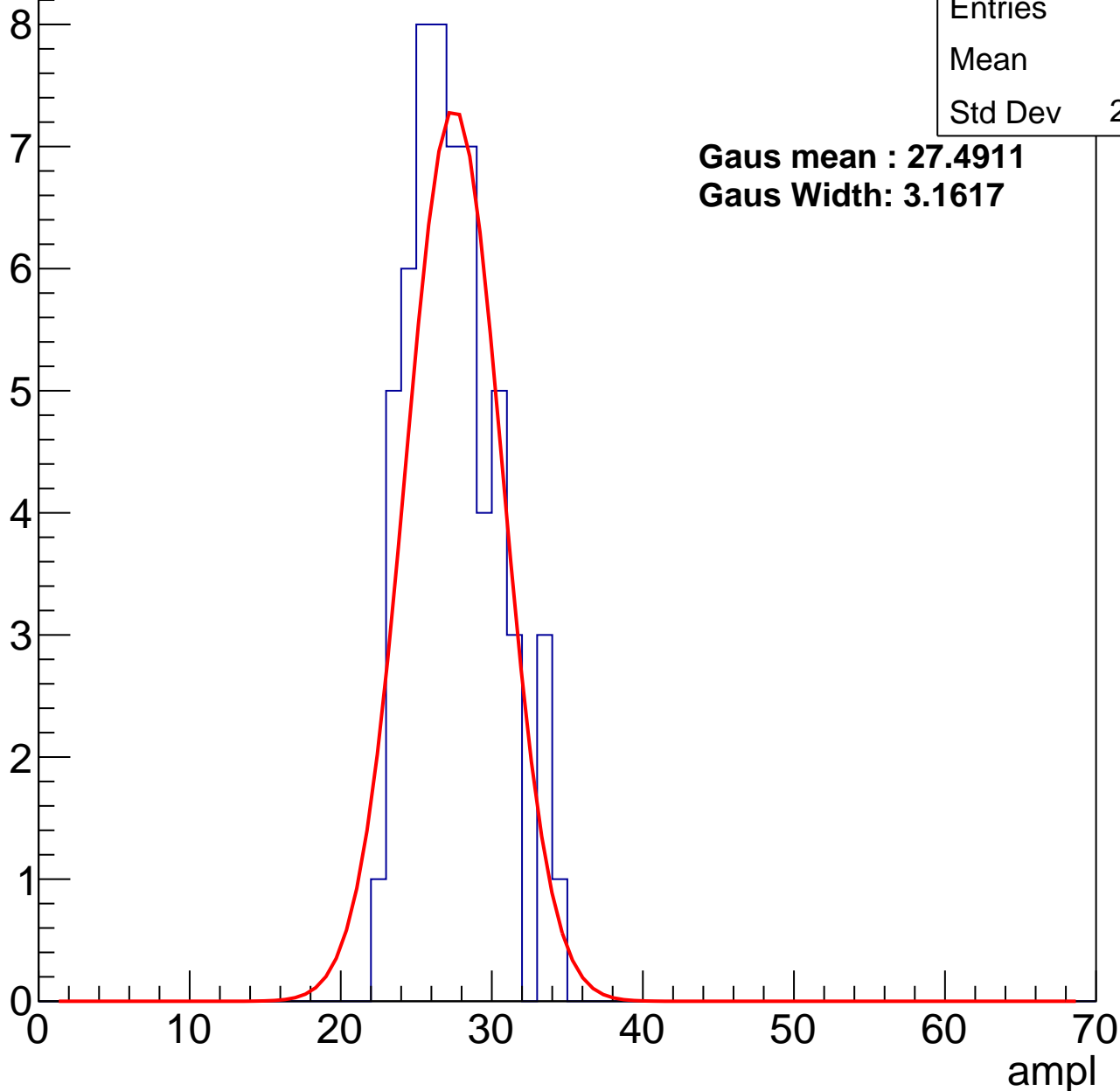
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 27 |
| Std Dev | 2.853 |

Gaus mean : 27.4911

Gaus Width: 3.1617



B0L102S, U20-ch52, adc1

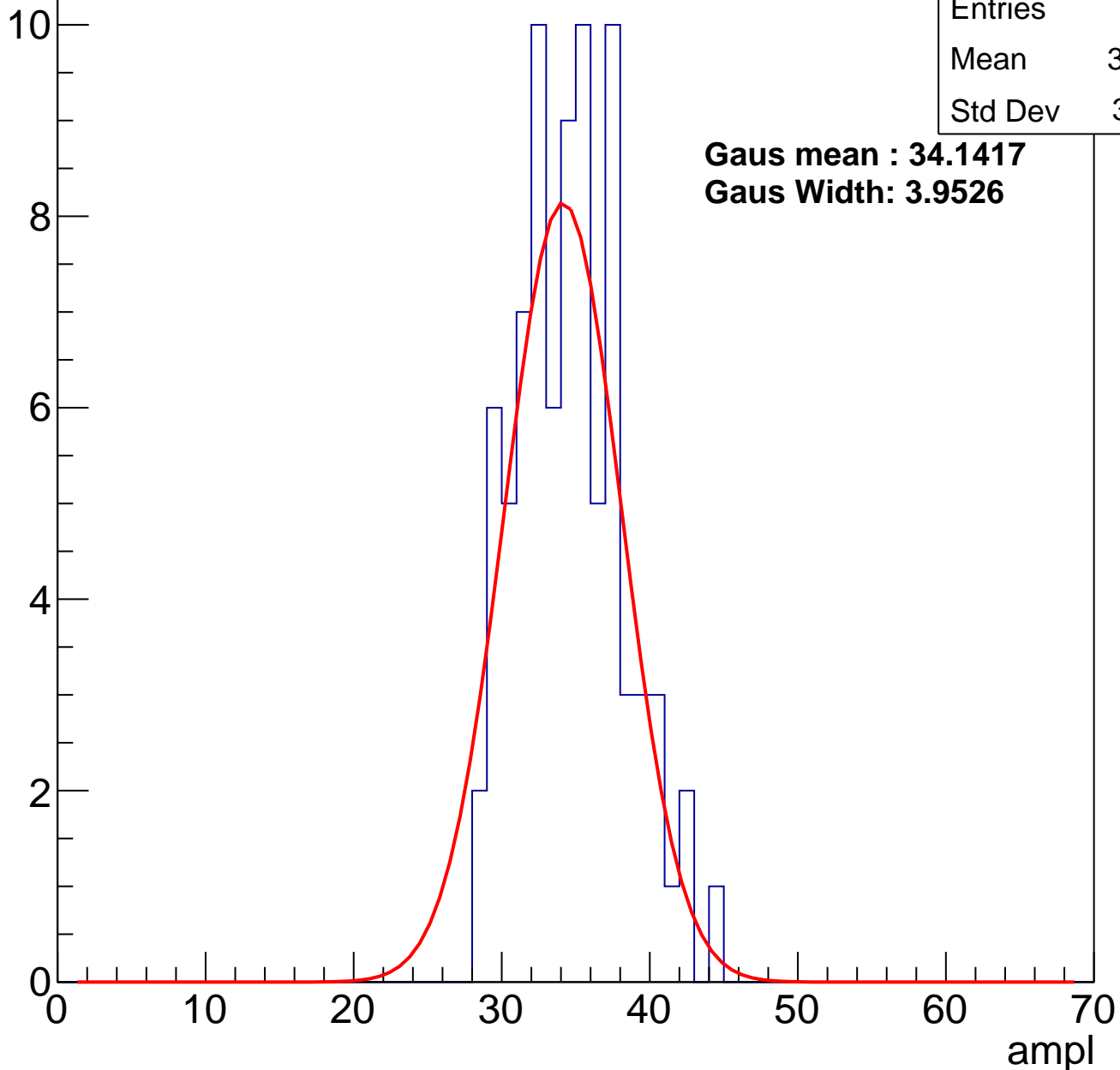
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 83 |
| Mean | 34.23 |
| Std Dev | 3.531 |

Gaus mean : 34.1417

Gaus Width: 3.9526

Entry



B0L102S, U20-ch52, adc2

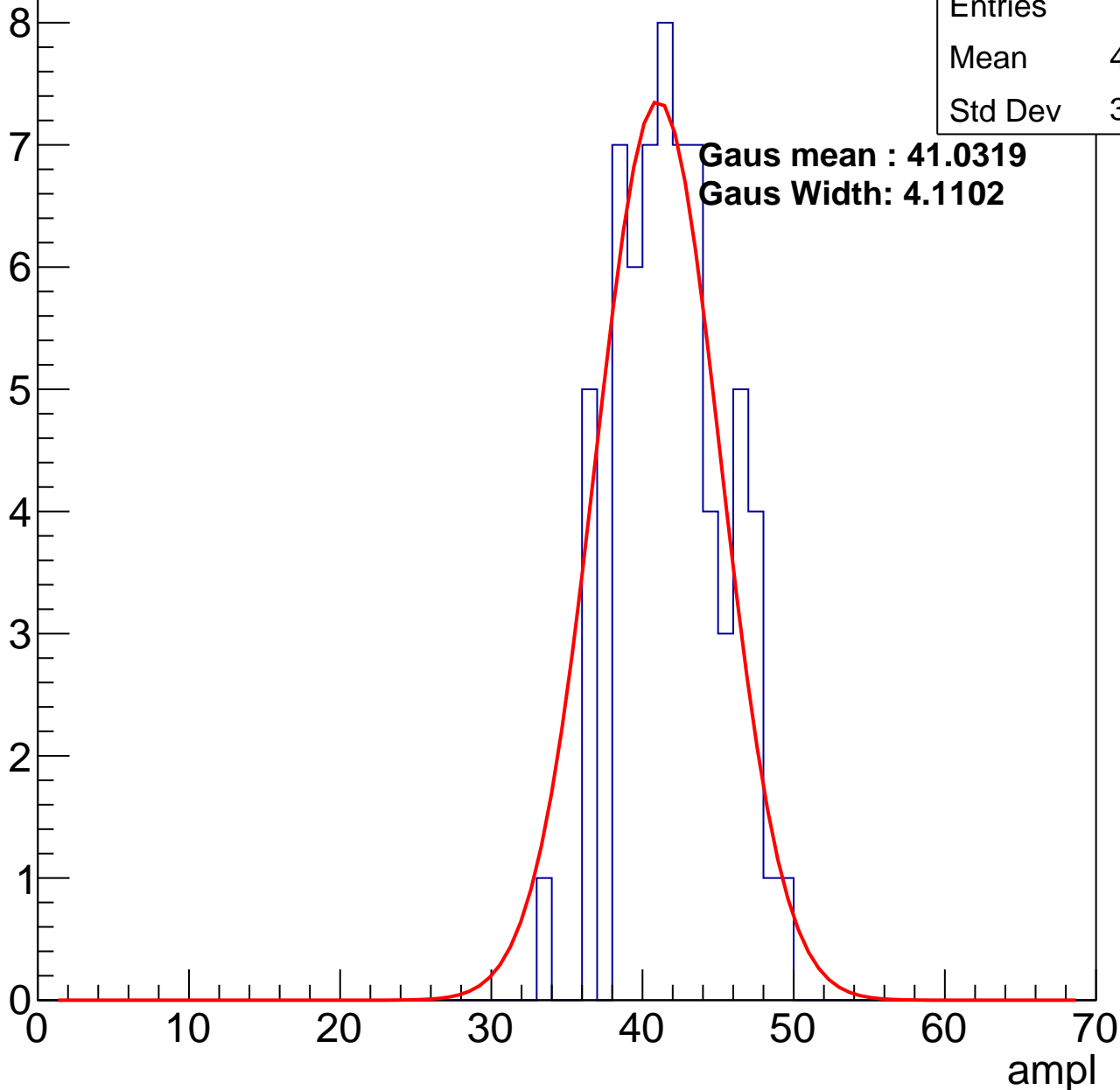
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 41.55 |
| Std Dev | 3.403 |

Gaus mean : 41.0319

Gaus Width: 4.1102

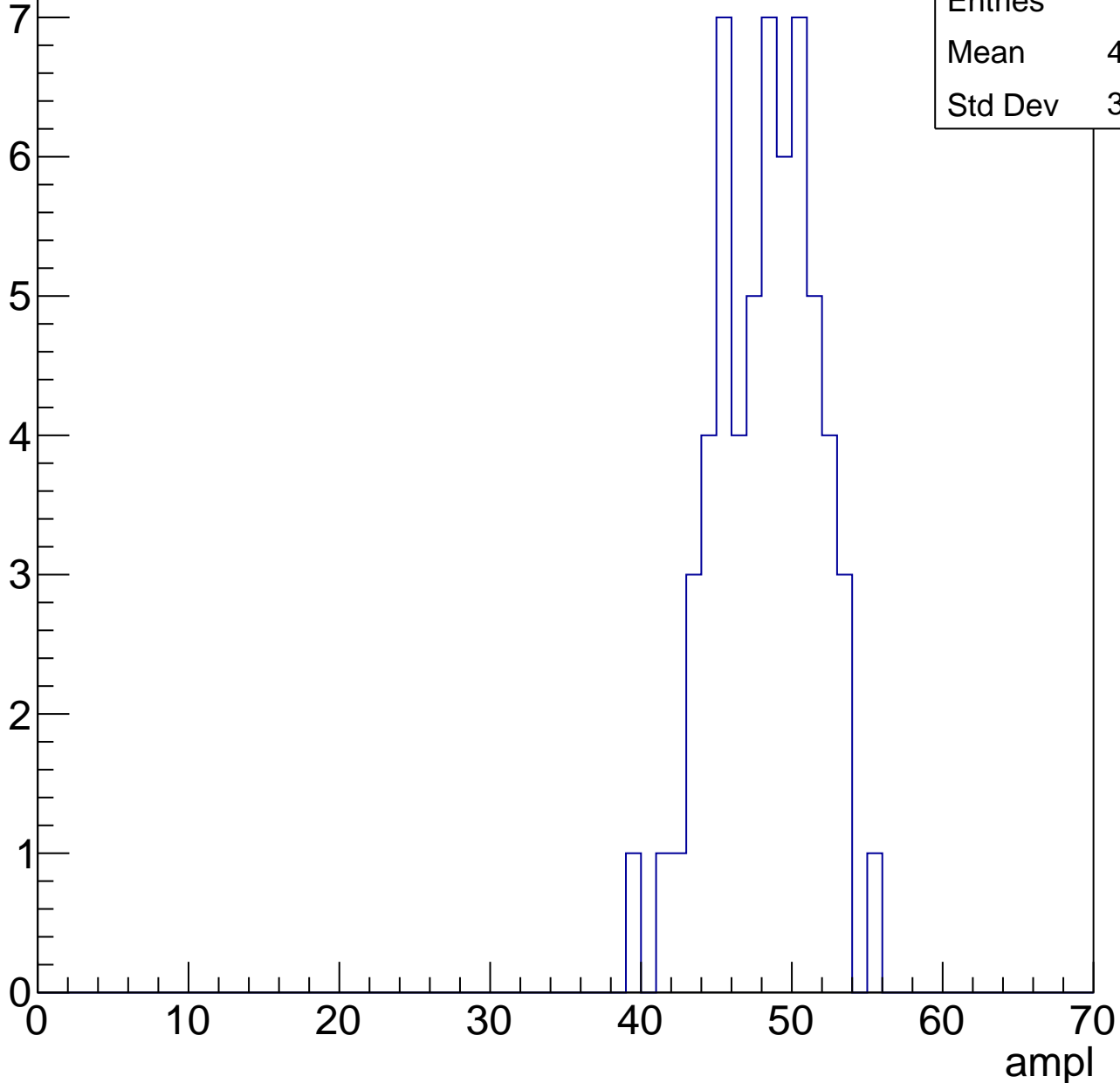


B0L102S, U20-ch52, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 47.76 |
| Std Dev | 3.326 |

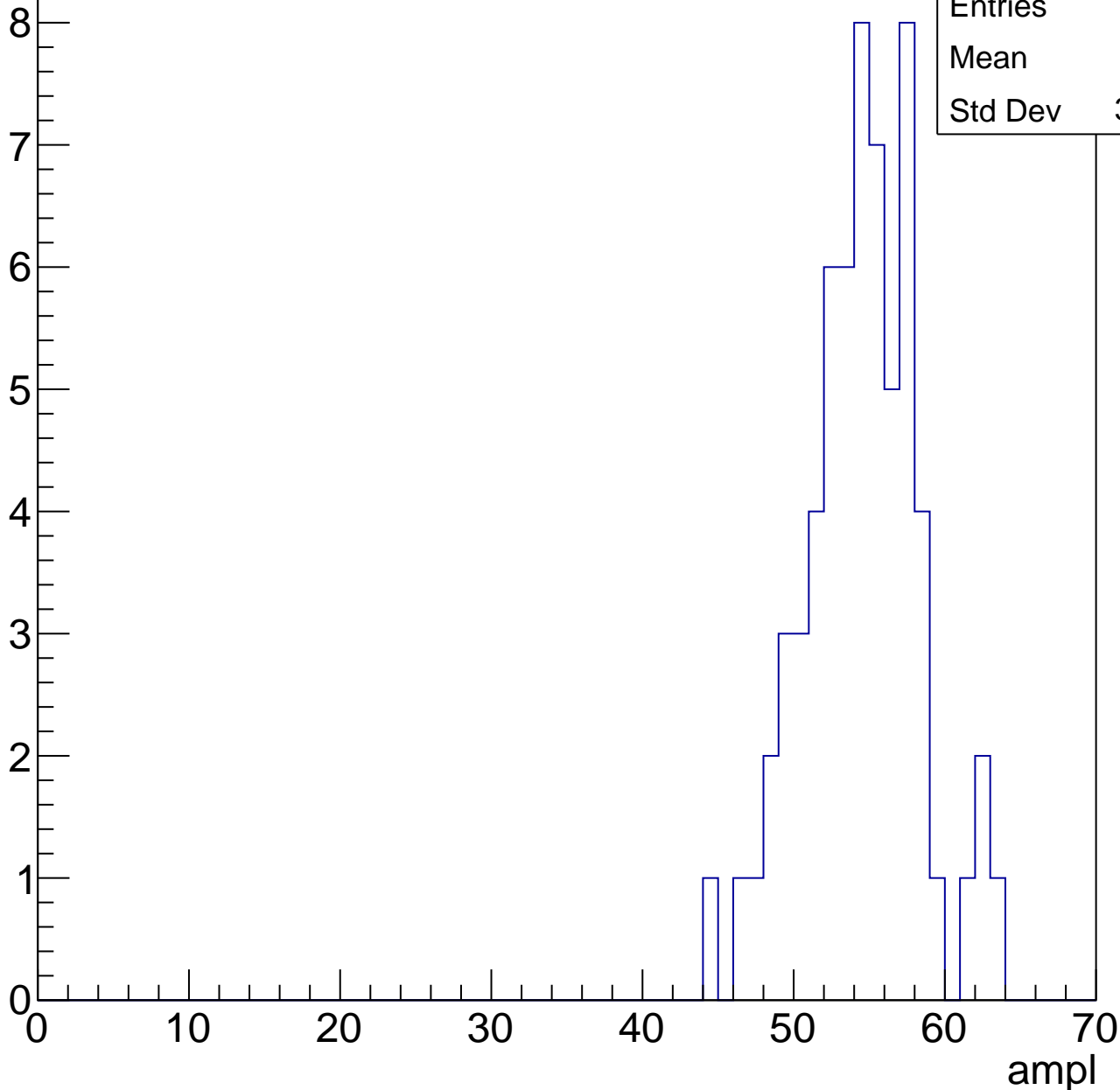


B0L102S, U20-ch52, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 54 |
| Std Dev | 3.791 |

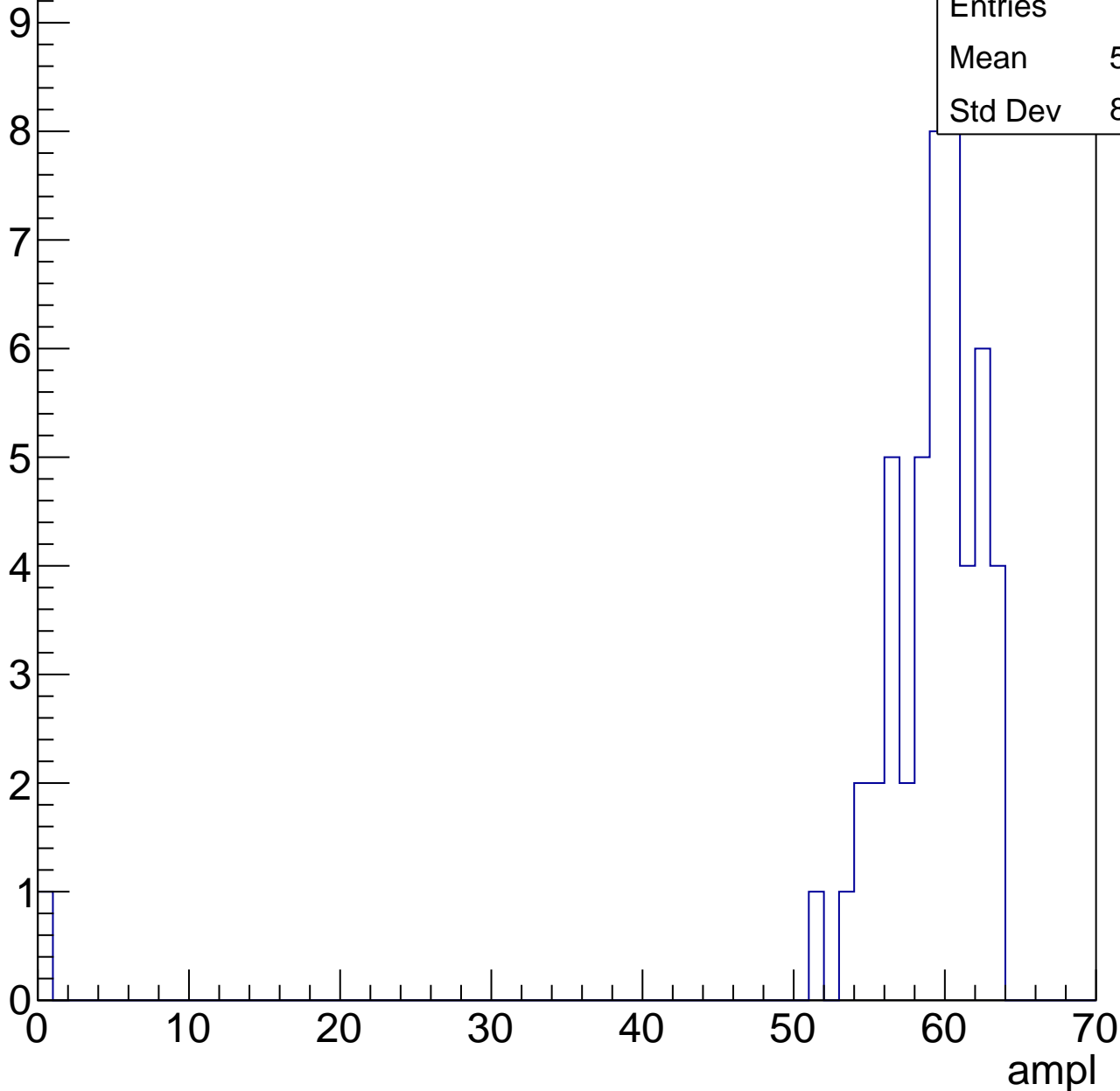


B0L102S, U20-ch52, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 57.72 |
| Std Dev | 8.697 |

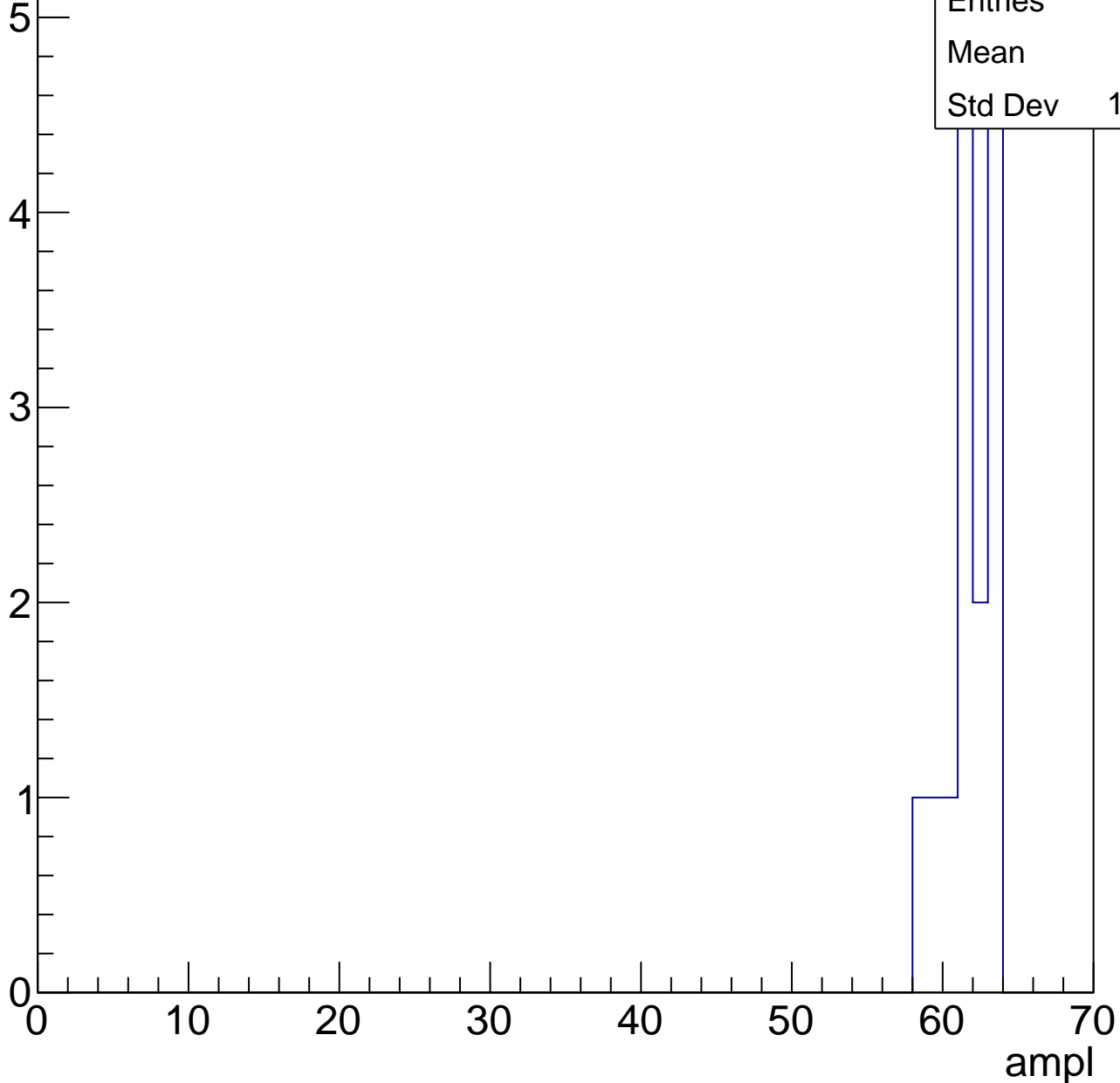


B0L102S, U20-ch52, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.4 |
| Std Dev | 1.497 |



B0L102S, U20-ch52, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

B0L102S, U20-ch53, adc0

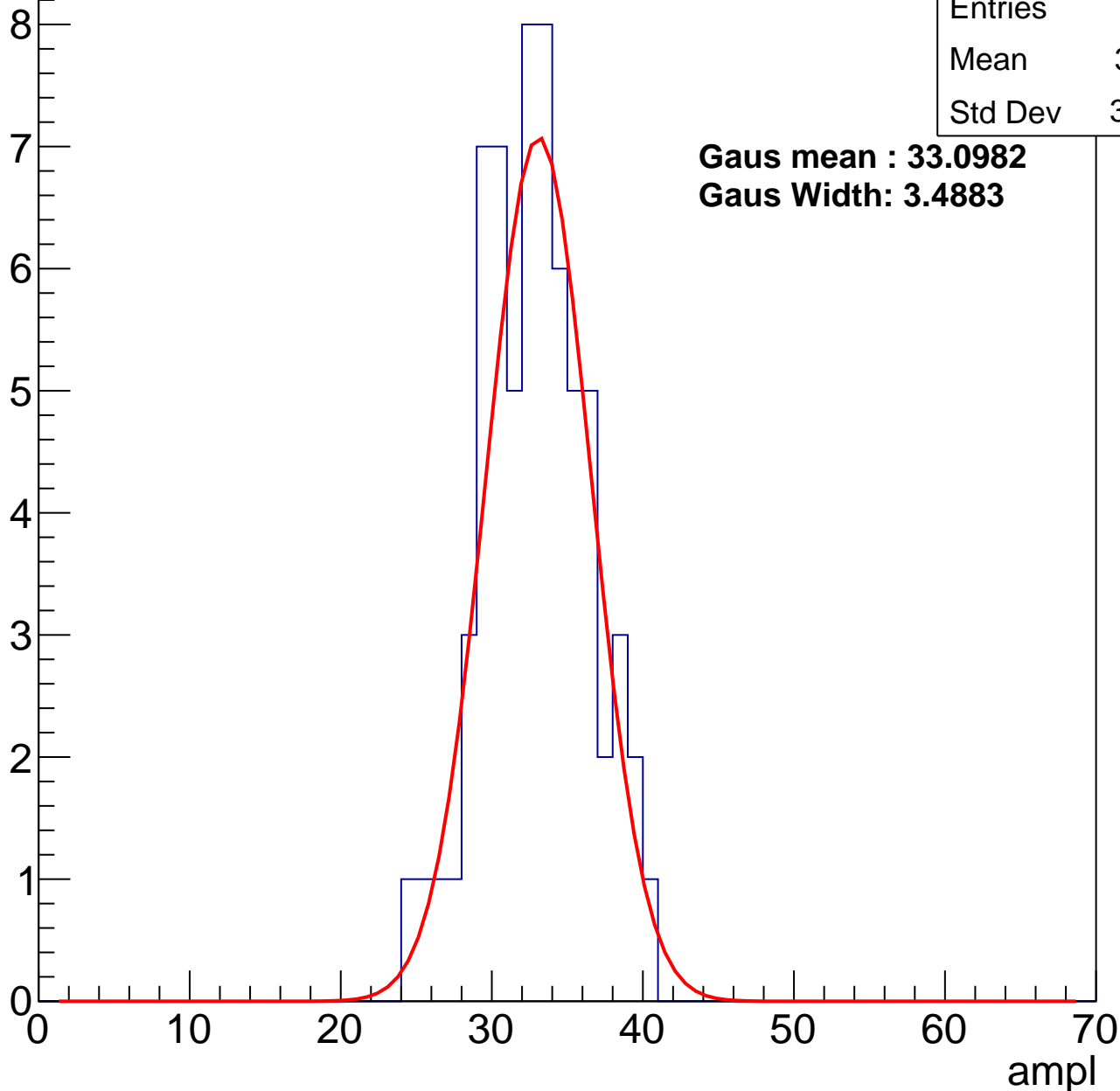
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 32.41 |
| Std Dev | 3.446 |

Gaus mean : 33.0982

Gaus Width: 3.4883



B0L102S, U20-ch53, adc1

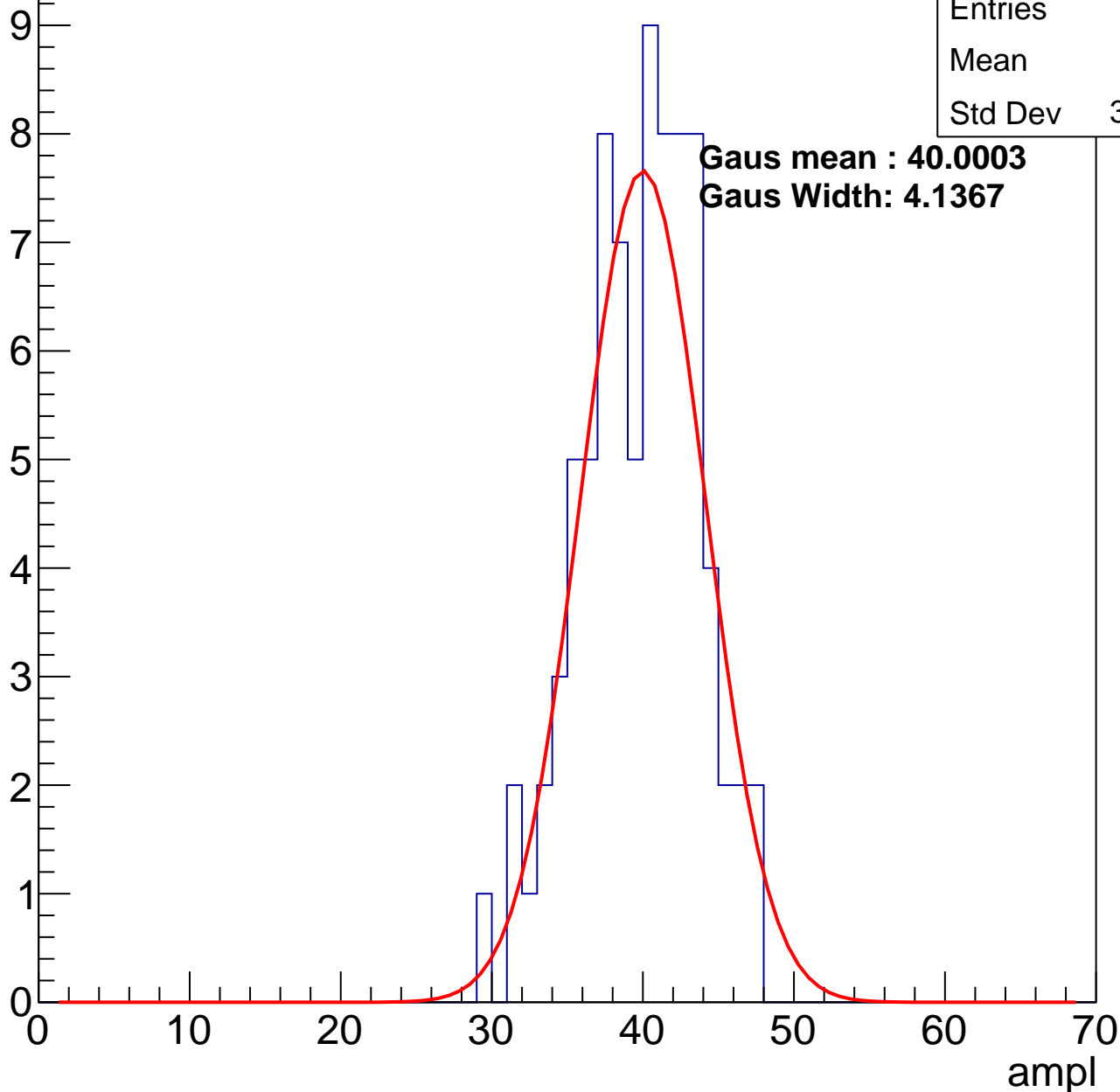
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 39.3 |
| Std Dev | 3.856 |

Gaus mean : 40.0003

Gaus Width: 4.1367



B0L102S, U20-ch53, adc2

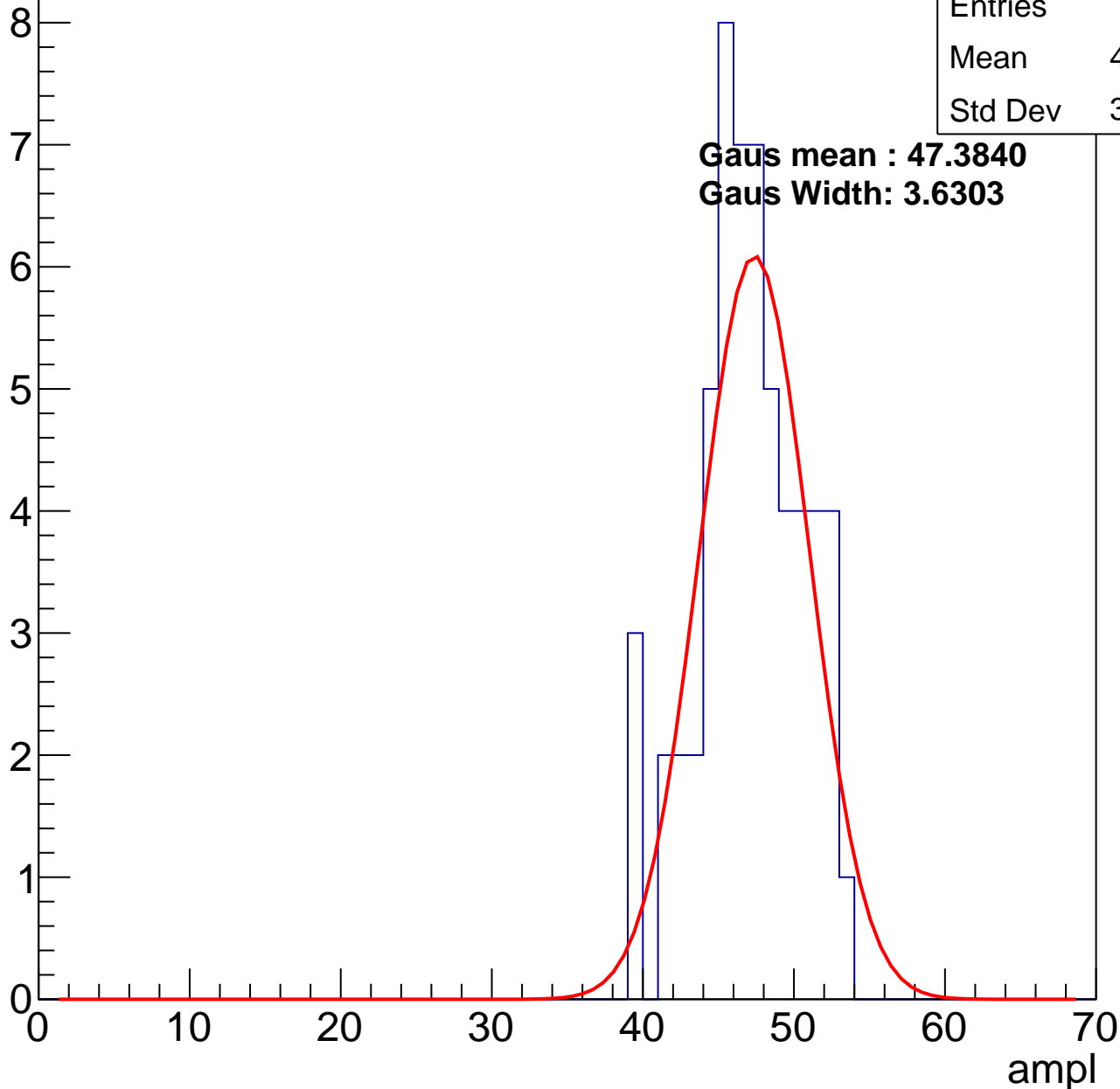
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 46.57 |
| Std Dev | 3.415 |

Gaus mean : 47.3840

Gaus Width: 3.6303

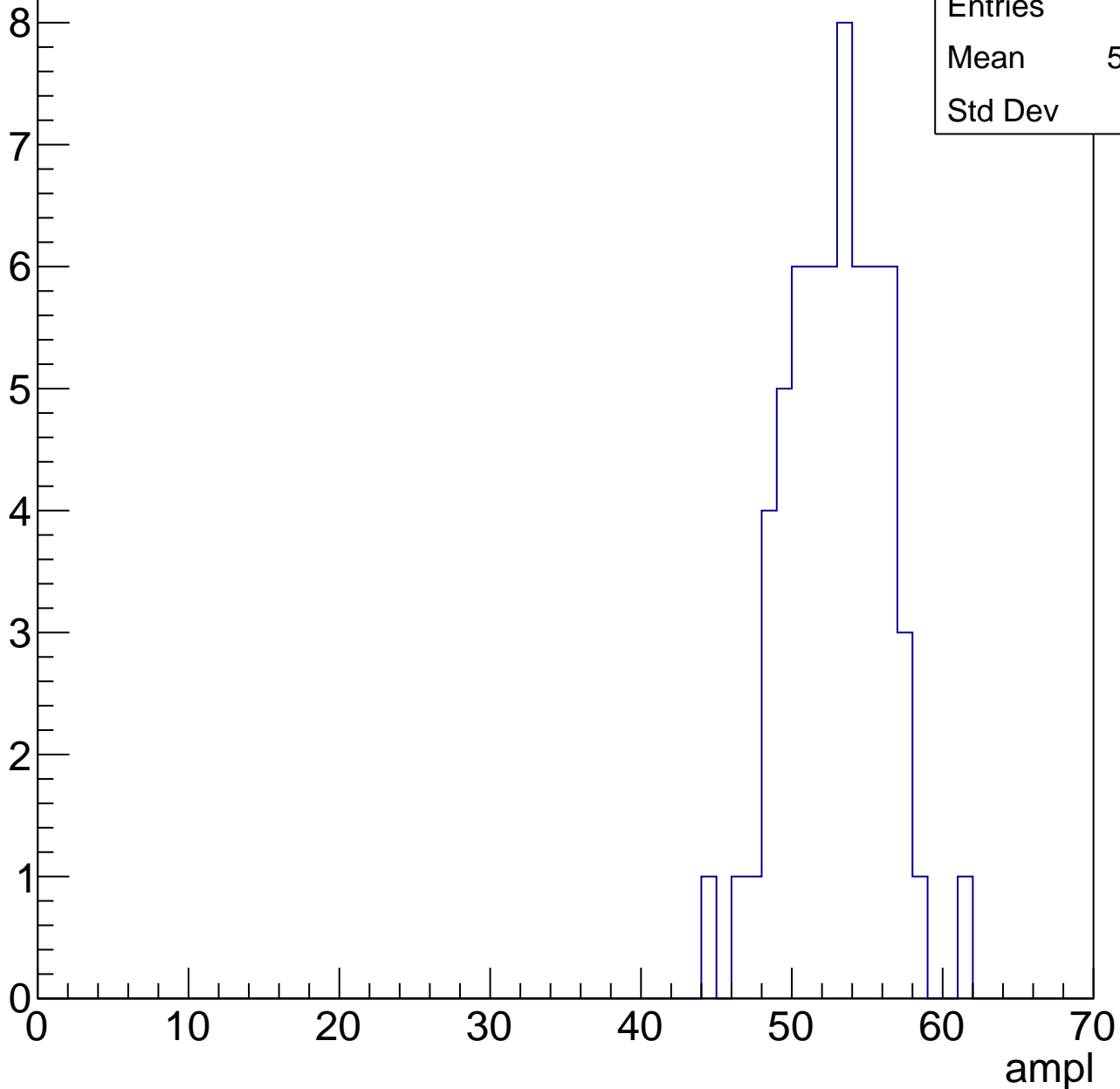


B0L102S, U20-ch53, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 52.39 |
| Std Dev | 3.21 |

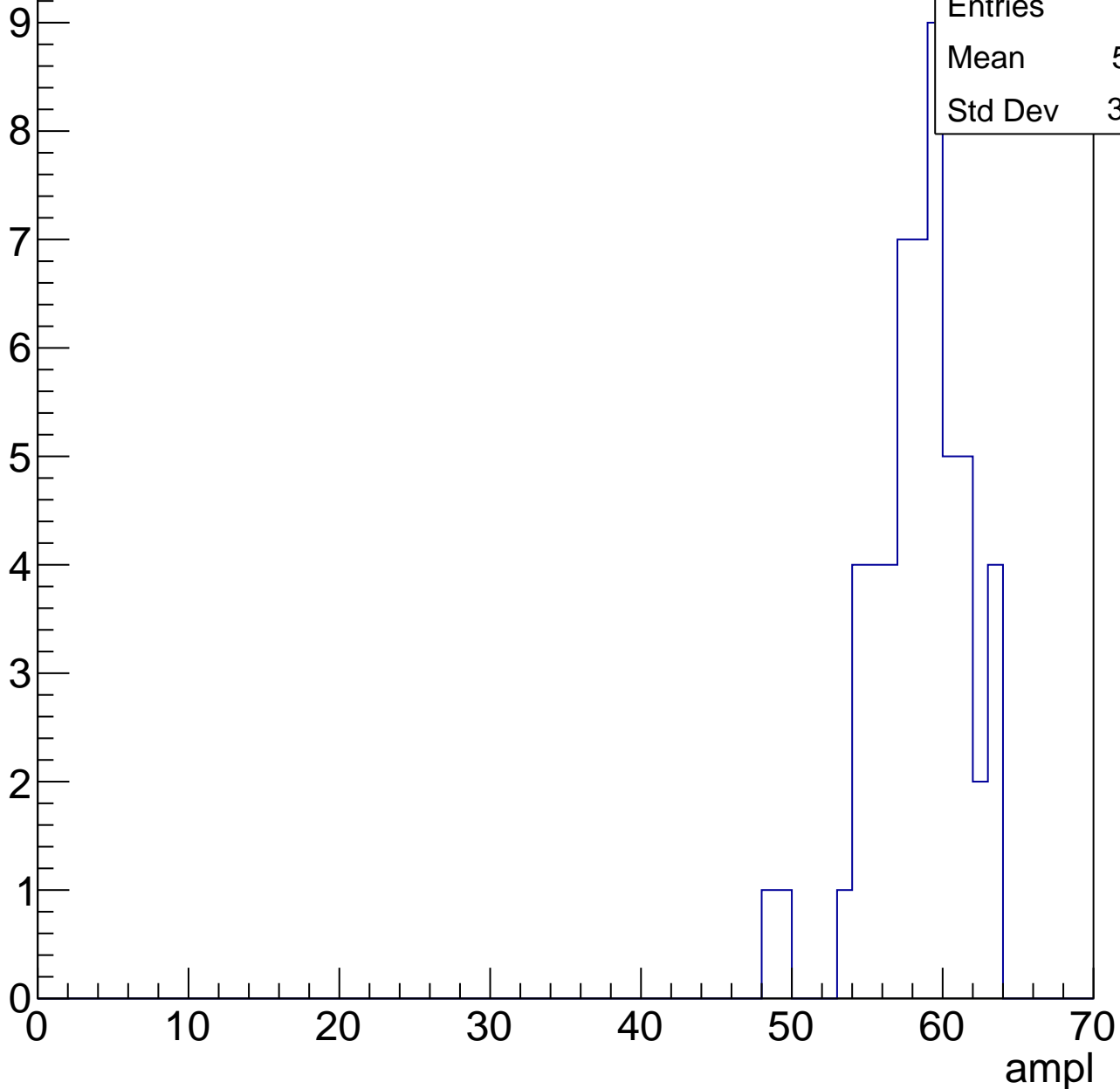


B0L102S, U20-ch53, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 57.91 |
| Std Dev | 3.146 |

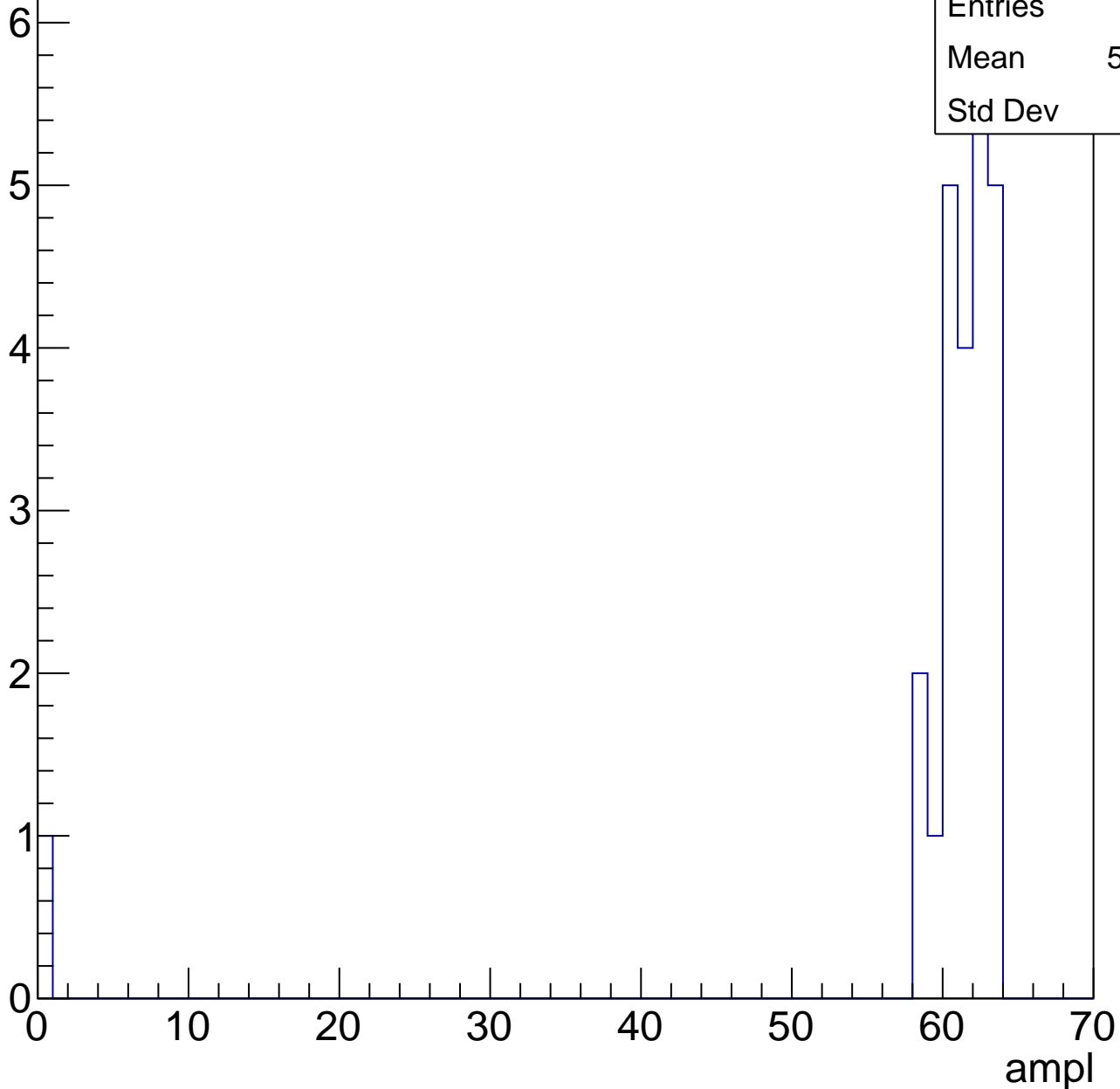


B0L102S, U20-ch53, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.58 |
| Std Dev | 12.3 |



B0L102S, U20-ch53, adc6

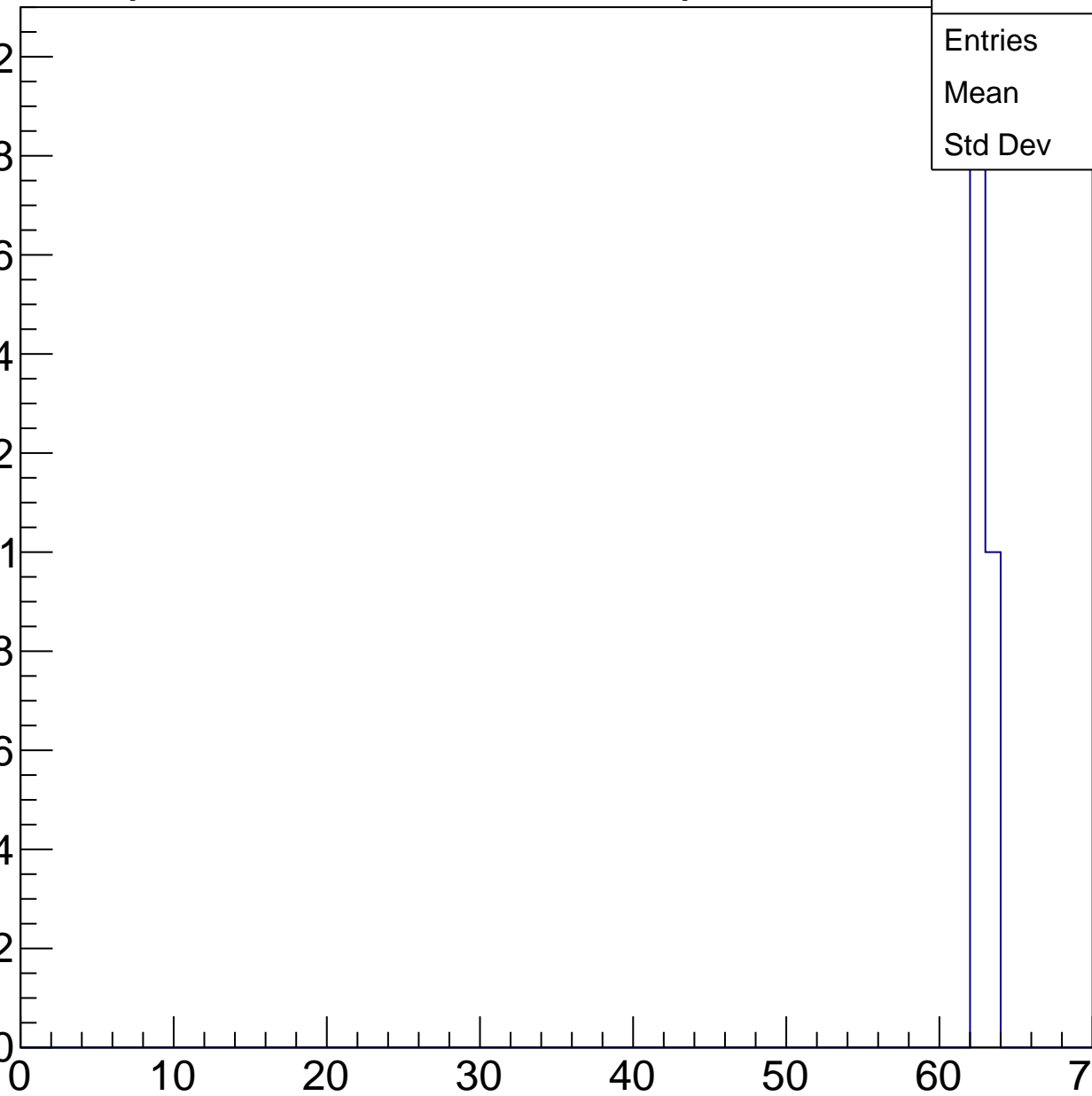
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.33 |
| Std Dev | 0.4714 |

ampl



B0L102S, U20-ch53, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch54, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 67 |
| Mean | 26.96 |
| Std Dev | 4.556 |

Gaus mean : 27.6480

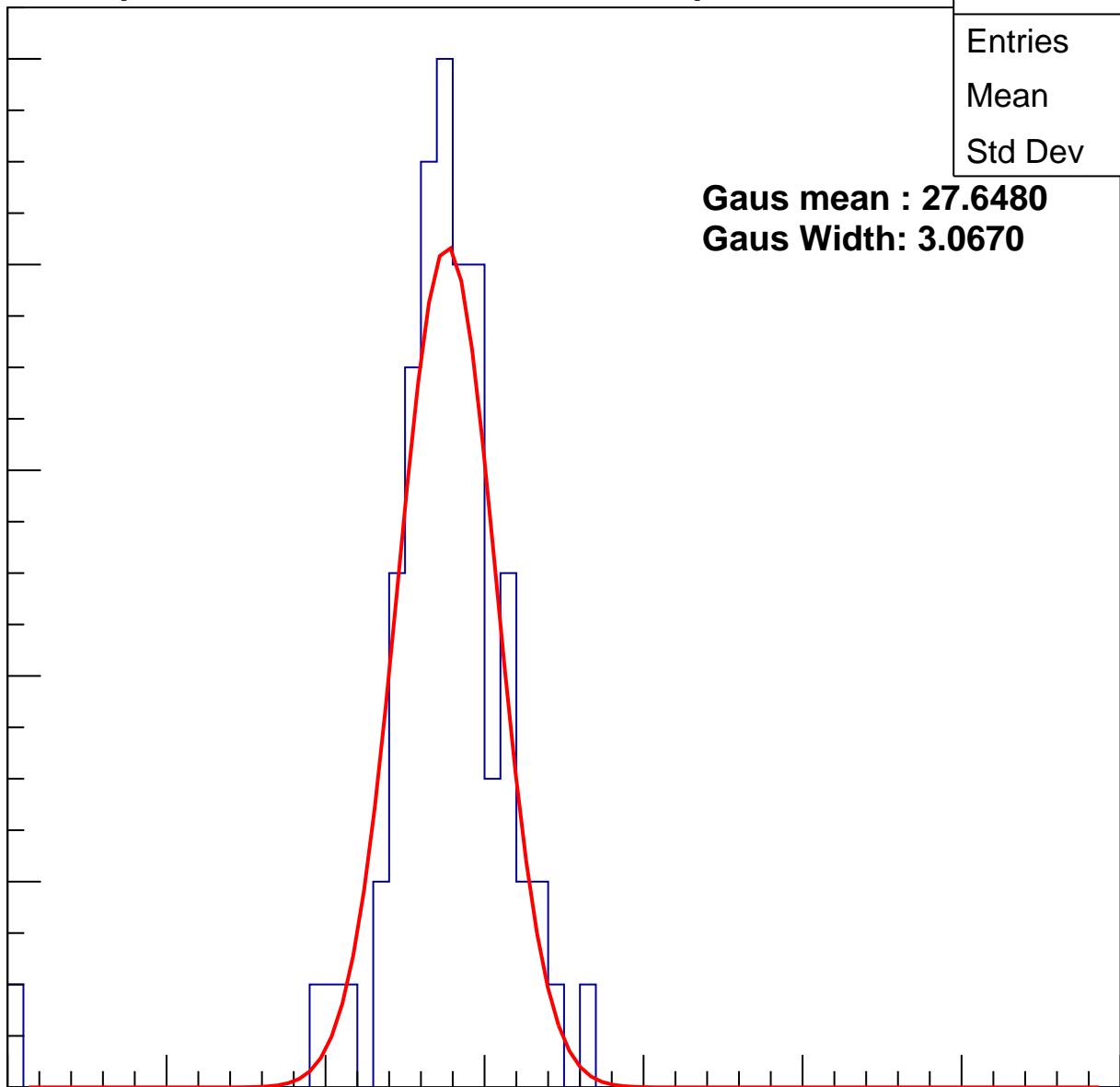
Gaus Width: 3.0670

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch54, adc1

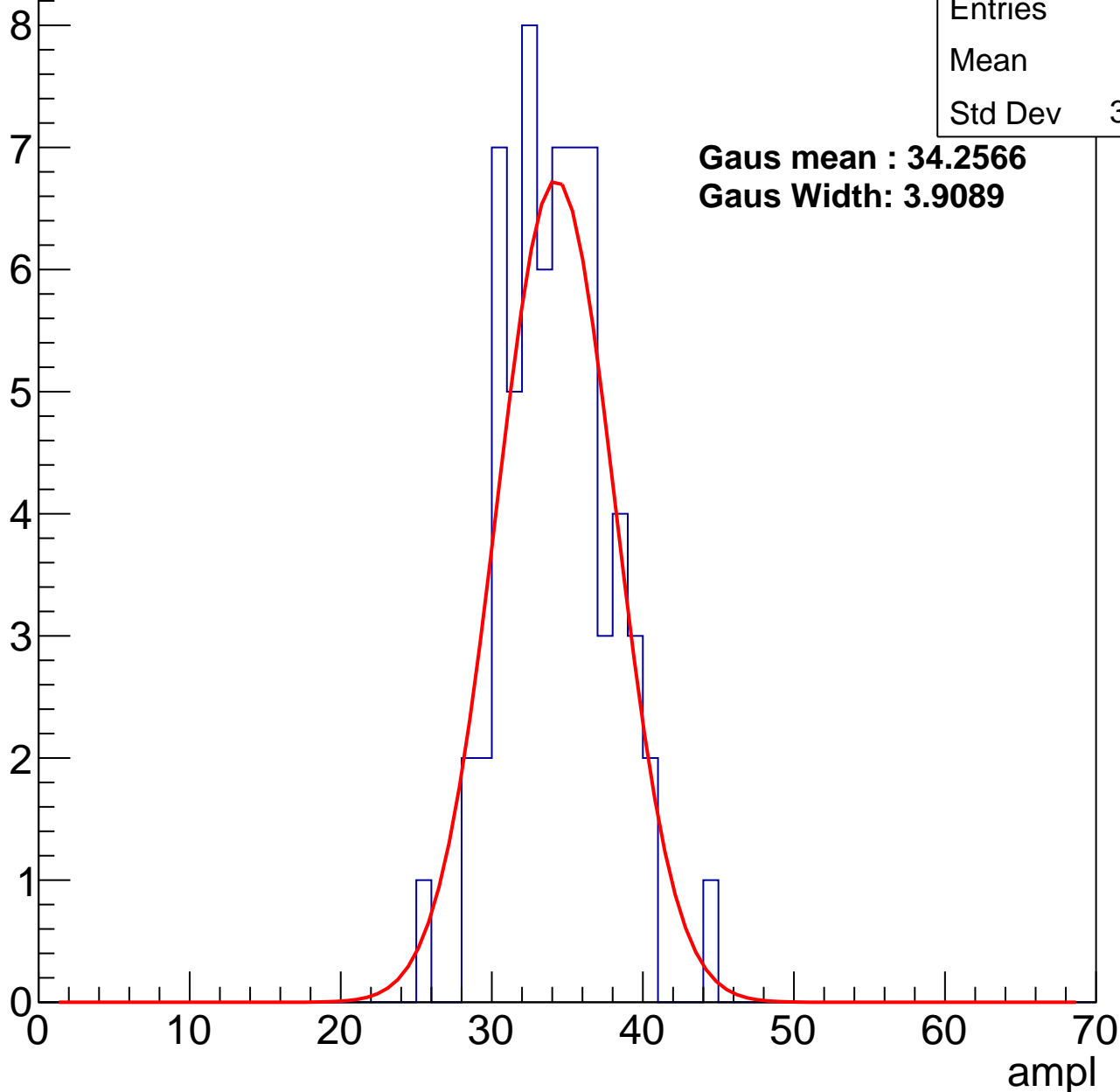
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 33.8 |
| Std Dev | 3.438 |

Gaus mean : 34.2566

Gaus Width: 3.9089



B0L102S, U20-ch54, adc2

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 61 |
| Mean | 40.21 |
| Std Dev | 3.595 |

Gaus mean : 41.2873

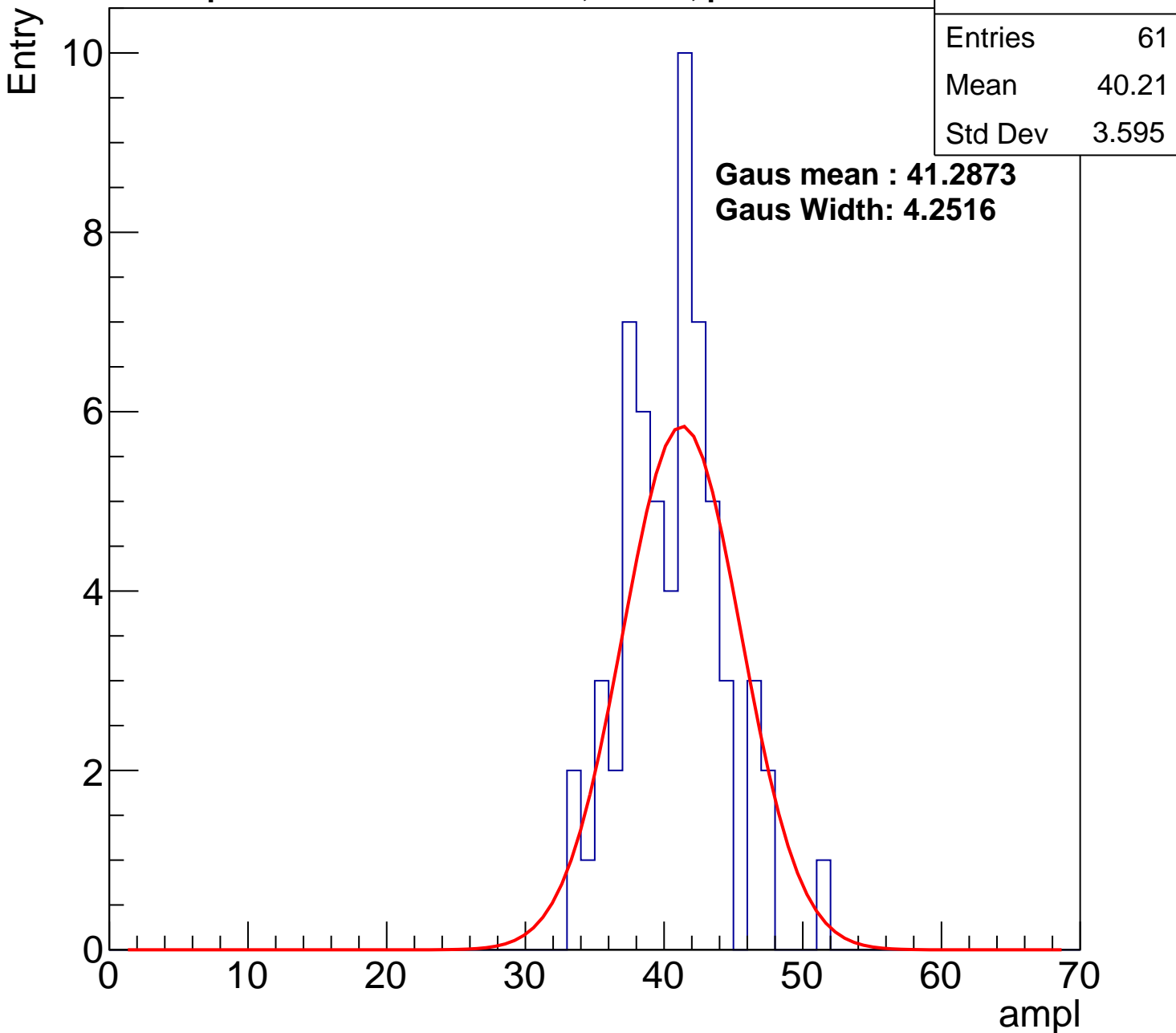
Gaus Width: 4.2516

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch54, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 46.37 |
| Std Dev | 3.662 |

Entry

10

8

6

4

2

0

0

10

20

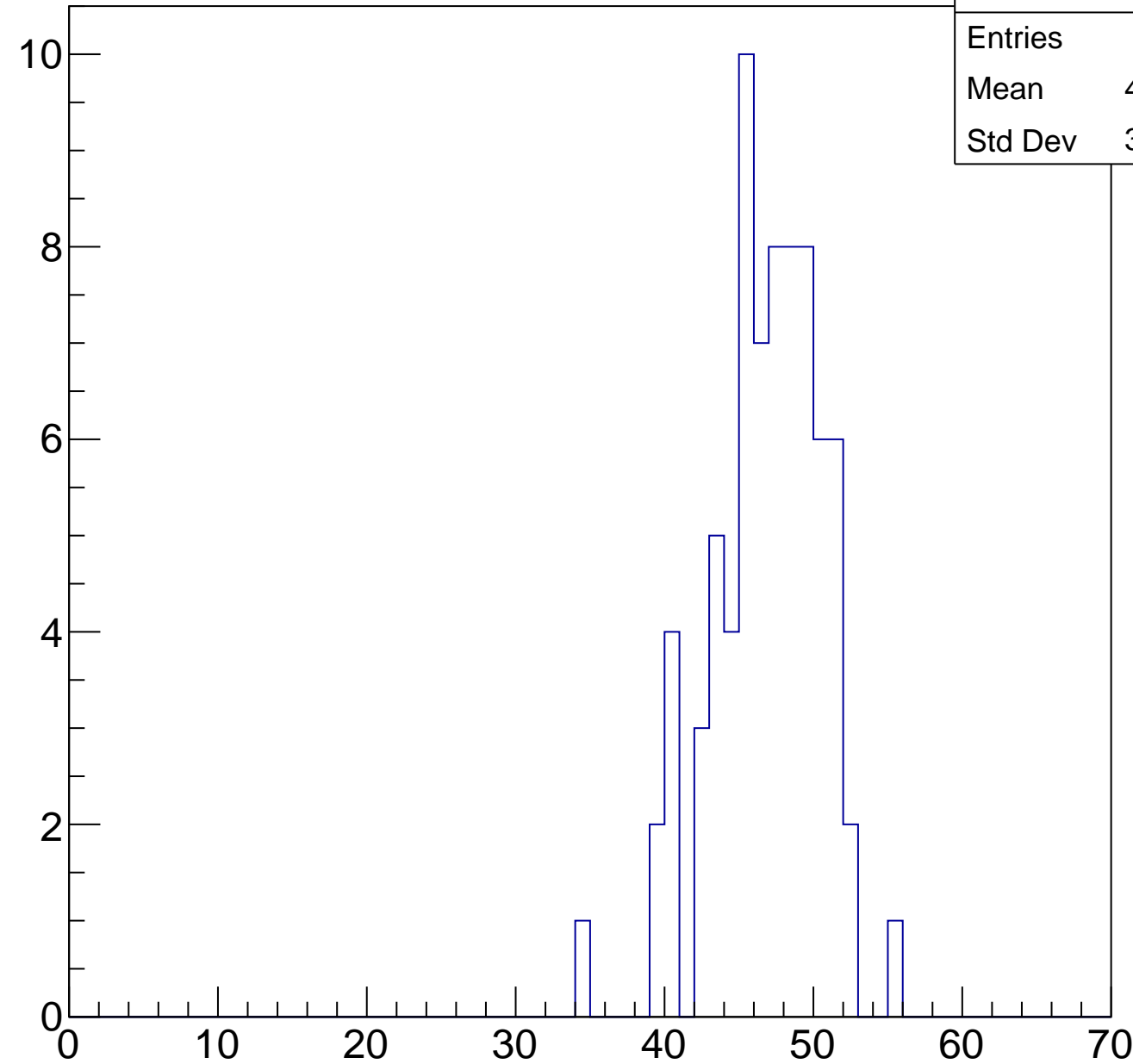
30

40

50

60

ampl

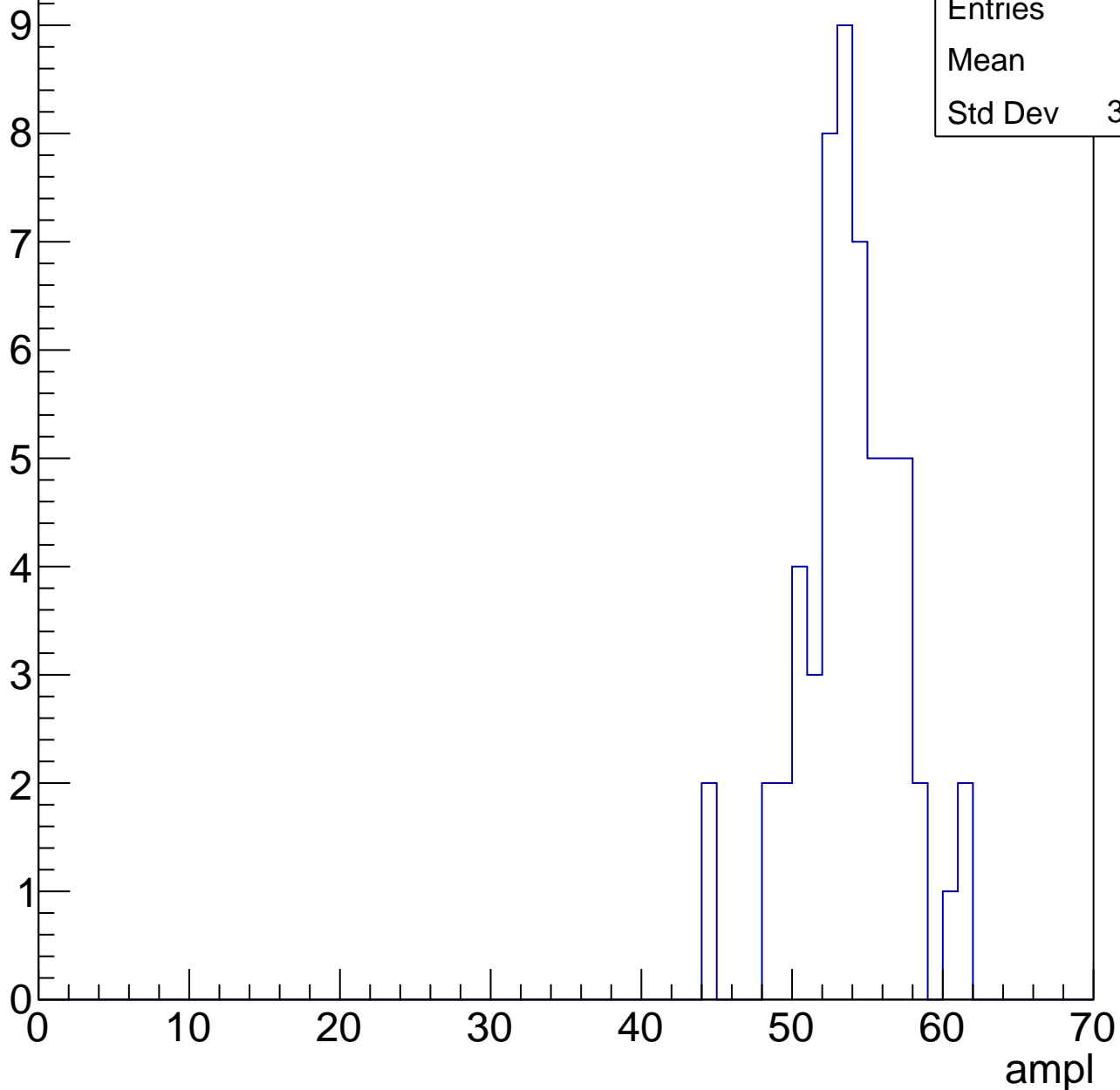


B0L102S, U20-ch54, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 53.4 |
| Std Dev | 3.428 |

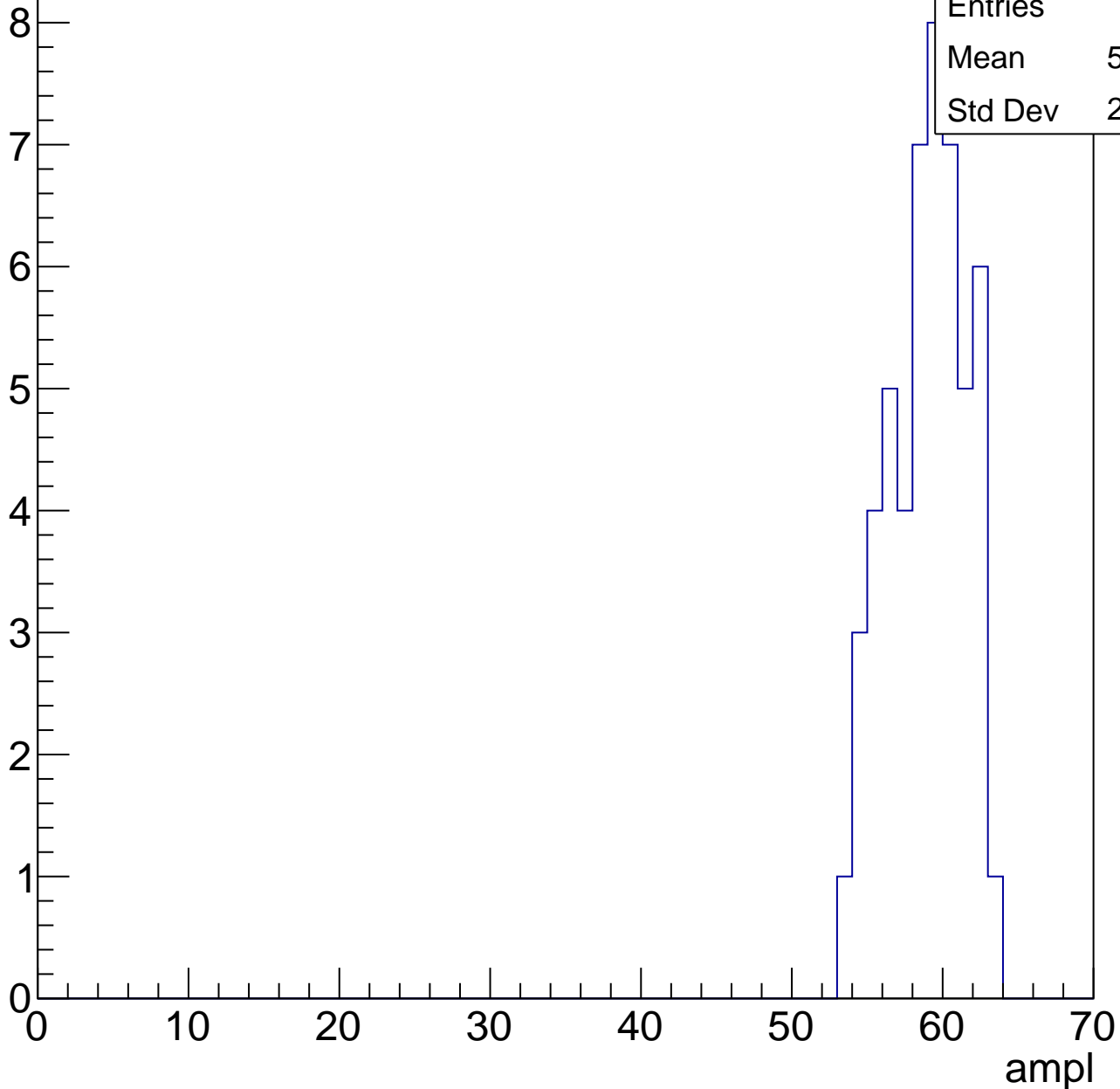


B0L102S, U20-ch54, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.45 |
| Std Dev | 2.523 |

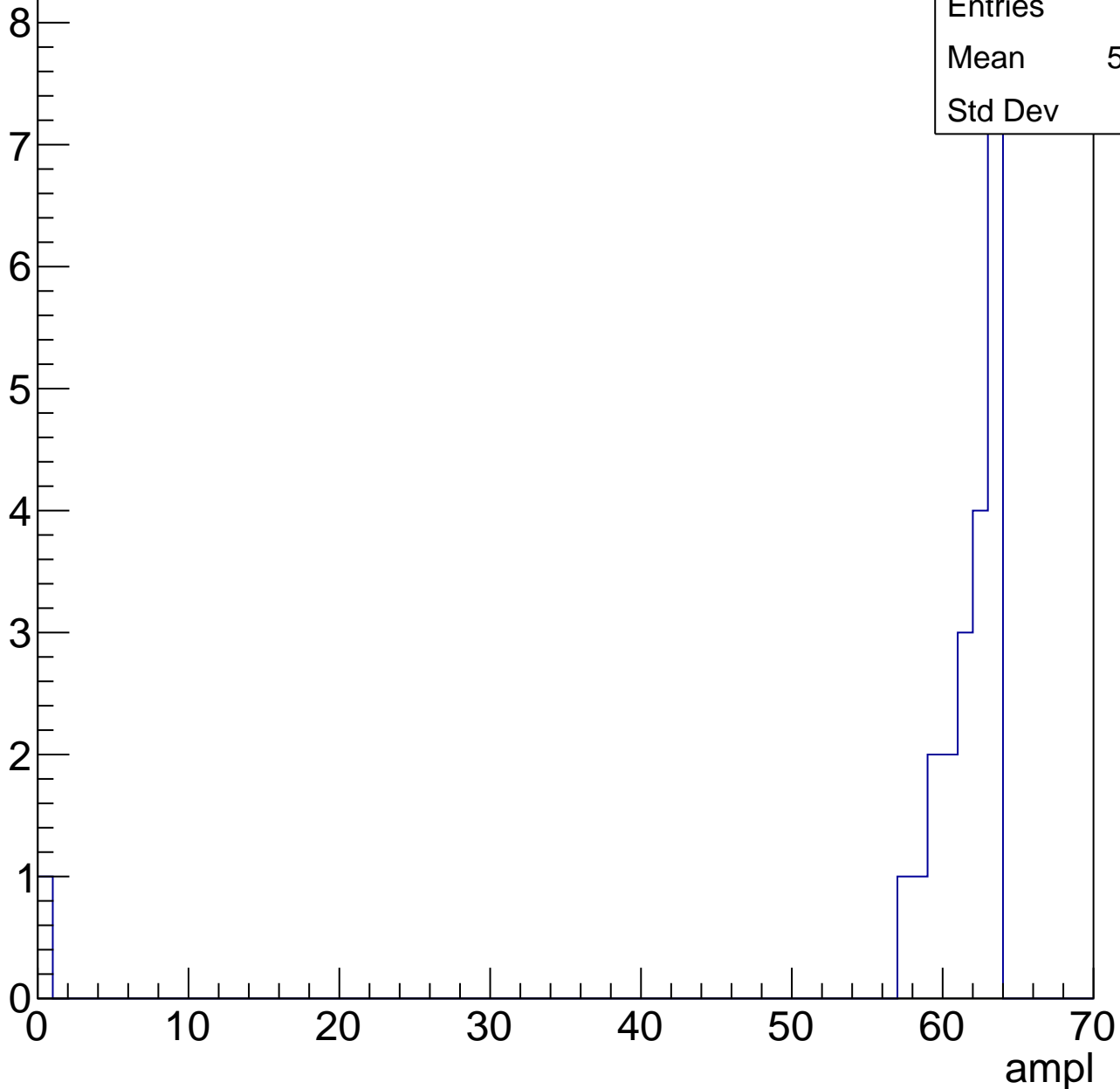


B0L102S, U20-ch54, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

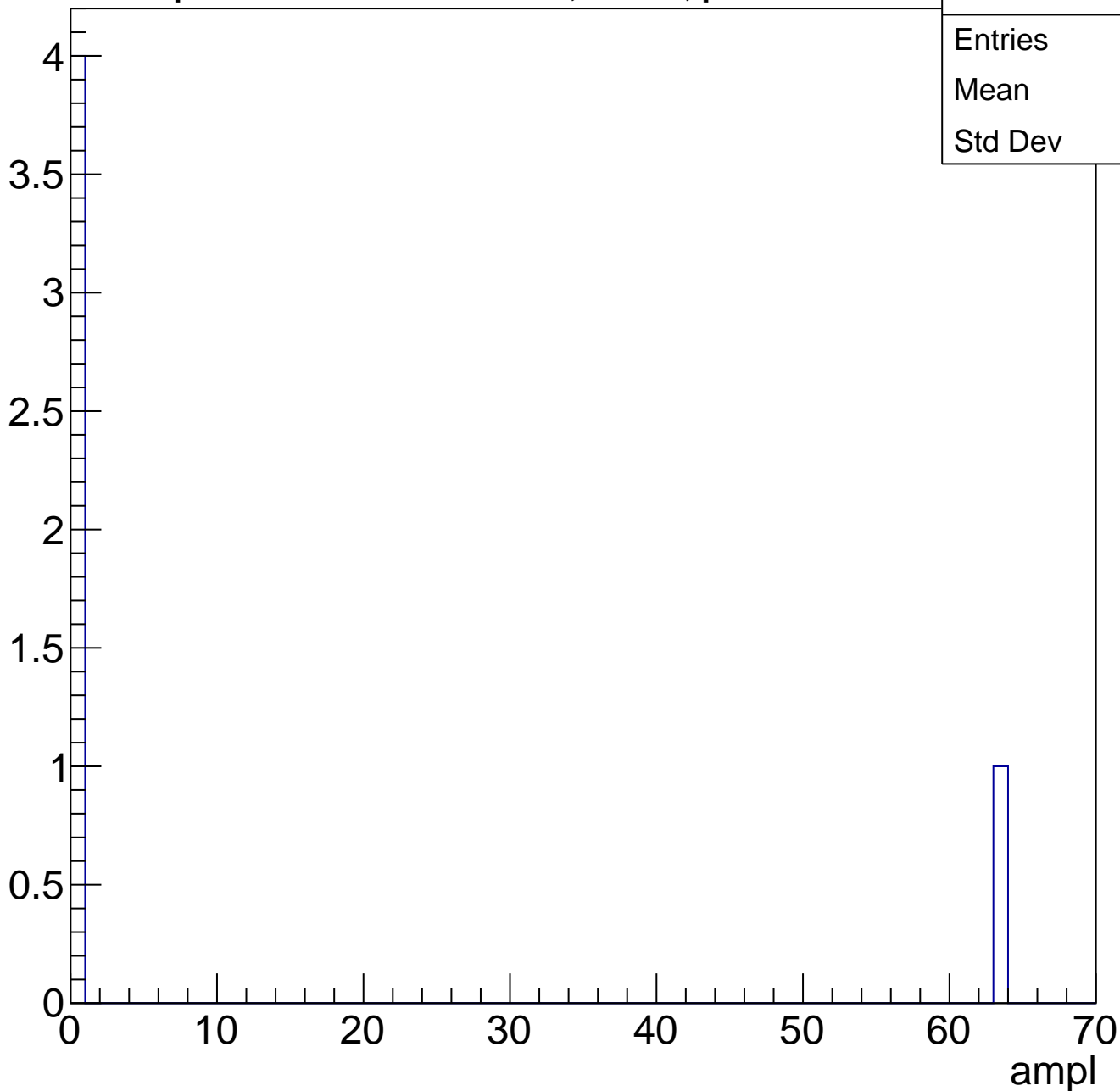
| | |
|---------|-------|
| Entries | 22 |
| Mean | 58.55 |
| Std Dev | 12.9 |



B0L102S, U20-ch54, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch55, adc0

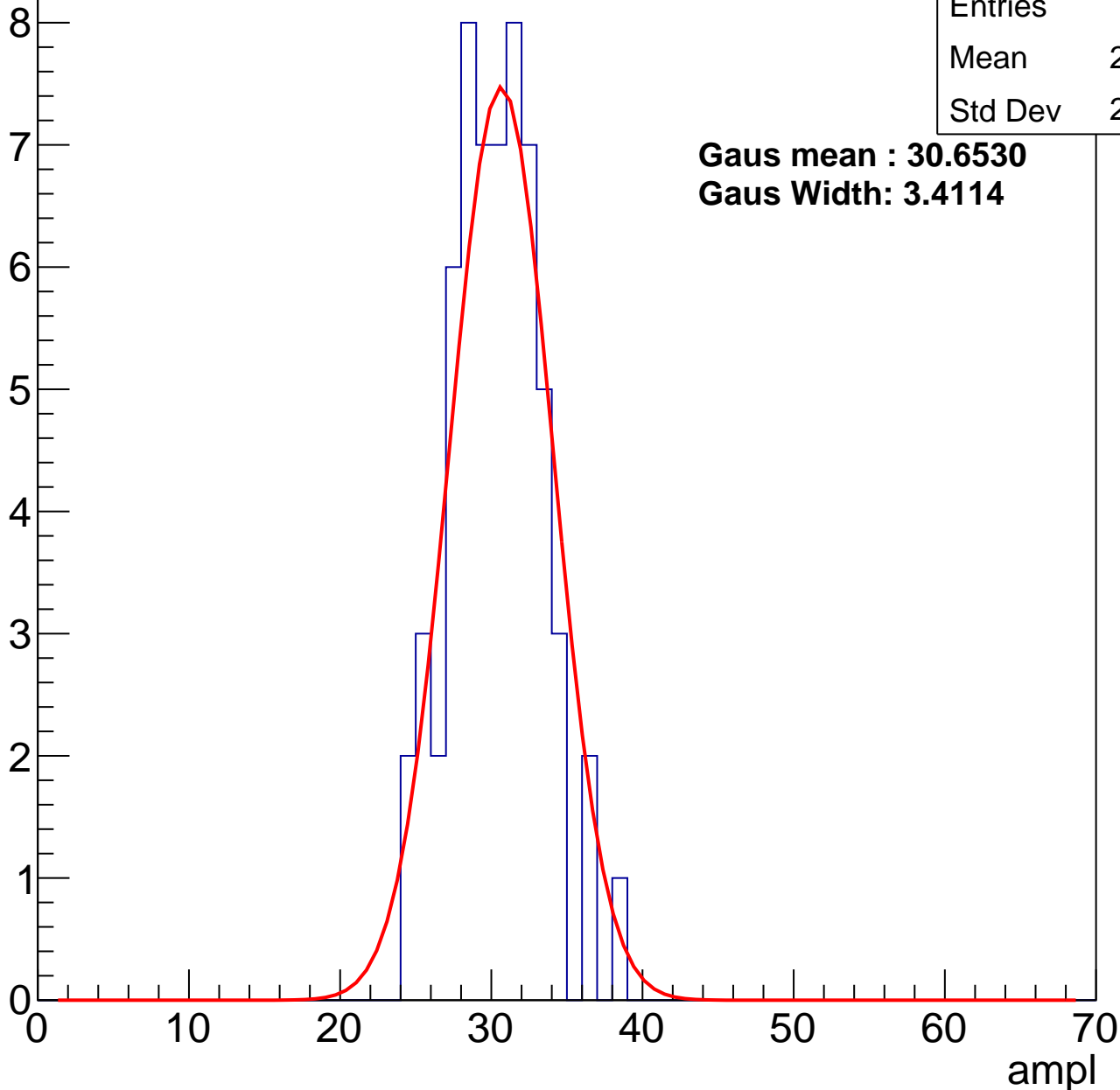
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.89 |
| Std Dev | 2.965 |

Gaus mean : 30.6530

Gaus Width: 3.4114



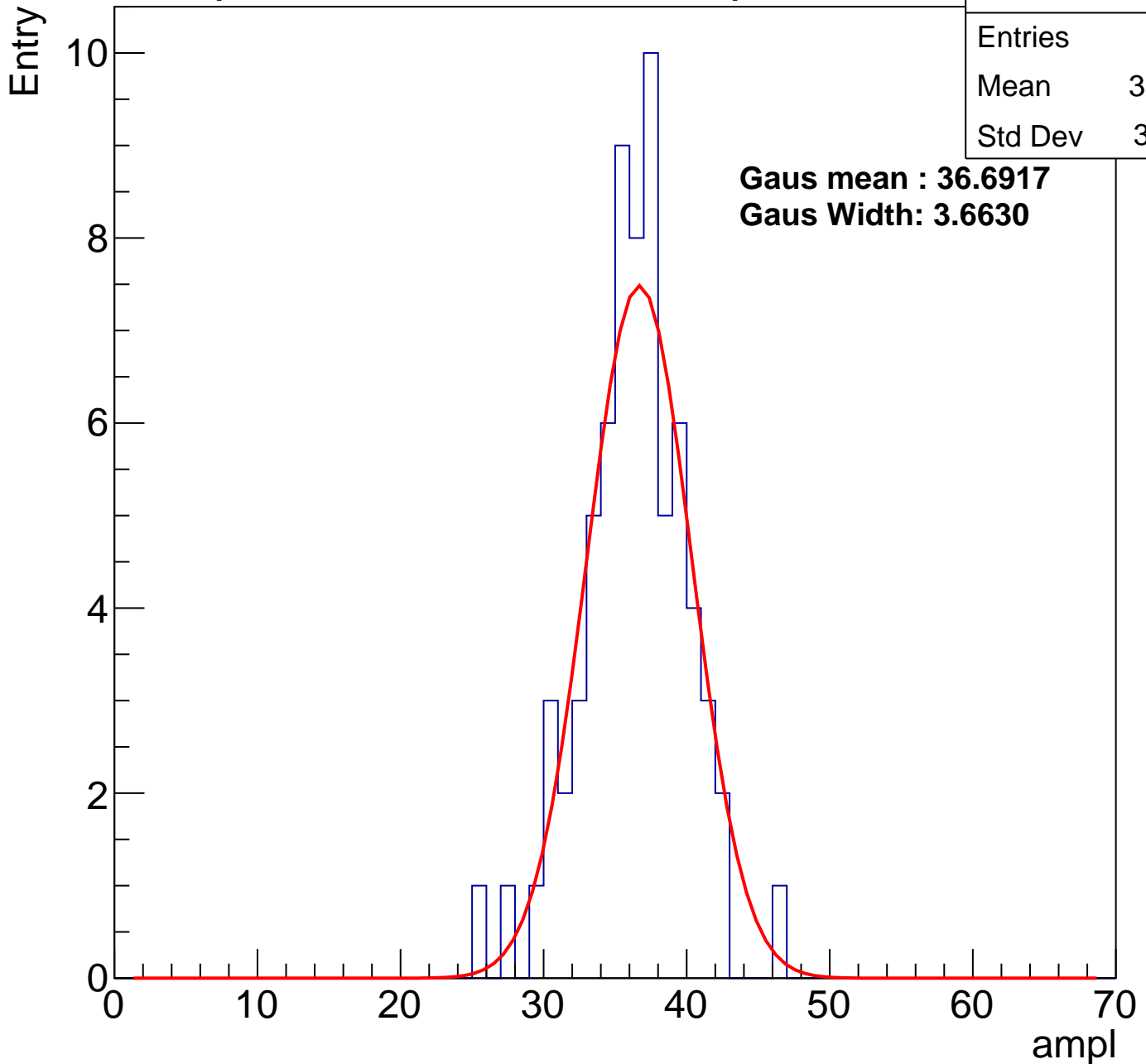
B0L102S, U20-ch55, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.83 |
| Std Dev | 3.641 |

Gaus mean : 36.6917

Gaus Width: 3.6630



B0L102S, U20-ch55, adc2

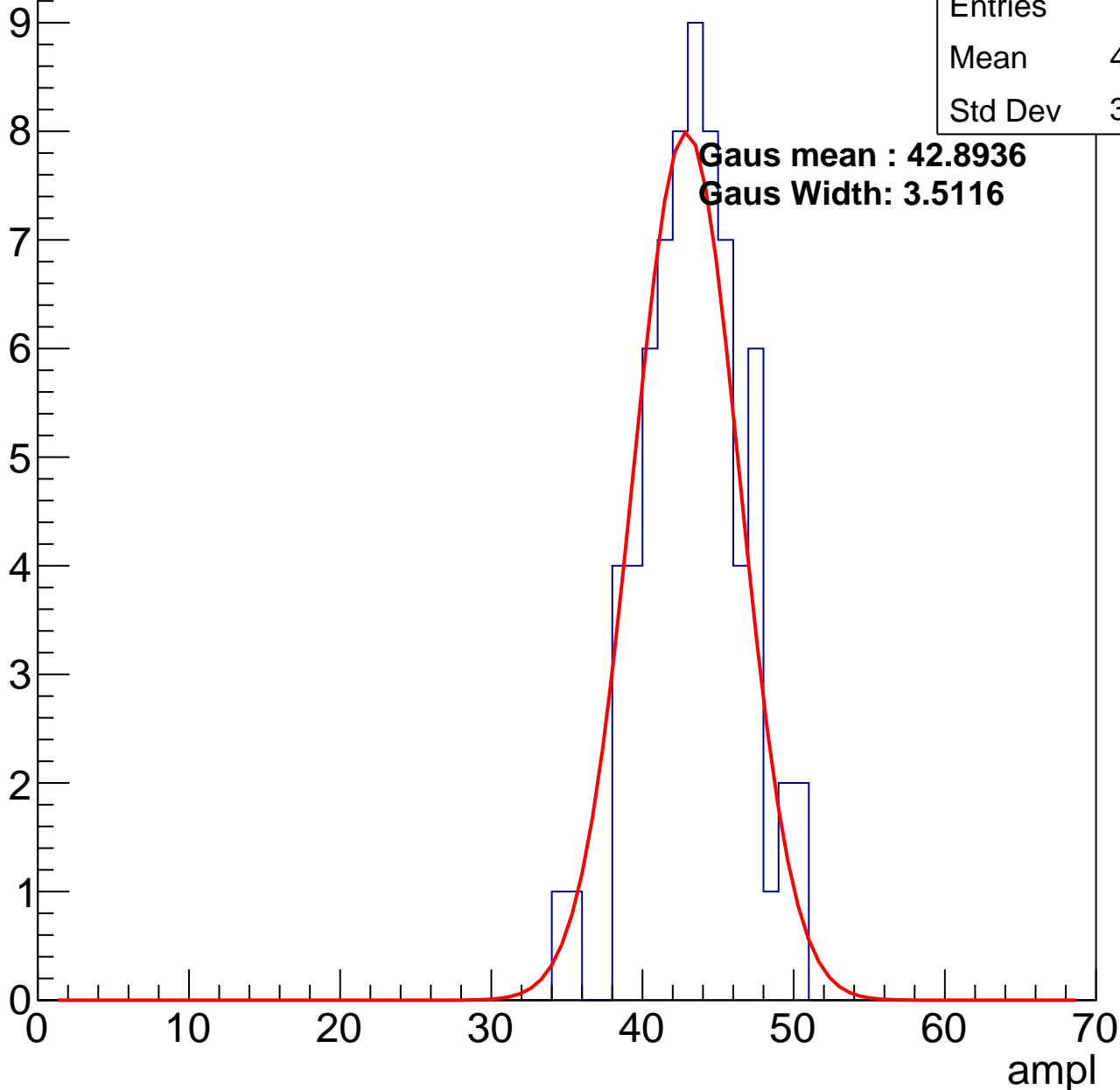
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.94 |
| Std Dev | 3.299 |

Gaus mean : 42.8936

Gaus Width: 3.5116

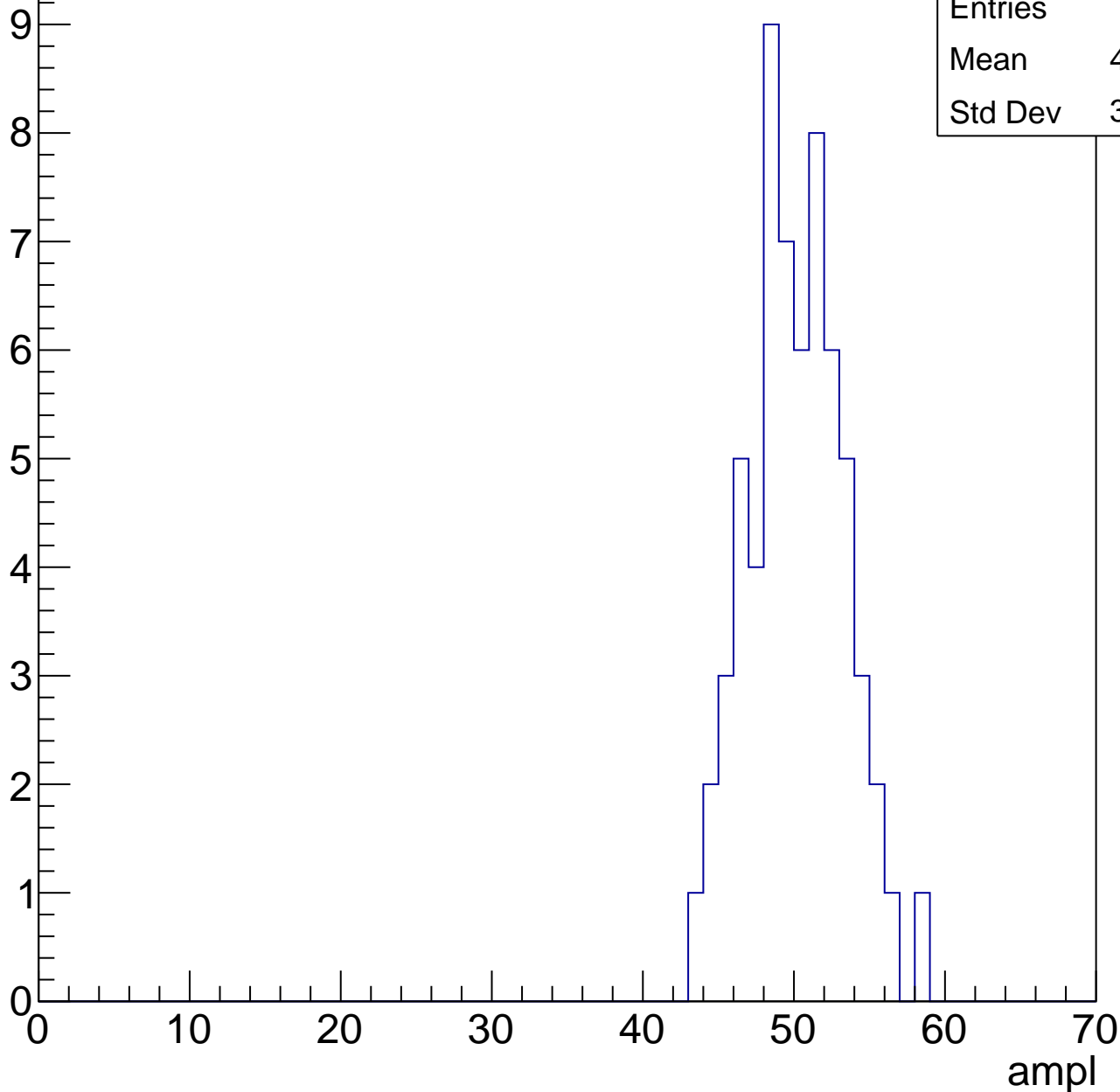


B0L102S, U20-ch55, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

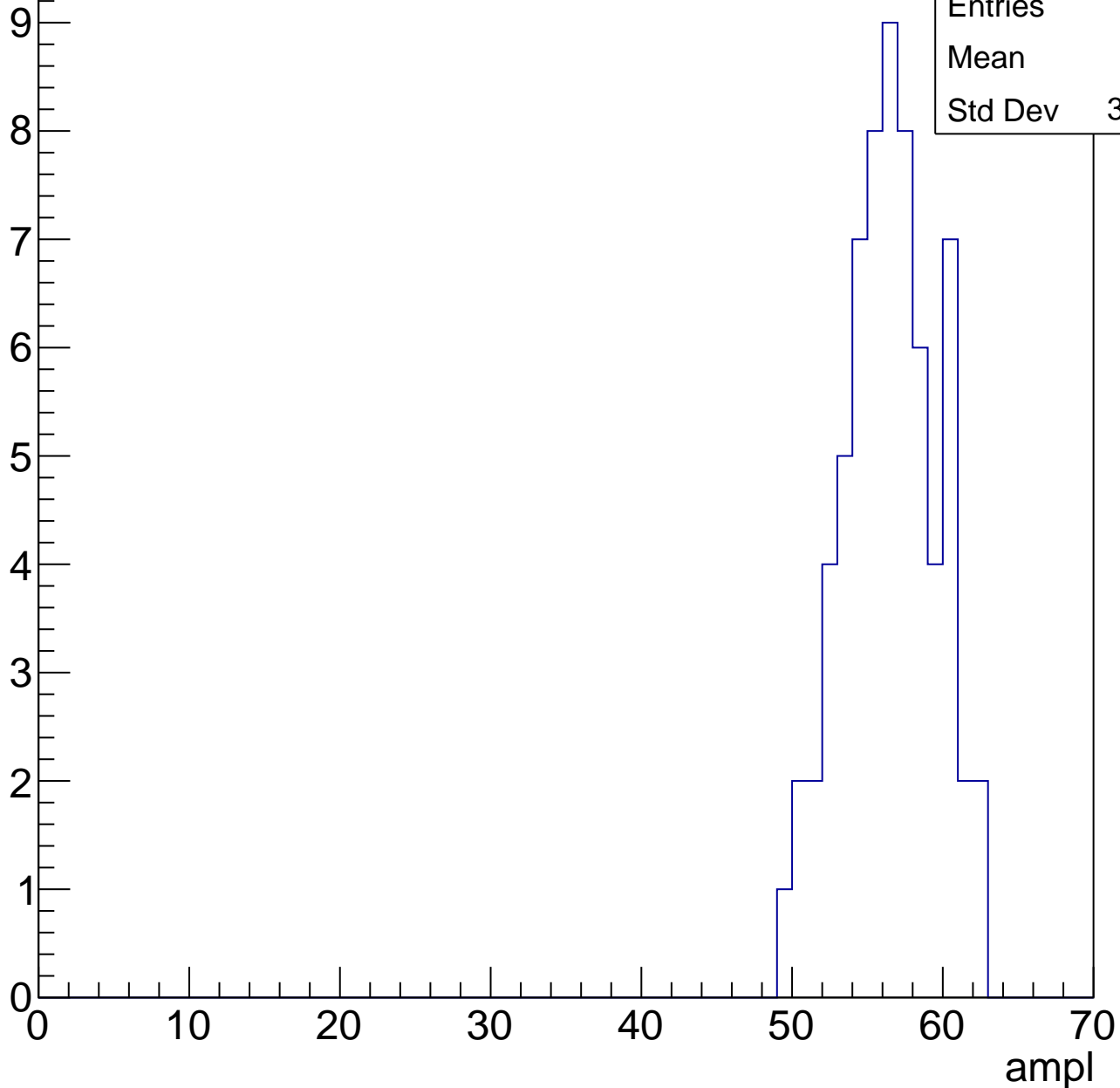
| | |
|---------|-------|
| Entries | 63 |
| Mean | 49.68 |
| Std Dev | 3.146 |



B0L102S, U20-ch55, adc4

calib_packv5_042523_0143.root, FC#12, port B1

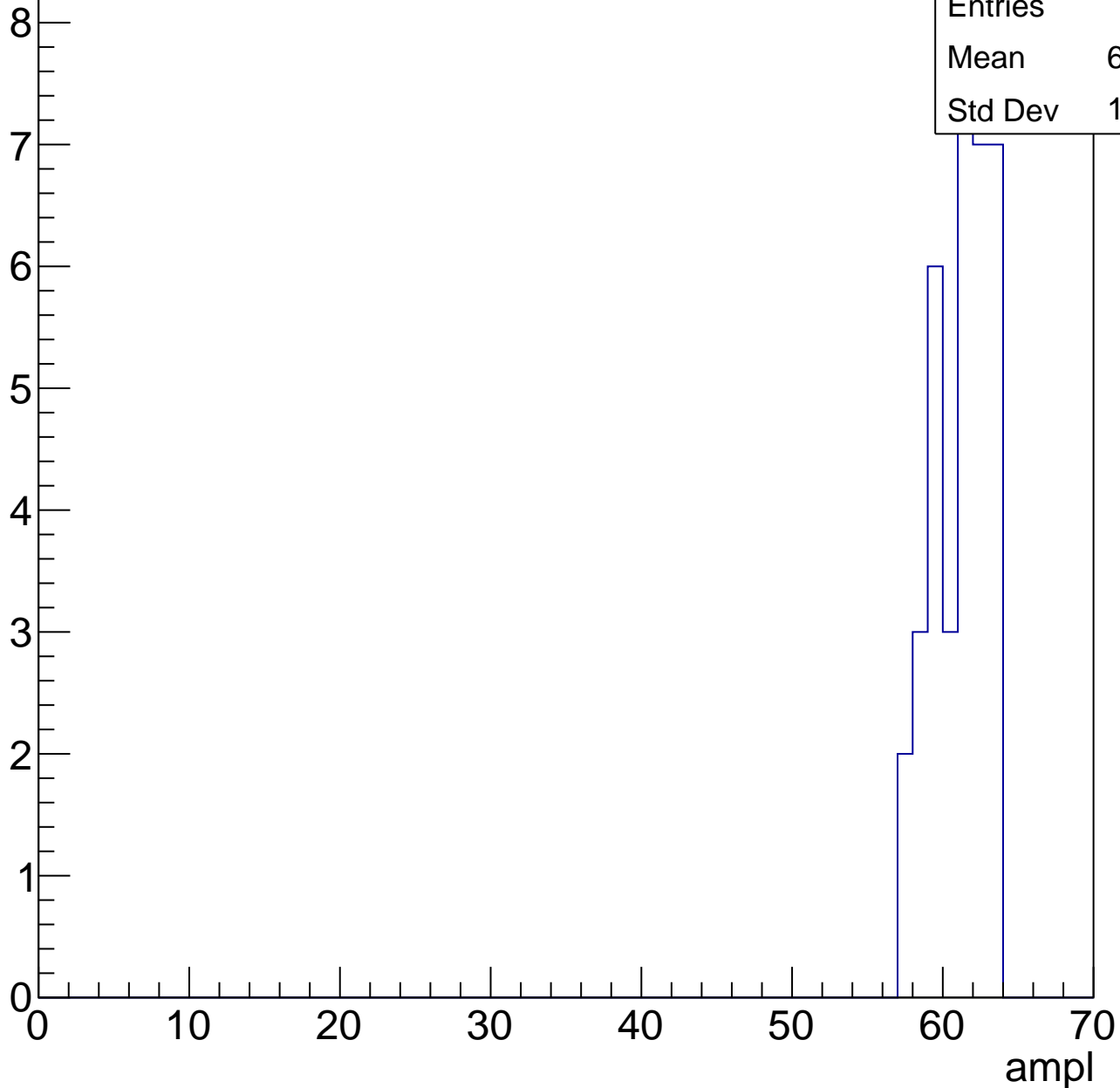
Entry



B0L102S, U20-ch55, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch55, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch55, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L102S, U20-ch56, adc0

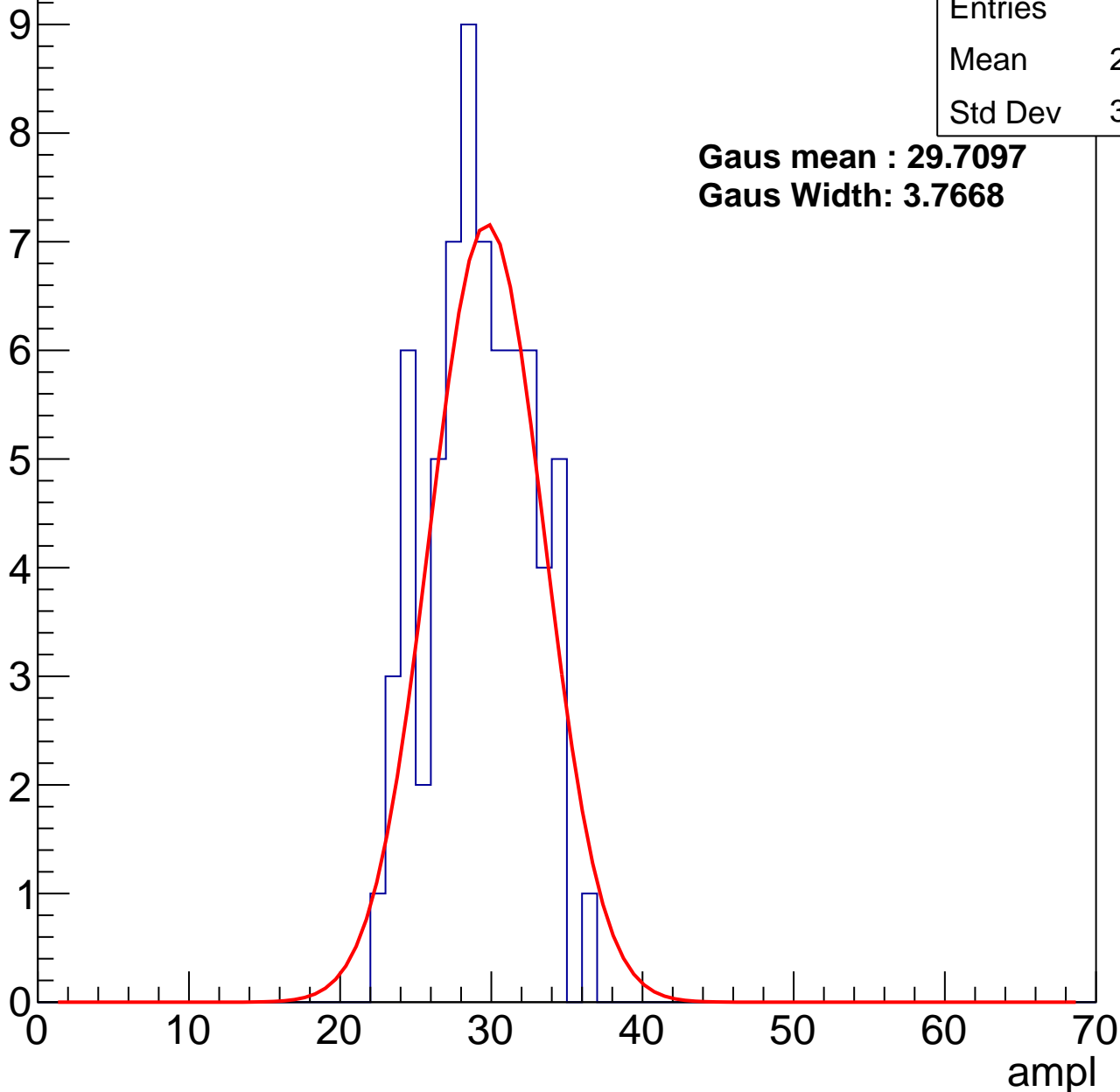
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.75 |
| Std Dev | 3.296 |

Gaus mean : 29.7097

Gaus Width: 3.7668



B0L102S, U20-ch56, adc1

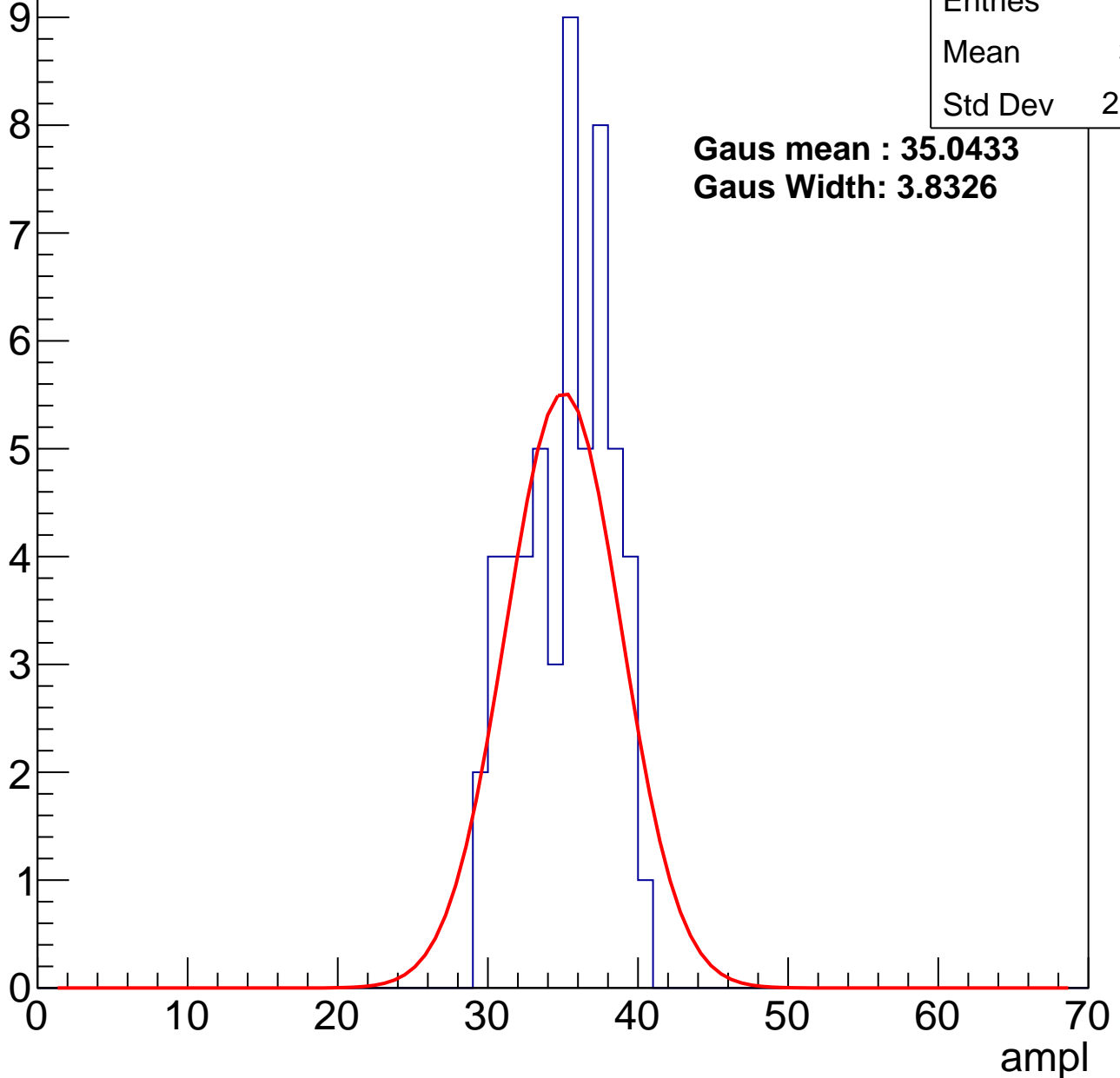
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 34.7 |
| Std Dev | 2.923 |

Gaus mean : 35.0433

Gaus Width: 3.8326



B0L102S, U20-ch56, adc2

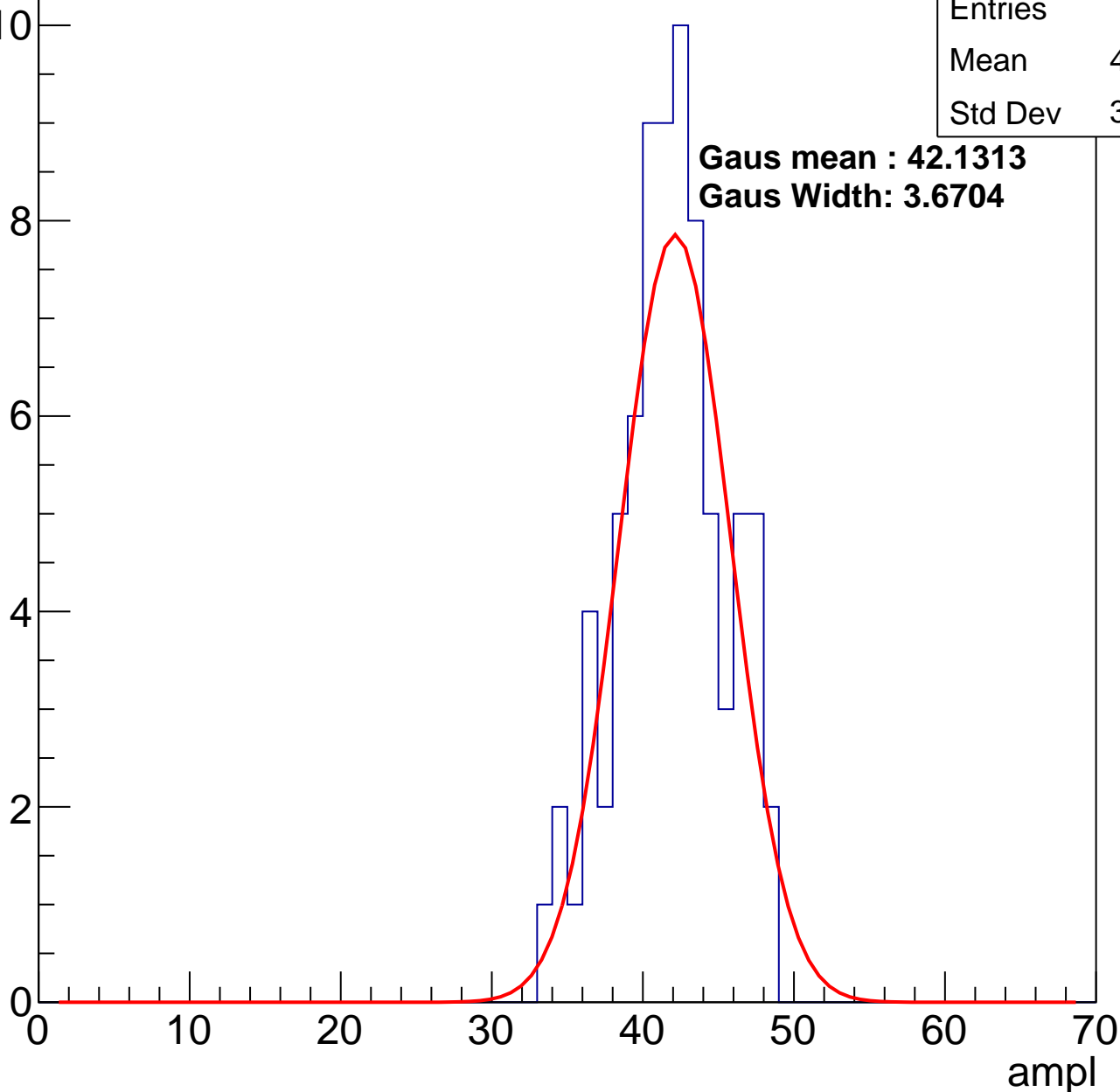
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 41.39 |
| Std Dev | 3.483 |

Gaus mean : 42.1313

Gaus Width: 3.6704

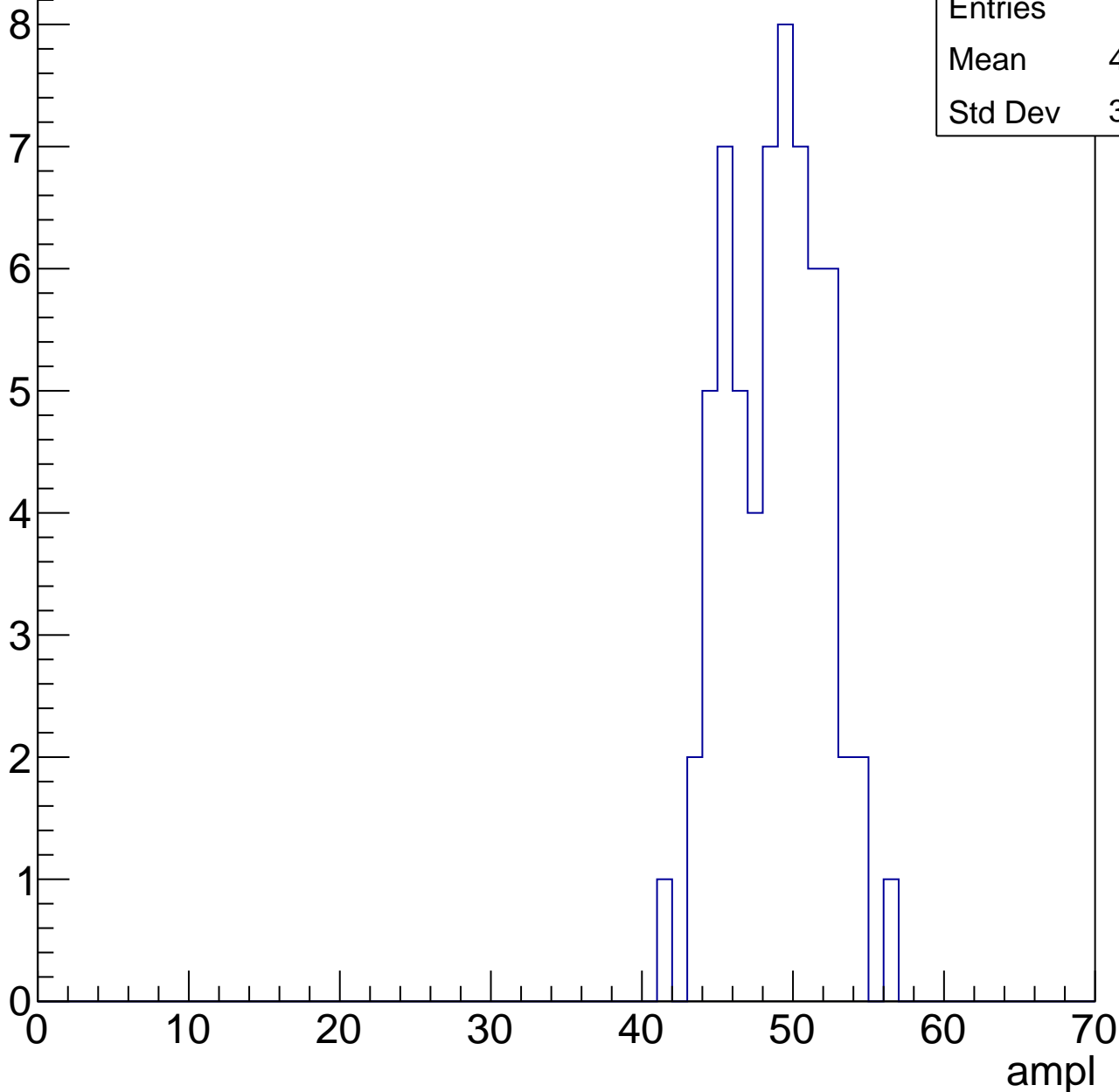


B0L102S, U20-ch56, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

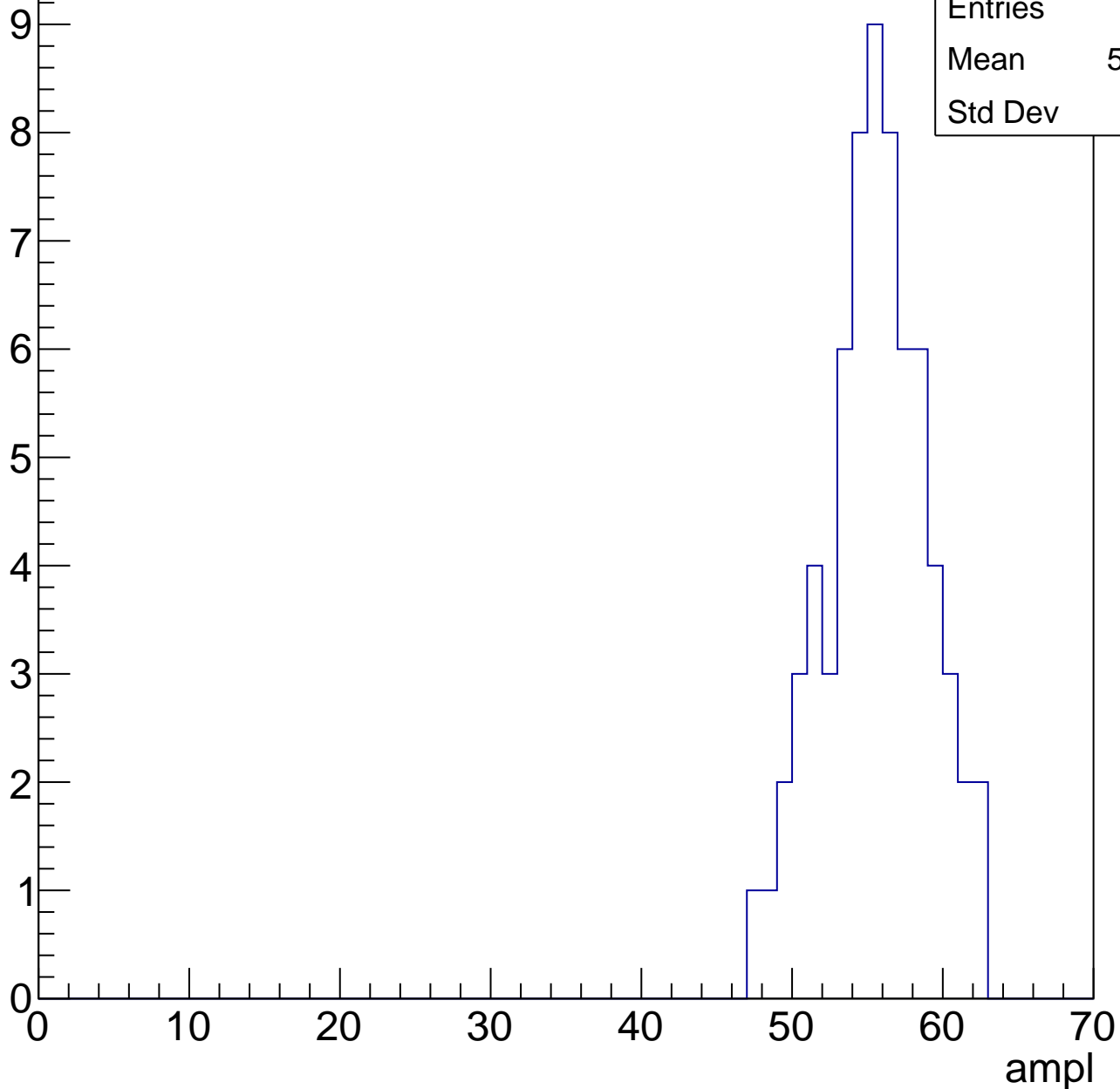
| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.35 |
| Std Dev | 3.163 |



B0L102S, U20-ch56, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

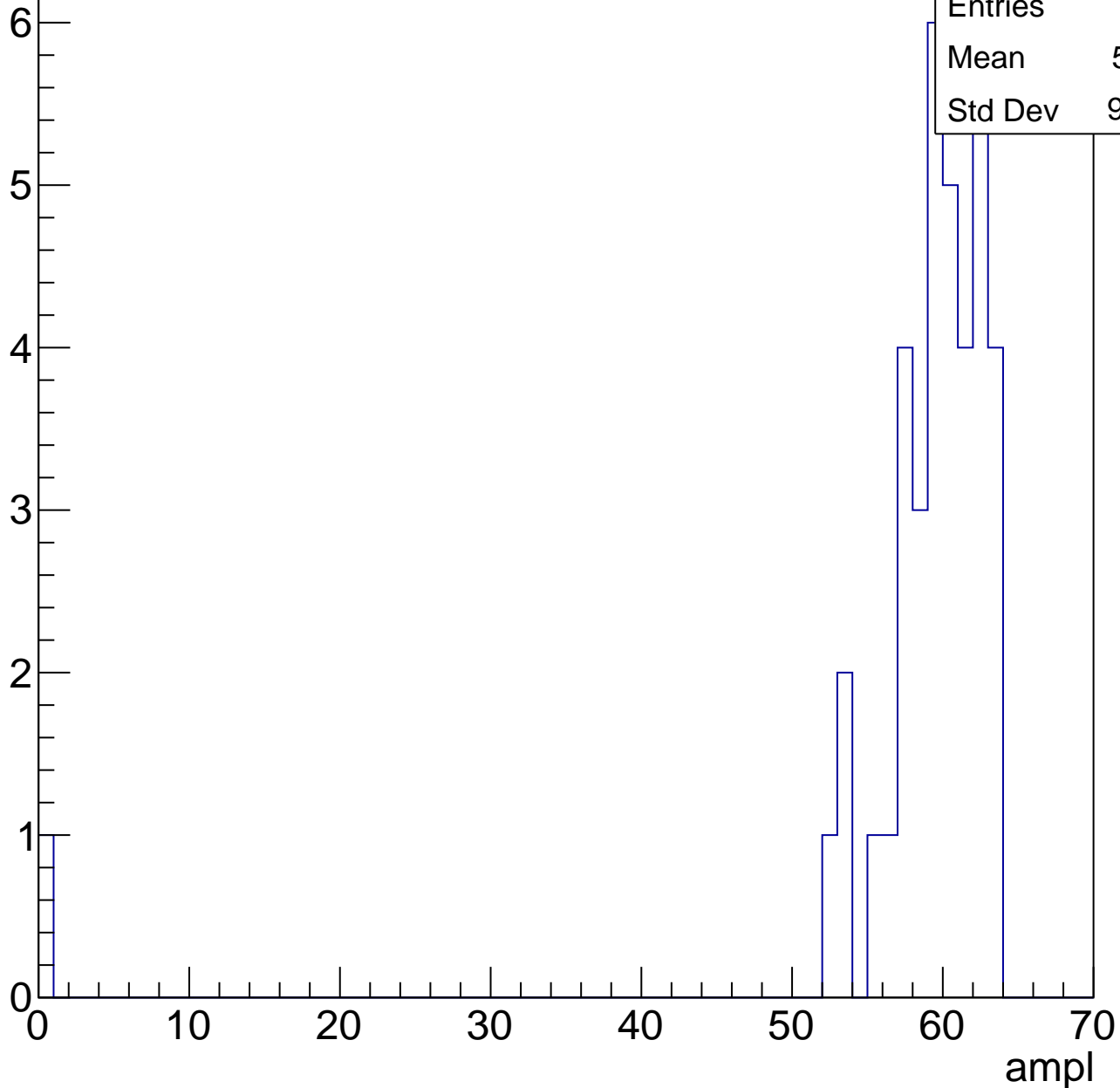


| | |
|---------|-------|
| Entries | 68 |
| Mean | 55.12 |
| Std Dev | 3.38 |

B0L102S, U20-ch56, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

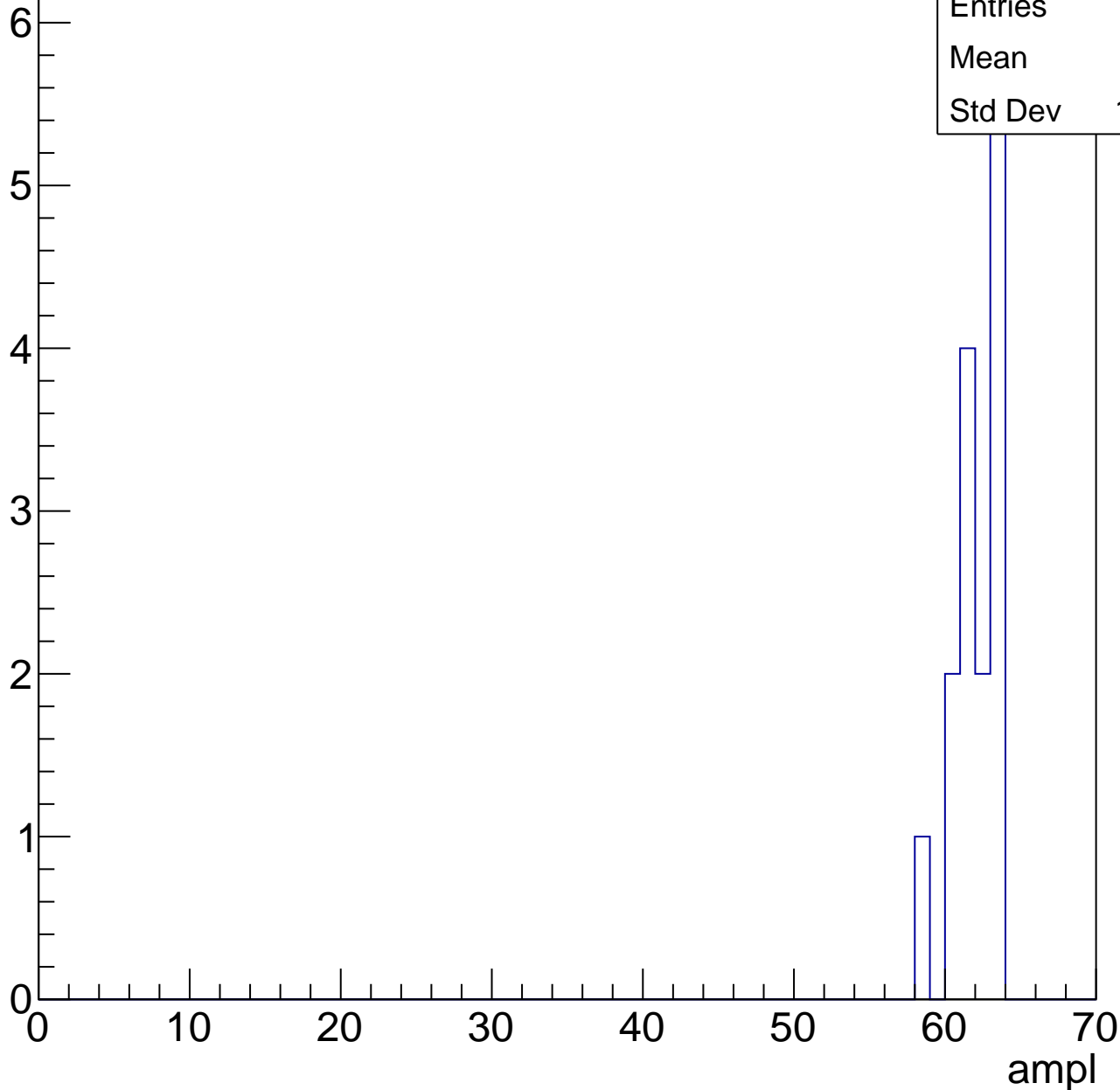


B0L102S, U20-ch56, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.6 |
| Std Dev | 1.451 |



B0L102S, U20-ch56, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L102S, U20-ch57, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 81 |
| Mean | 30.52 |
| Std Dev | 3.389 |

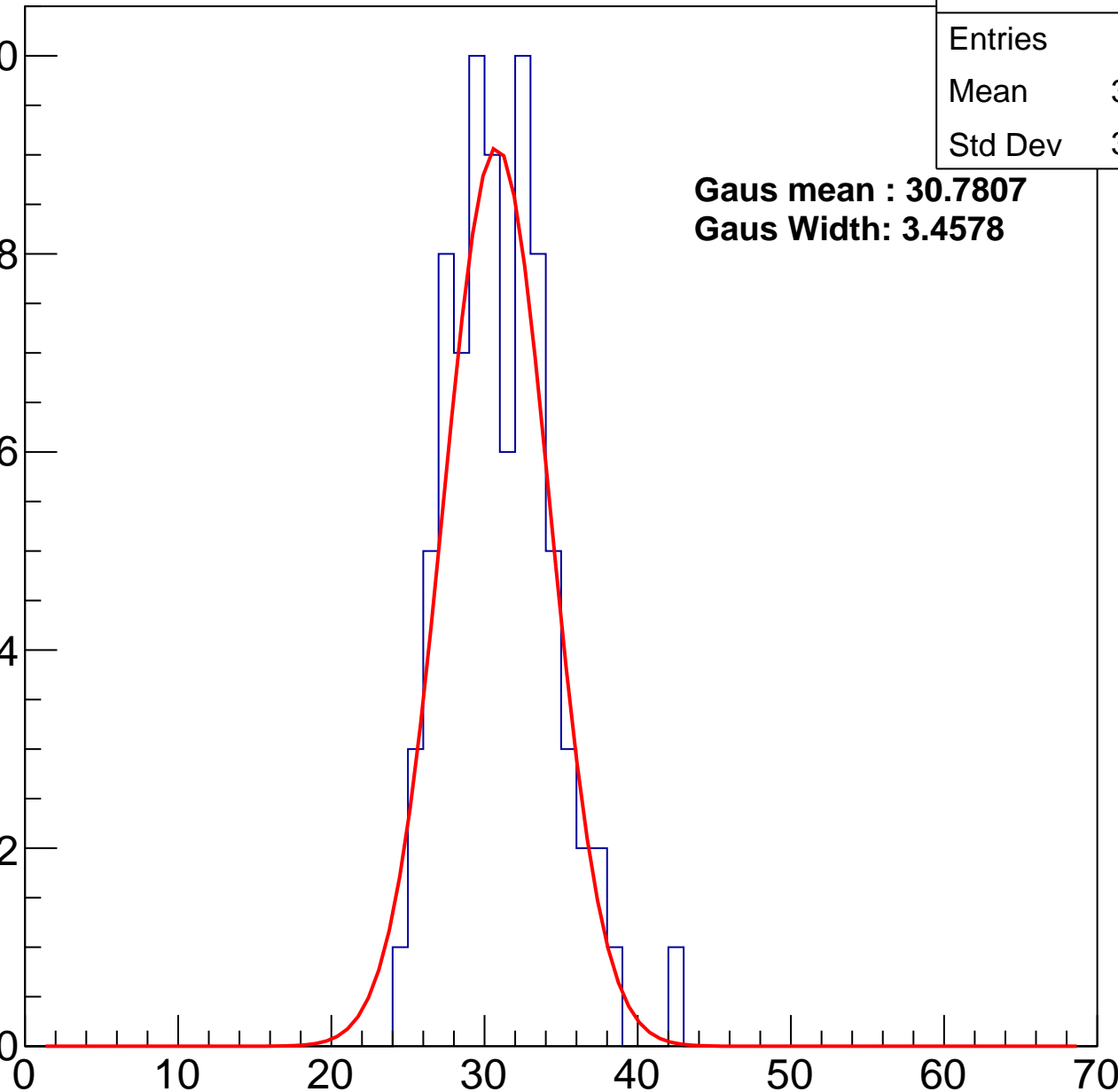
Gaus mean : 30.7807

Gaus Width: 3.4578

Entry

10
8
6
4
2
0

ampl



B0L102S, U20-ch57, adc1

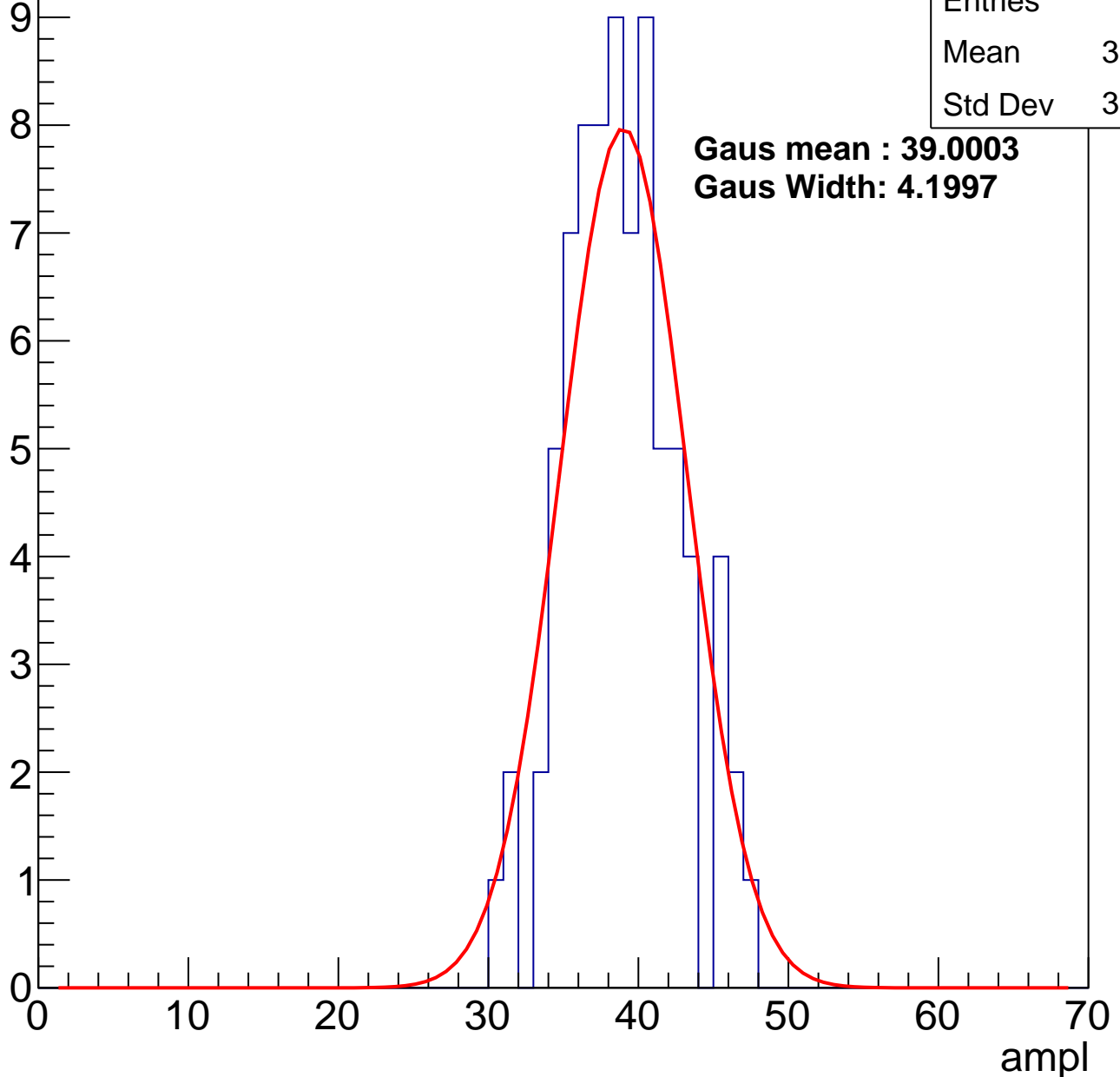
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 38.46 |
| Std Dev | 3.648 |

Gaus mean : 39.0003

Gaus Width: 4.1997



B0L102S, U20-ch57, adc2

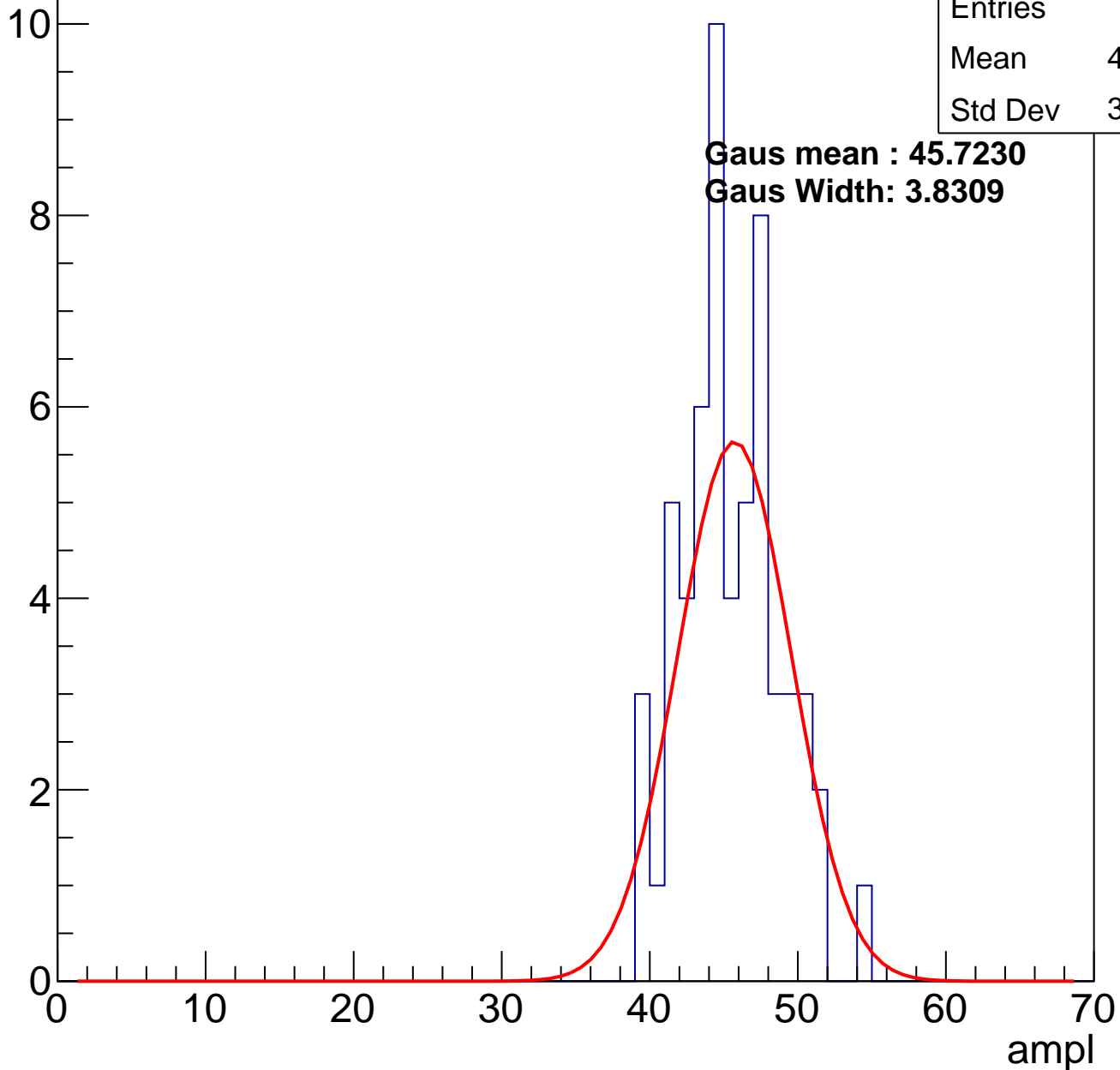
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 58 |
| Mean | 45.02 |
| Std Dev | 3.277 |

Gaus mean : 45.7230

Gaus Width: 3.8309

Entry

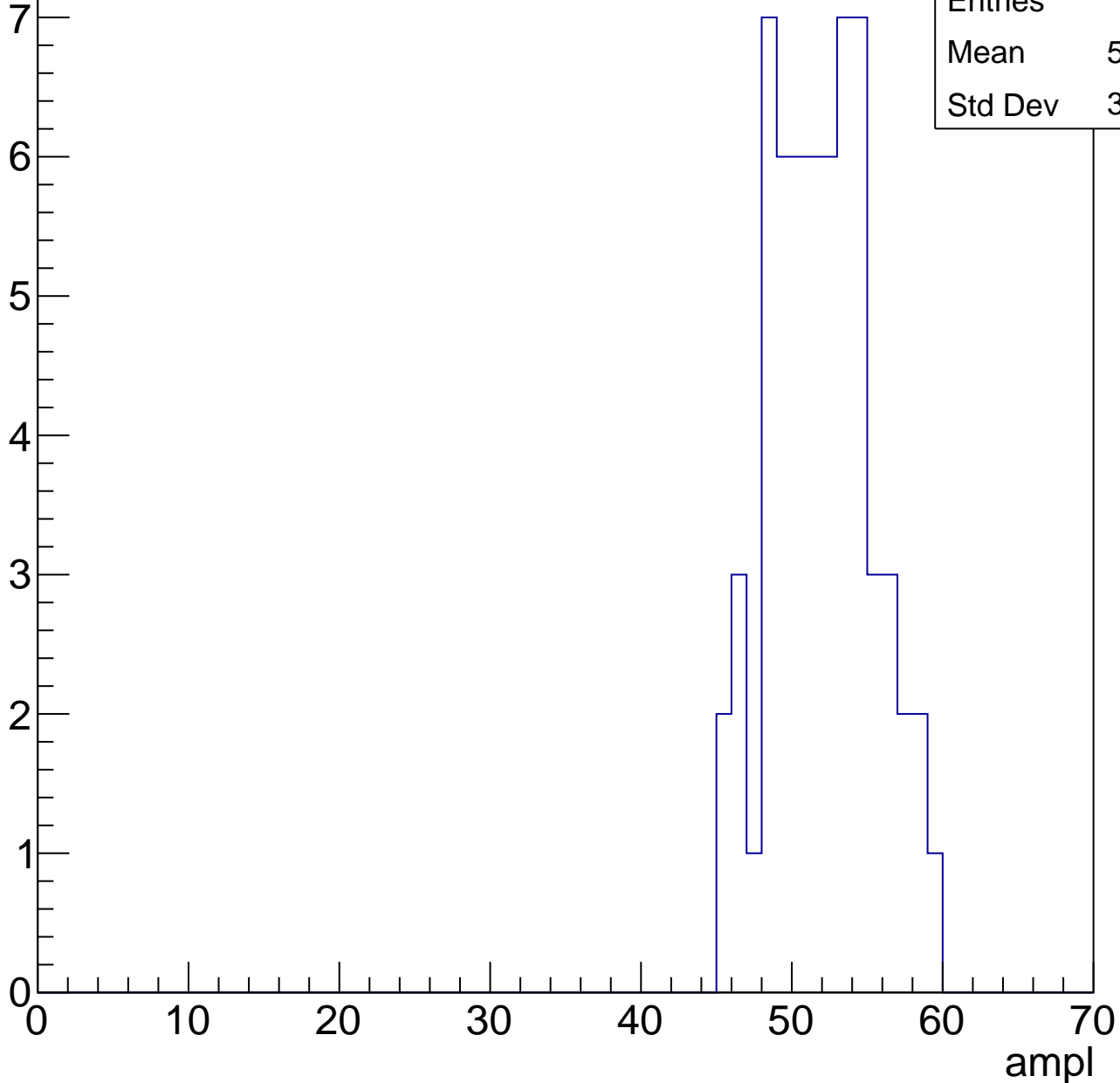


B0L102S, U20-ch57, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 51.52 |
| Std Dev | 3.344 |

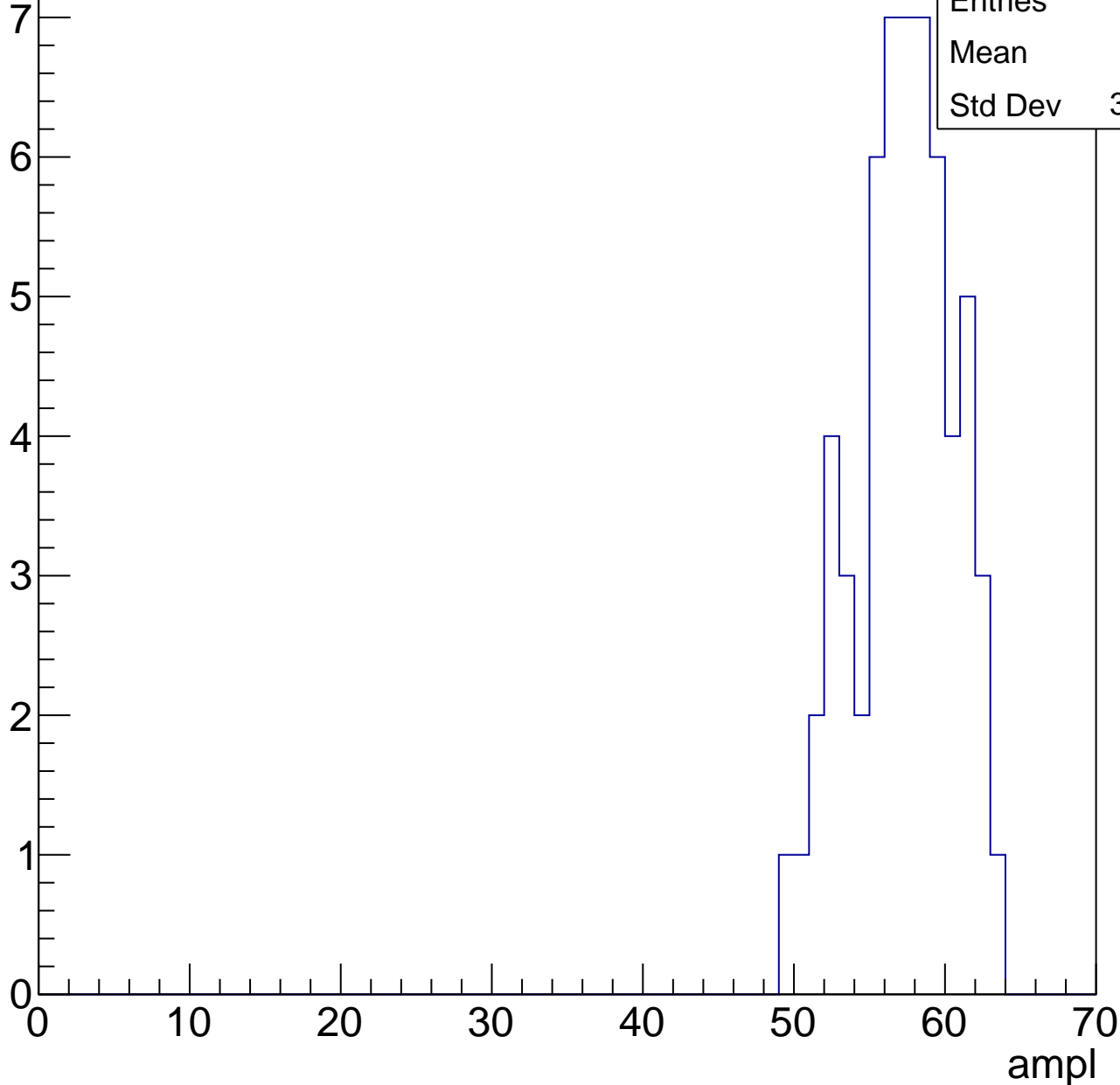


B0L102S, U20-ch57, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.8 |
| Std Dev | 3.287 |

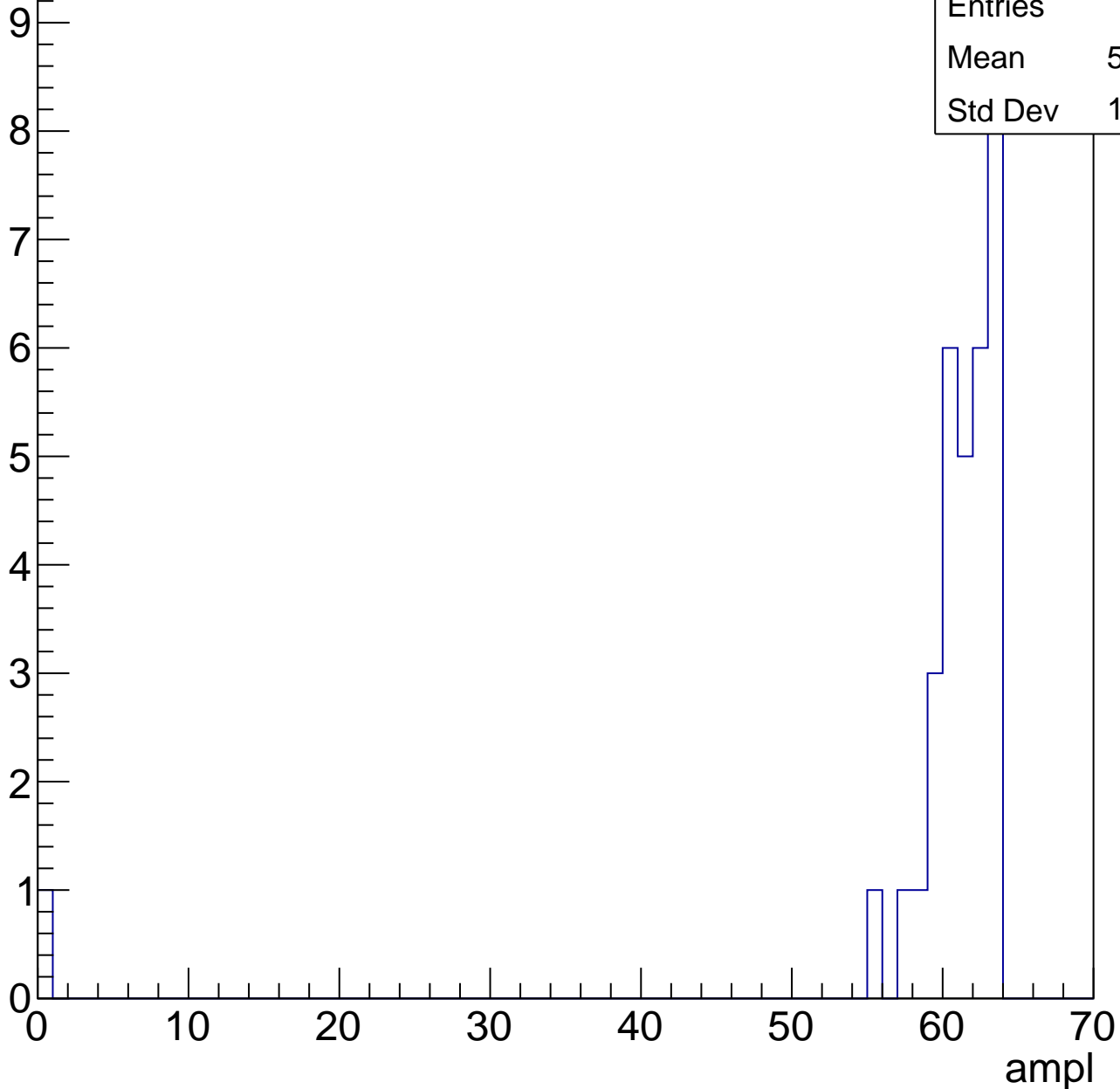


B0L102S, U20-ch57, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 33 |
| Mean | 59.12 |
| Std Dev | 10.63 |



B0L102S, U20-ch57, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch57, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl



B0L102S, U20-ch58, adc0

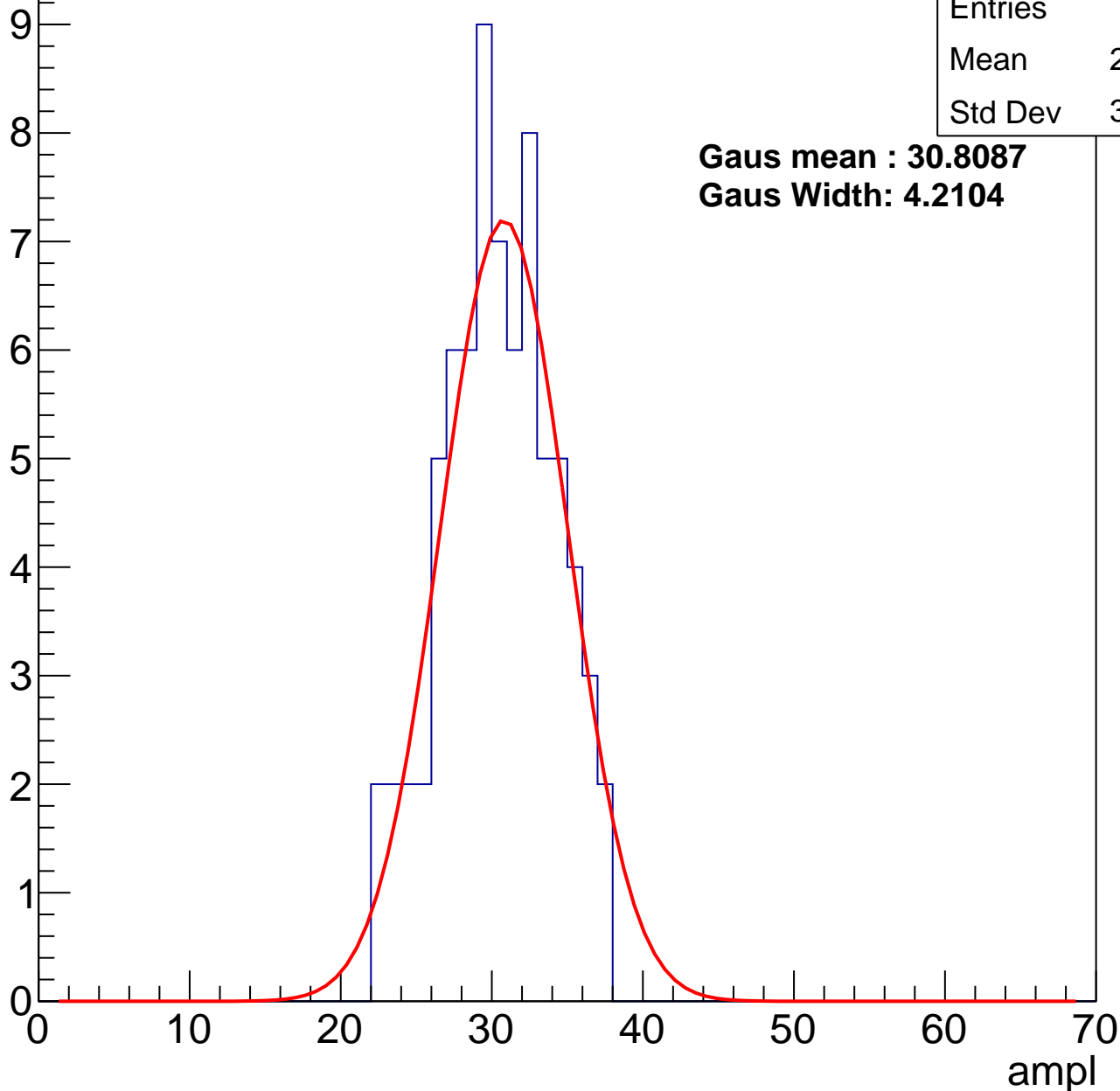
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.97 |
| Std Dev | 3.646 |

Gaus mean : 30.8087

Gaus Width: 4.2104



B0L102S, U20-ch58, adc1

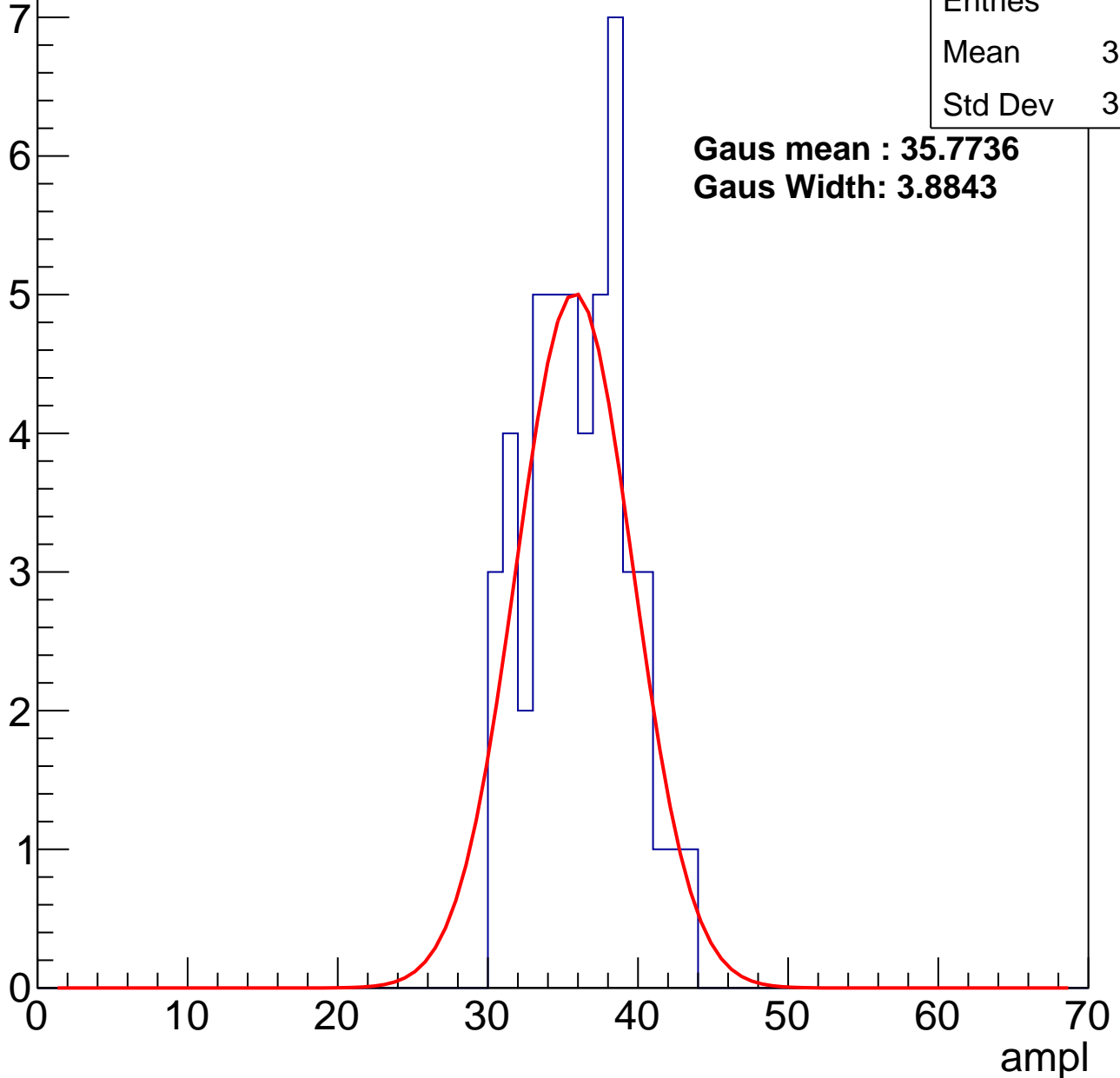
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 35.63 |
| Std Dev | 3.262 |

Gaus mean : 35.7736

Gaus Width: 3.8843



B0L102S, U20-ch58, adc2

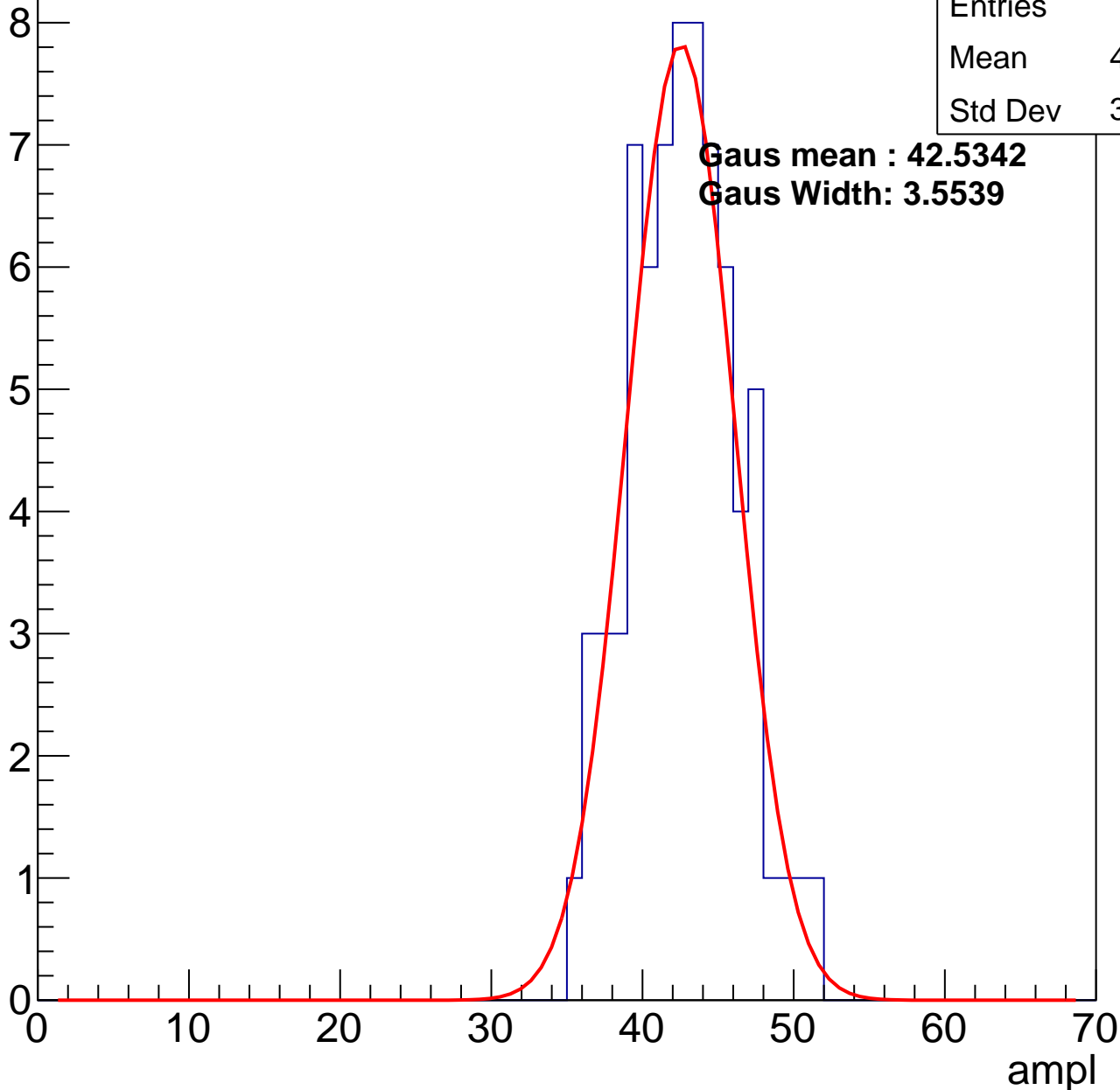
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 42.26 |
| Std Dev | 3.492 |

Gaus mean : 42.5342

Gaus Width: 3.5539

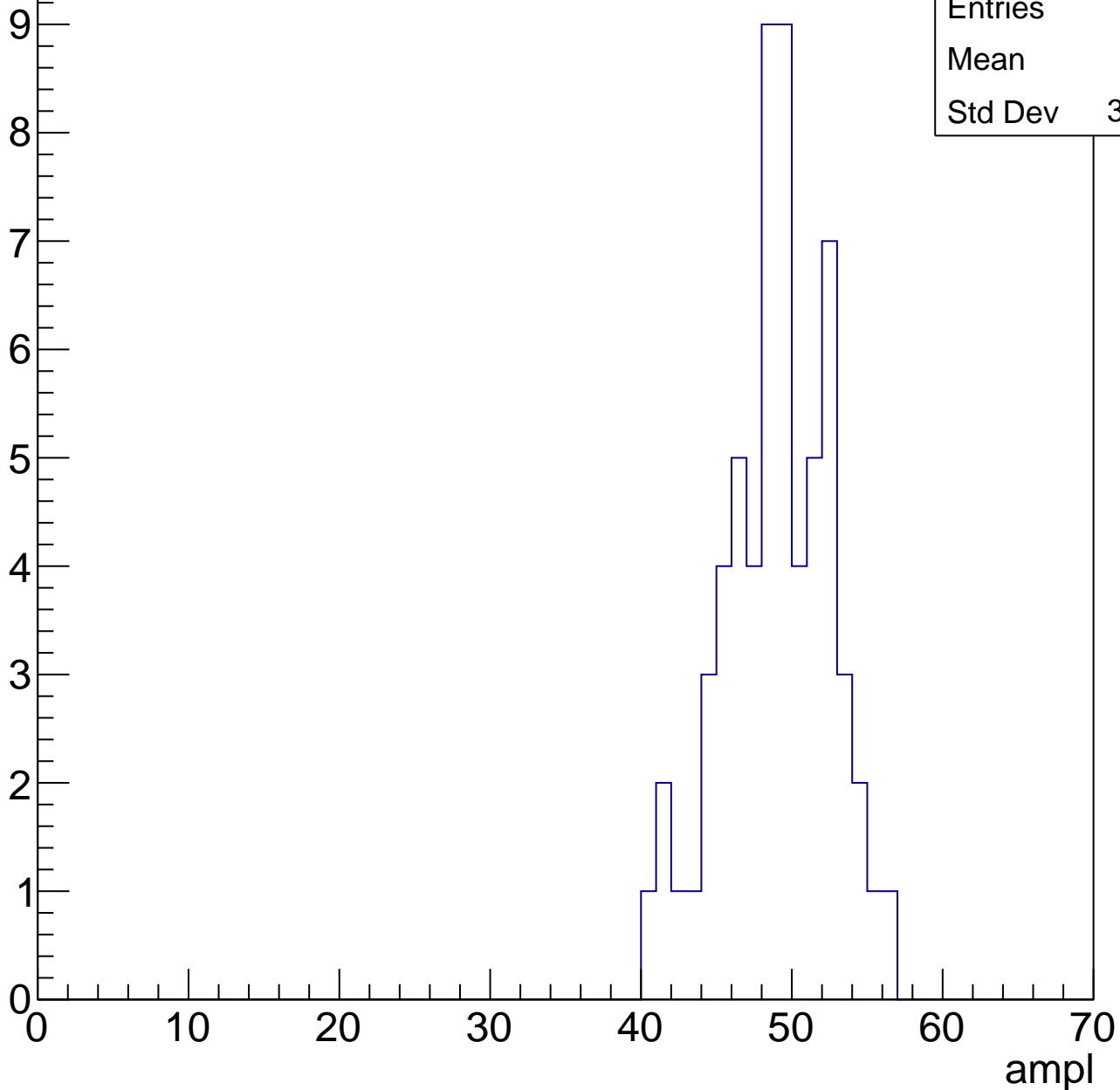


B0L102S, U20-ch58, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 48.5 |
| Std Dev | 3.495 |

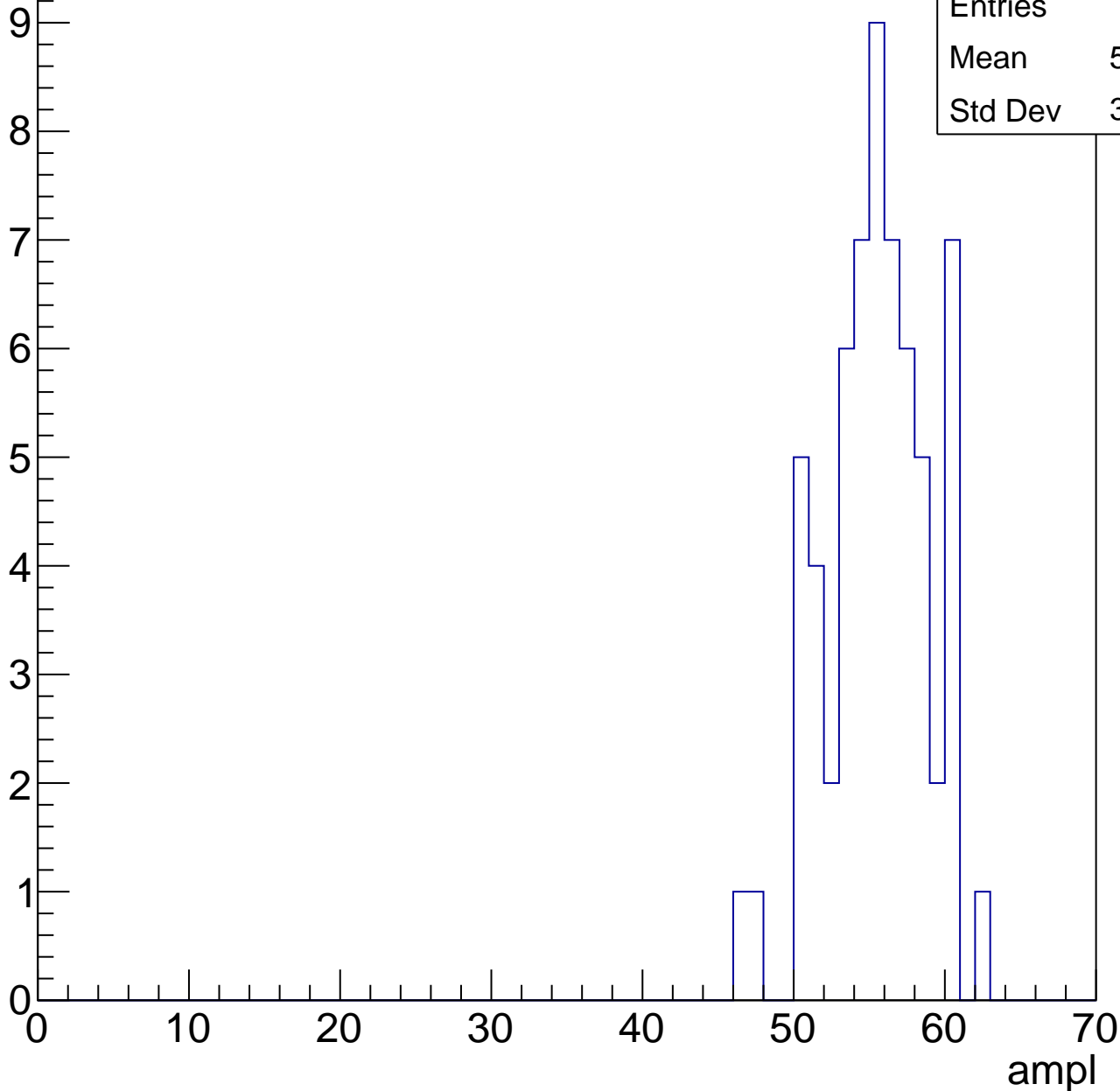


B0L102S, U20-ch58, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 55.02 |
| Std Dev | 3.369 |

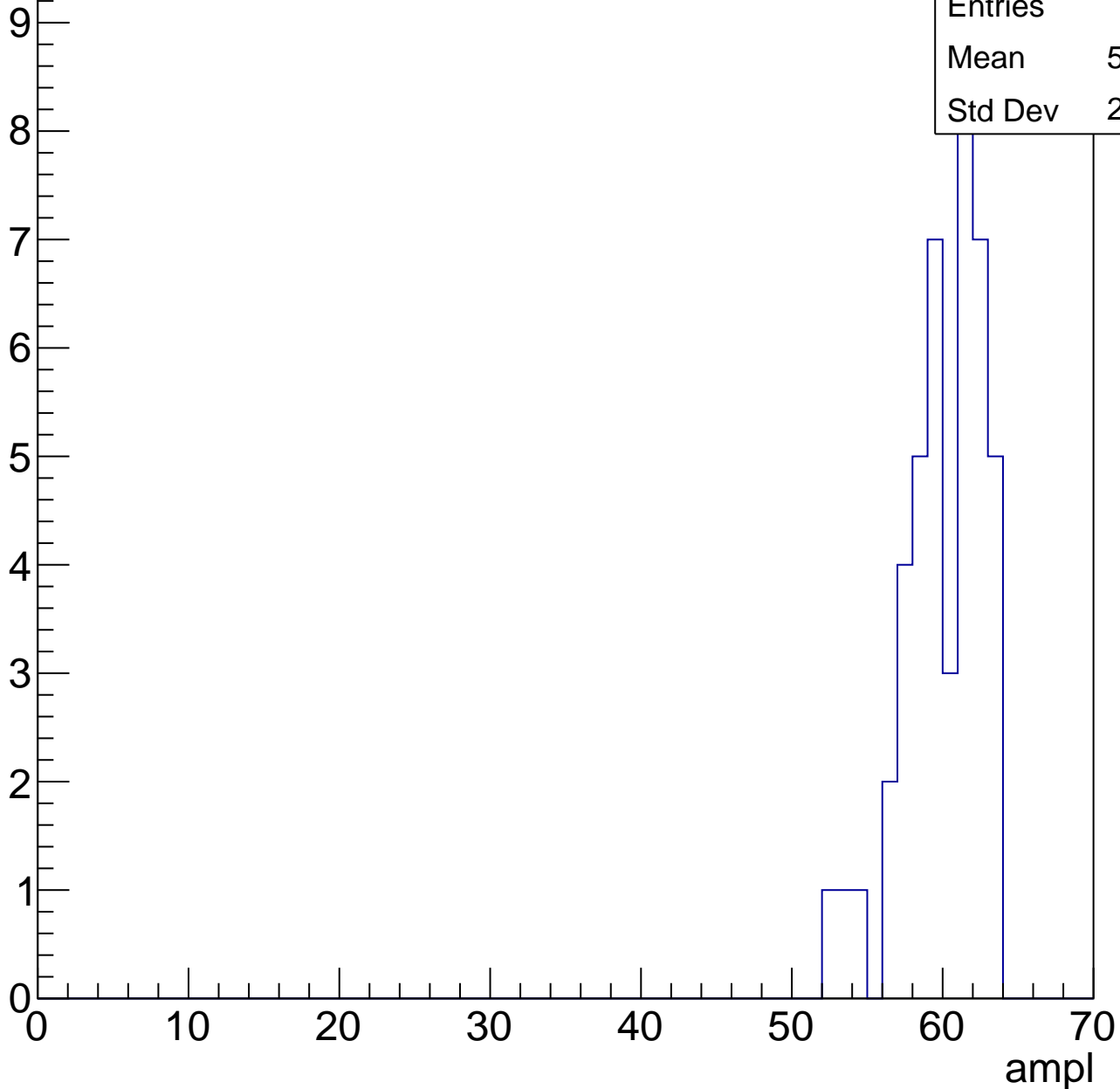


B0L102S, U20-ch58, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.56 |
| Std Dev | 2.655 |

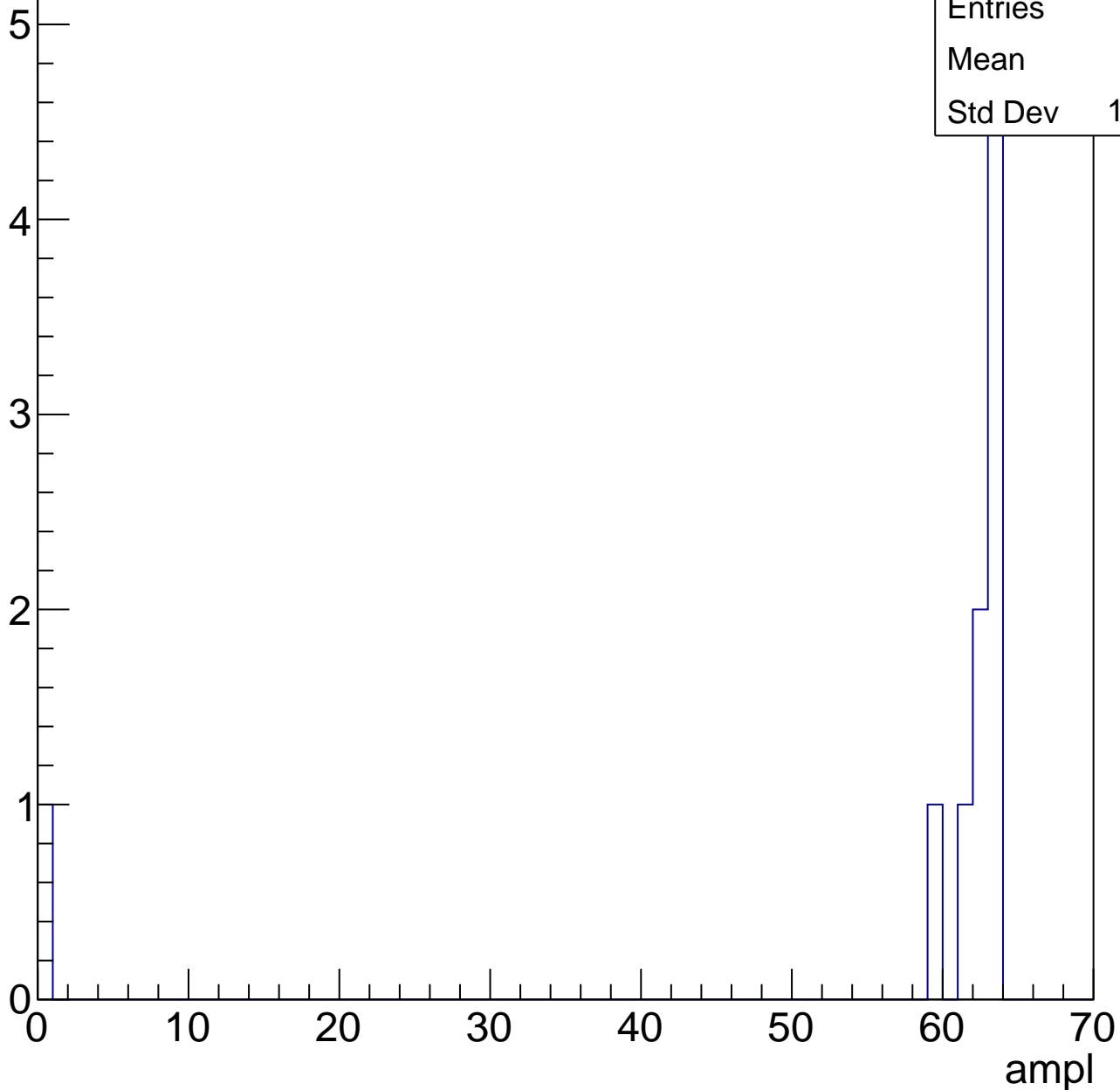


B0L102S, U20-ch58, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 55.9 |
| Std Dev | 18.67 |



B0L102S, U20-ch58, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch59, adc0

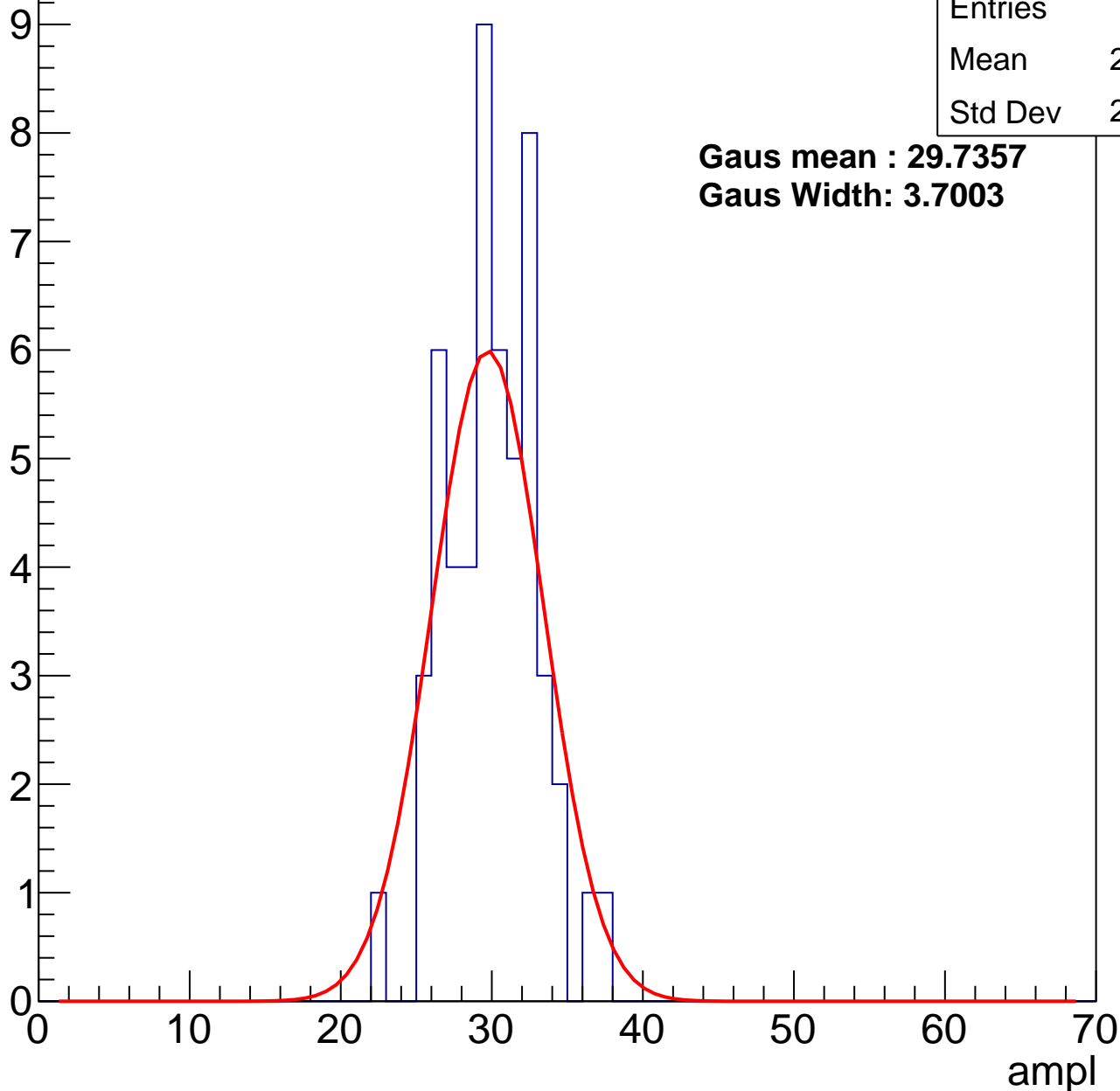
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.53 |
| Std Dev | 2.963 |

Gaus mean : 29.7357

Gaus Width: 3.7003



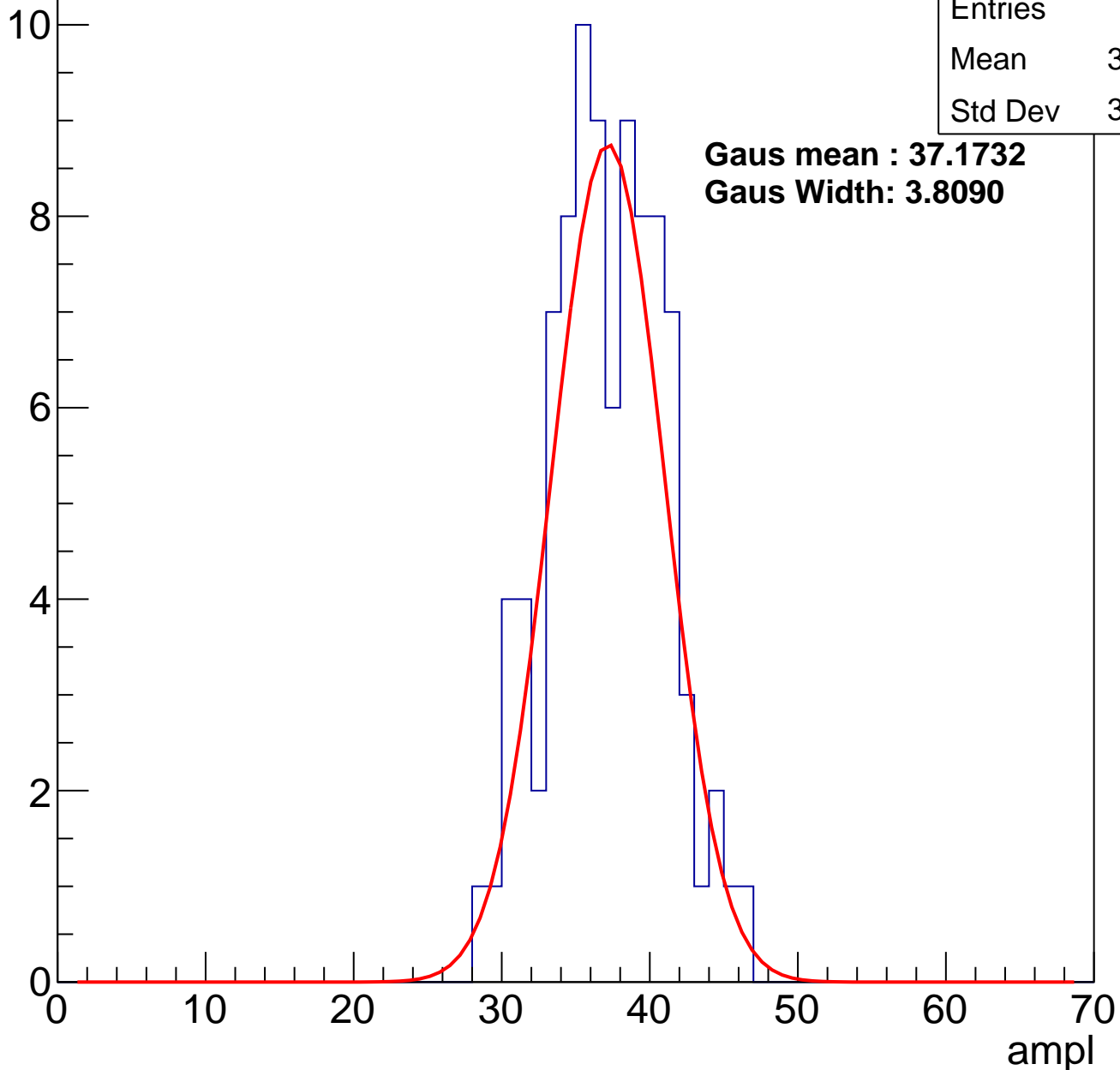
B0L102S, U20-ch59, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 92 |
| Mean | 36.66 |
| Std Dev | 3.812 |

Gaus mean : 37.1732
Gaus Width: 3.8090

Entry



B0L102S, U20-ch59, adc2

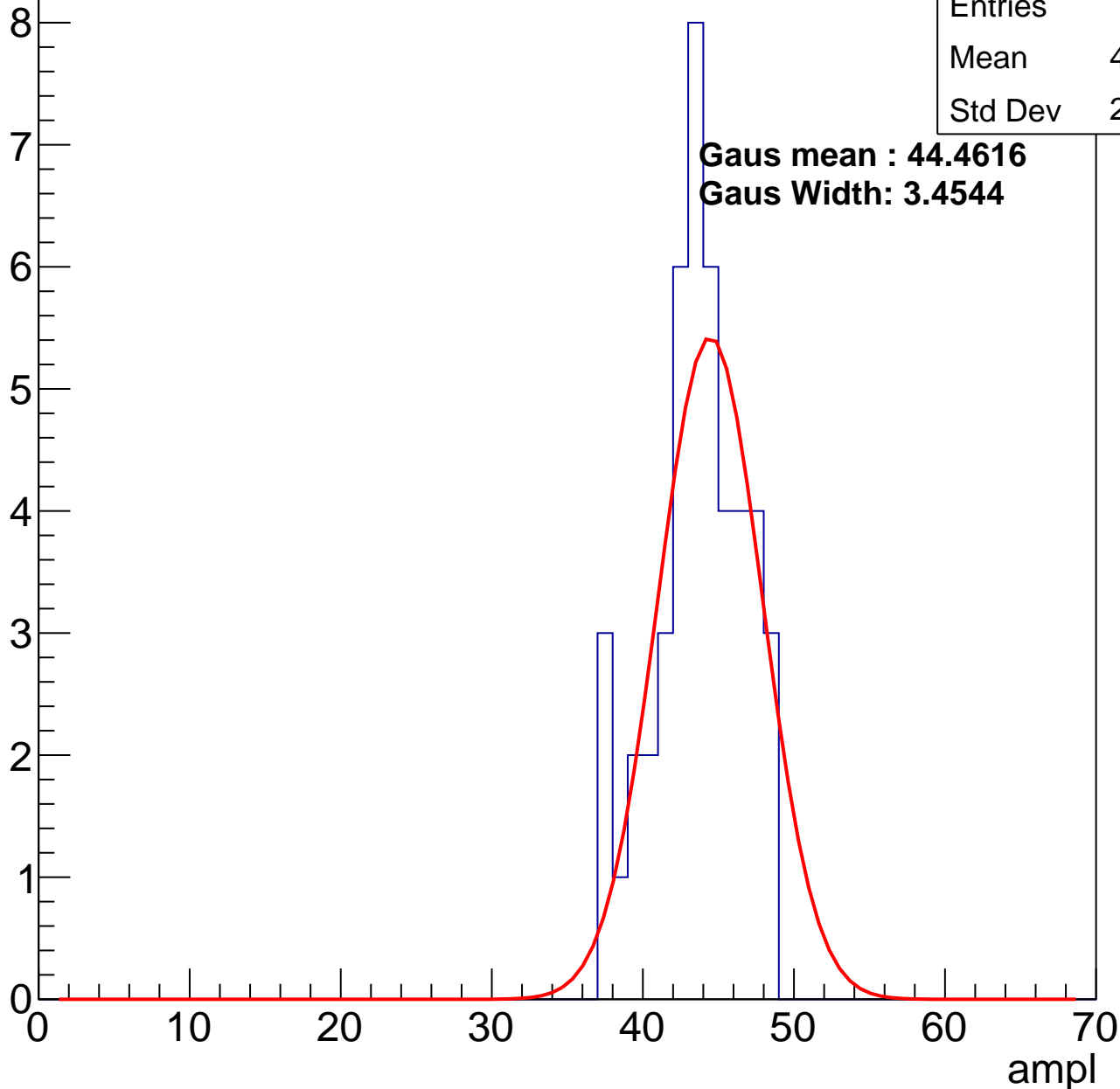
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 43.17 |
| Std Dev | 2.936 |

Gaus mean : 44.4616

Gaus Width: 3.4544

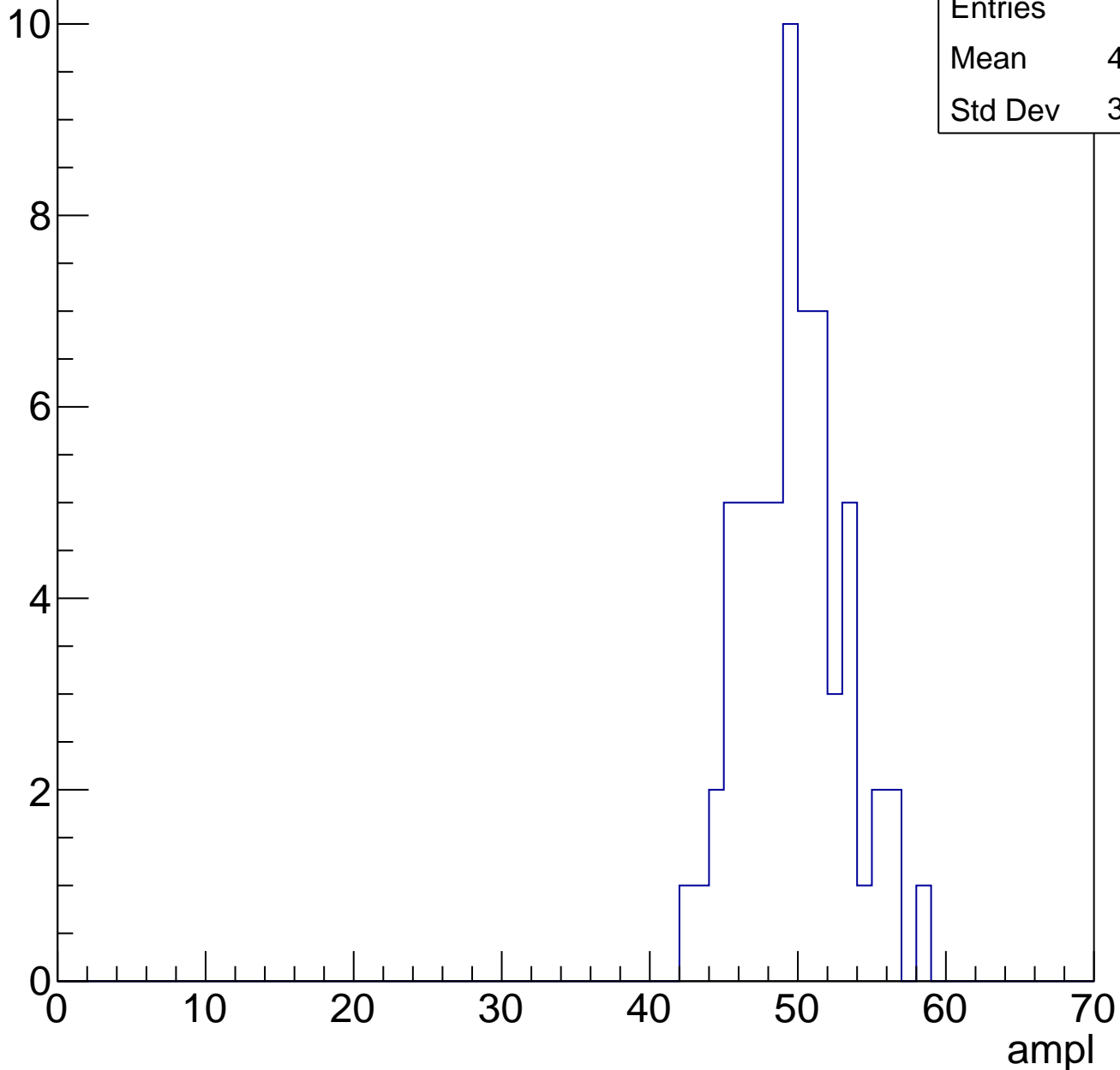


B0L102S, U20-ch59, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.27 |
| Std Dev | 3.356 |

Entry

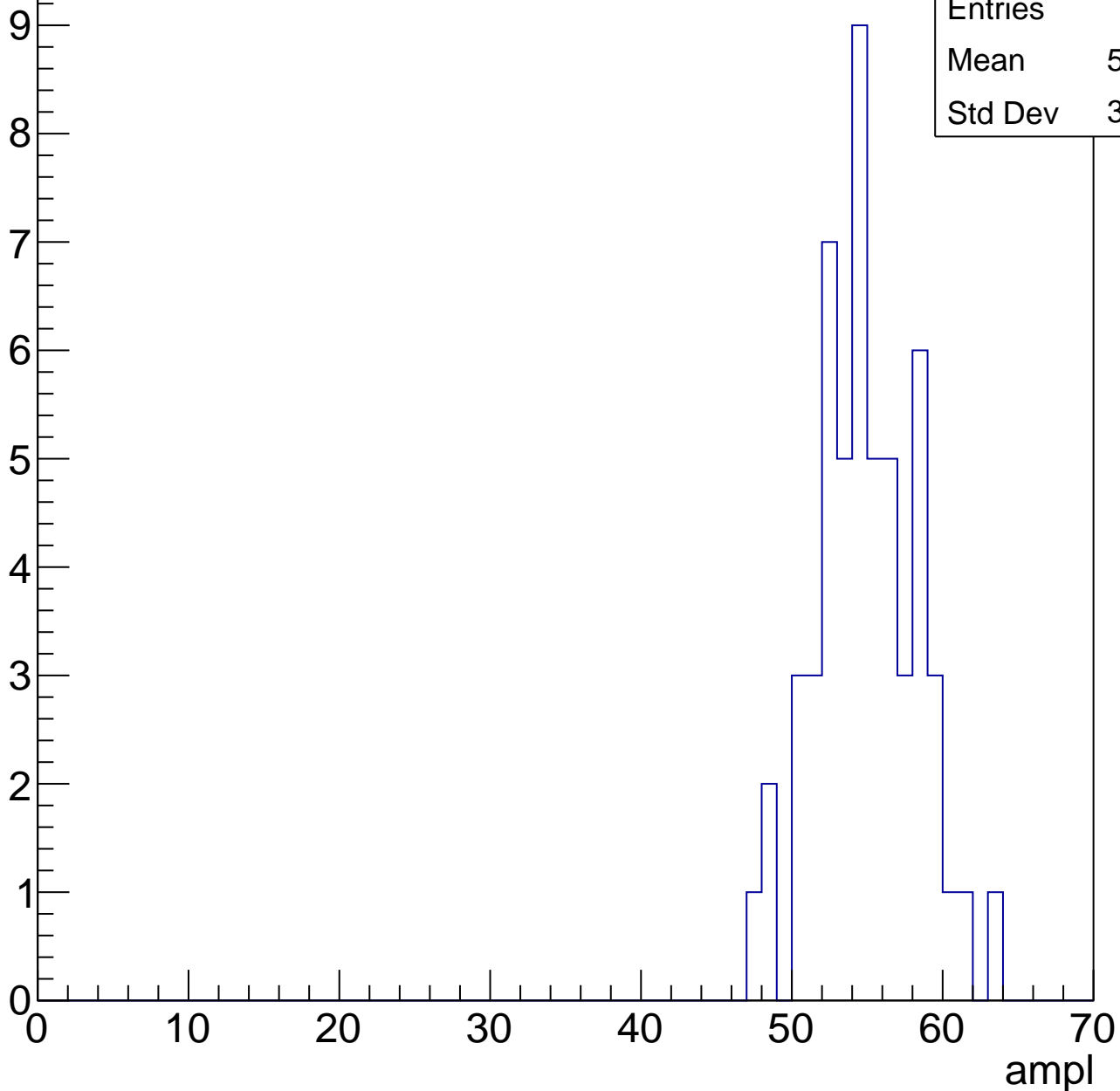


B0L102S, U20-ch59, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.47 |
| Std Dev | 3.319 |

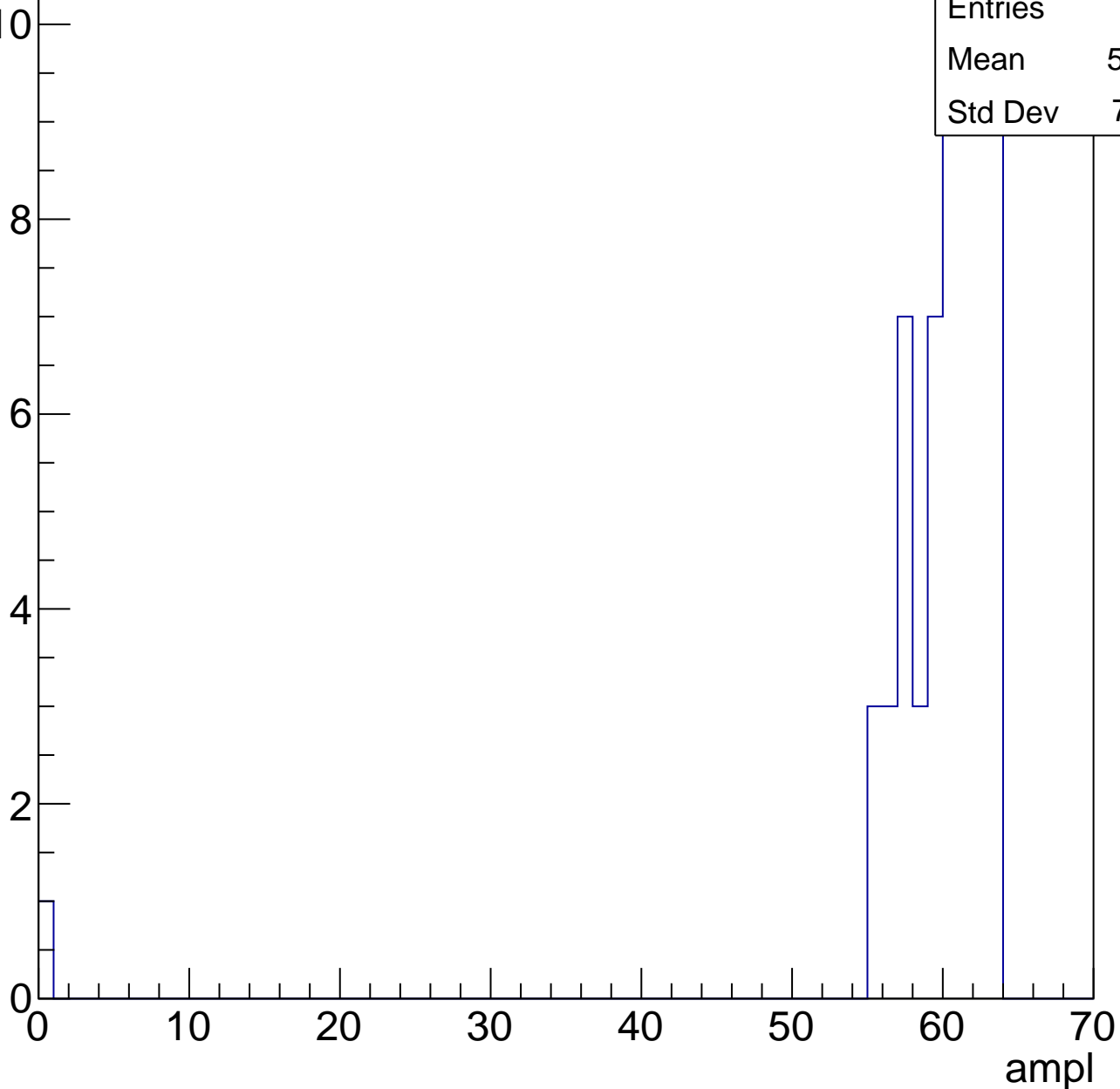


B0L102S, U20-ch59, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 58.93 |
| Std Dev | 7.961 |



B0L102S, U20-ch59, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch59, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch60, adc0

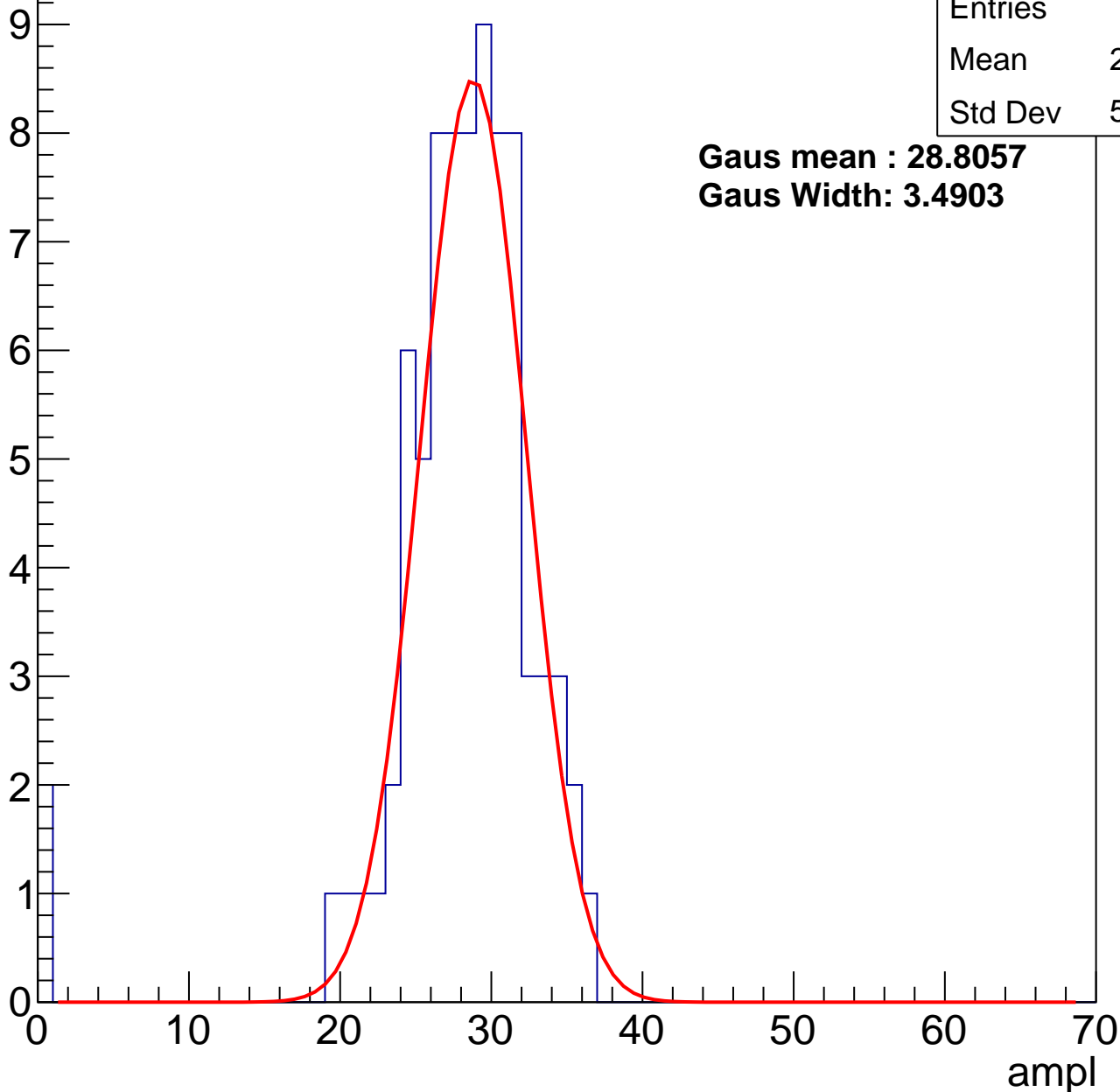
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 27.46 |
| Std Dev | 5.603 |

Gaus mean : 28.8057

Gaus Width: 3.4903



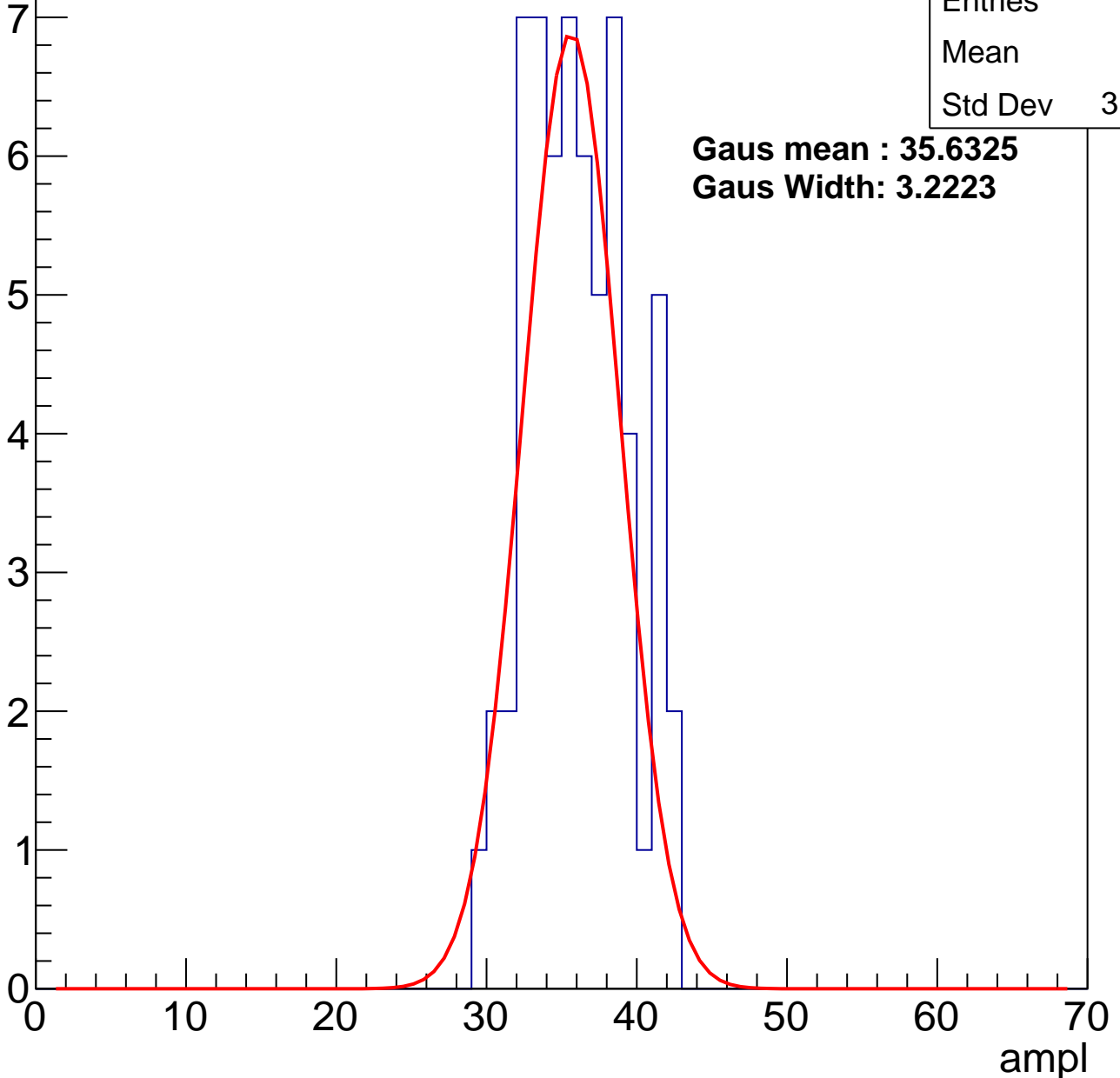
B0L102S, U20-ch60, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.6 |
| Std Dev | 3.235 |

Gaus mean : 35.6325
Gaus Width: 3.2223



B0L102S, U20-ch60, adc2

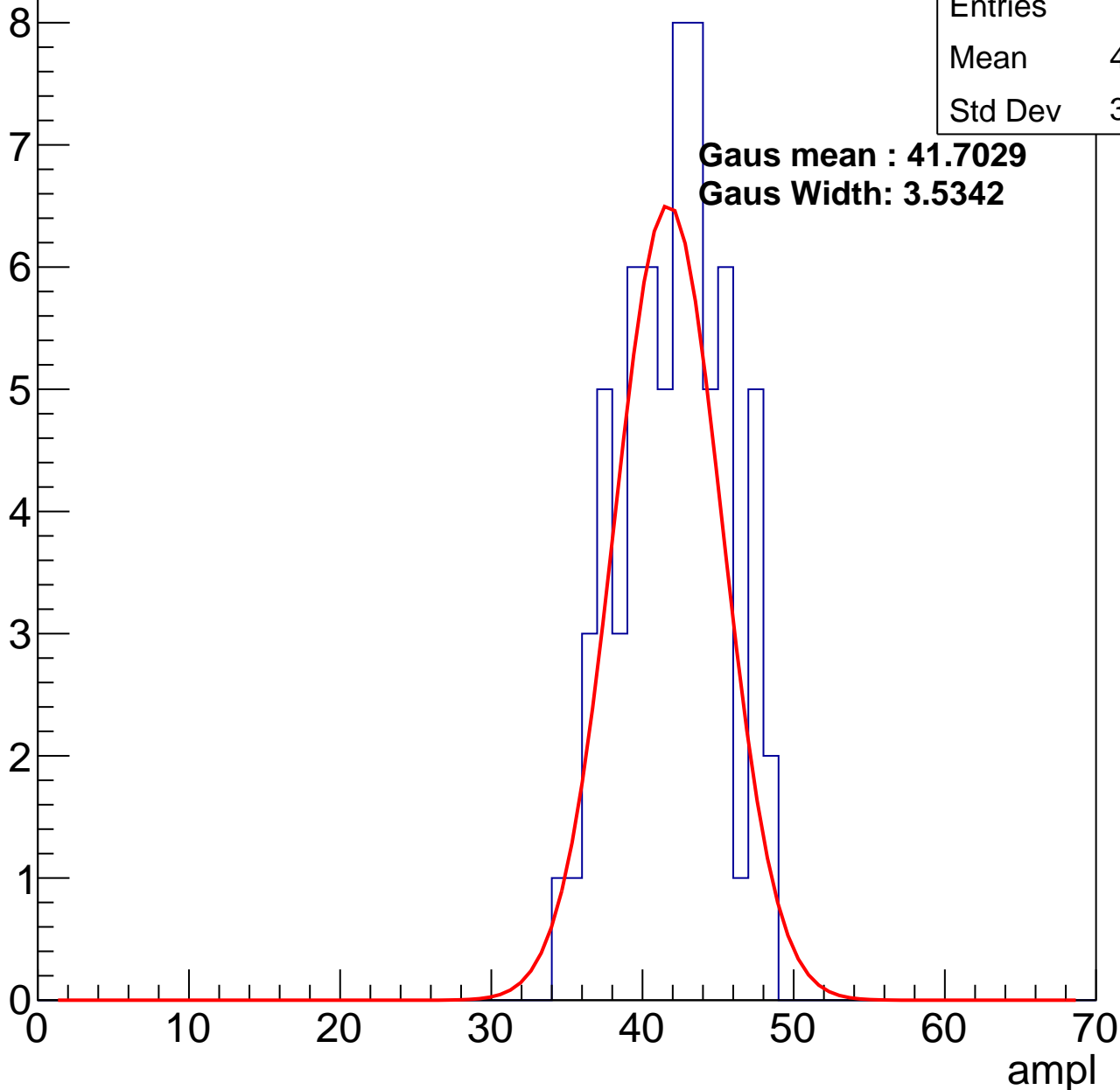
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 41.57 |
| Std Dev | 3.419 |

Gaus mean : 41.7029

Gaus Width: 3.5342

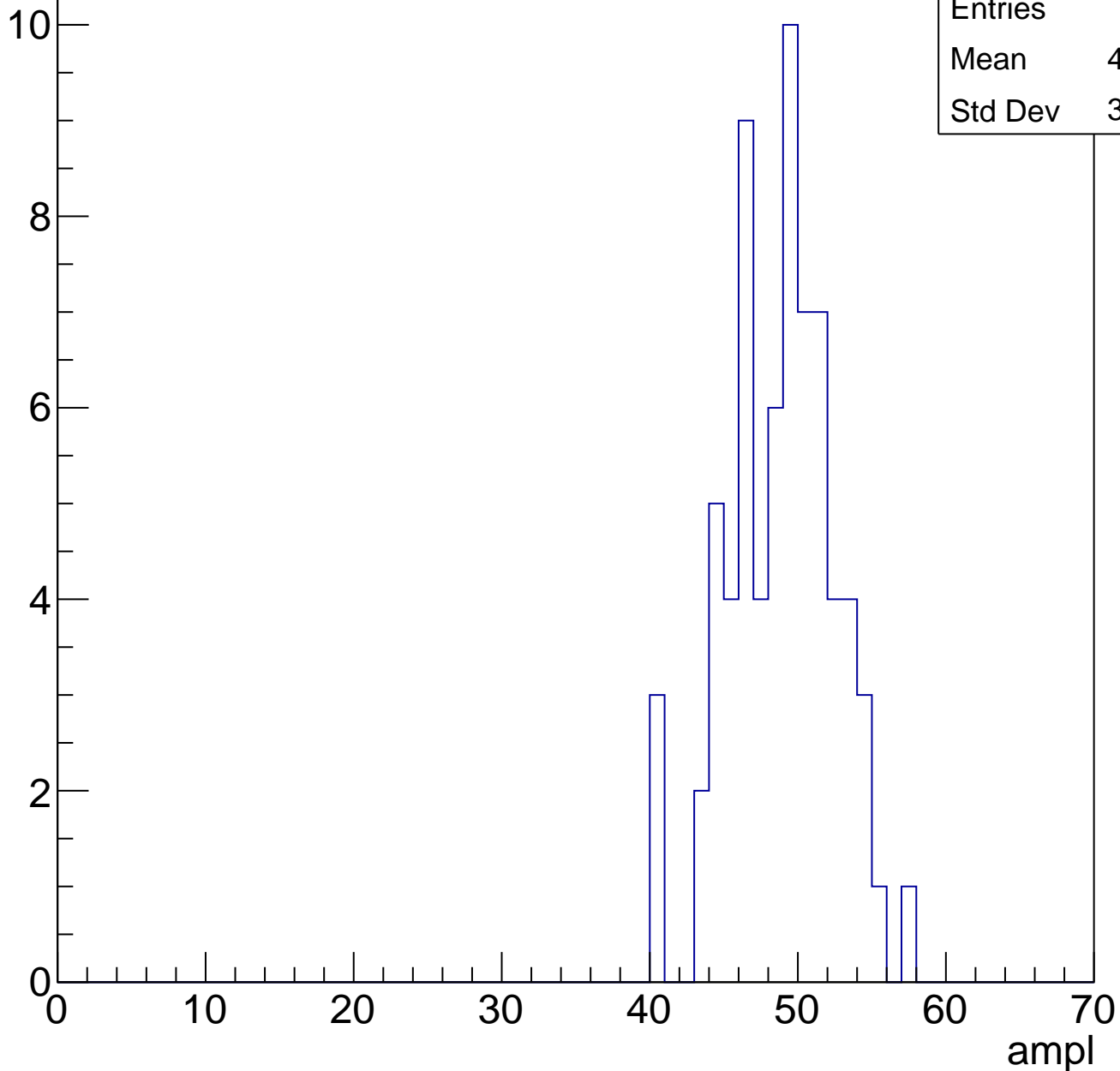


B0L102S, U20-ch60, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 48.39 |
| Std Dev | 3.567 |

Entry

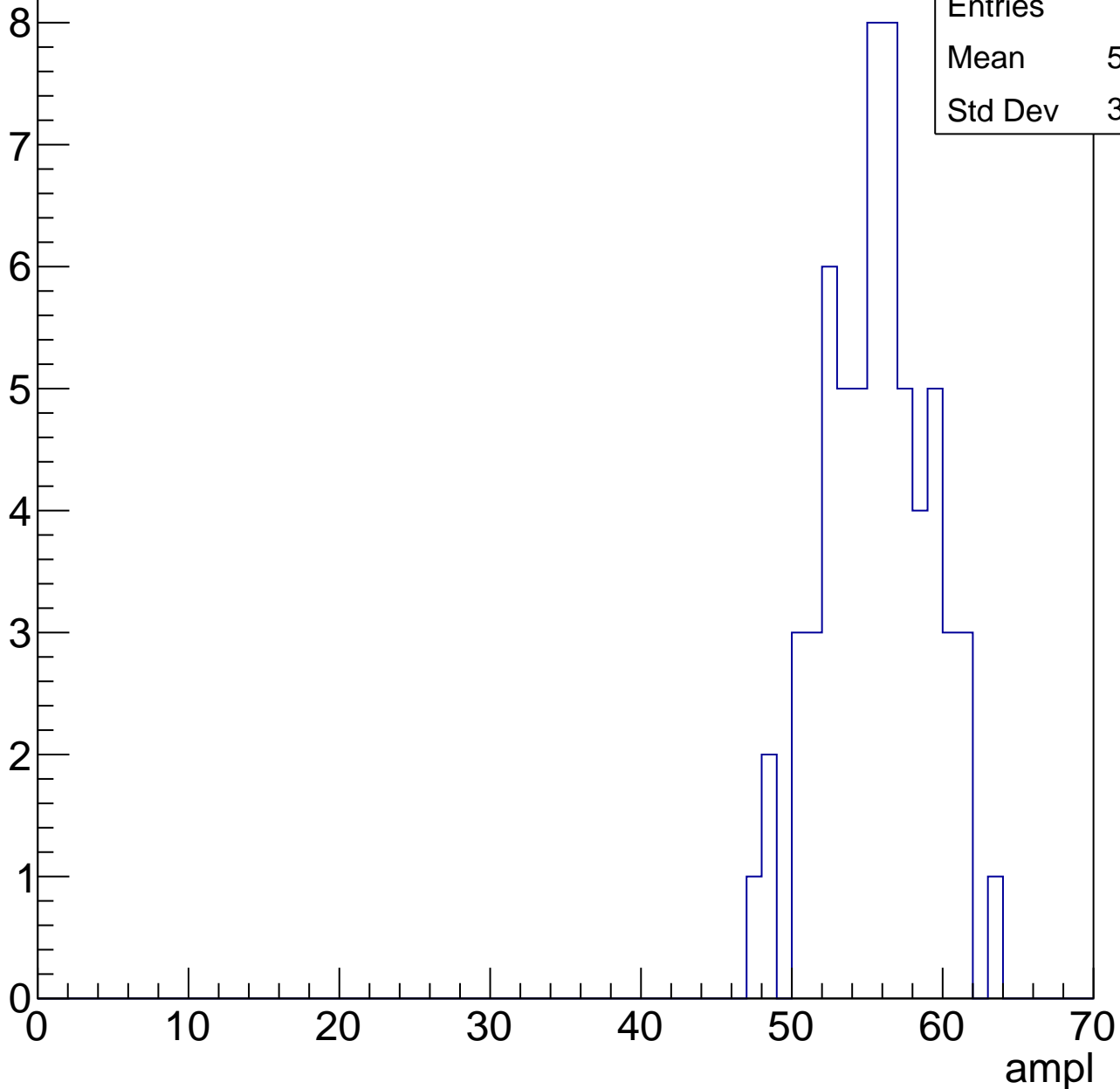


B0L102S, U20-ch60, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 55.15 |
| Std Dev | 3.487 |



B0L102S, U20-ch60, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 57.98

Std Dev 9.315

ampl

0

10

20

30

40

50

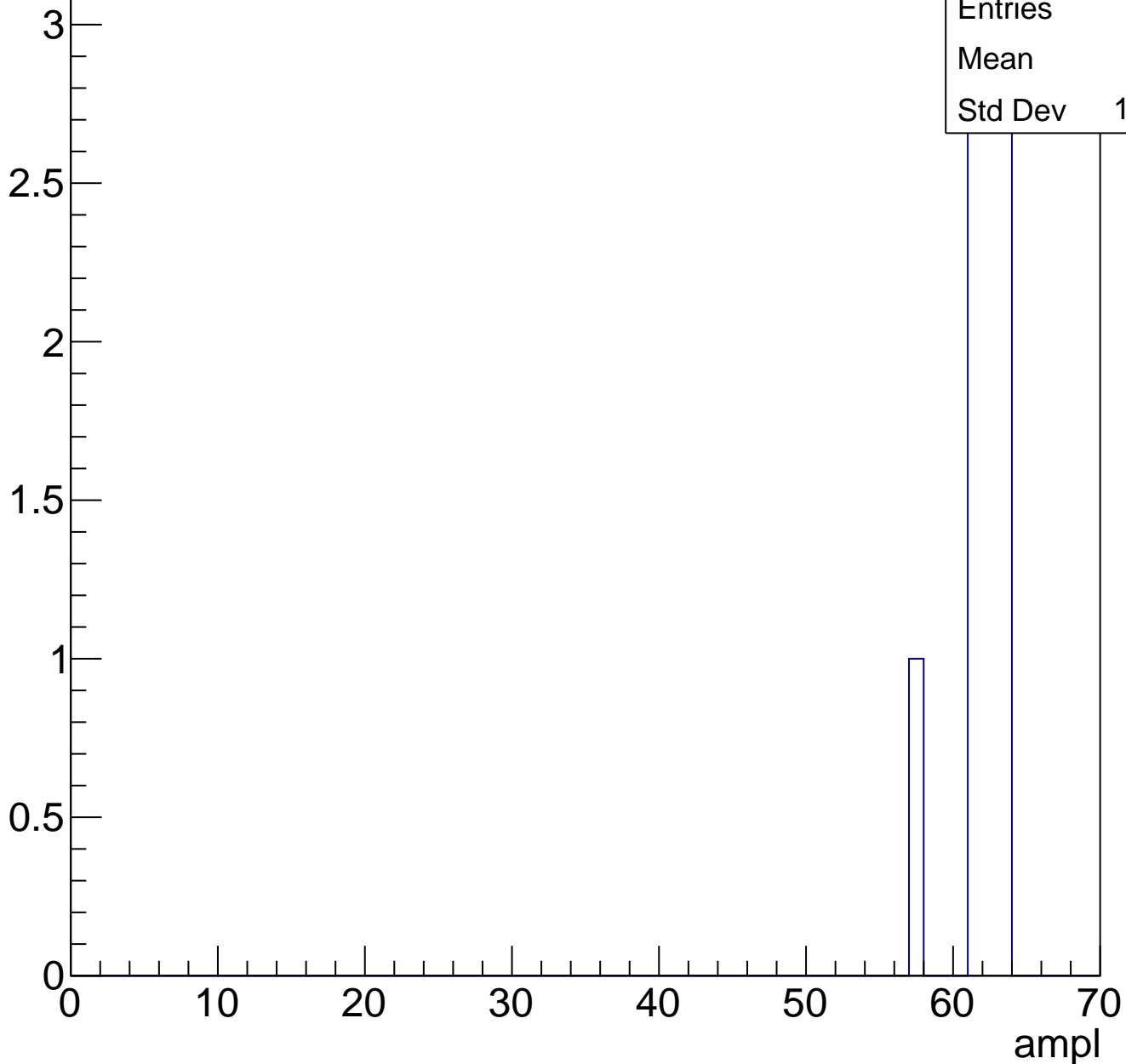
60

70

B0L102S, U20-ch60, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch60, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch61, adc0

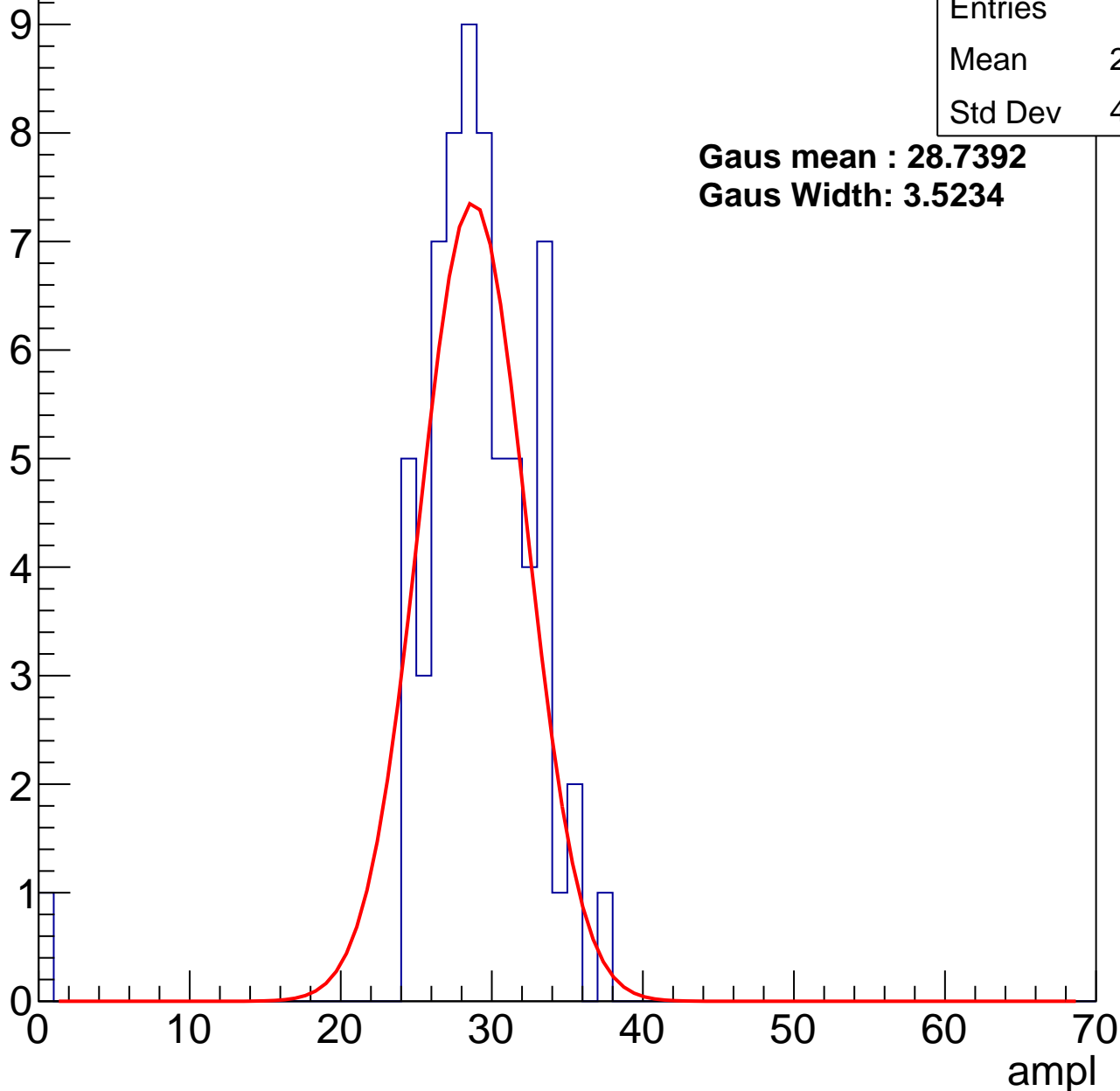
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.52 |
| Std Dev | 4.665 |

Gaus mean : 28.7392

Gaus Width: 3.5234



B0L102S, U20-ch61, adc1

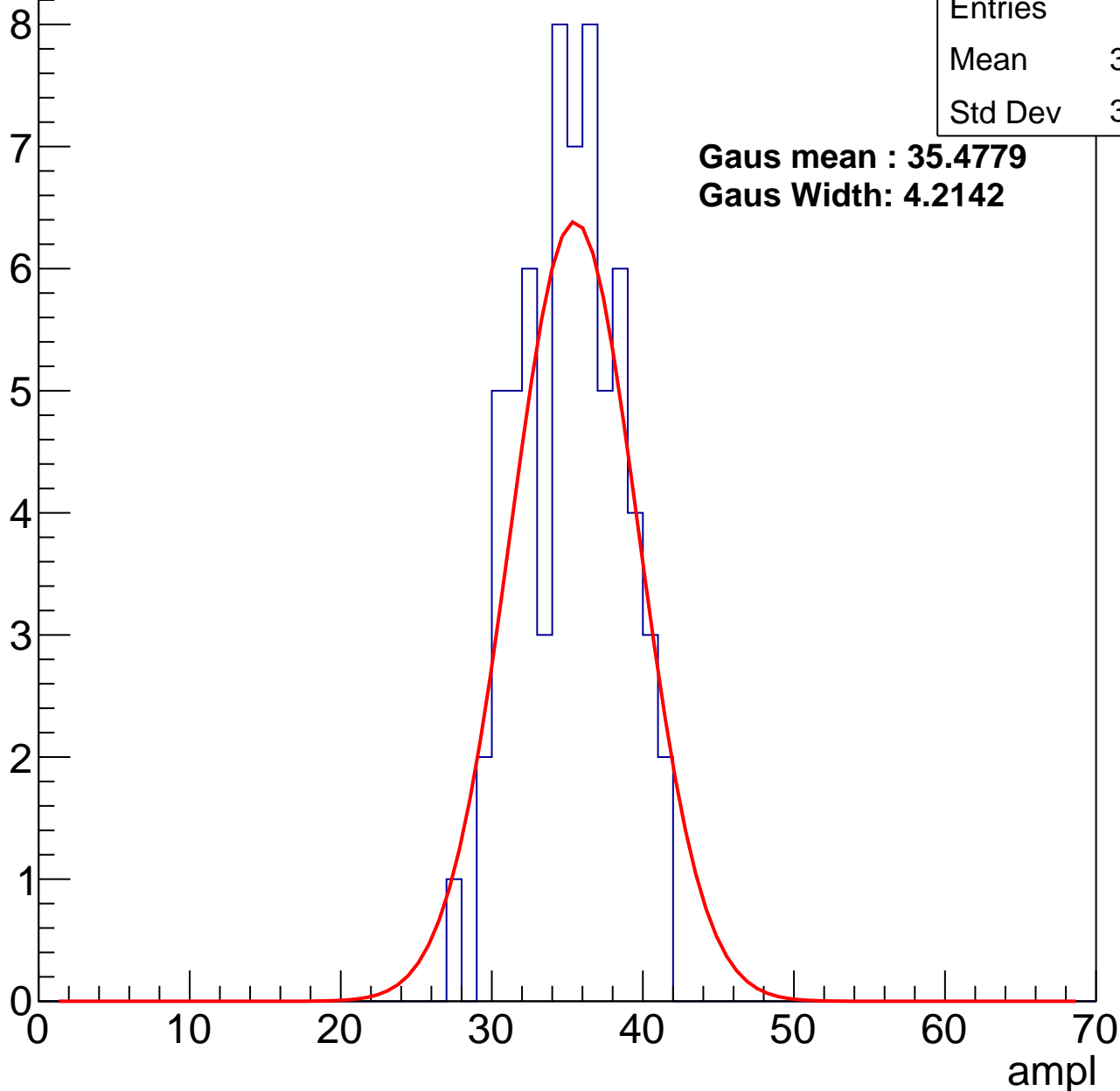
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 34.72 |
| Std Dev | 3.289 |

Gaus mean : 35.4779

Gaus Width: 4.2142



B0L102S, U20-ch61, adc2

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 90 |
| Mean | 42.72 |
| Std Dev | 3.733 |

Gaus mean : 42.9986

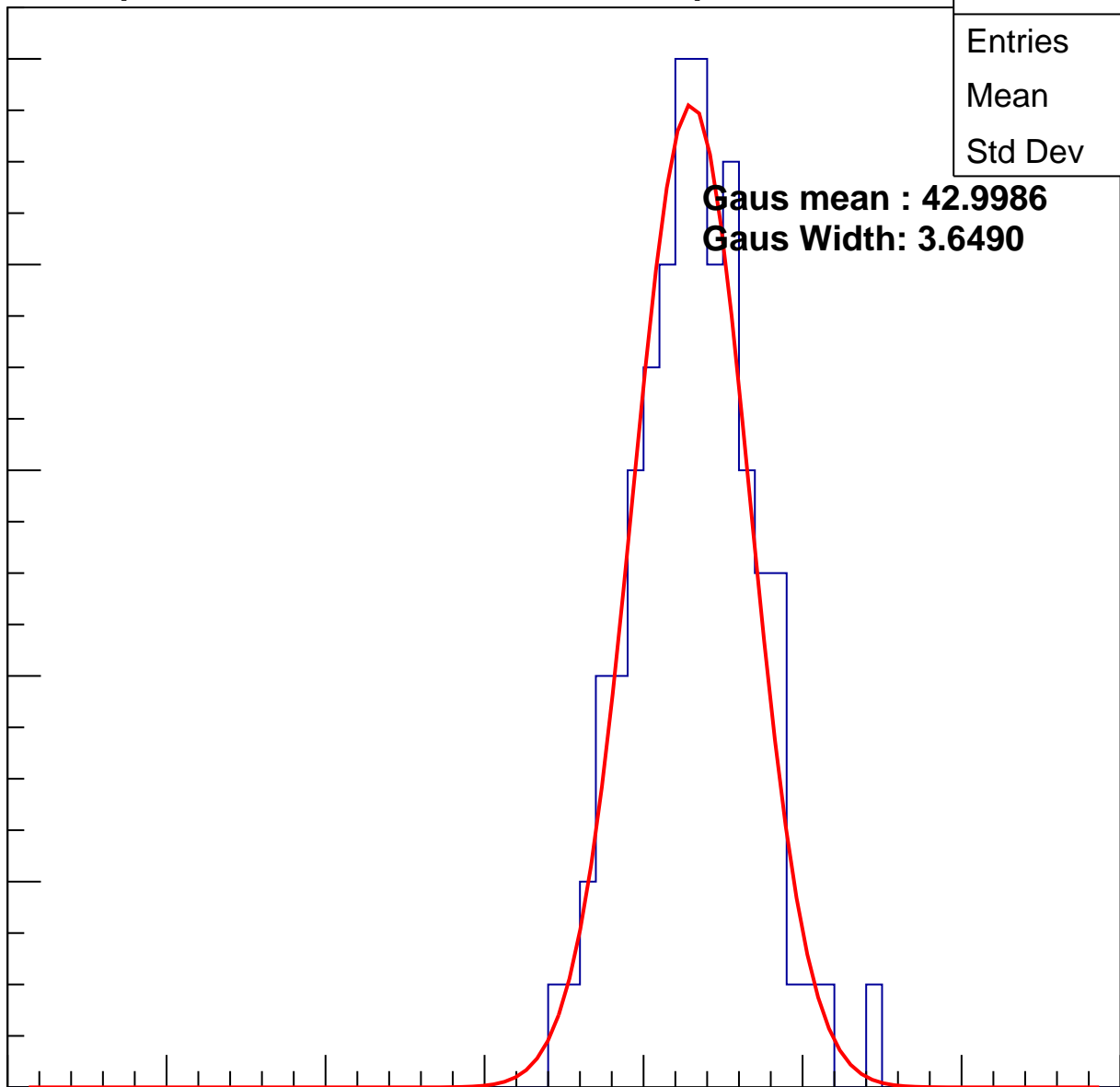
Gaus Width: 3.6490

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

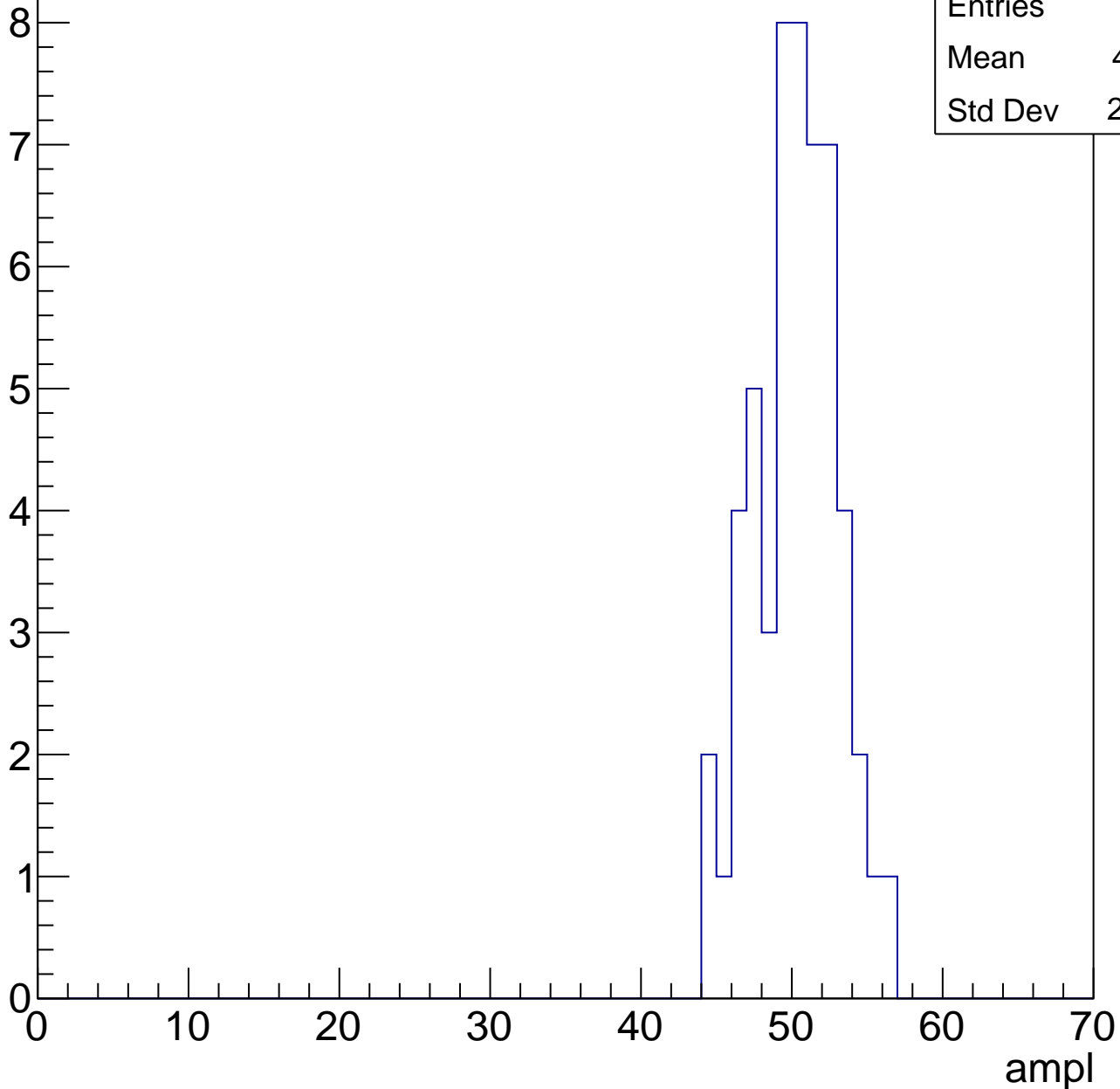


B0L102S, U20-ch61, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 49.81 |
| Std Dev | 2.706 |

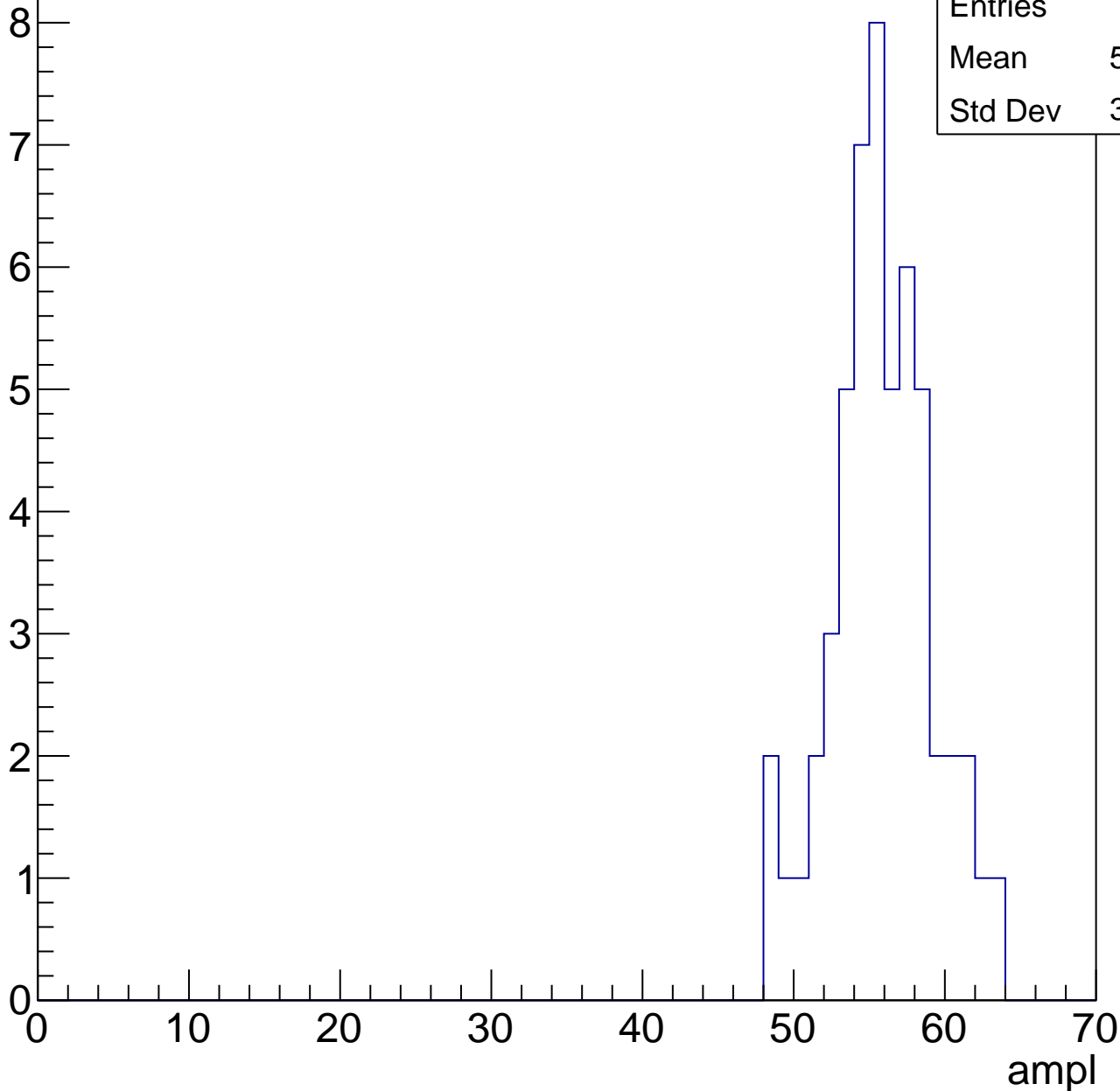


B0L102S, U20-ch61, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.34 |
| Std Dev | 3.308 |

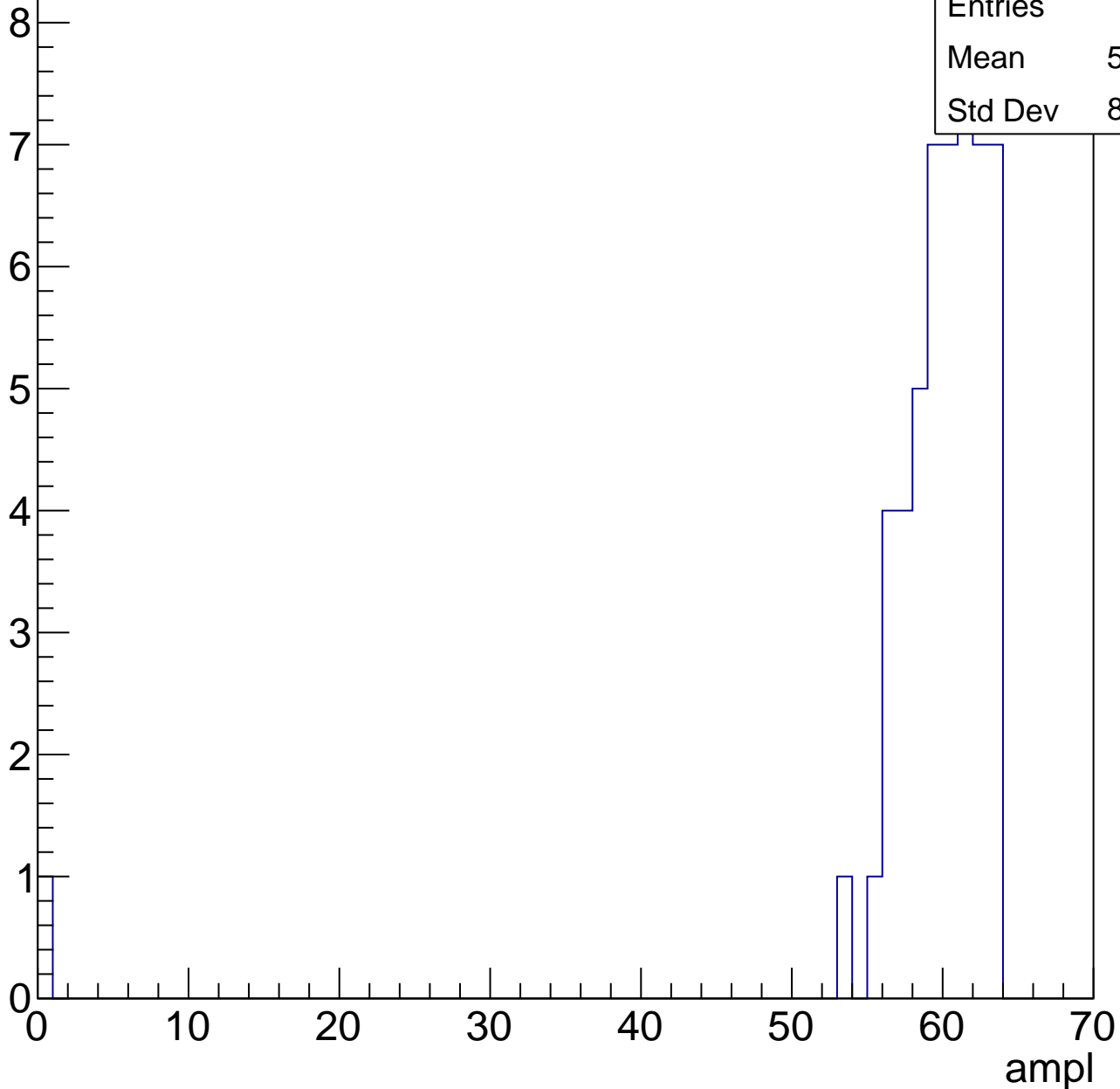


B0L102S, U20-ch61, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.58 |
| Std Dev | 8.543 |



B0L102S, U20-ch61, adc6

calib_packv5_042523_0143.root, FC#12, port B1

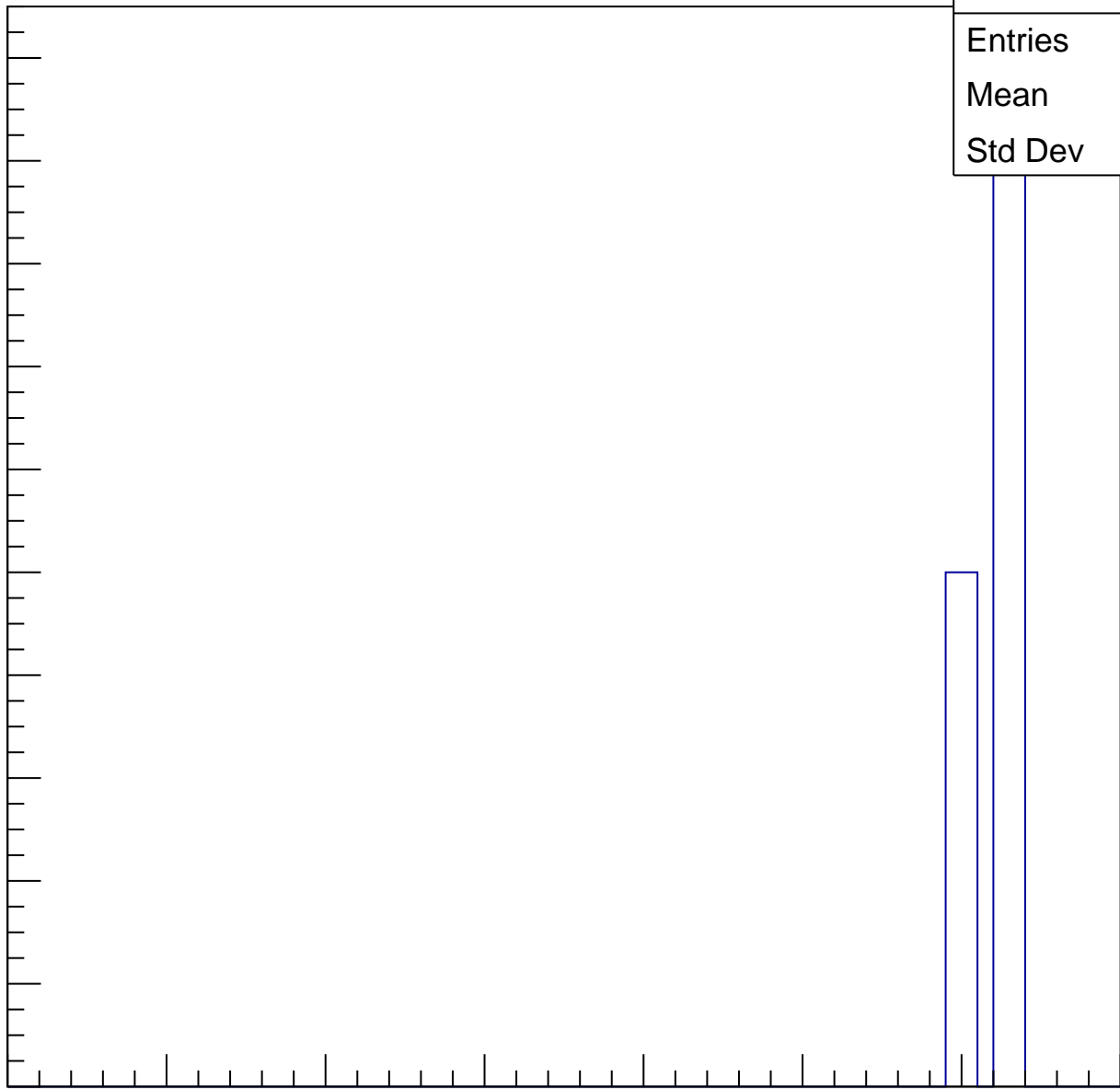
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 6 |
| Mean | 61.5 |
| Std Dev | 1.5 |

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch61, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch62, adc0

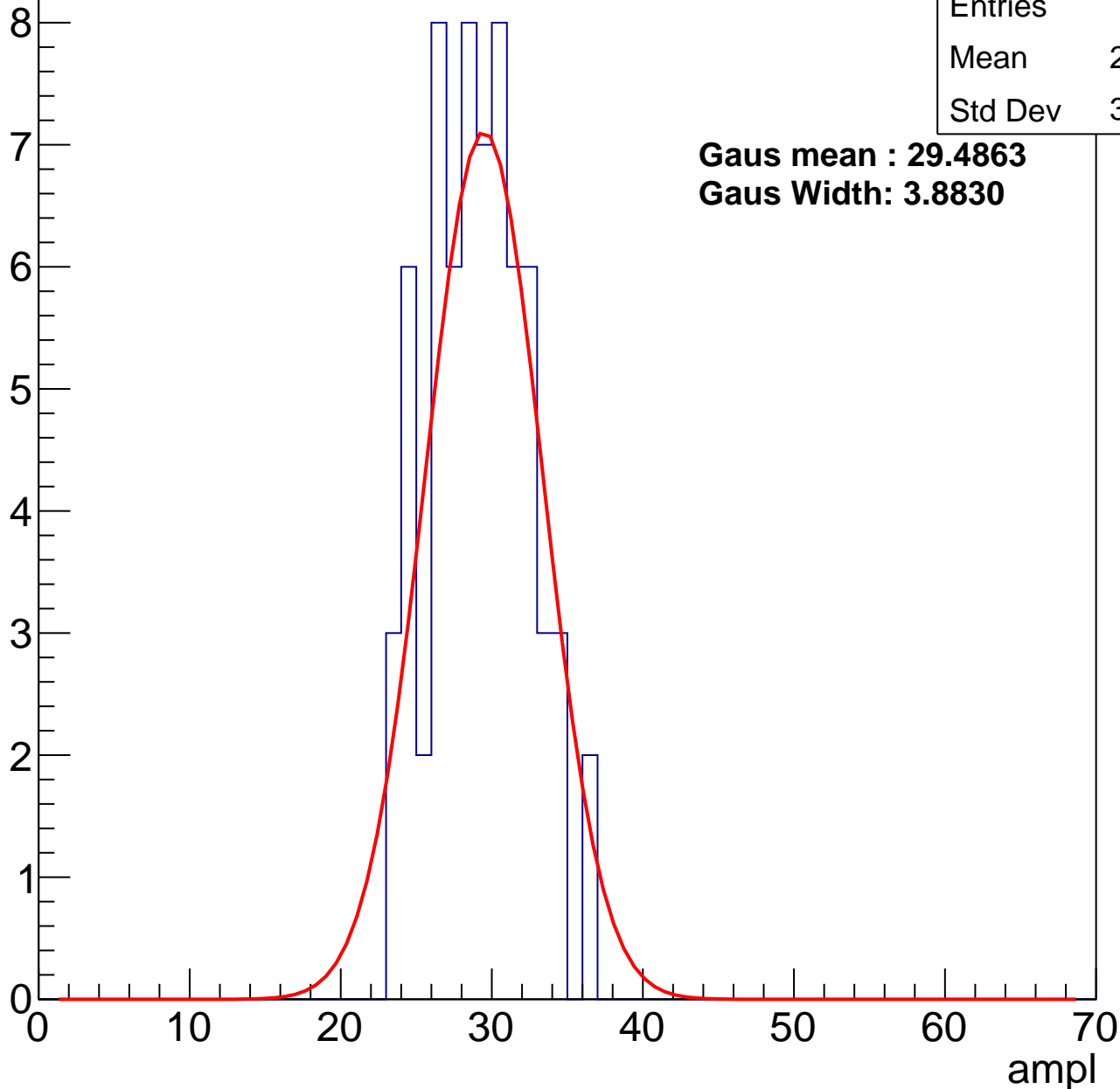
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.69 |
| Std Dev | 3.196 |

Gaus mean : 29.4863

Gaus Width: 3.8830



B0L102S, U20-ch62, adc1

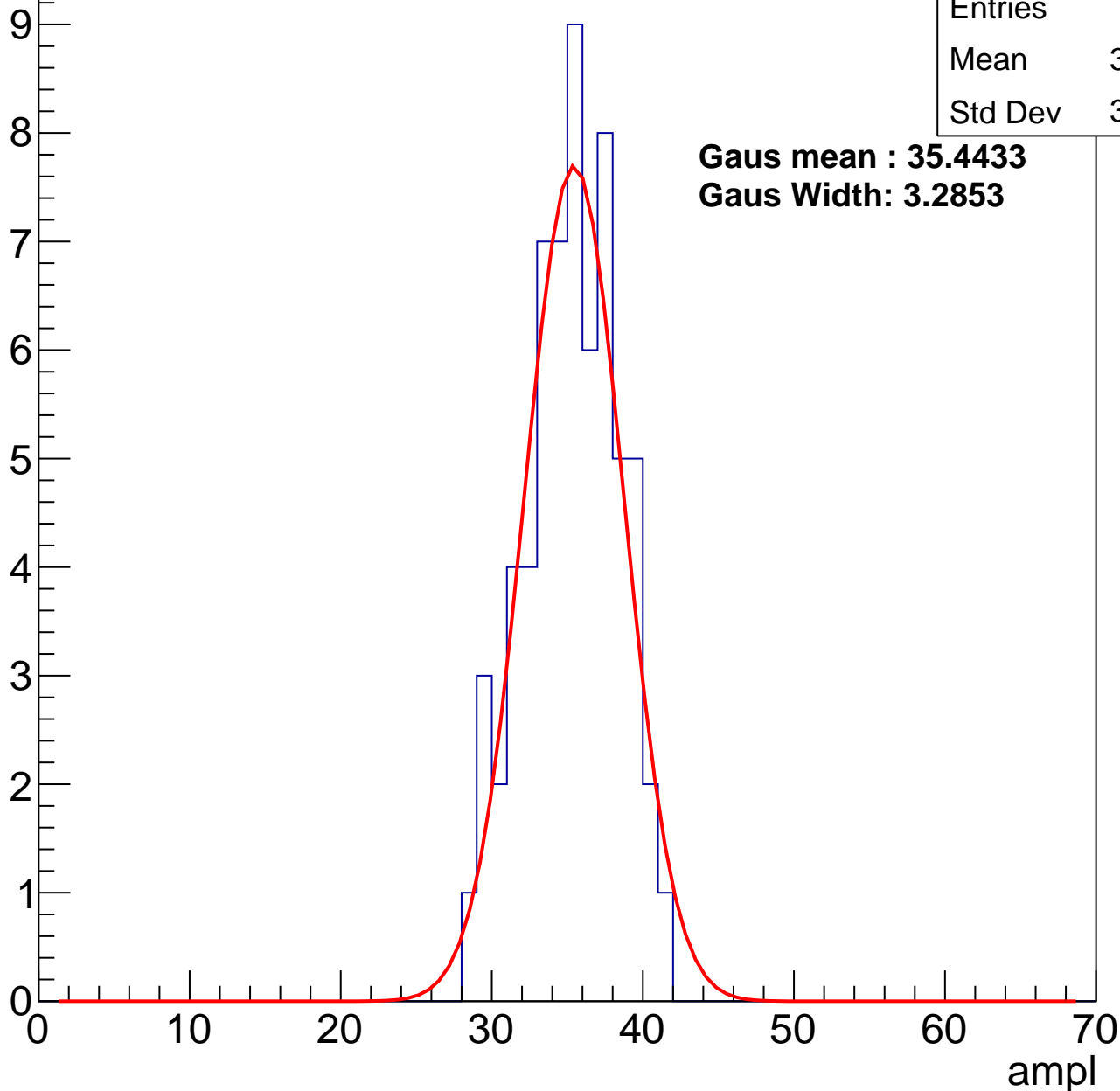
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 34.83 |
| Std Dev | 3.034 |

Gaus mean : 35.4433

Gaus Width: 3.2853



B0L102S, U20-ch62, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.4 |
| Std Dev | 3.204 |

Gaus mean : 43.3039

Gaus Width: 3.8915

10

8

6

4

2

0

0

10

20

30

40

50

60

70

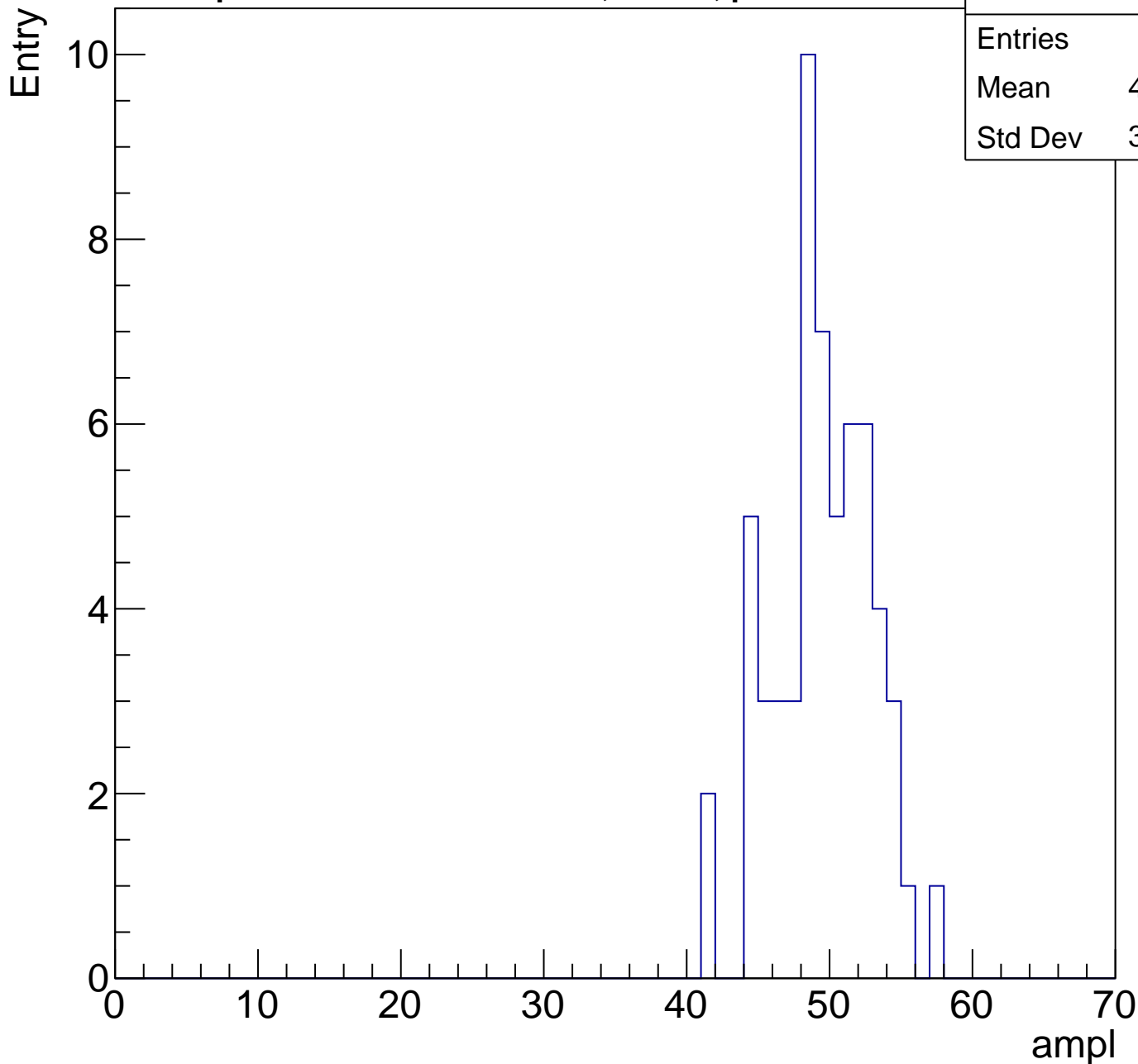
ampl



B0L102S, U20-ch62, adc3

calib_packv5_042523_0143.root, FC#12, port B1

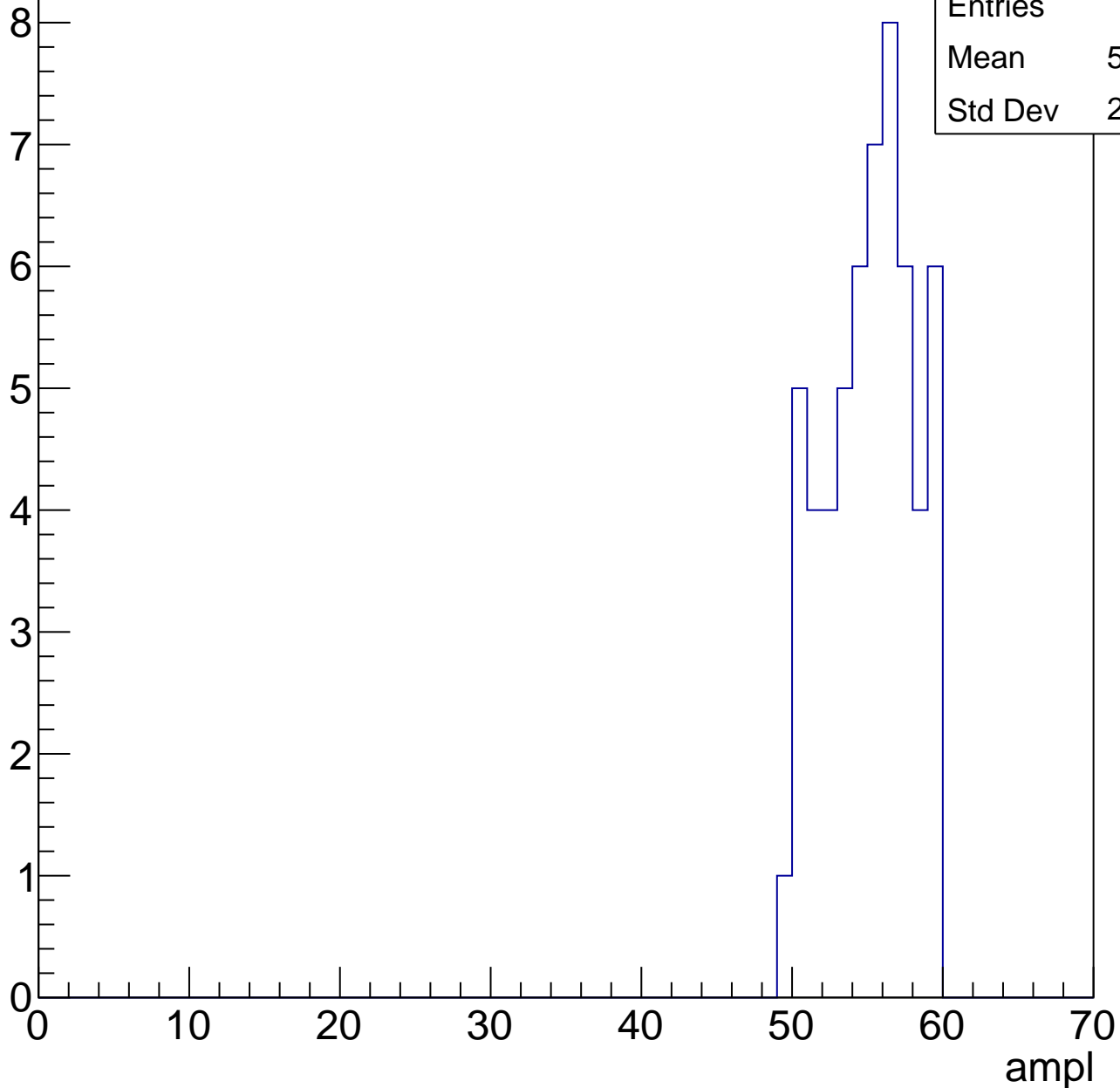
| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.03 |
| Std Dev | 3.375 |



B0L102S, U20-ch62, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

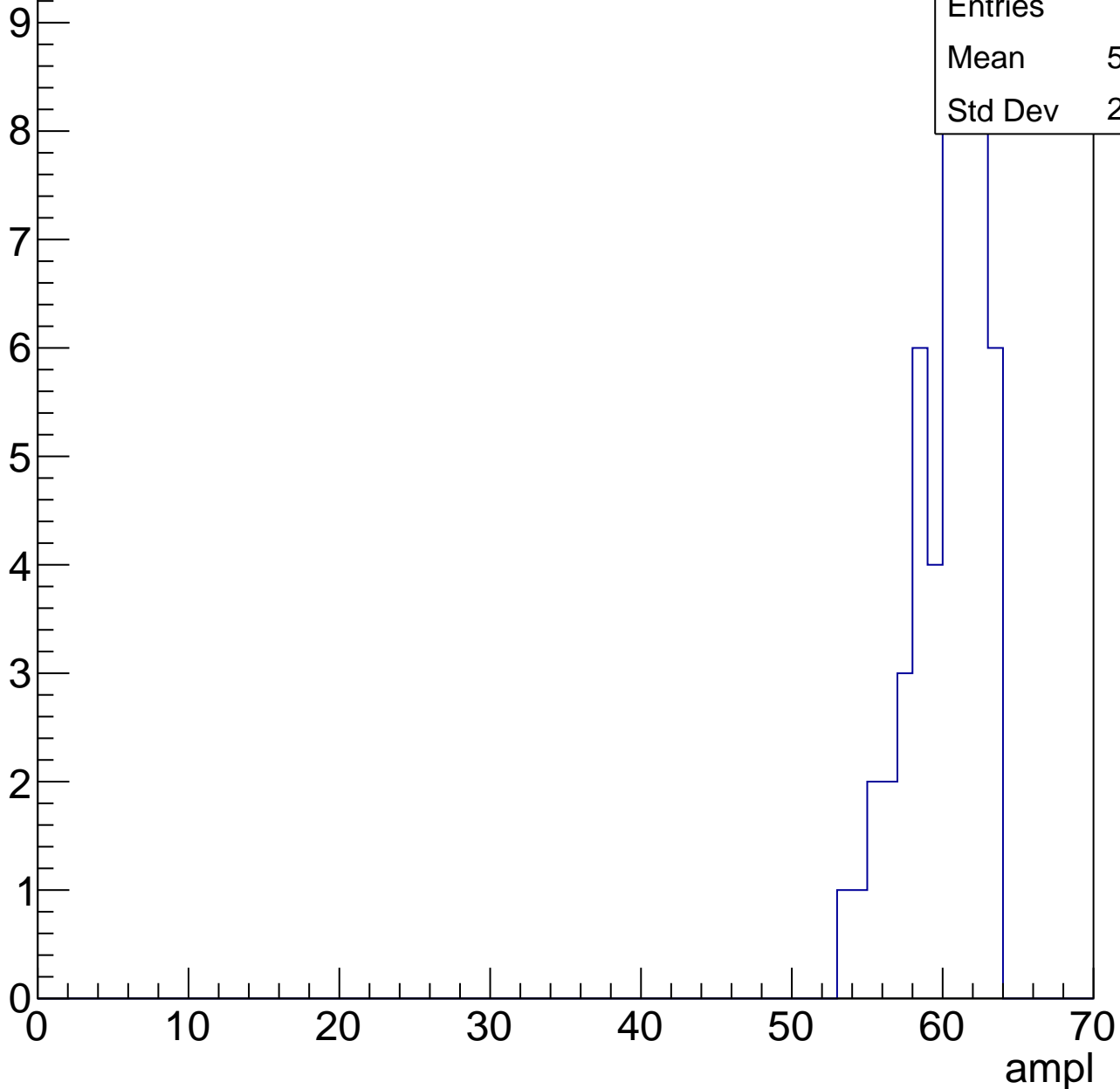


| | |
|---------|-------|
| Entries | 56 |
| Mean | 54.66 |
| Std Dev | 2.818 |

B0L102S, U20-ch62, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

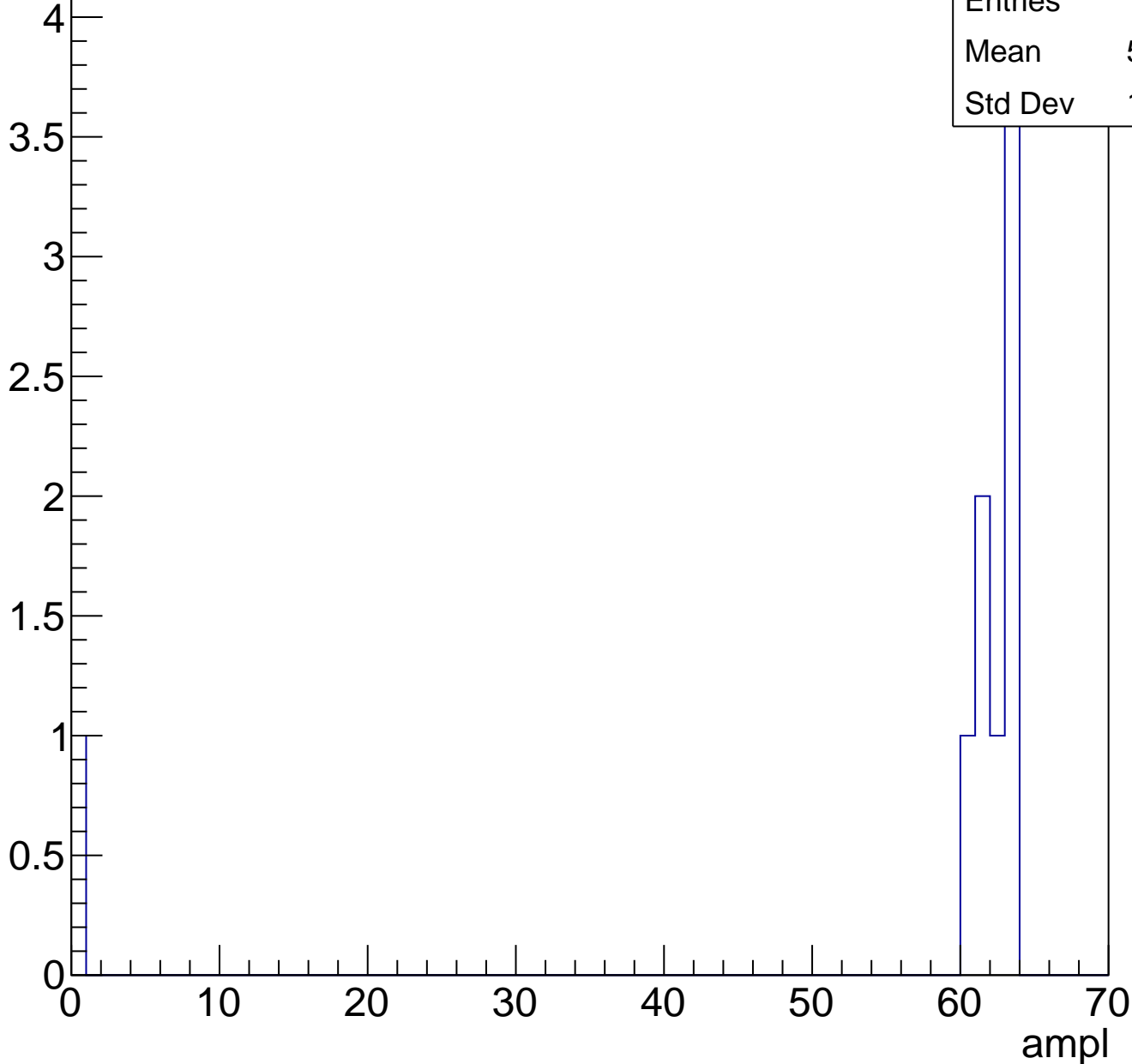


| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.76 |
| Std Dev | 2.494 |

B0L102S, U20-ch62, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch62, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch63, adc0

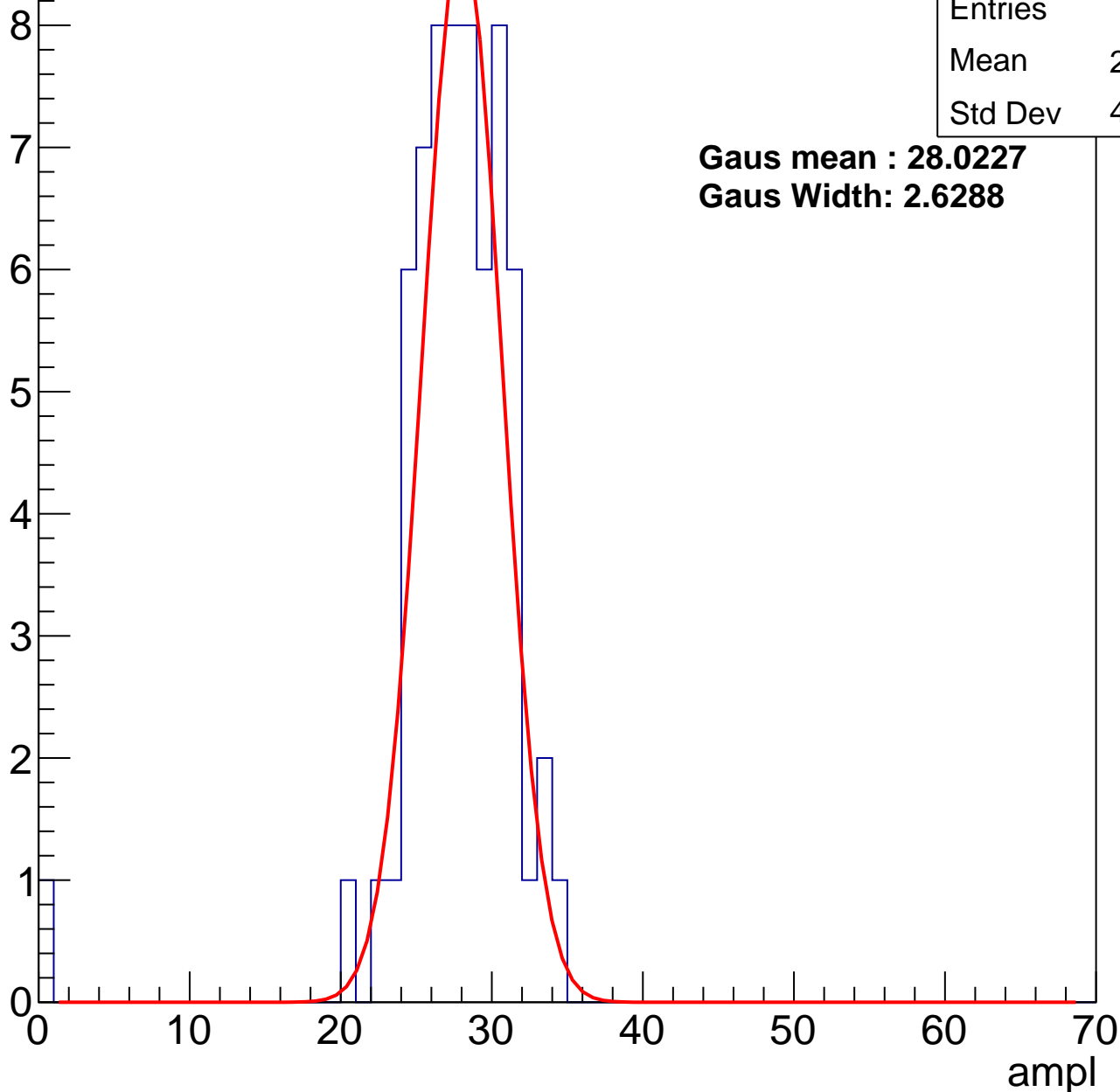
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 27.14 |
| Std Dev | 4.392 |

Gaus mean : 28.0227

Gaus Width: 2.6288



B0L102S, U20-ch63, adc1

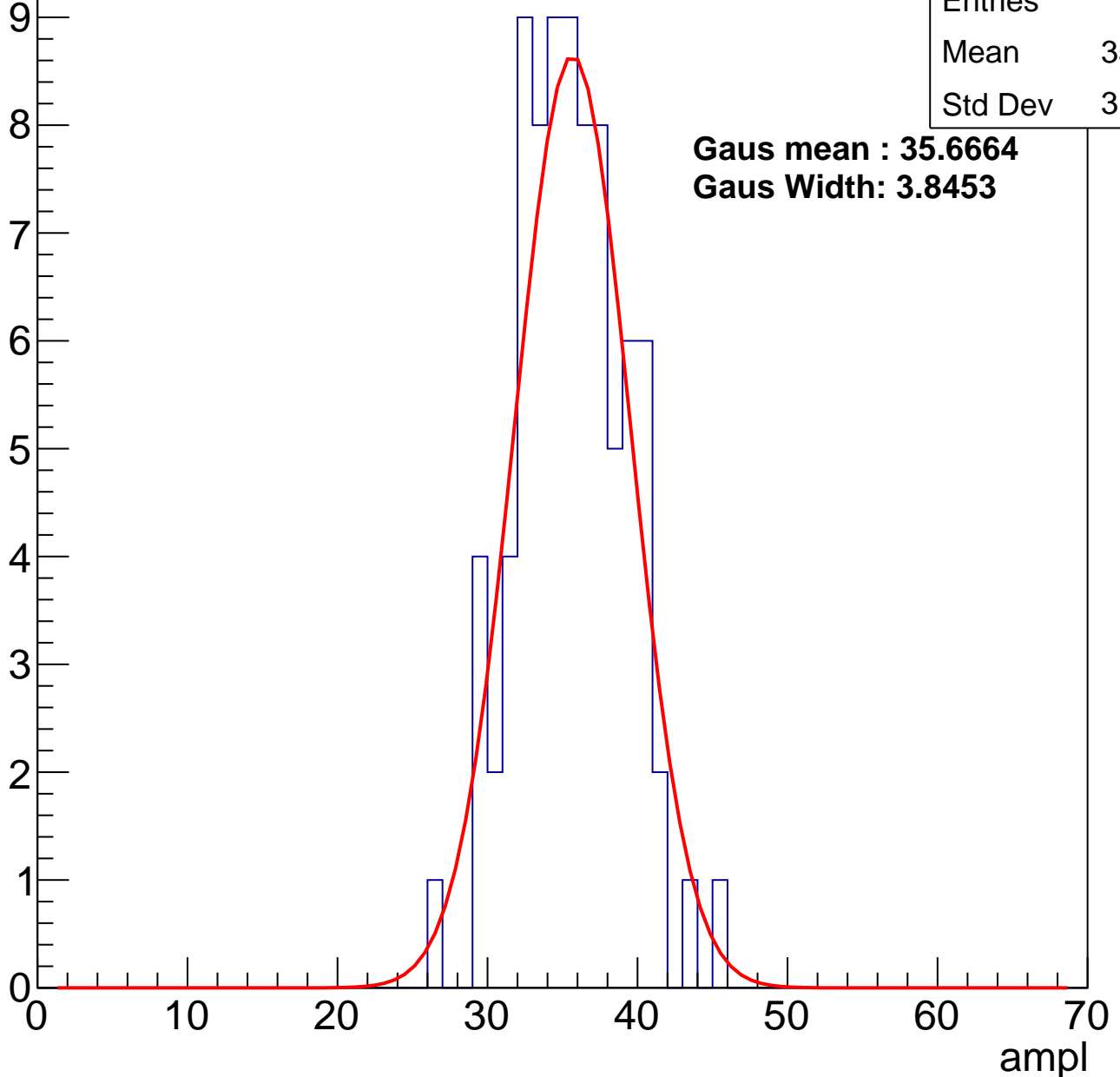
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 35.14 |
| Std Dev | 3.516 |

Gaus mean : 35.6664

Gaus Width: 3.8453



B0L102S, U20-ch63, adc2

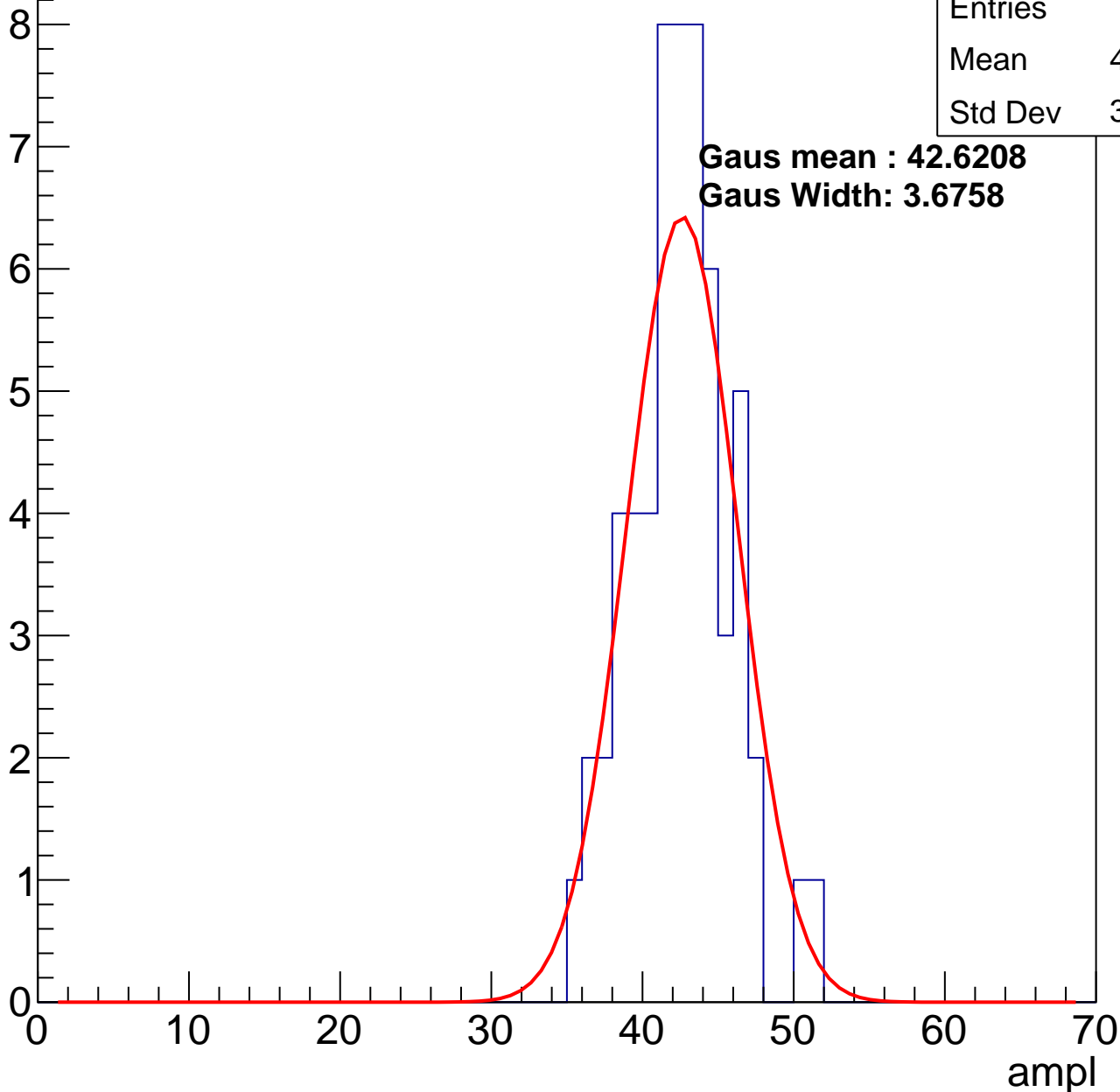
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.05 |
| Std Dev | 3.265 |

Gaus mean : 42.6208

Gaus Width: 3.6758



B0L102S, U20-ch63, adc3

calib_packv5_042523_0143.root, FC#12, port B1

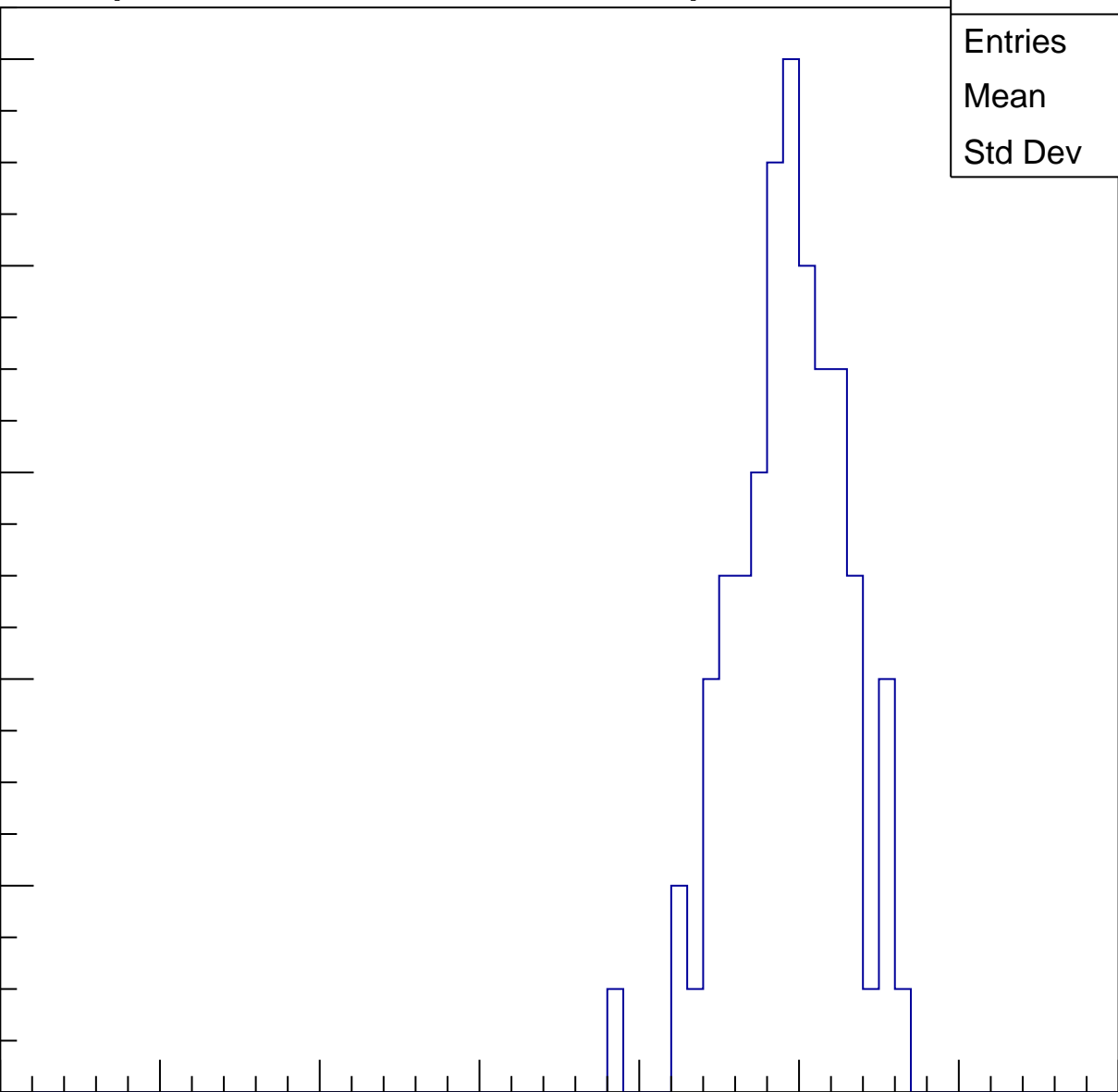
| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.89 |
| Std Dev | 3.459 |

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

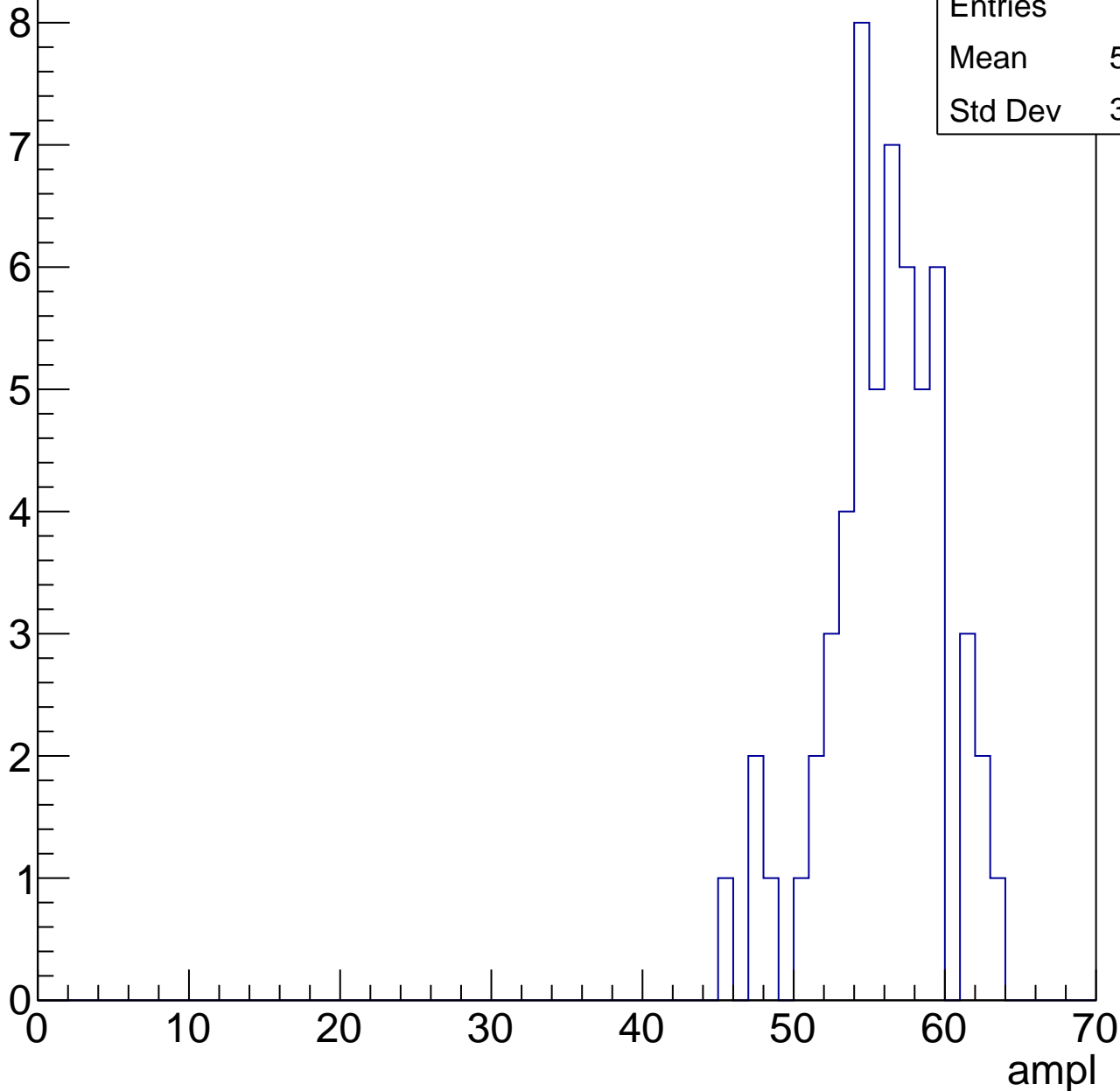


B0L102S, U20-ch63, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.47 |
| Std Dev | 3.779 |

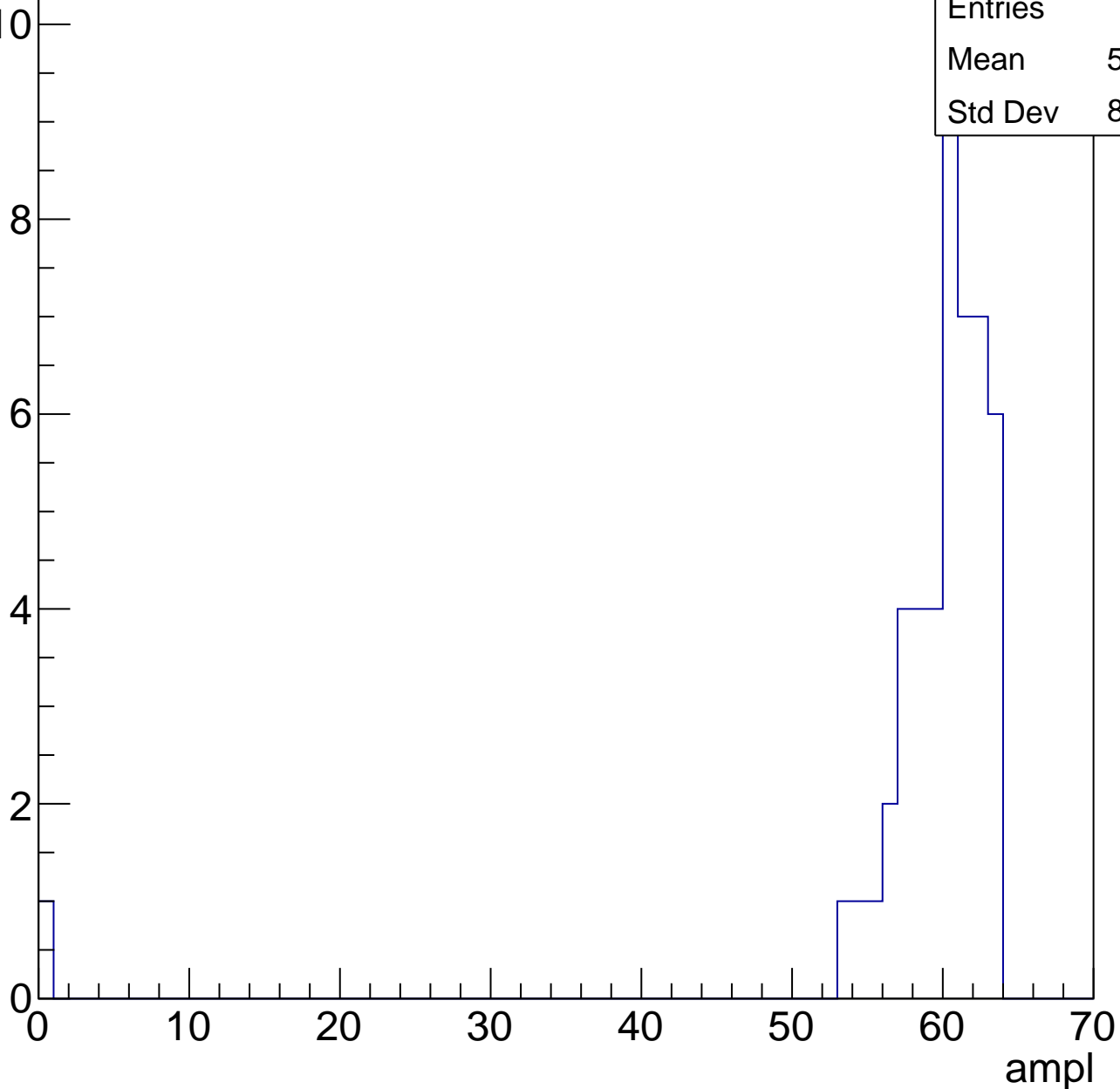


B0L102S, U20-ch63, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

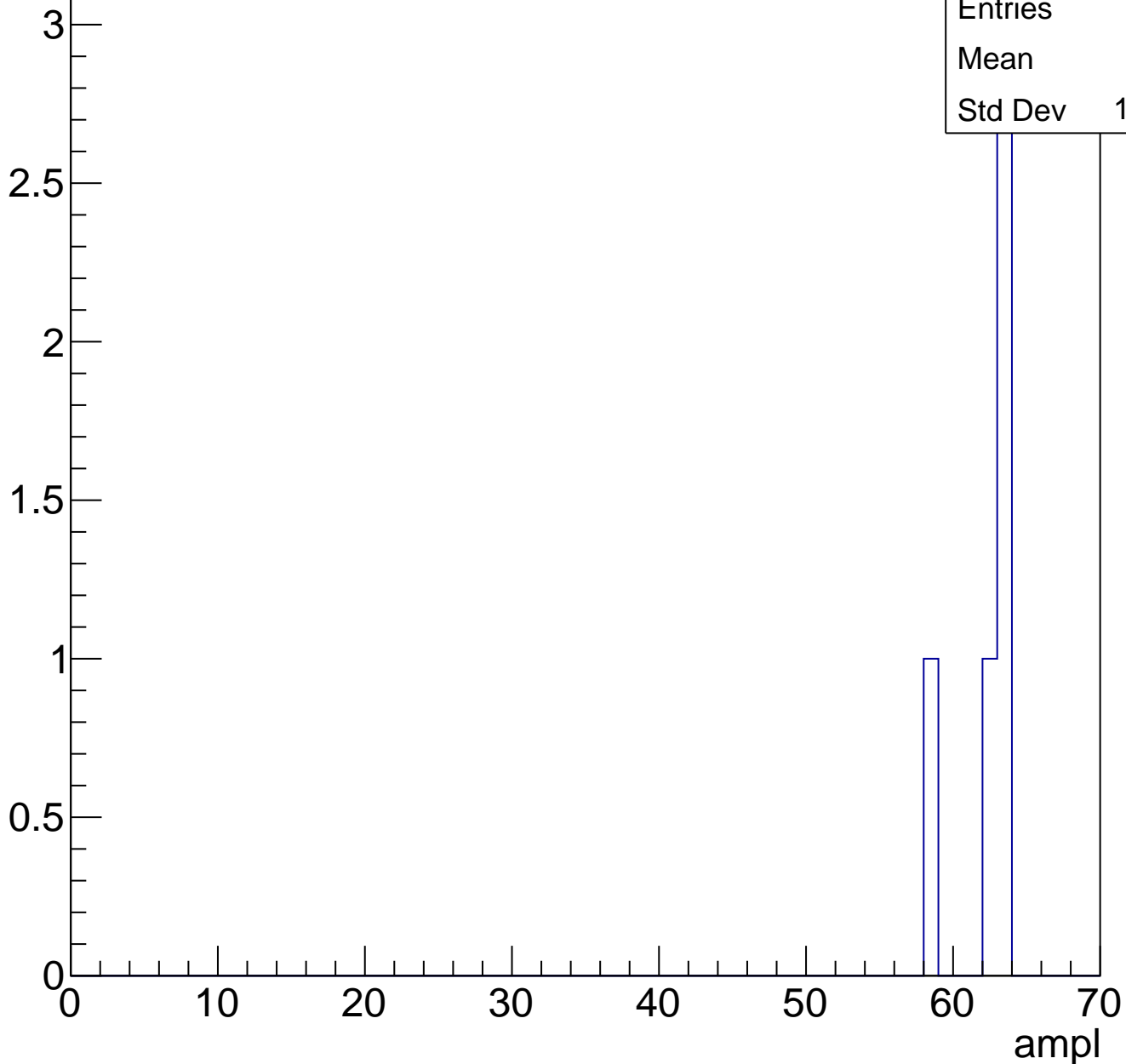
| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.52 |
| Std Dev | 8.876 |



B0L102S, U20-ch63, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch63, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch64, adc0

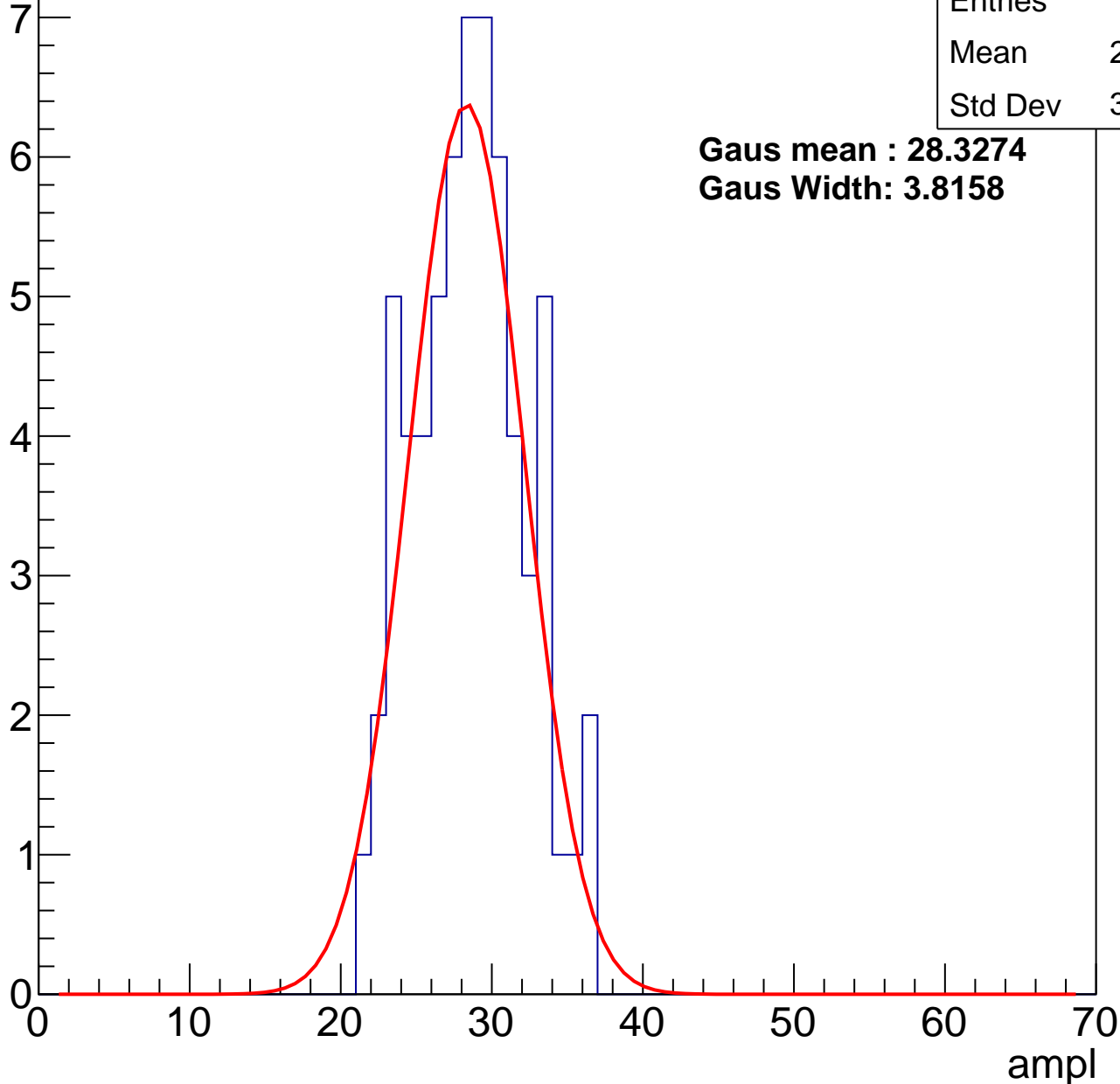
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.14 |
| Std Dev | 3.616 |

Gaus mean : 28.3274

Gaus Width: 3.8158



B0L102S, U20-ch64, adc1

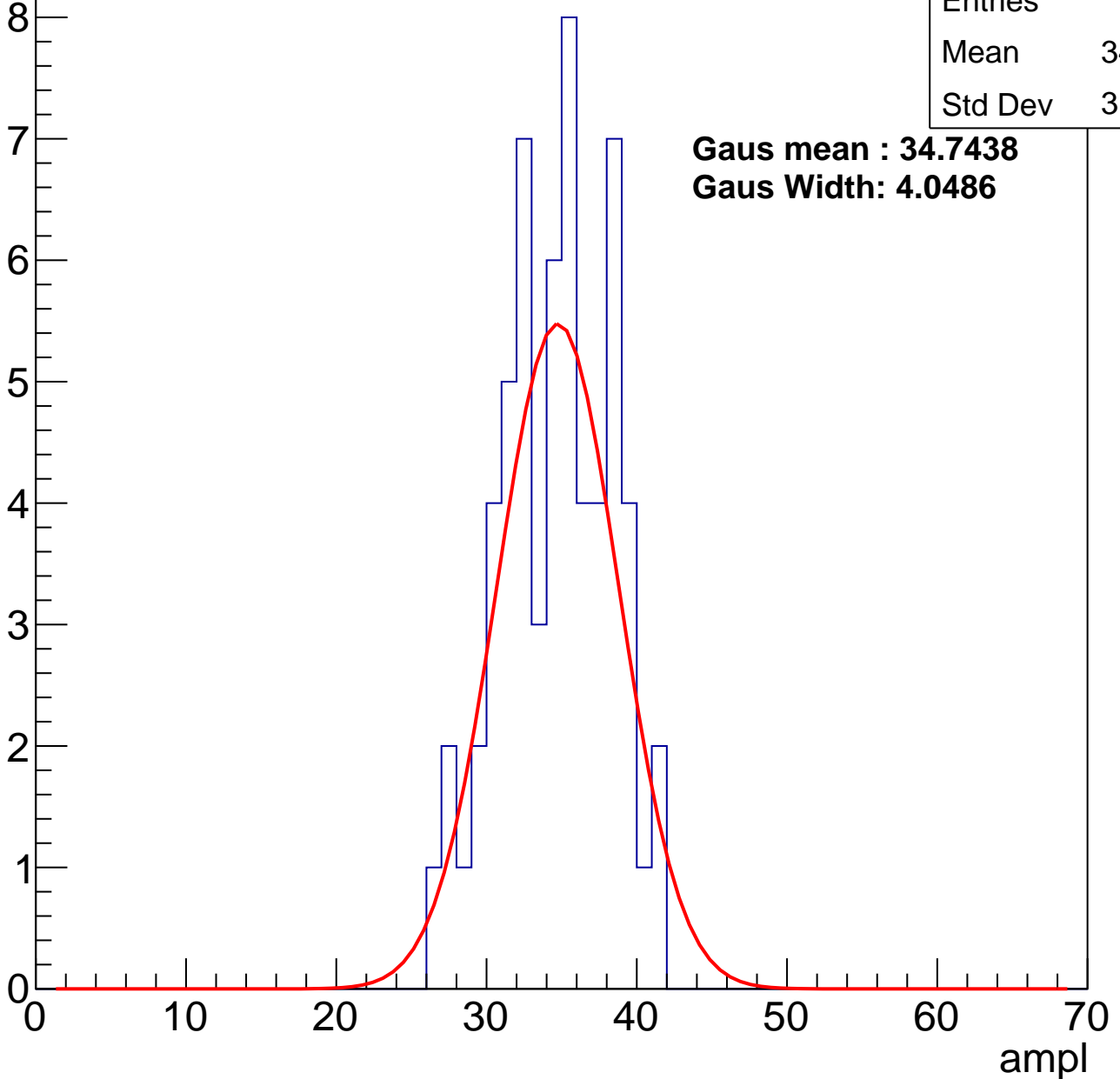
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 34.16 |
| Std Dev | 3.604 |

Gaus mean : 34.7438

Gaus Width: 4.0486



B0L102S, U20-ch64, adc2

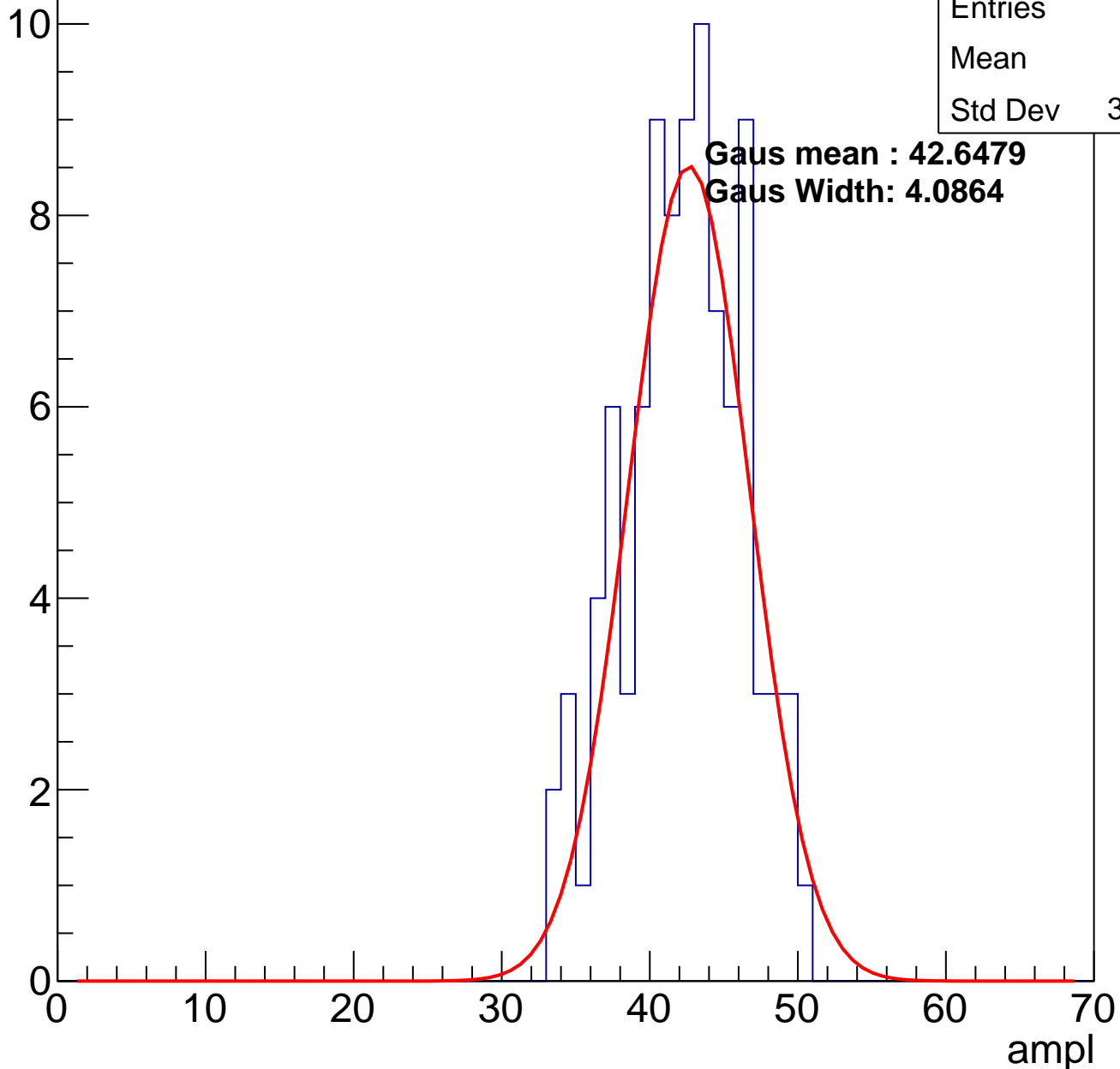
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 93 |
| Mean | 41.8 |
| Std Dev | 3.975 |

Gaus mean : 42.6479

Gaus Width: 4.0864

Entry

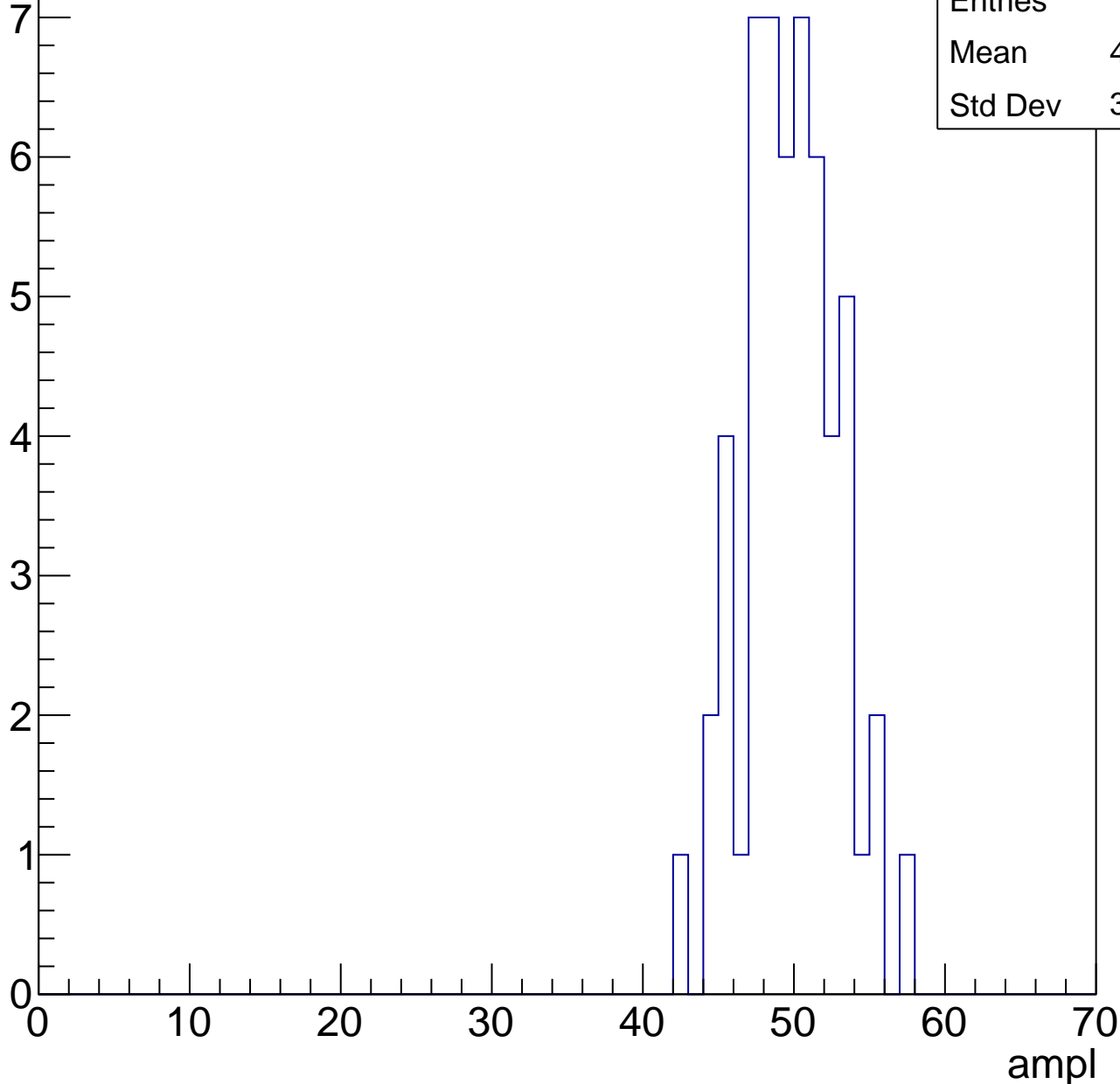


B0L102S, U20-ch64, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 49.35 |
| Std Dev | 3.068 |

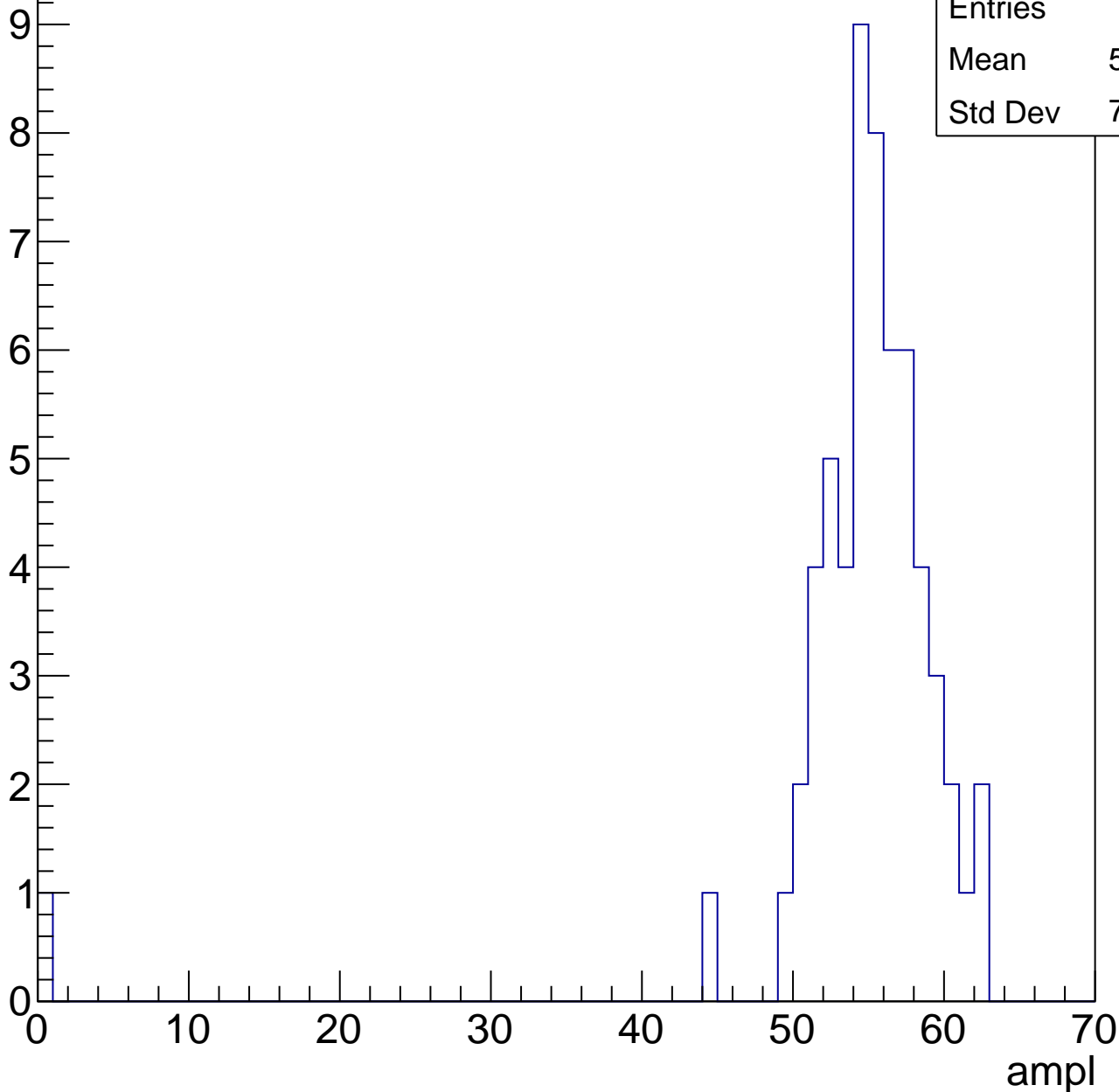


B0L102S, U20-ch64, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.02 |
| Std Dev | 7.819 |

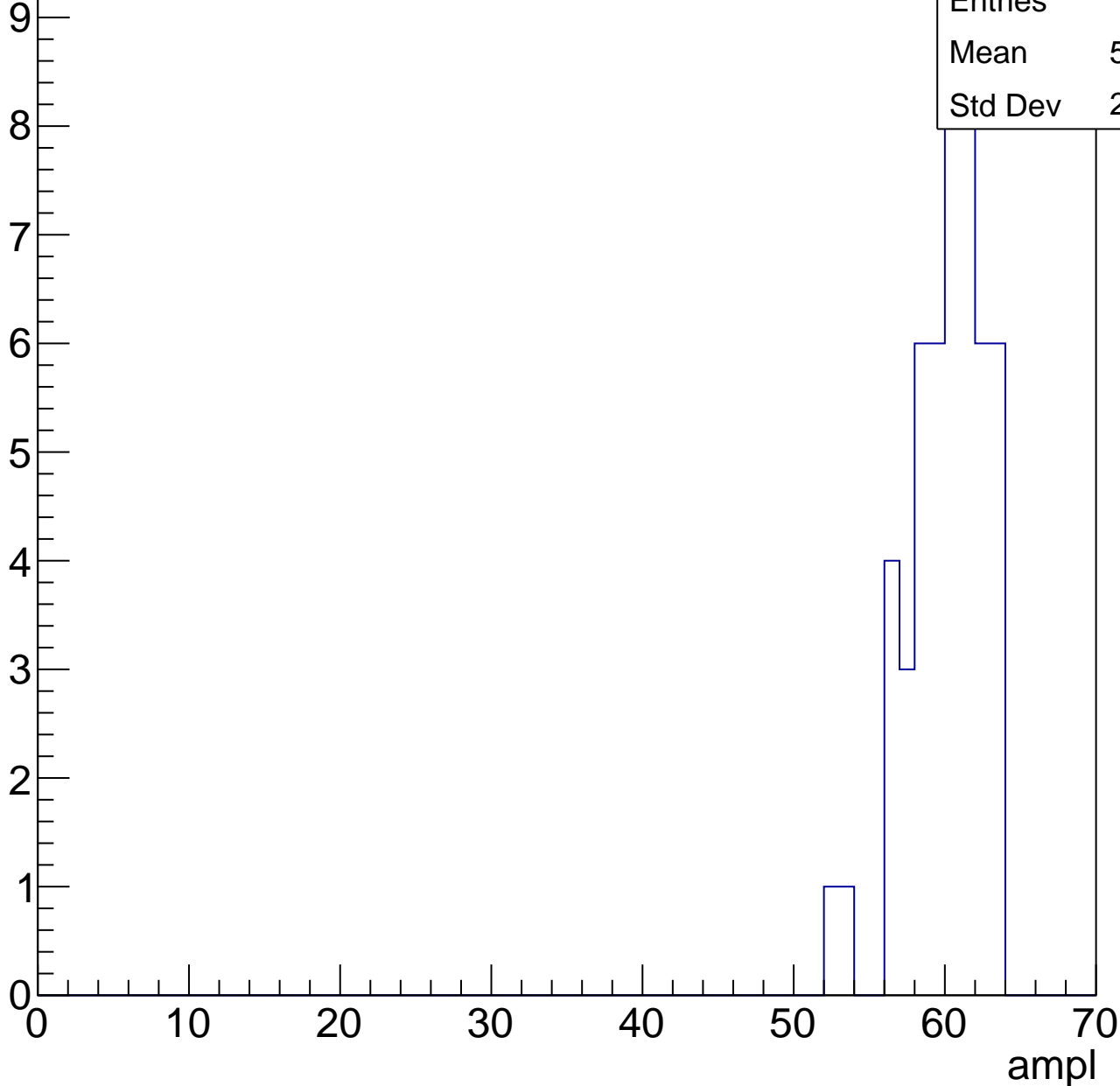


B0L102S, U20-ch64, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

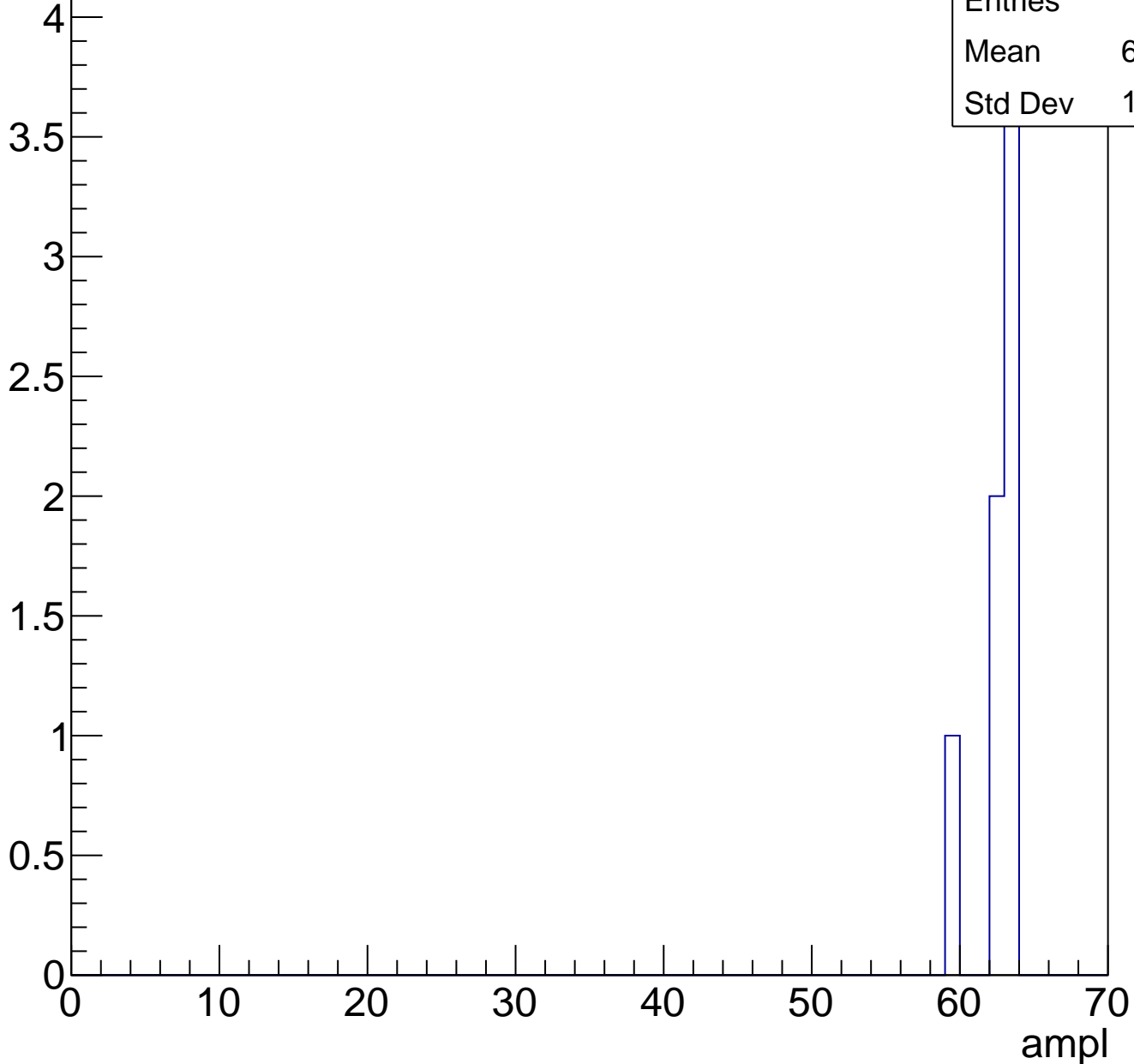
| | |
|---------|-------|
| Entries | 50 |
| Mean | 59.62 |
| Std Dev | 2.505 |



B0L102S, U20-ch64, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch64, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch65, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 76 |
| Mean | 27.47 |
| Std Dev | 4.767 |

Gaus mean : 28.3082

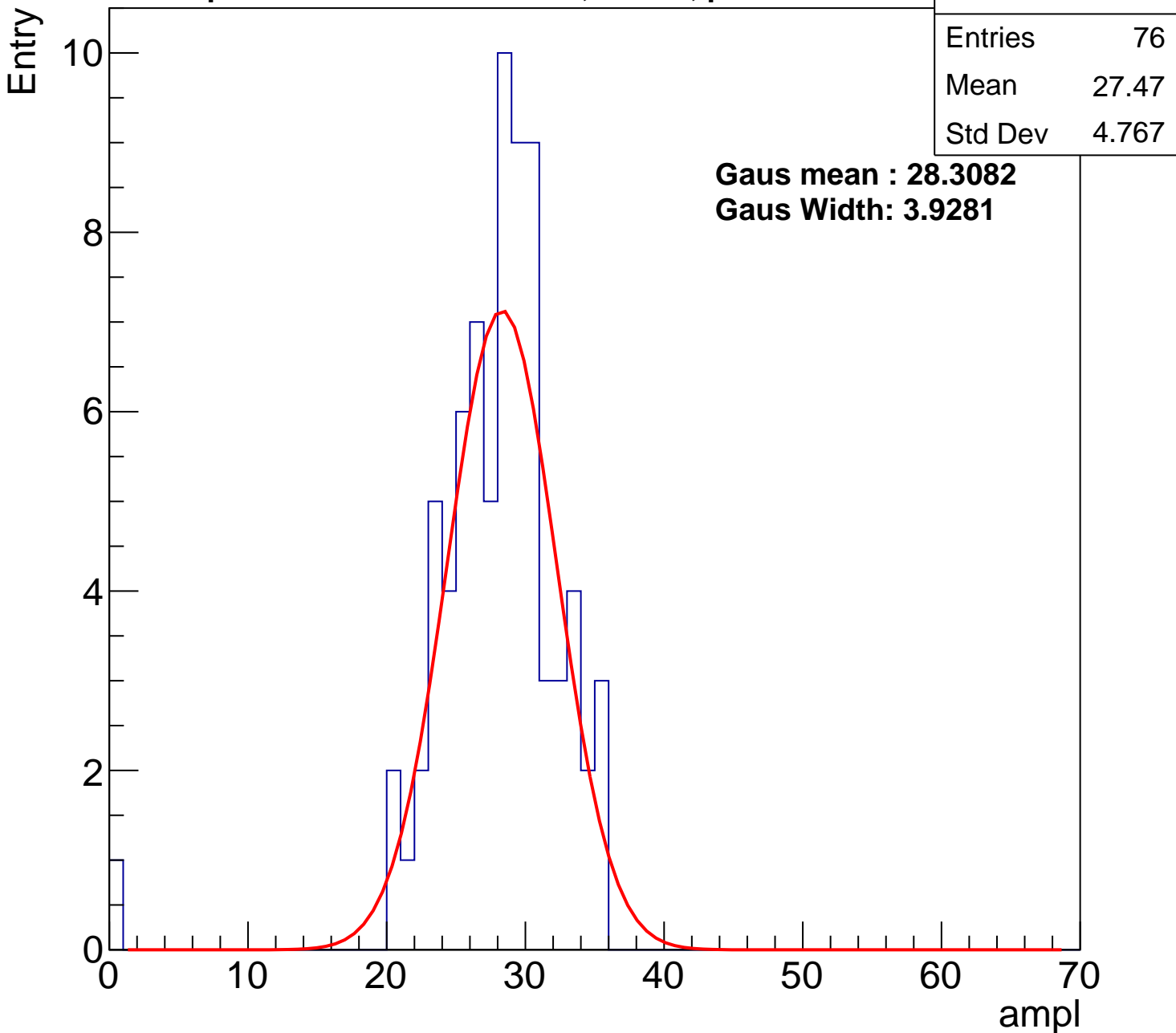
Gaus Width: 3.9281

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



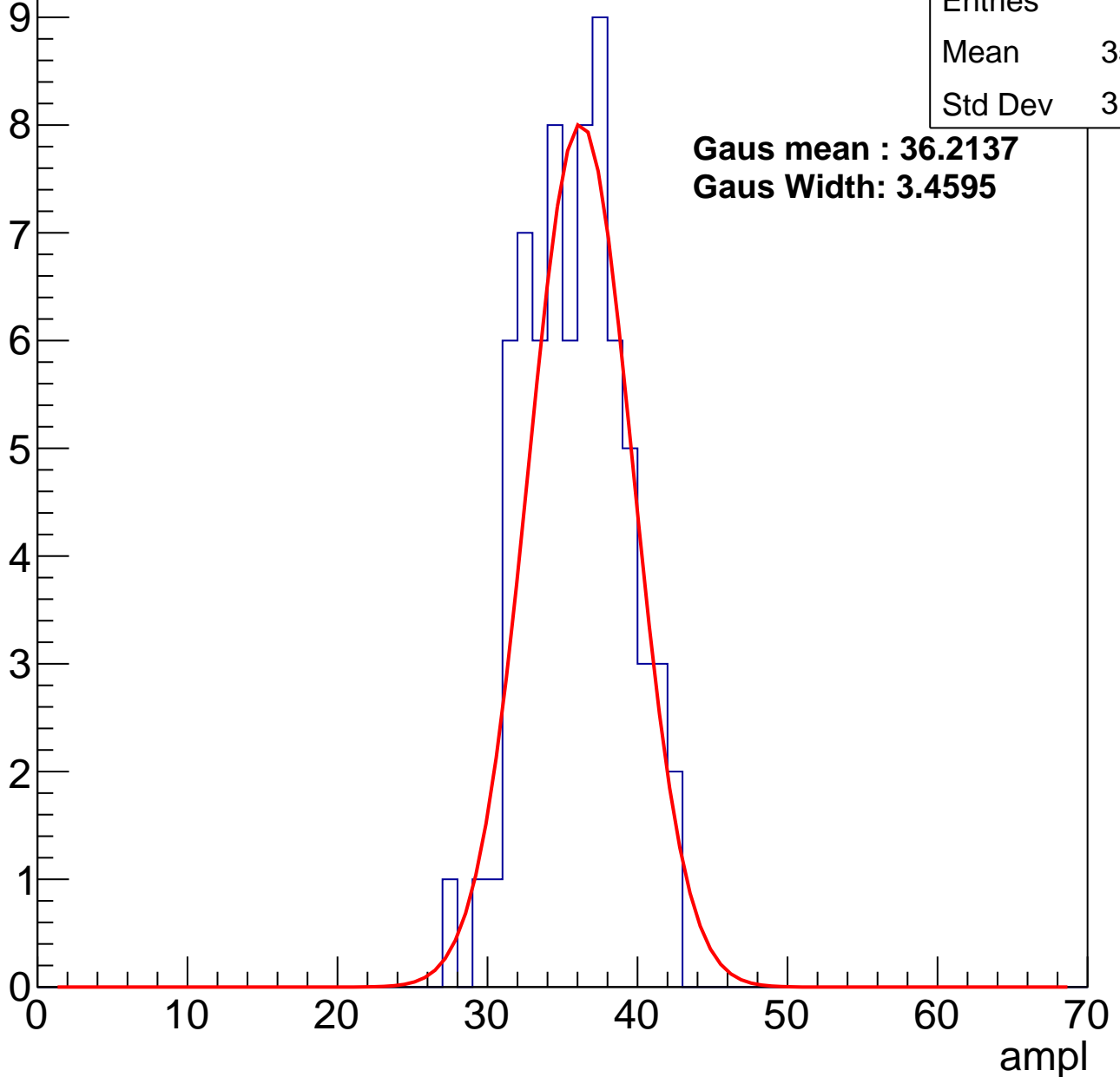
B0L102S, U20-ch65, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.38 |
| Std Dev | 3.259 |

Gaus mean : 36.2137
Gaus Width: 3.4595



B0L102S, U20-ch65, adc2

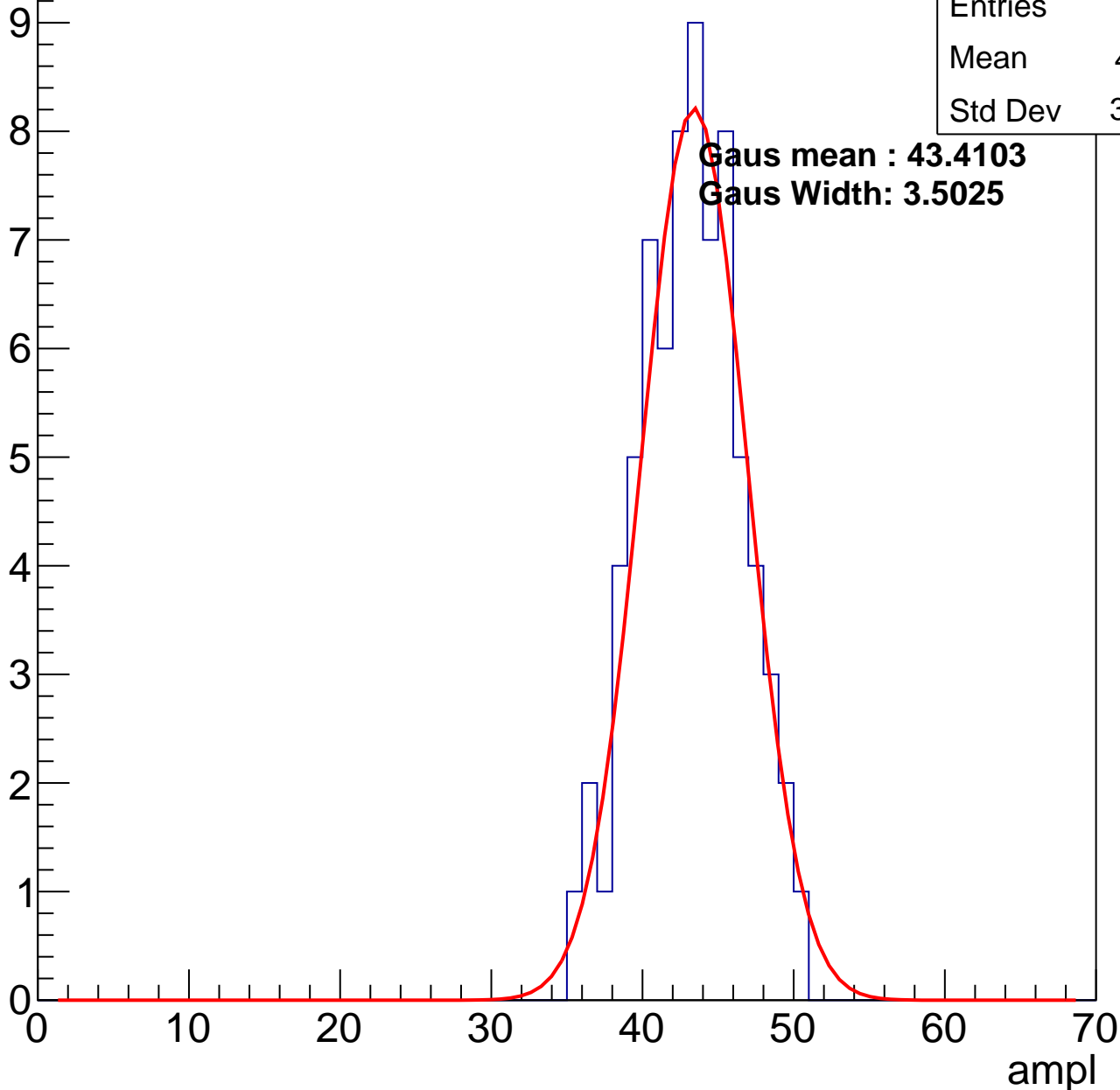
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 42.71 |
| Std Dev | 3.333 |

Gaus mean : 43.4103

Gaus Width: 3.5025

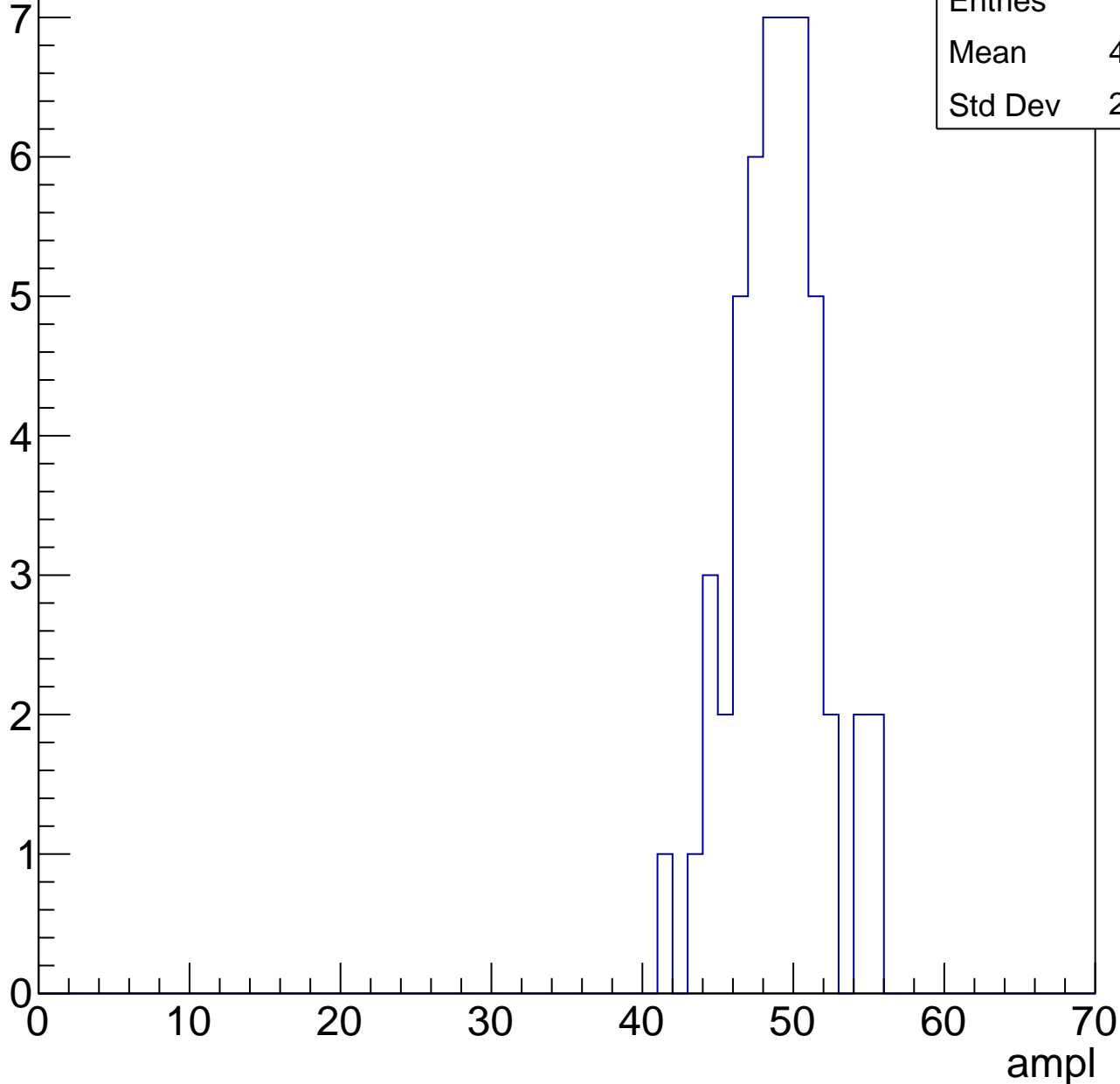


B0L102S, U20-ch65, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 48.48 |
| Std Dev | 2.955 |



B0L102S, U20-ch65, adc4

calib_packv5_042523_0143.root, FC#12, port B1

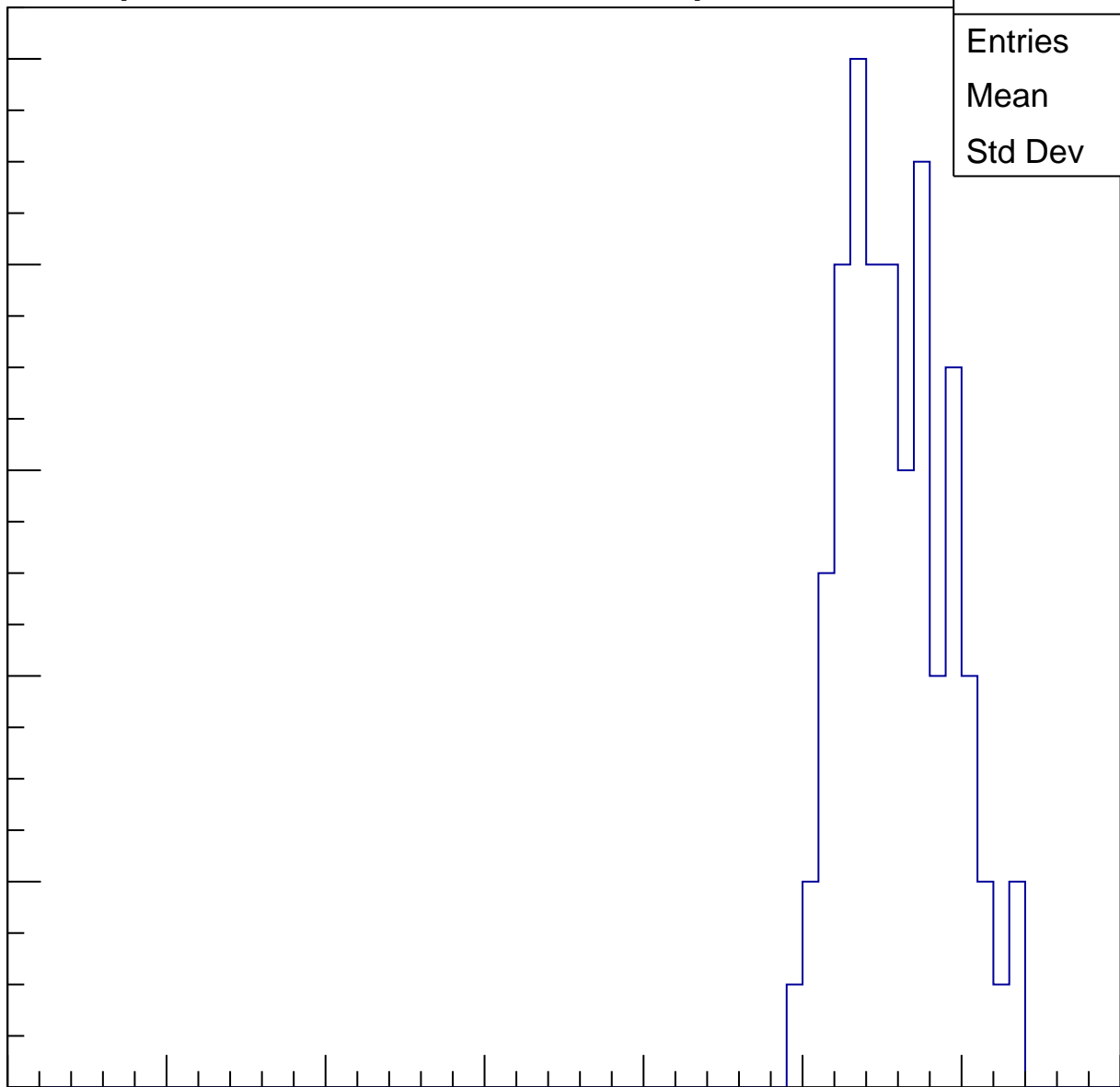
| | |
|---------|-------|
| Entries | 77 |
| Mean | 55.4 |
| Std Dev | 3.248 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

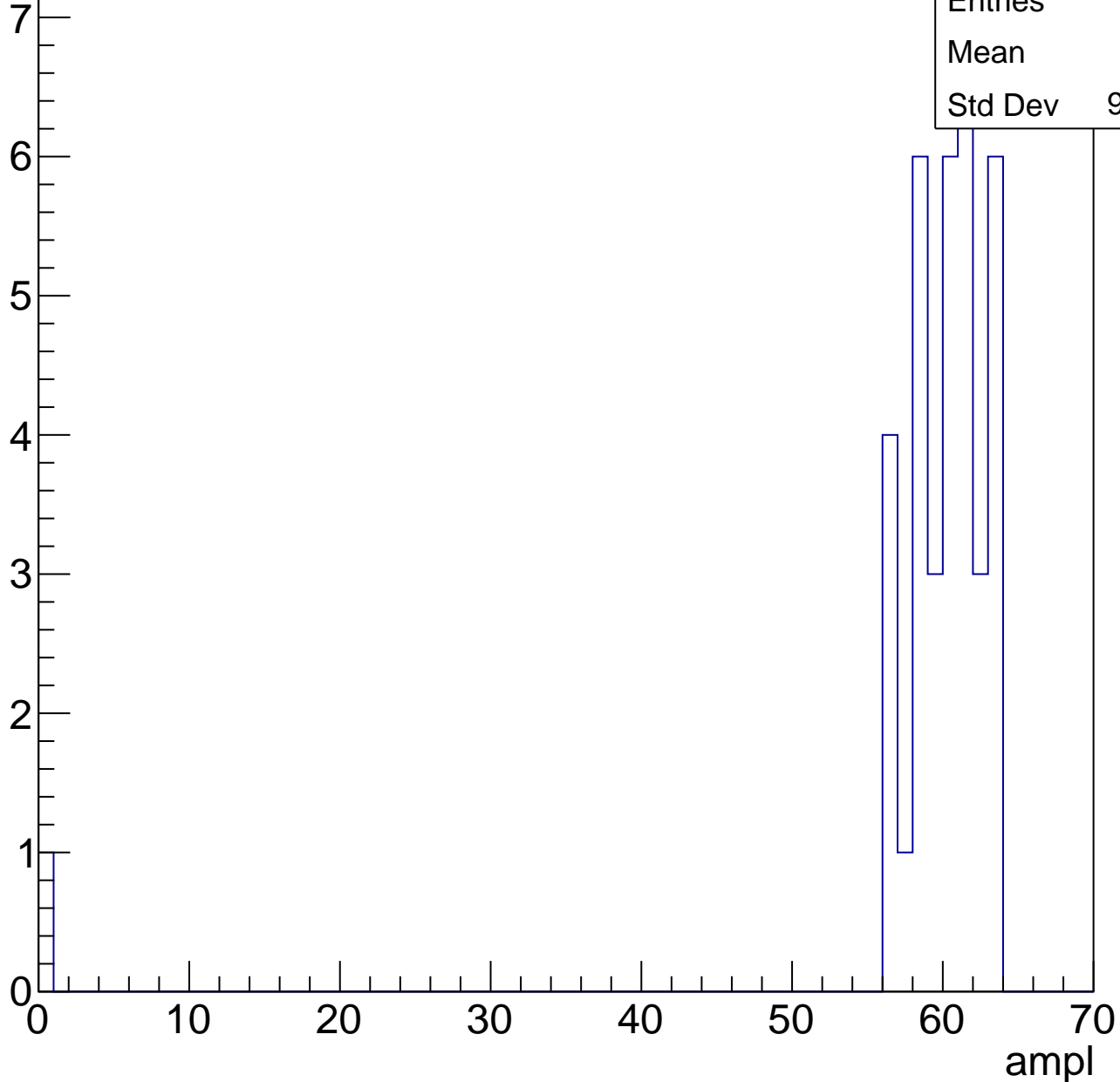
ampl



B0L102S, U20-ch65, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

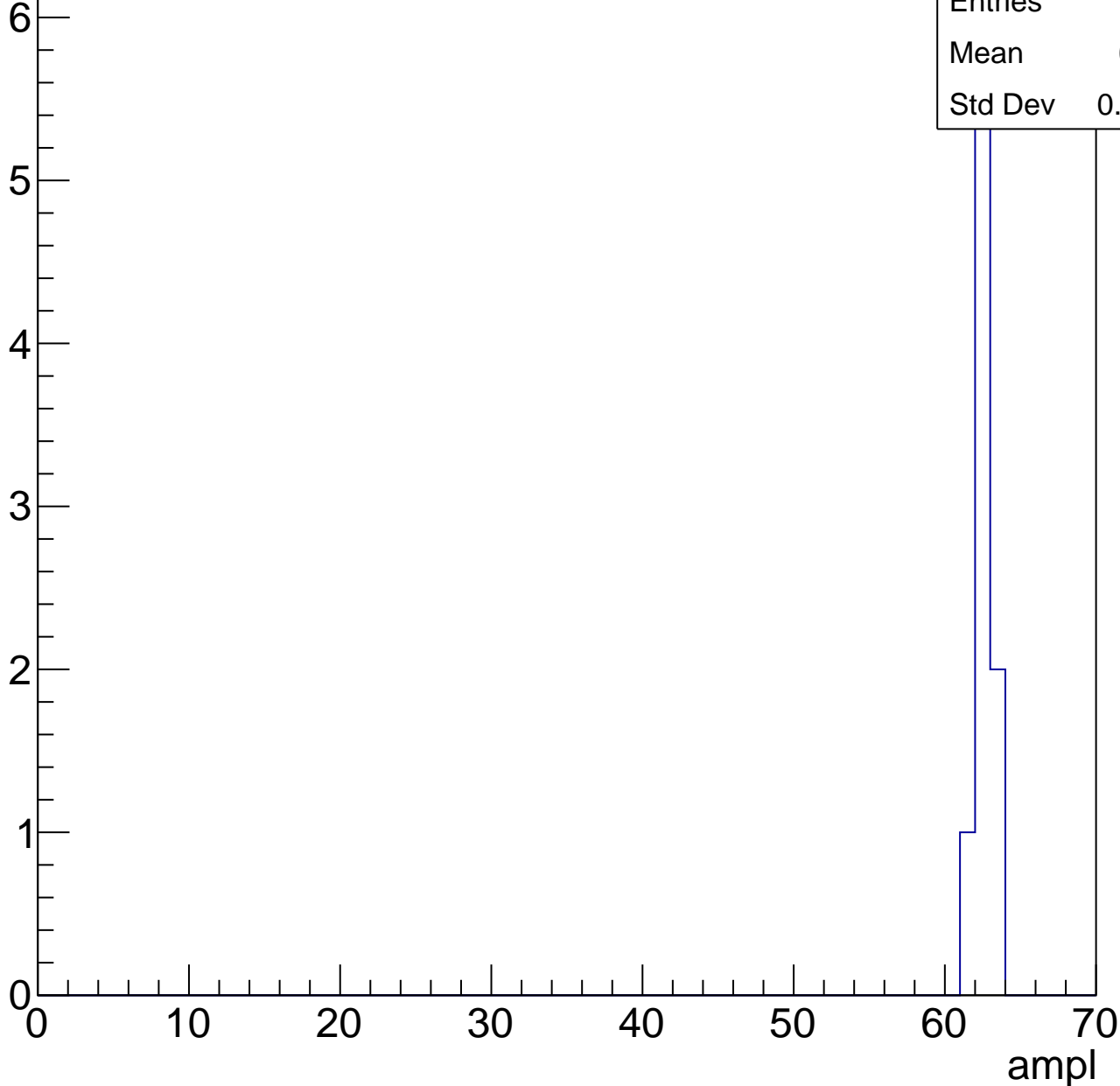


B0L102S, U20-ch65, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|--------|
| Entries | 9 |
| Mean | 62.11 |
| Std Dev | 0.5666 |



B0L102S, U20-ch65, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch66, adc0

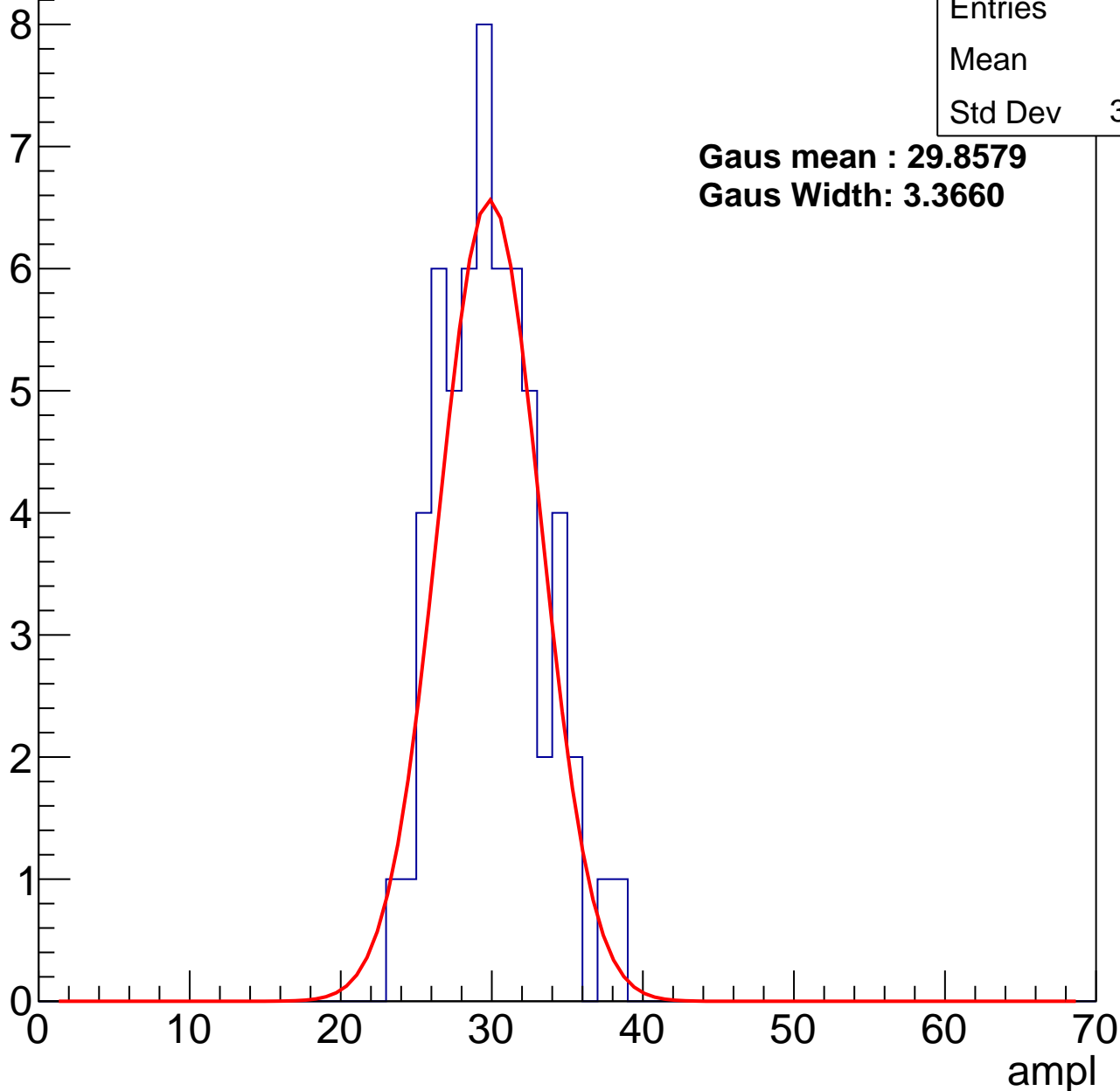
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 29.5 |
| Std Dev | 3.255 |

Gaus mean : 29.8579

Gaus Width: 3.3660



B0L102S, U20-ch66, adc1

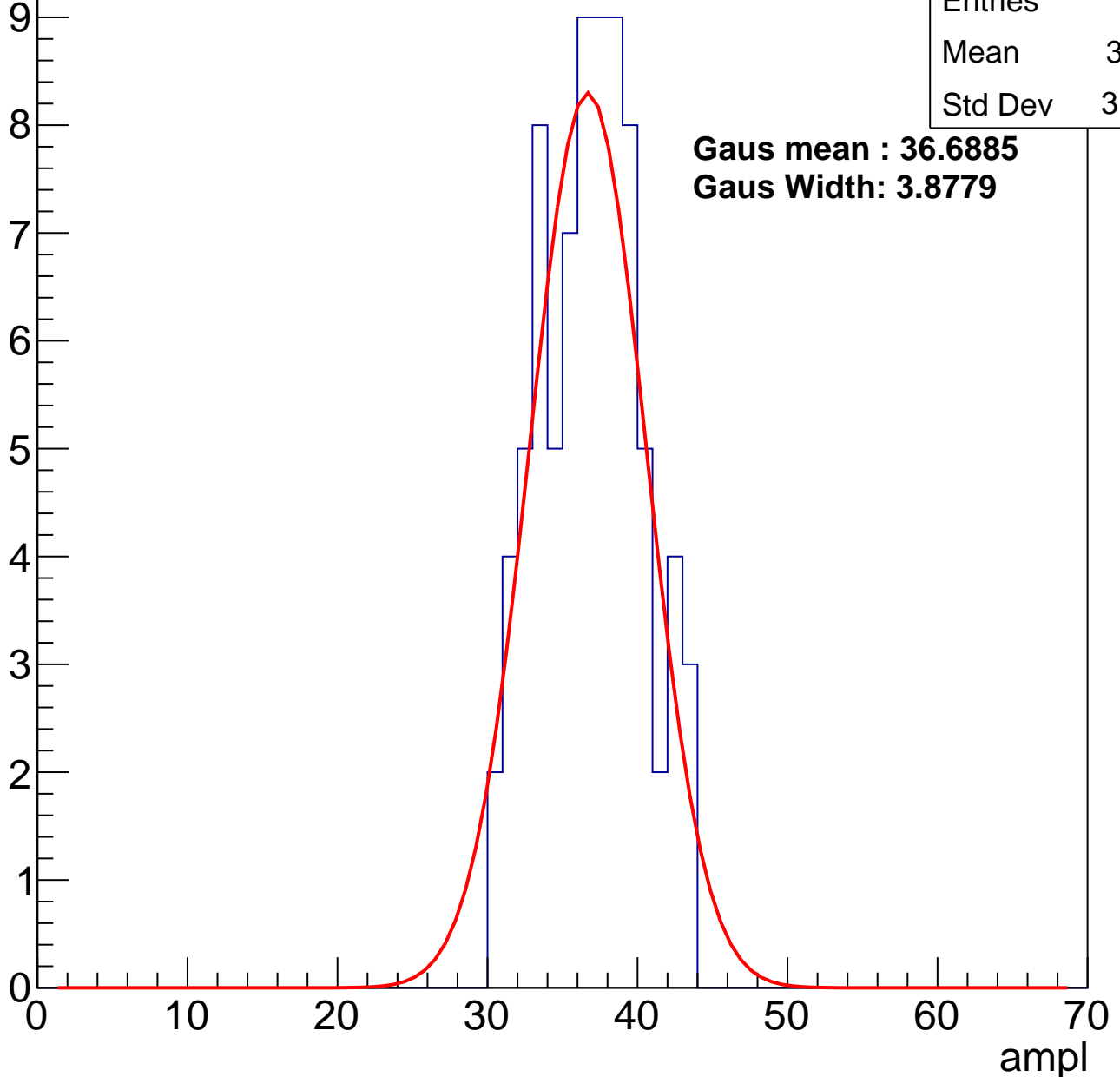
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.41 |
| Std Dev | 3.308 |

Gaus mean : 36.6885

Gaus Width: 3.8779



B0L102S, U20-ch66, adc2

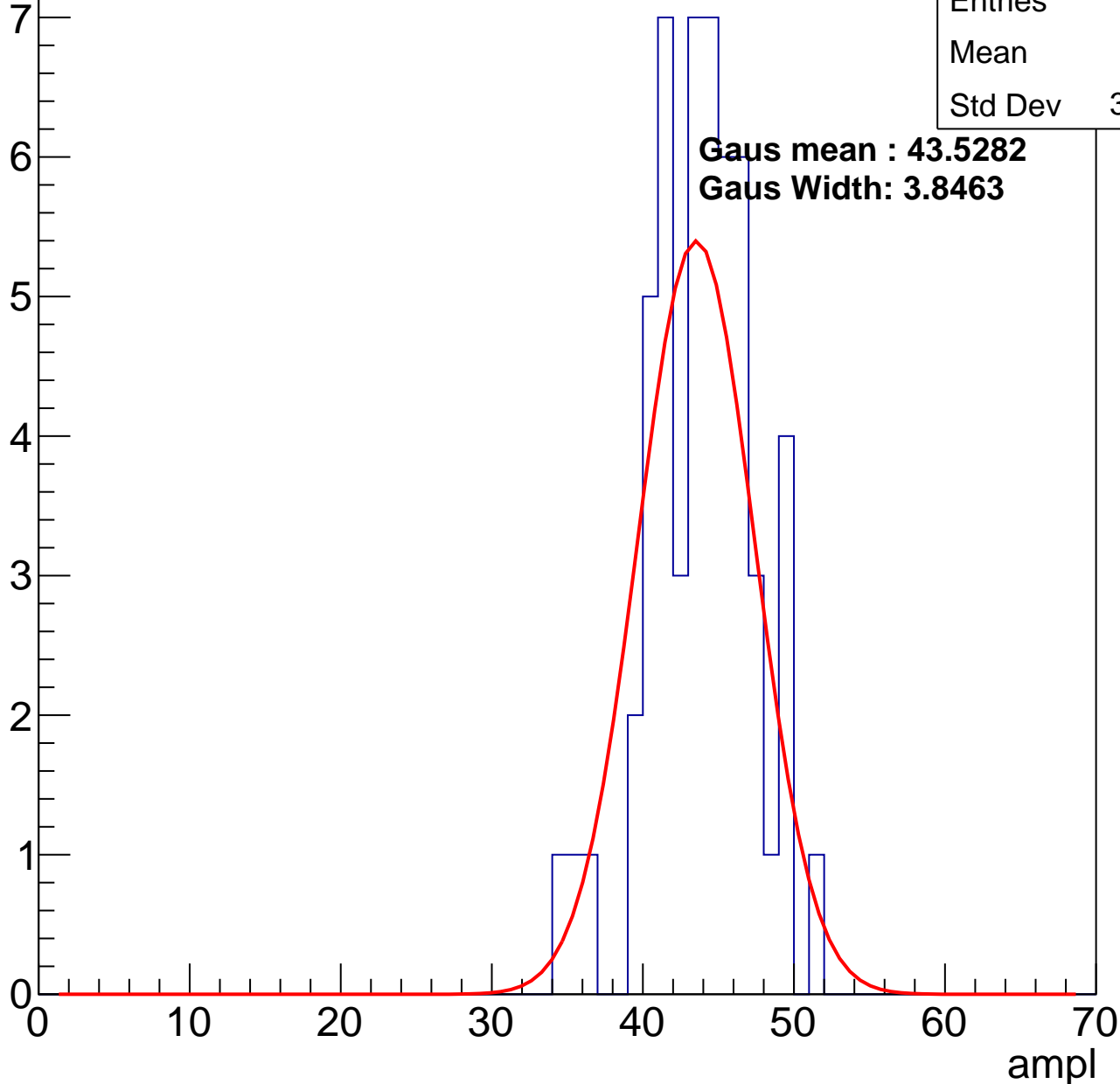
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.4 |
| Std Dev | 3.462 |

Gaus mean : 43.5282

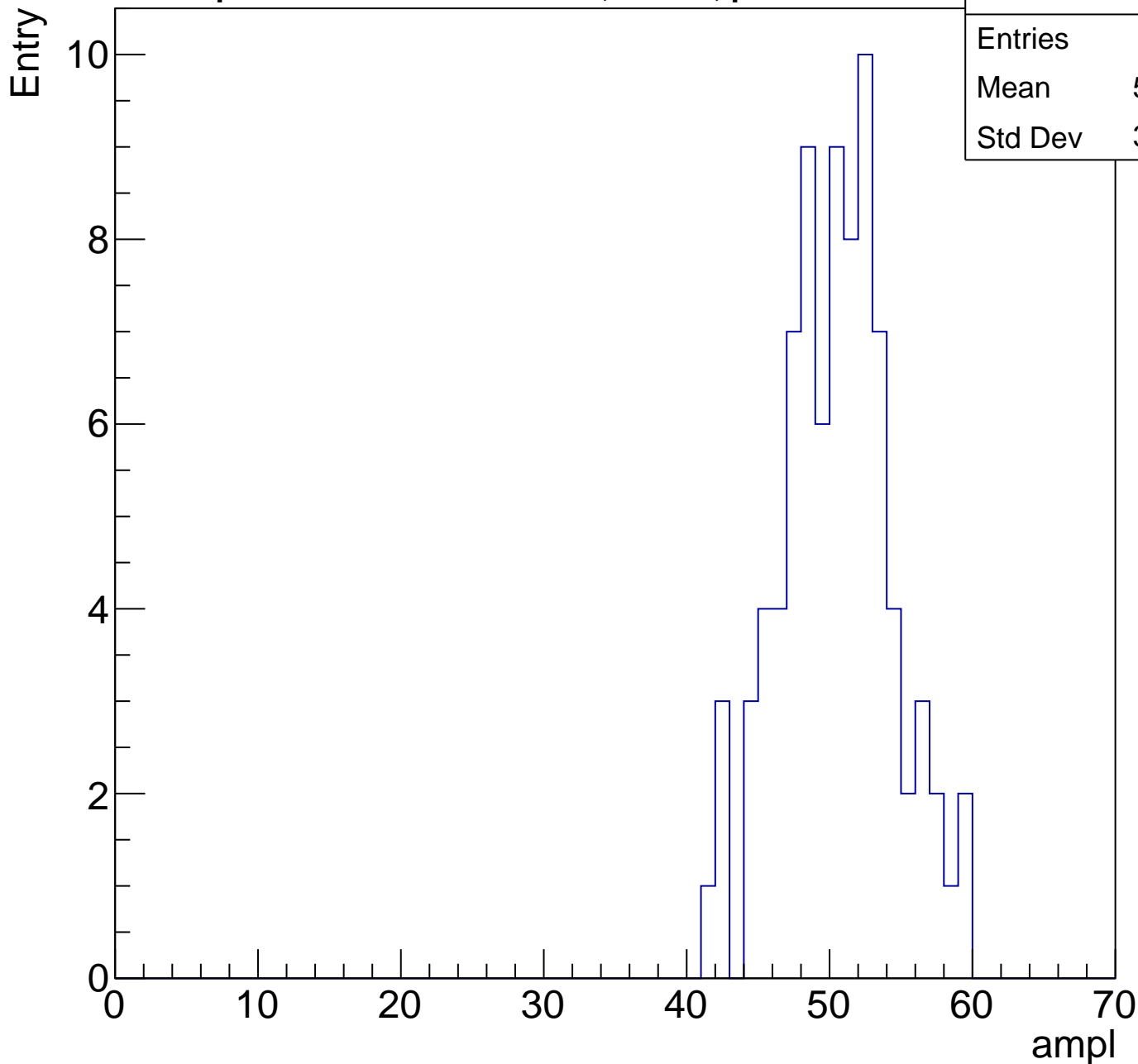
Gaus Width: 3.8463



B0L102S, U20-ch66, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 85 |
| Mean | 50.01 |
| Std Dev | 3.921 |

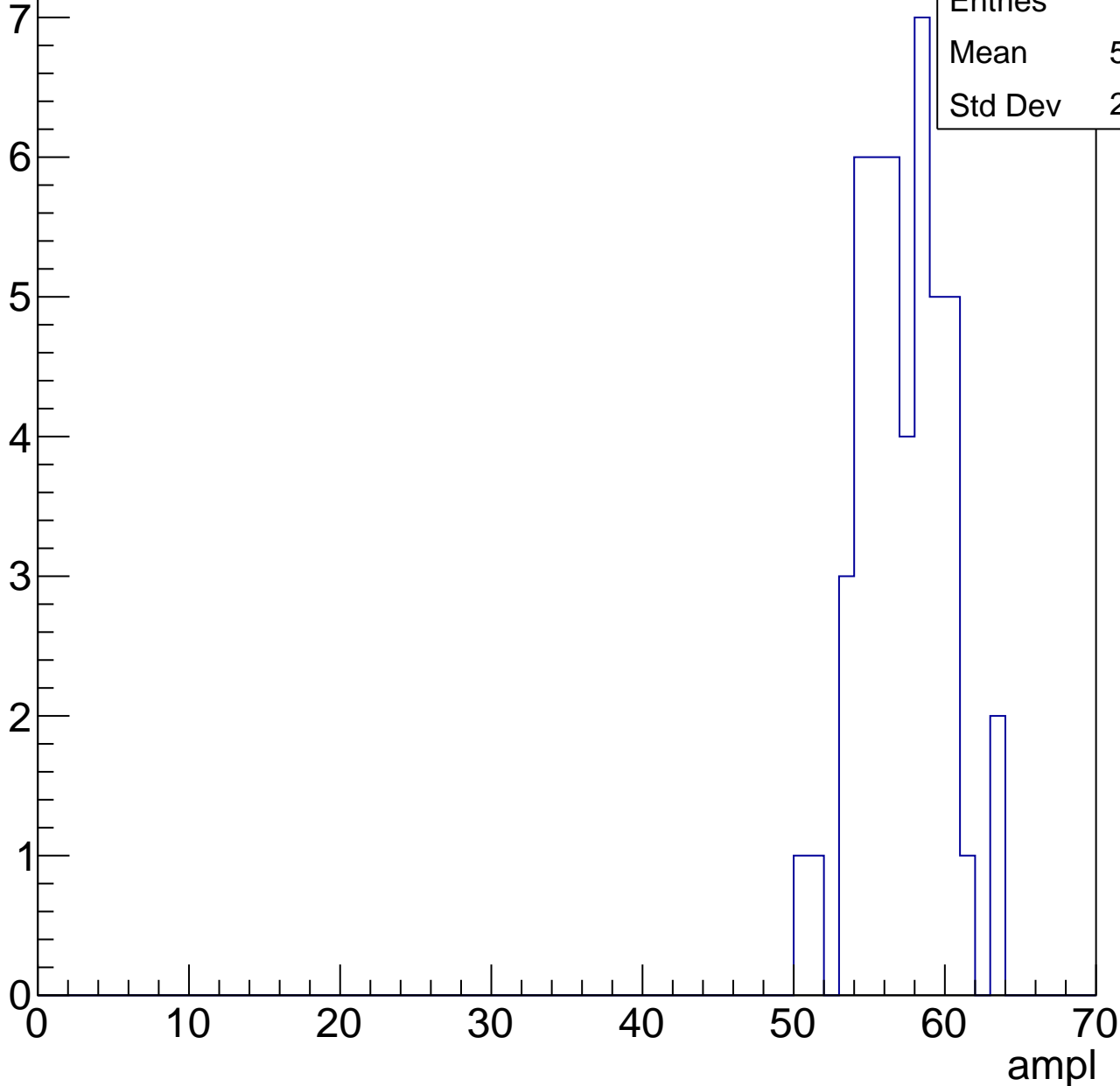


B0L102S, U20-ch66, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 56.72 |
| Std Dev | 2.819 |

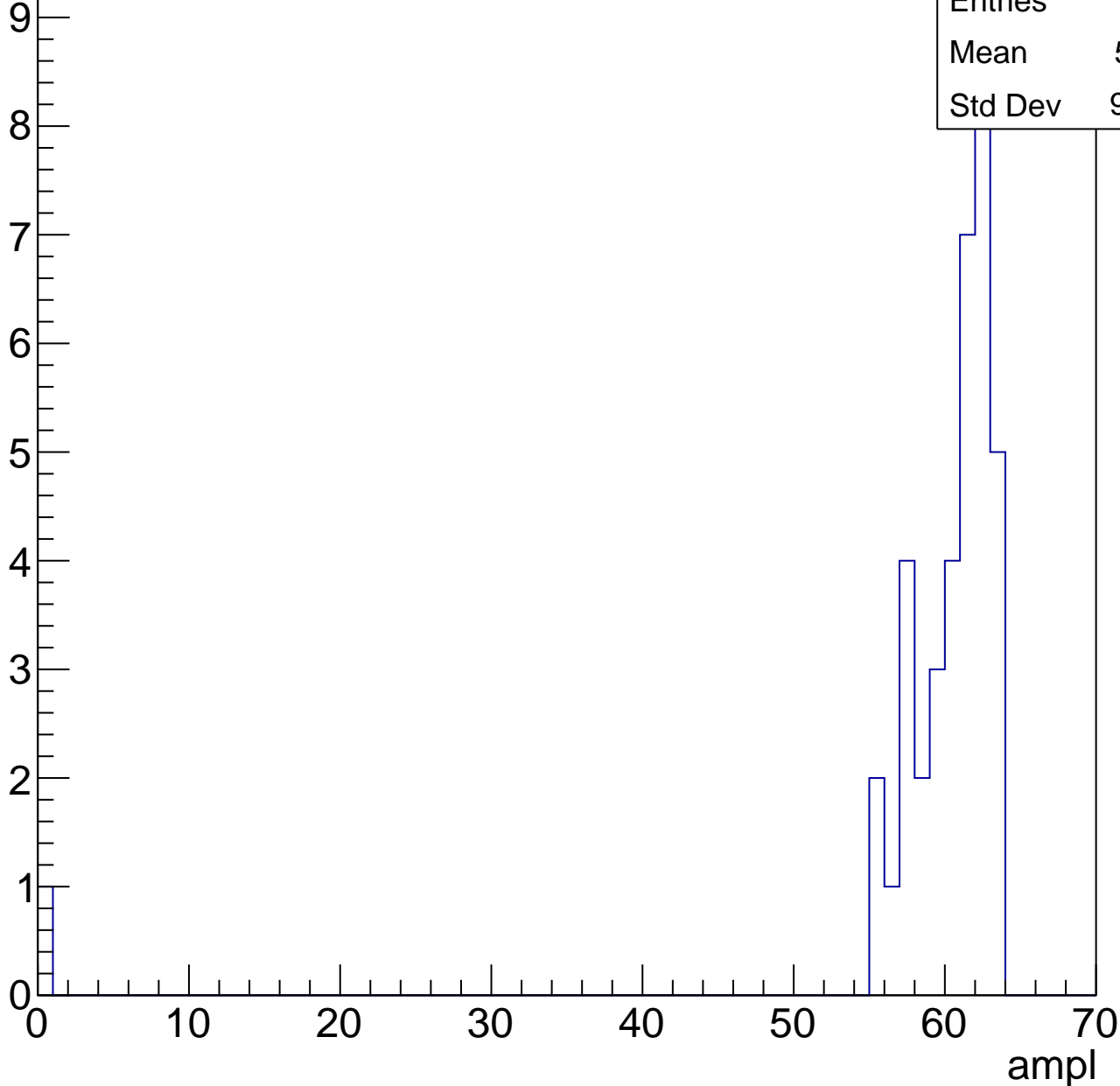


B0L102S, U20-ch66, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 58.61 |
| Std Dev | 9.904 |



B0L102S, U20-ch66, adc6

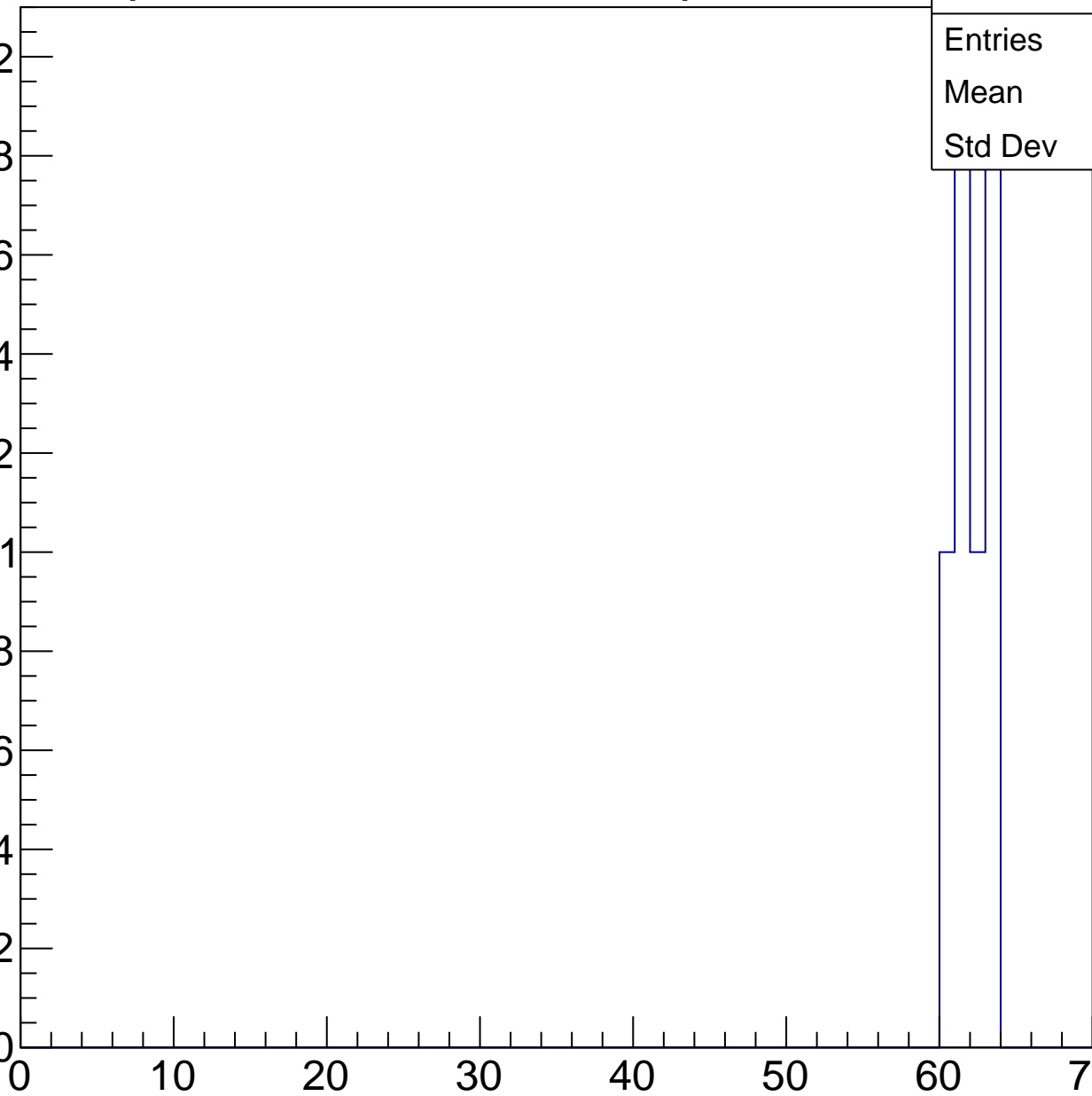
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.67 |
| Std Dev | 1.106 |

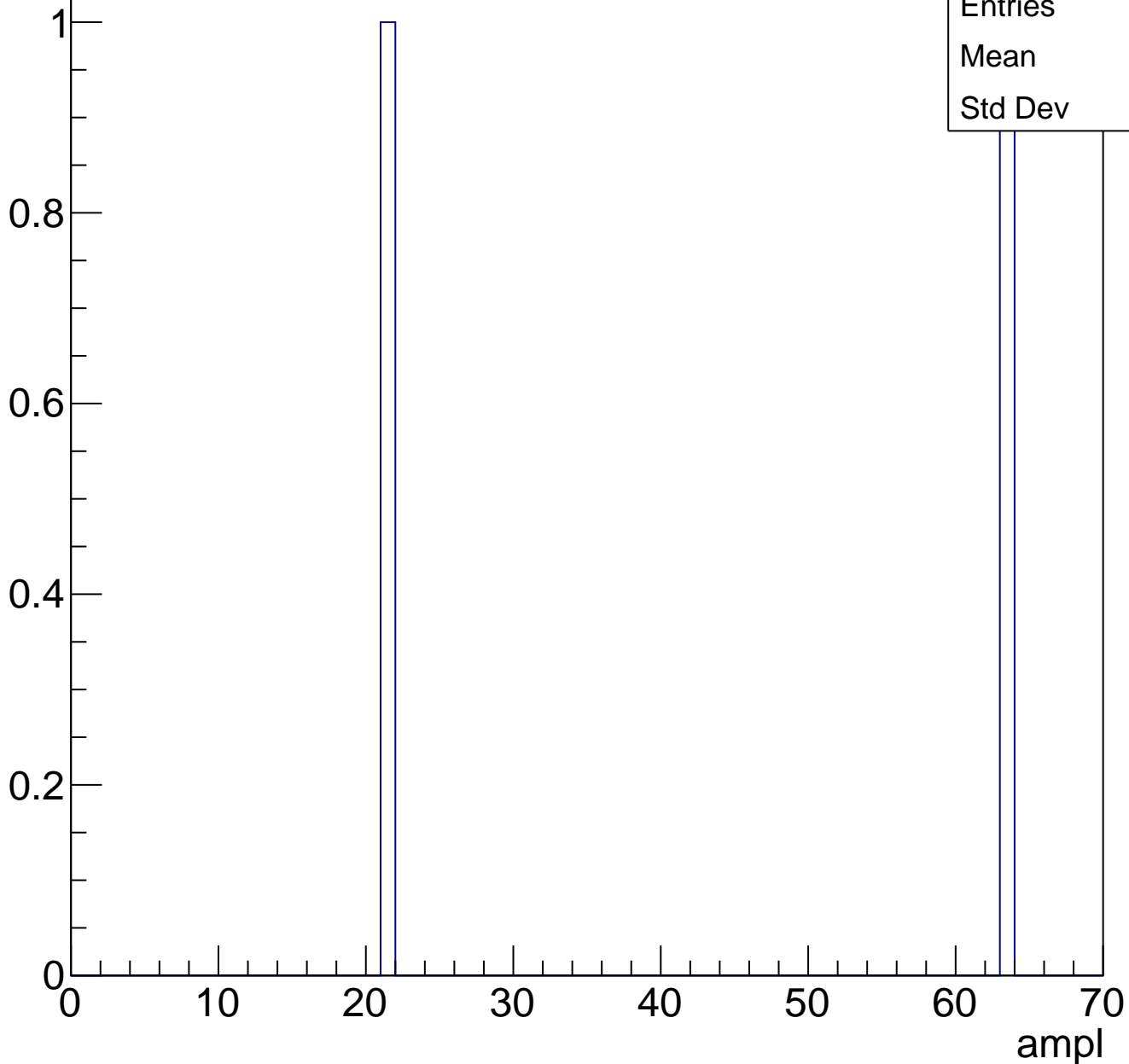
ampl



B0L102S, U20-ch66, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch67, adc0

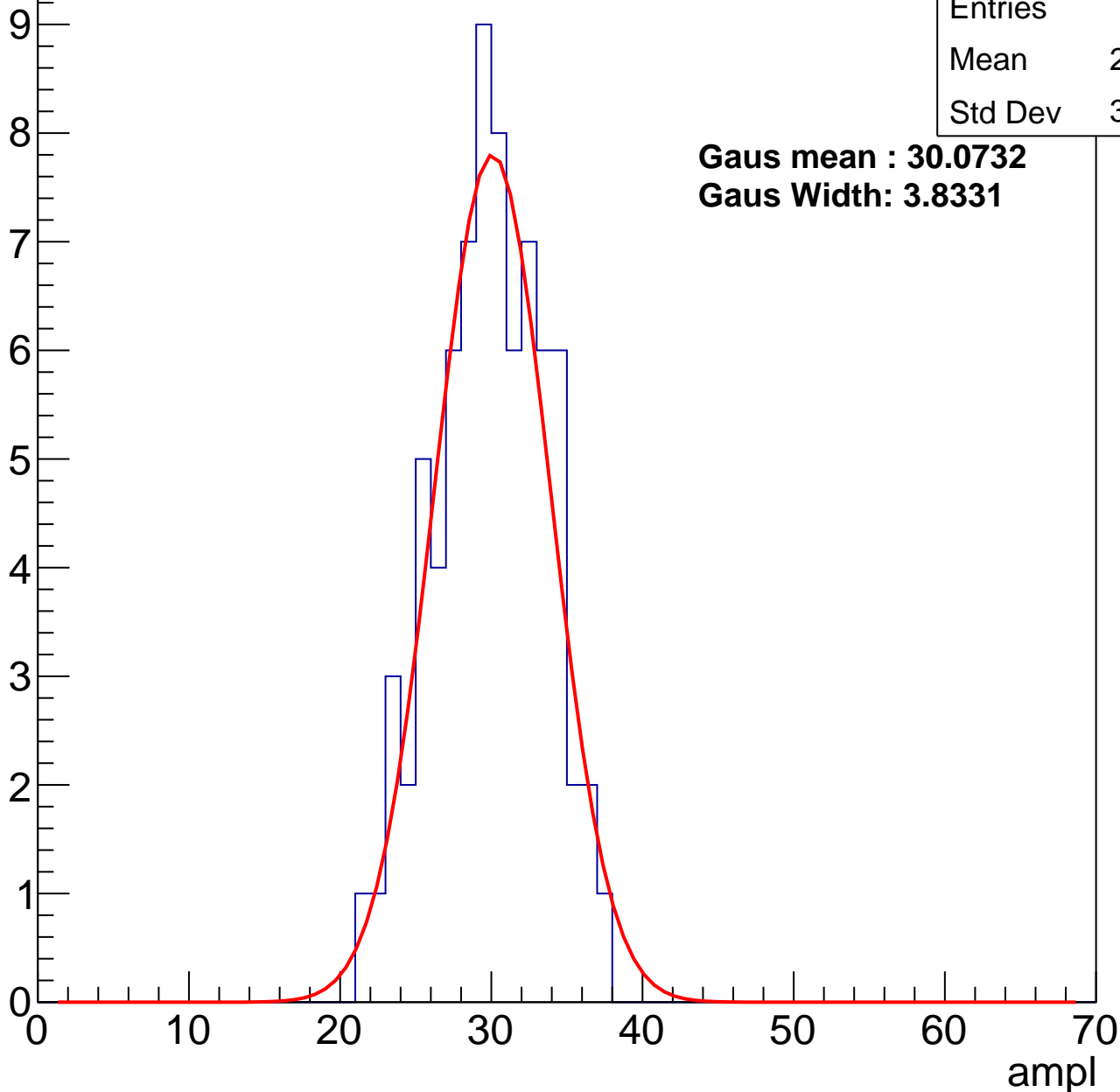
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.46 |
| Std Dev | 3.585 |

Gaus mean : 30.0732

Gaus Width: 3.8331



B0L102S, U20-ch67, adc1

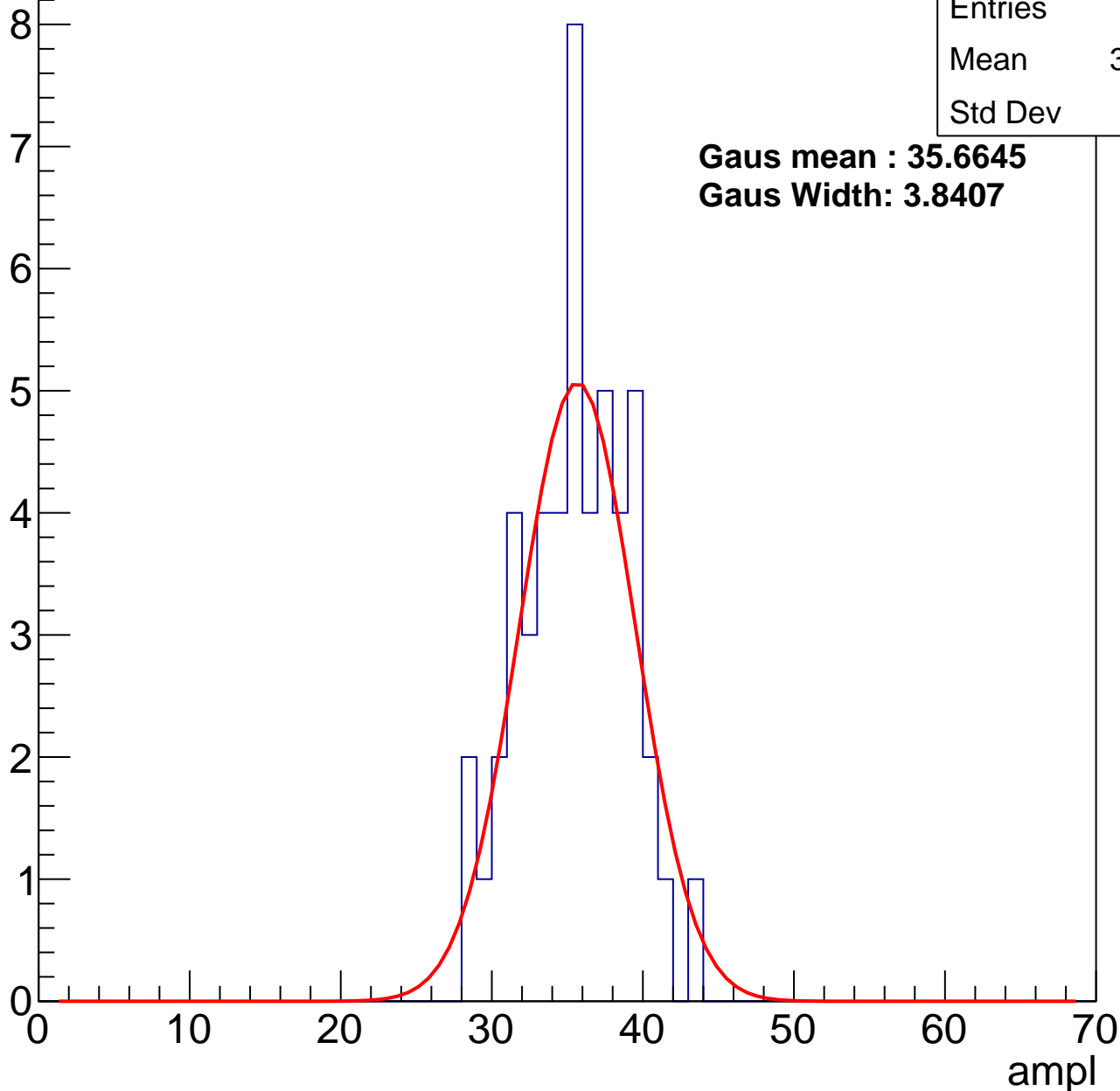
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 35.06 |
| Std Dev | 3.42 |

Gaus mean : 35.6645

Gaus Width: 3.8407



B0L102S, U20-ch67, adc2

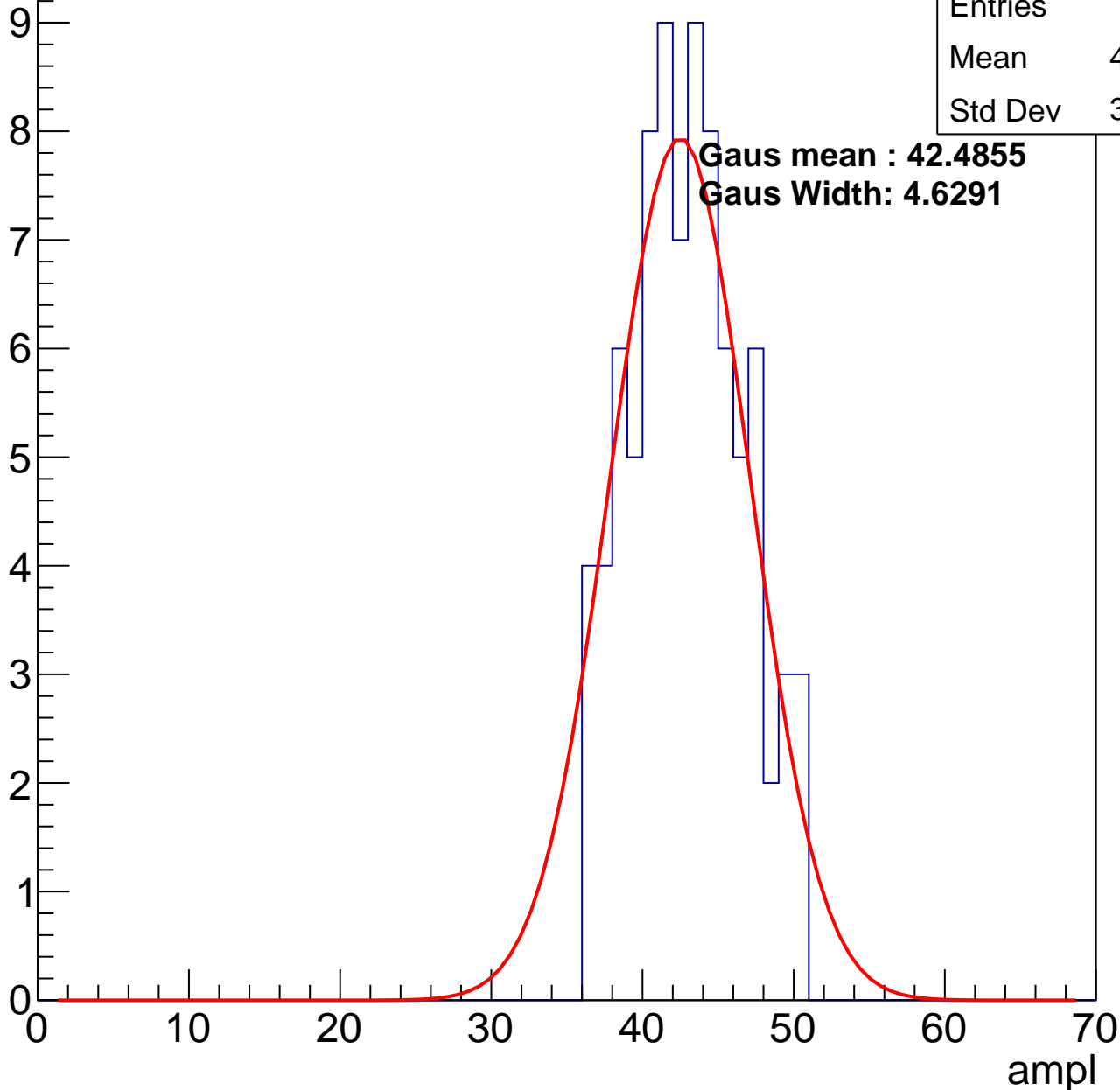
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 42.49 |
| Std Dev | 3.664 |

Gaus mean : 42.4855

Gaus Width: 4.6291

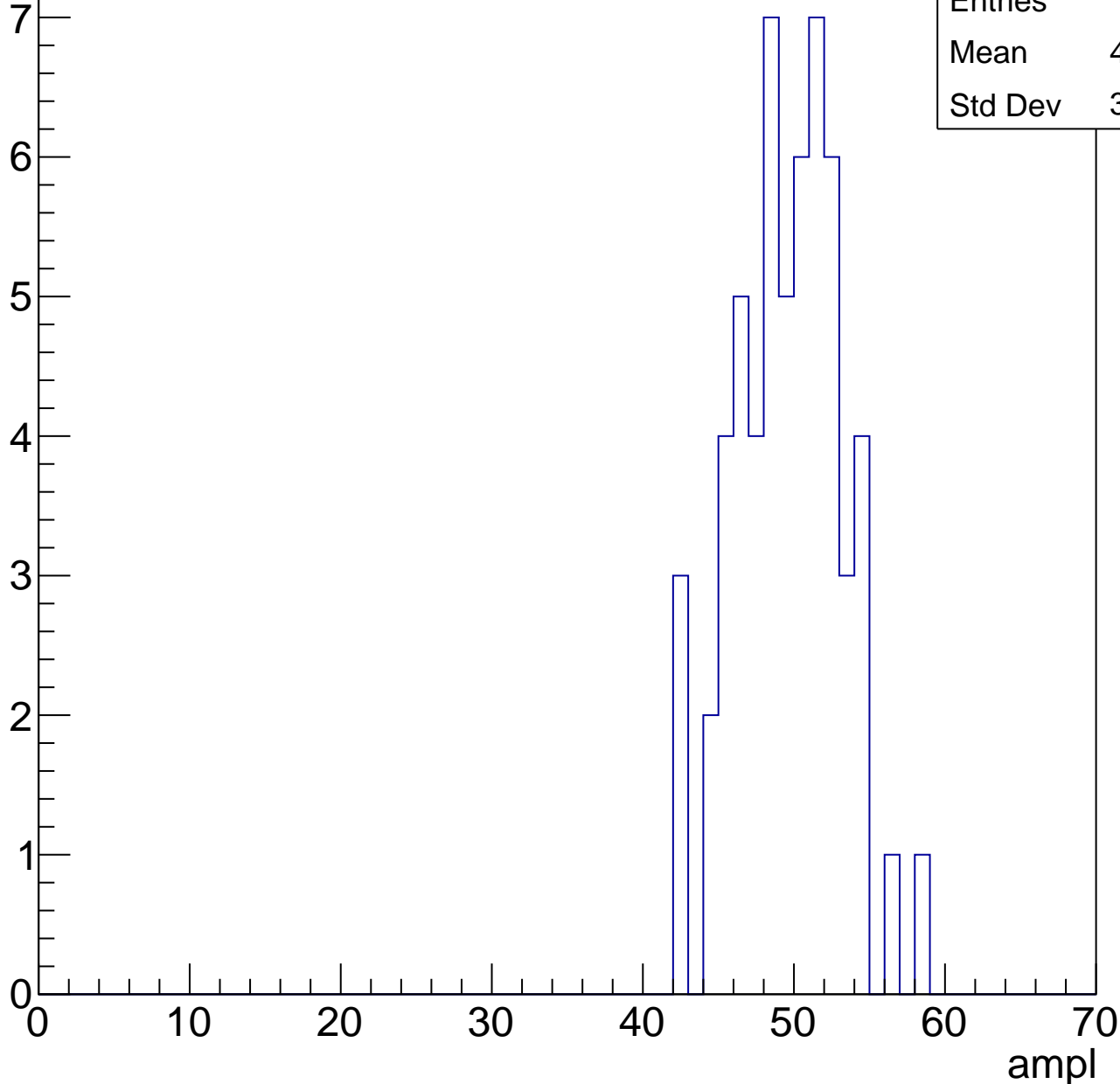


B0L102S, U20-ch67, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 49.16 |
| Std Dev | 3.458 |

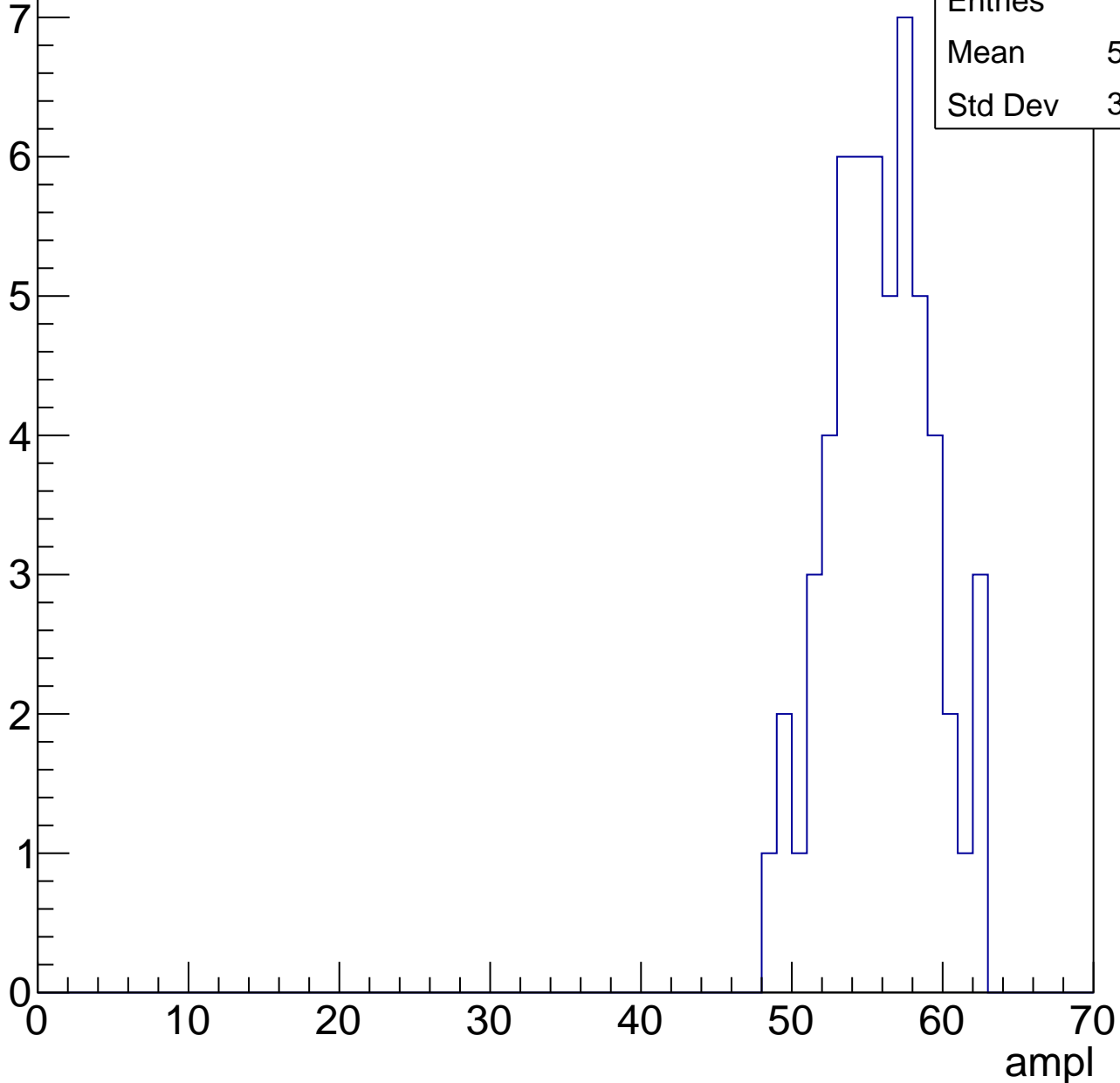


B0L102S, U20-ch67, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.38 |
| Std Dev | 3.346 |

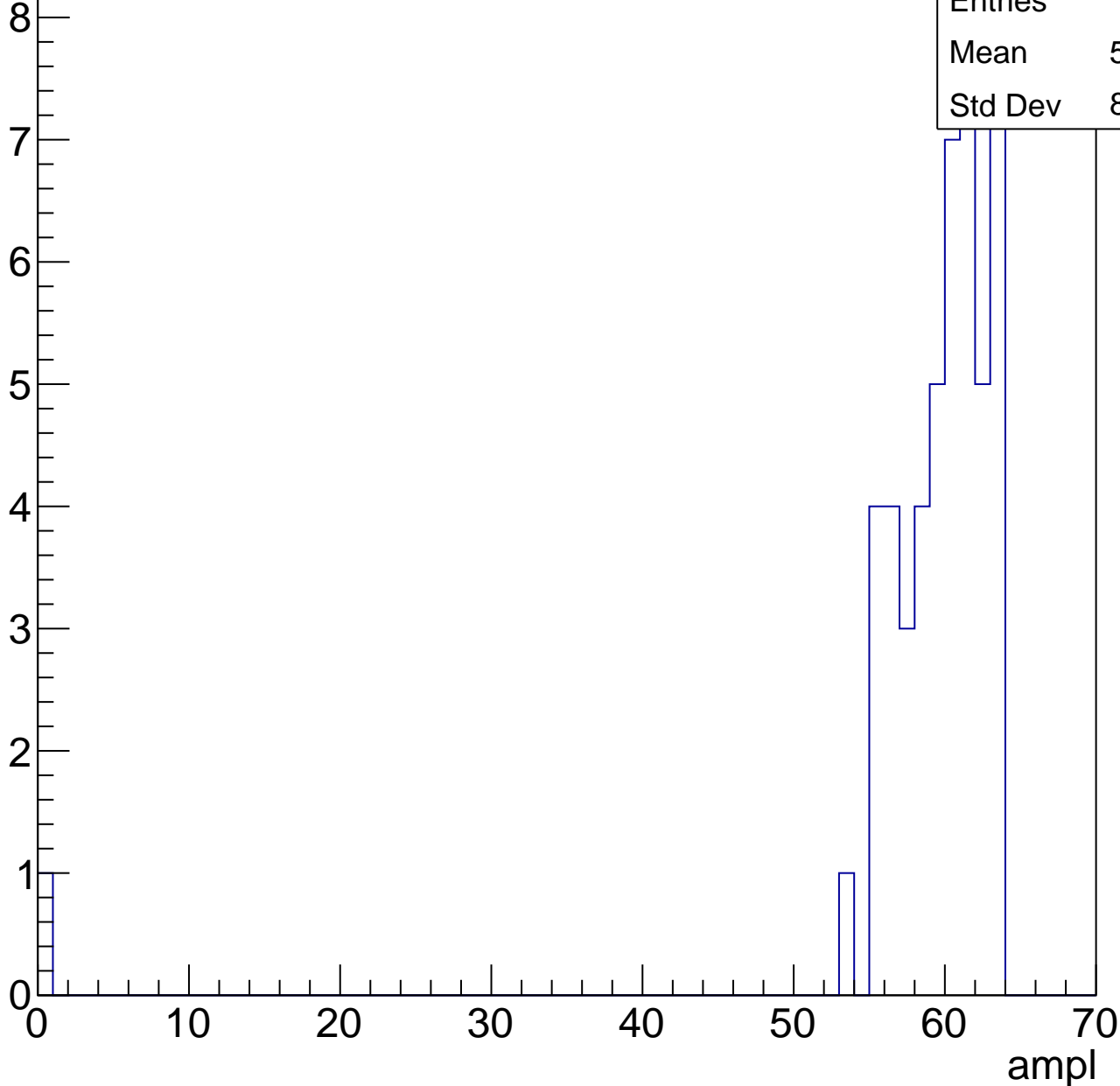


B0L102S, U20-ch67, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

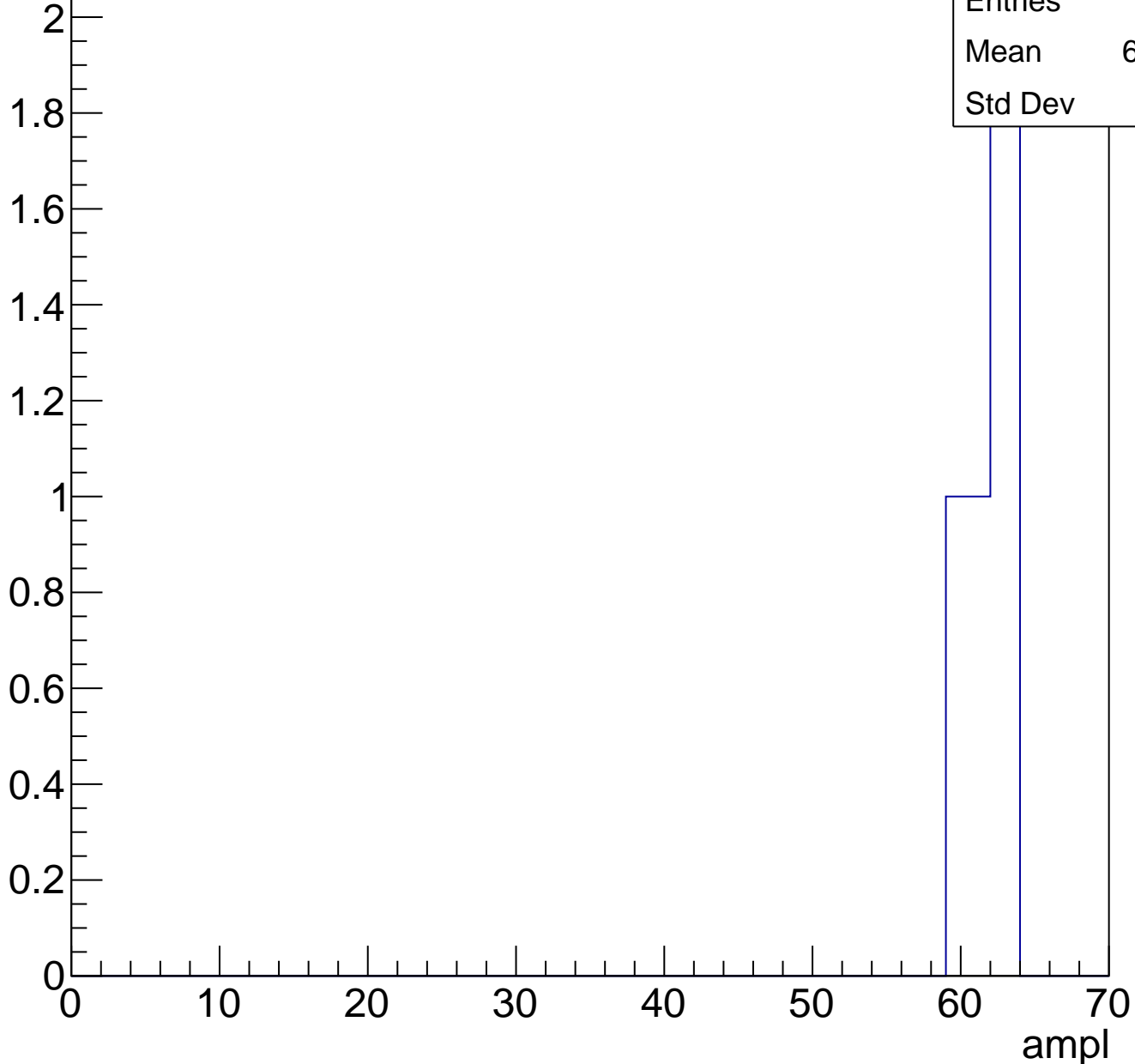
| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.34 |
| Std Dev | 8.744 |



B0L102S, U20-ch67, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch67, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch68, adc0

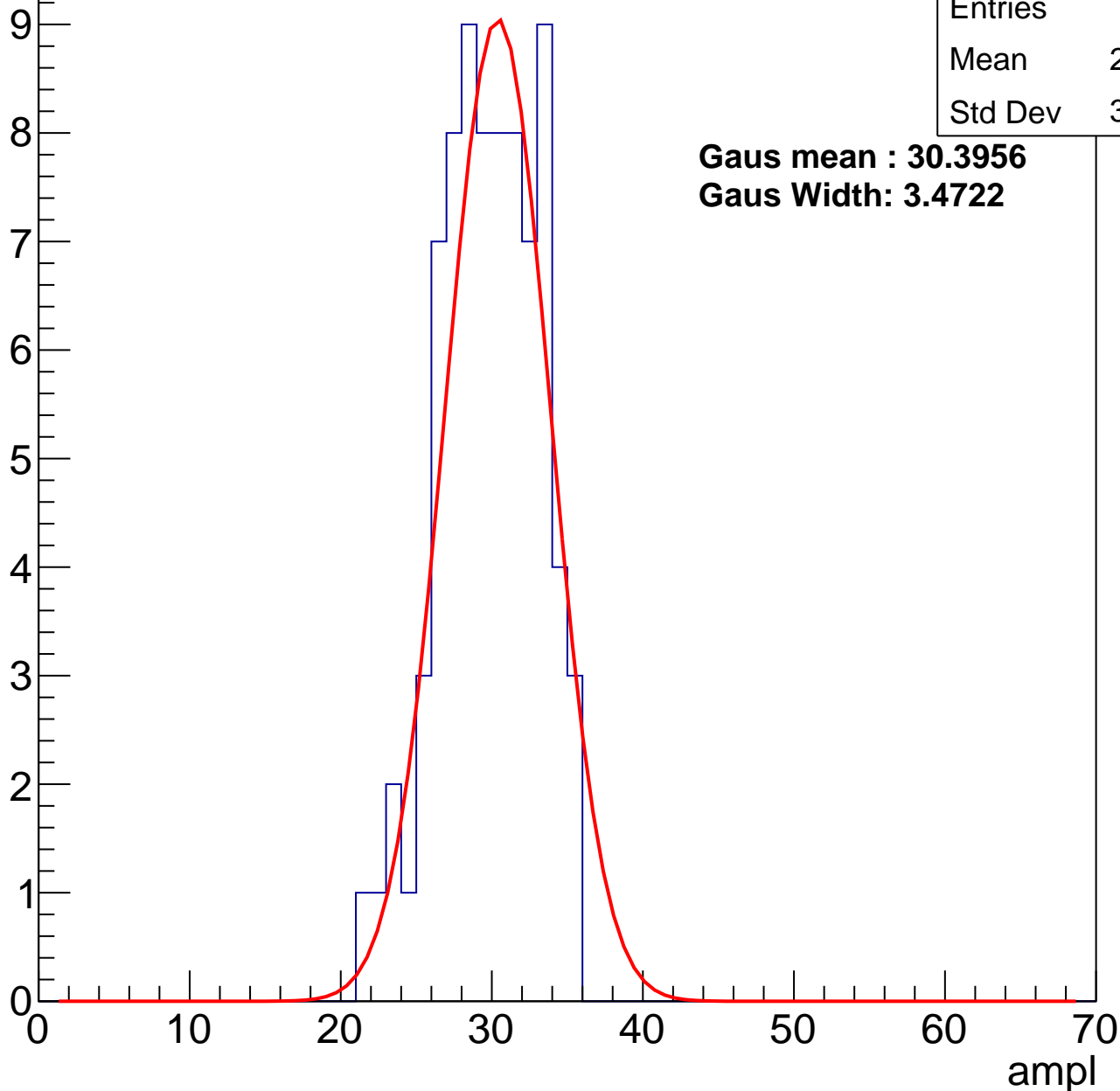
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.37 |
| Std Dev | 3.195 |

Gaus mean : 30.3956

Gaus Width: 3.4722



B0L102S, U20-ch68, adc1

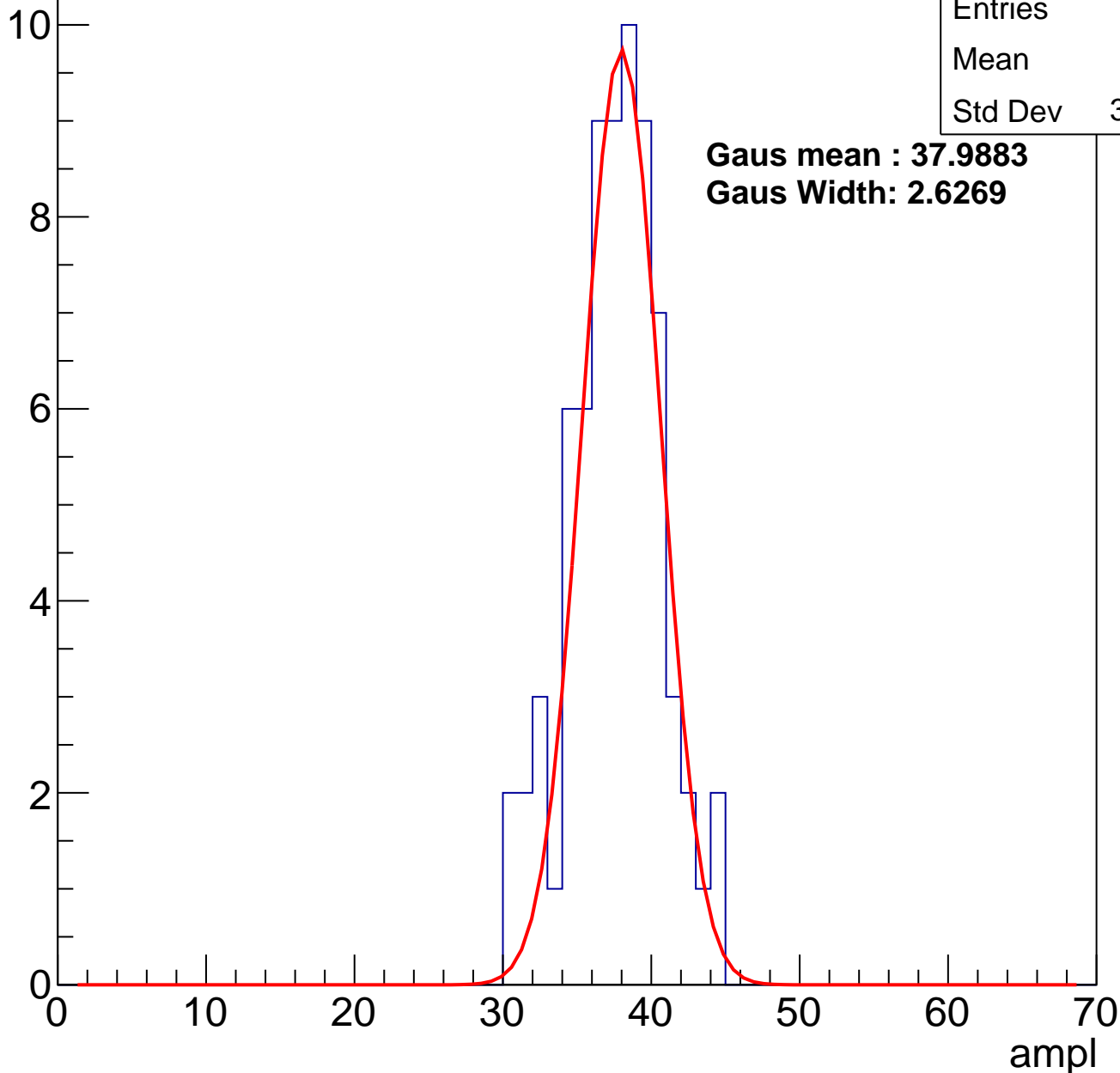
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.1 |
| Std Dev | 3.092 |

Gaus mean : 37.9883

Gaus Width: 2.6269

Entry



B0L102S, U20-ch68, adc2

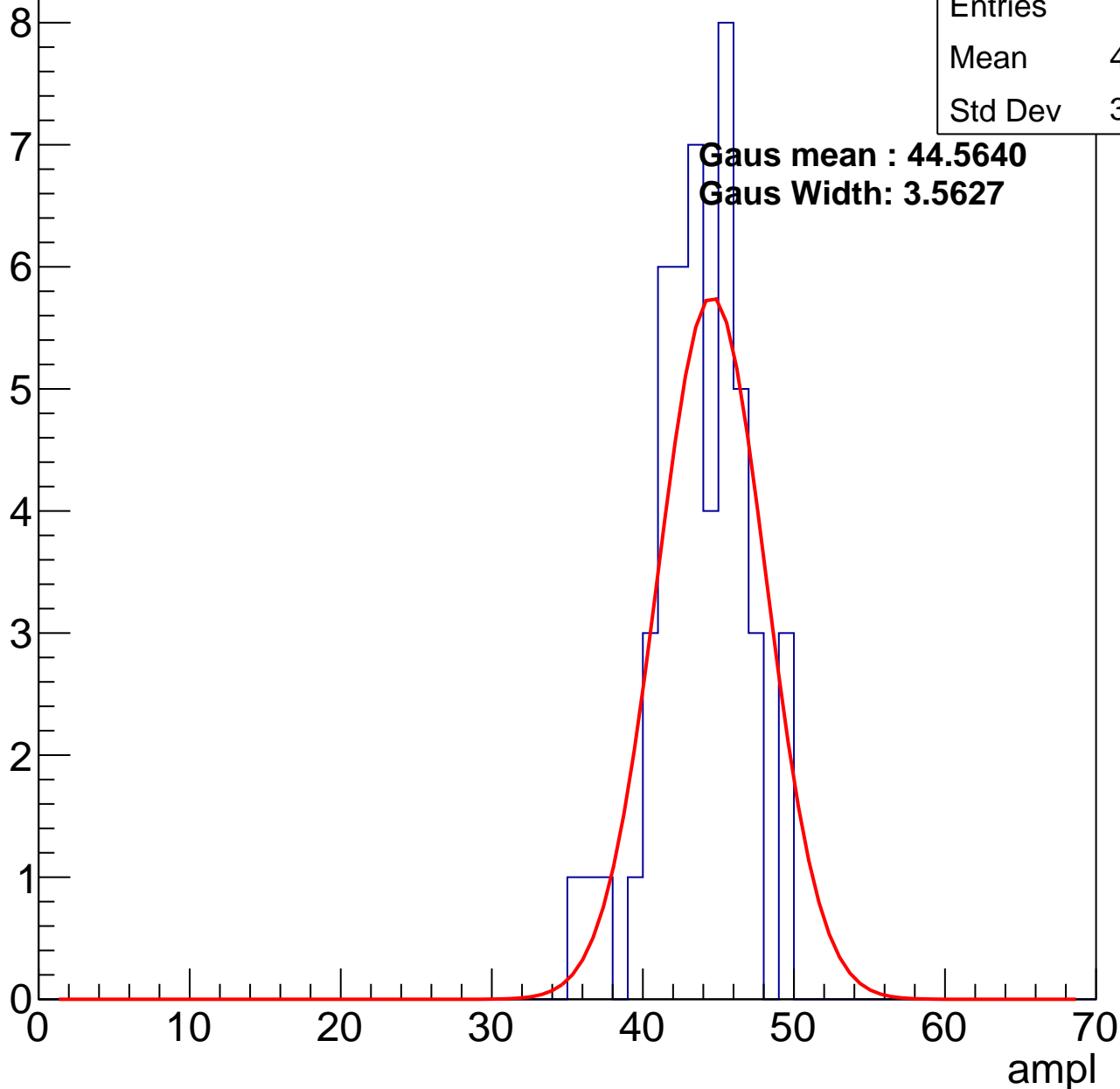
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.27 |
| Std Dev | 3.049 |

Gaus mean : 44.5640

Gaus Width: 3.5627



B0L102S, U20-ch68, adc3

calib_packv5_042523_0143.root, FC#12, port B1

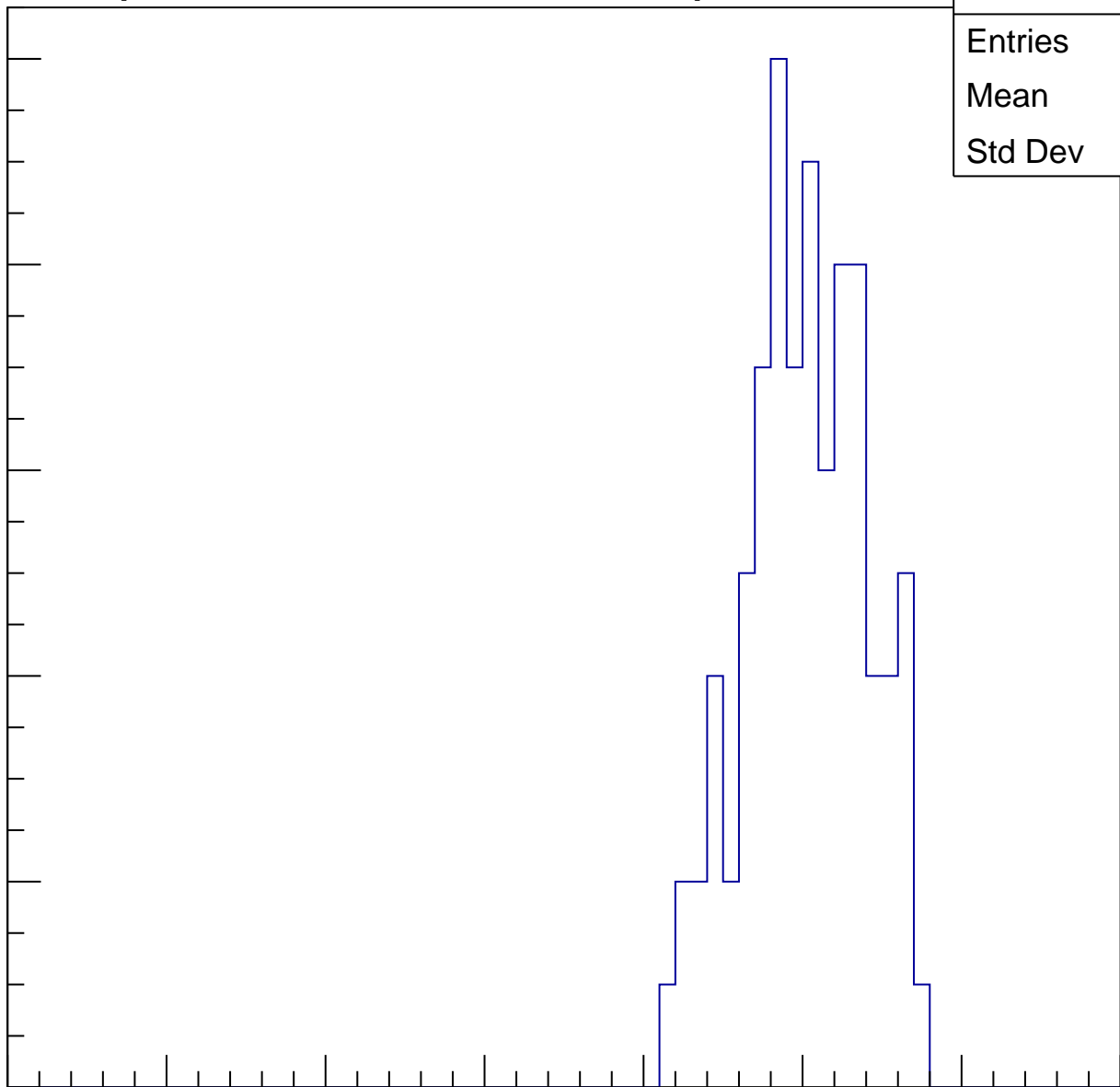
| | |
|---------|-------|
| Entries | 85 |
| Mean | 49.74 |
| Std Dev | 3.739 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

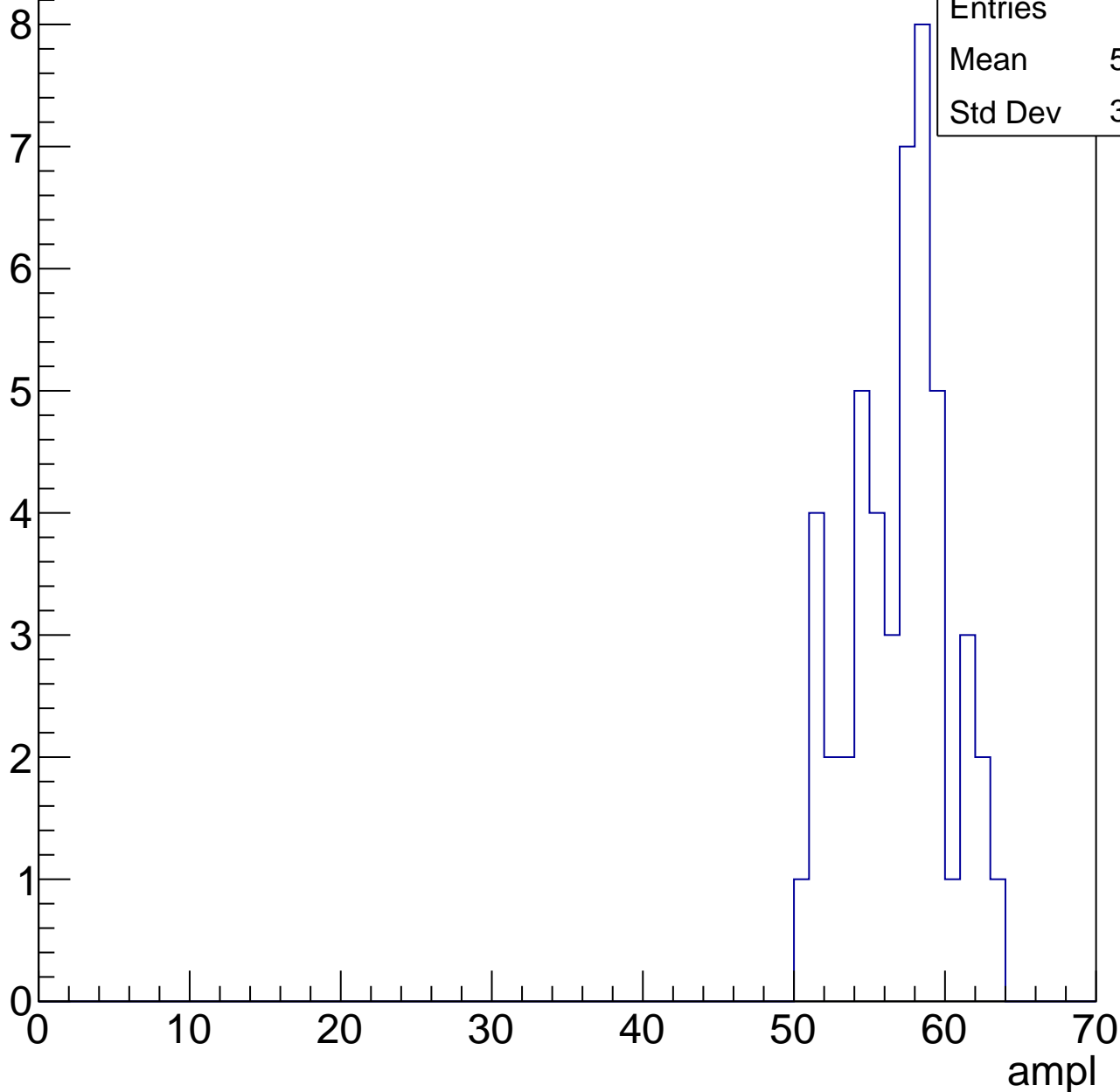


B0L102S, U20-ch68, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

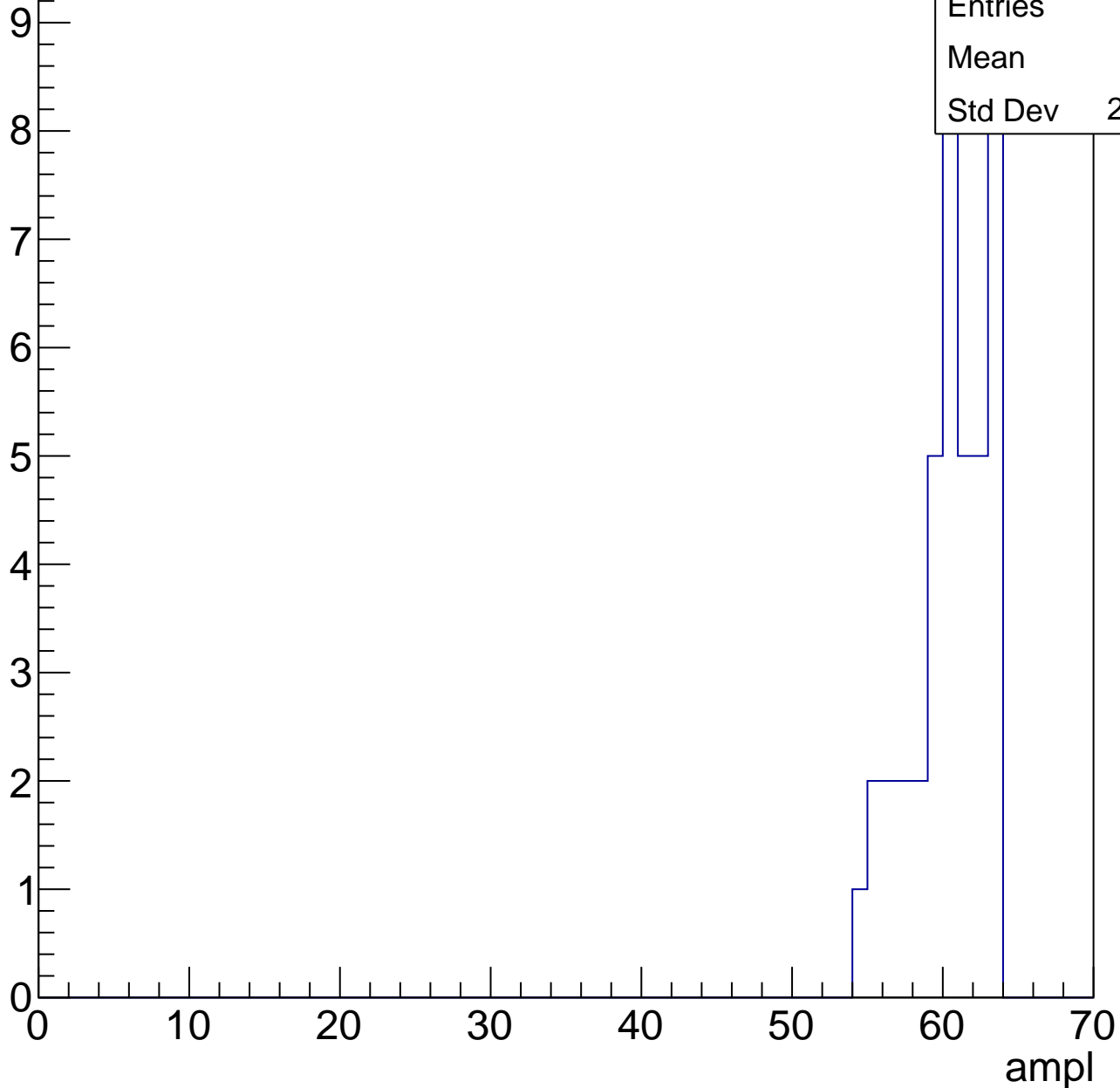
| | |
|---------|-------|
| Entries | 48 |
| Mean | 56.46 |
| Std Dev | 3.214 |



B0L102S, U20-ch68, adc5

calib_packv5_042523_0143.root, FC#12, port B1

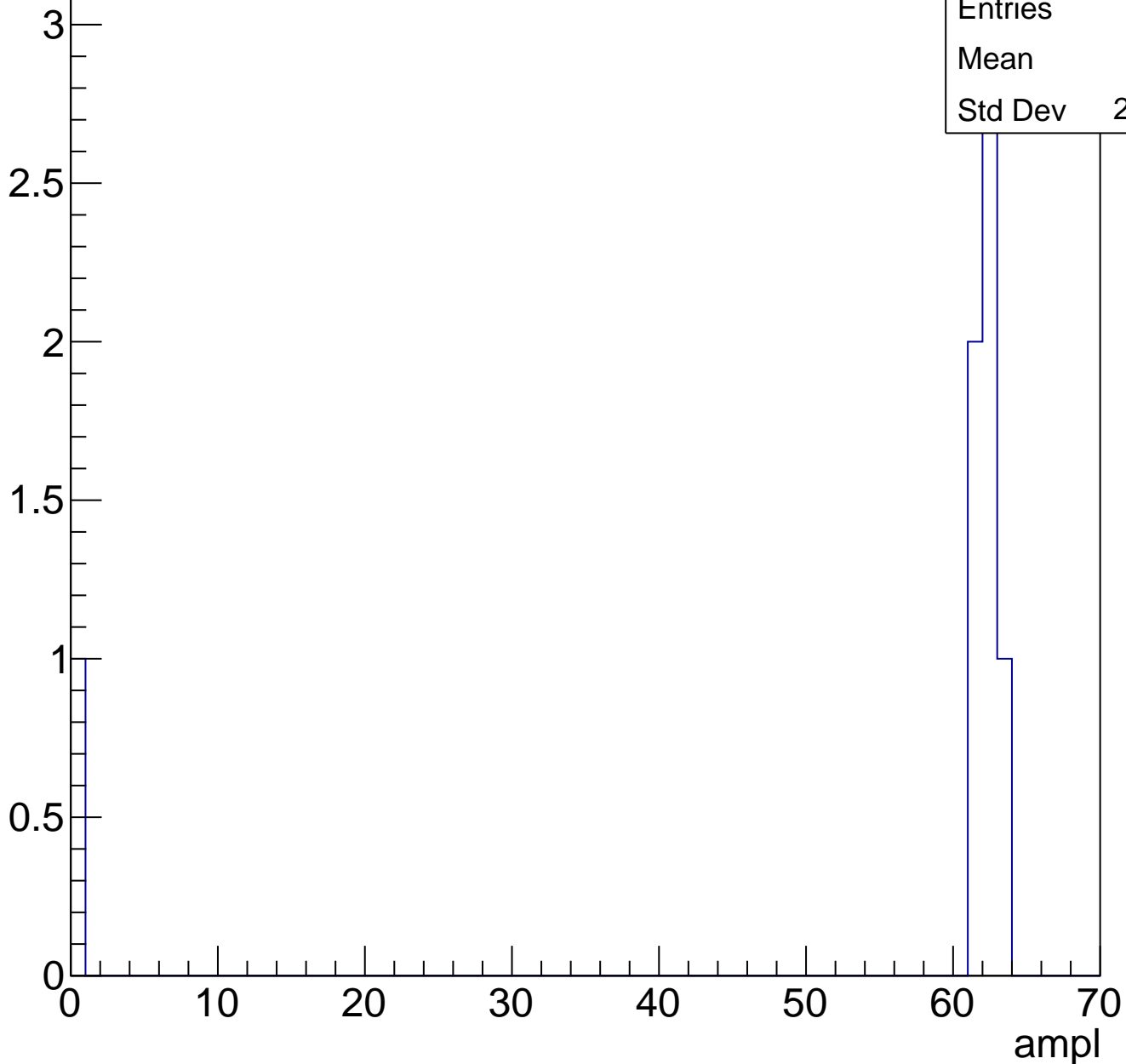
Entry



B0L102S, U20-ch68, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 7 |
| Mean | 53 |
| Std Dev | 21.65 |

B0L102S, U20-ch68, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch69, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 27.16 |
| Std Dev | 4.802 |

Gaus mean : 27.4878

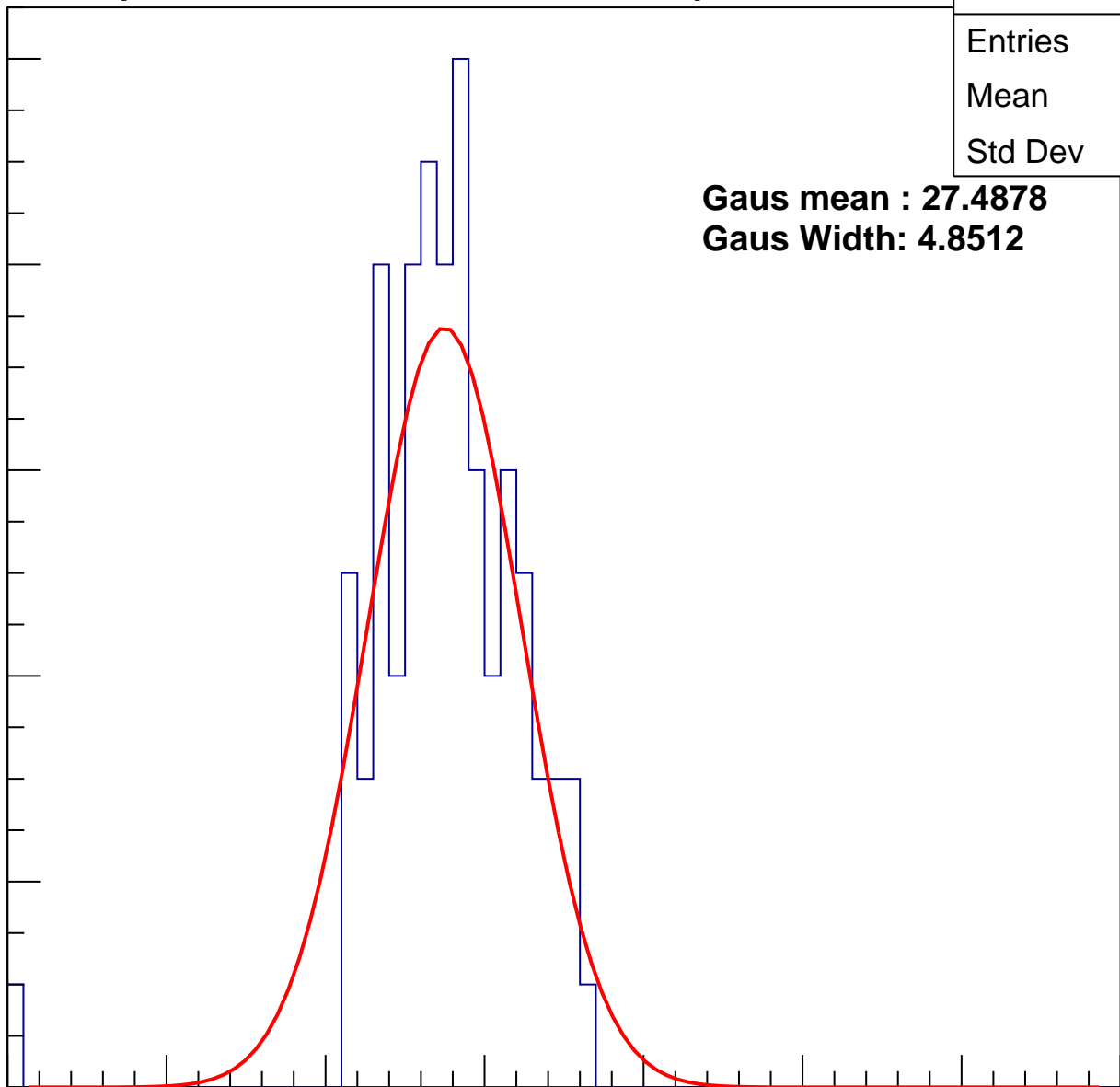
Gaus Width: 4.8512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



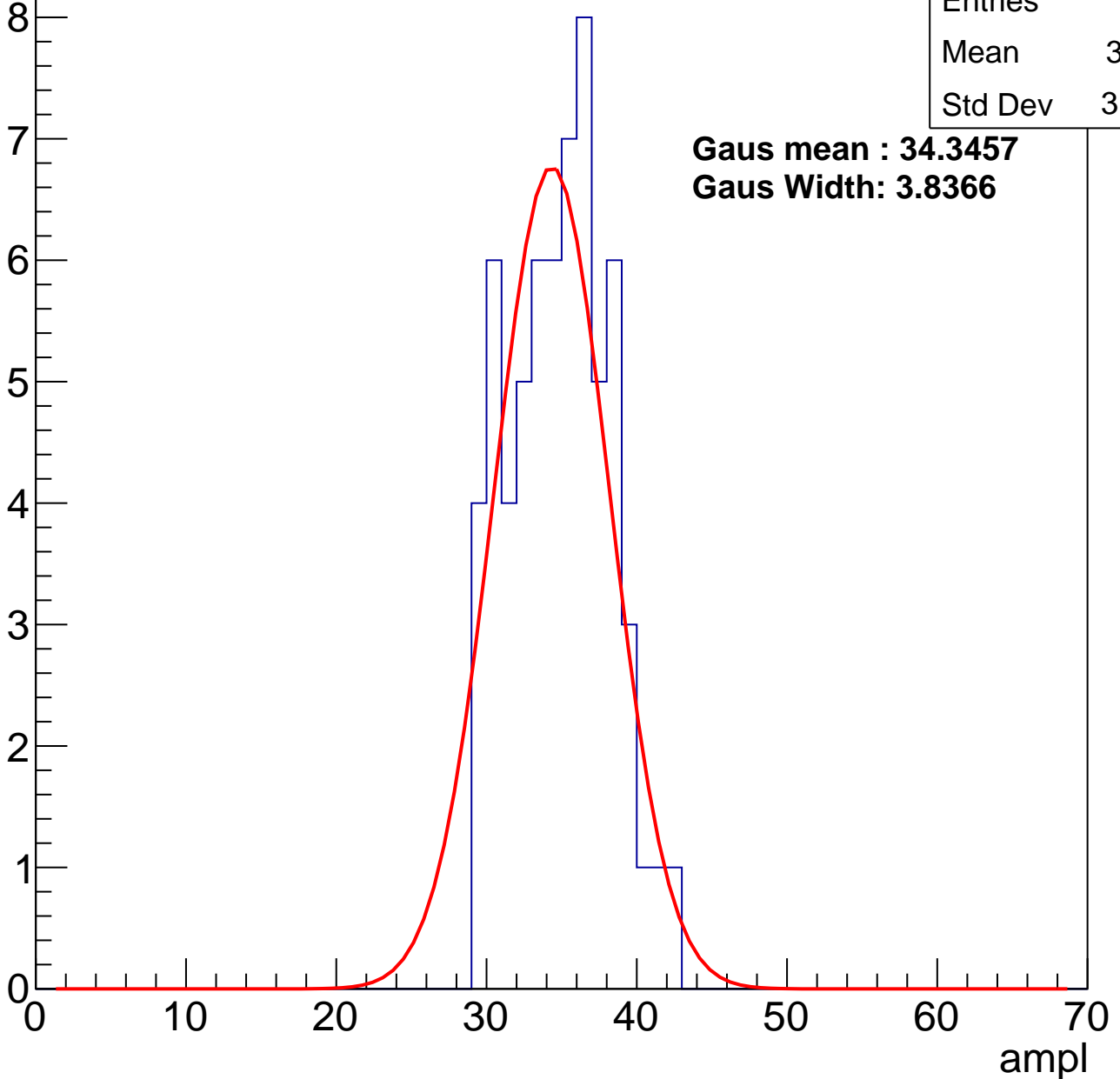
B0L102S, U20-ch69, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 34.41 |
| Std Dev | 3.215 |

Gaus mean : 34.3457
Gaus Width: 3.8366



B0L102S, U20-ch69, adc2

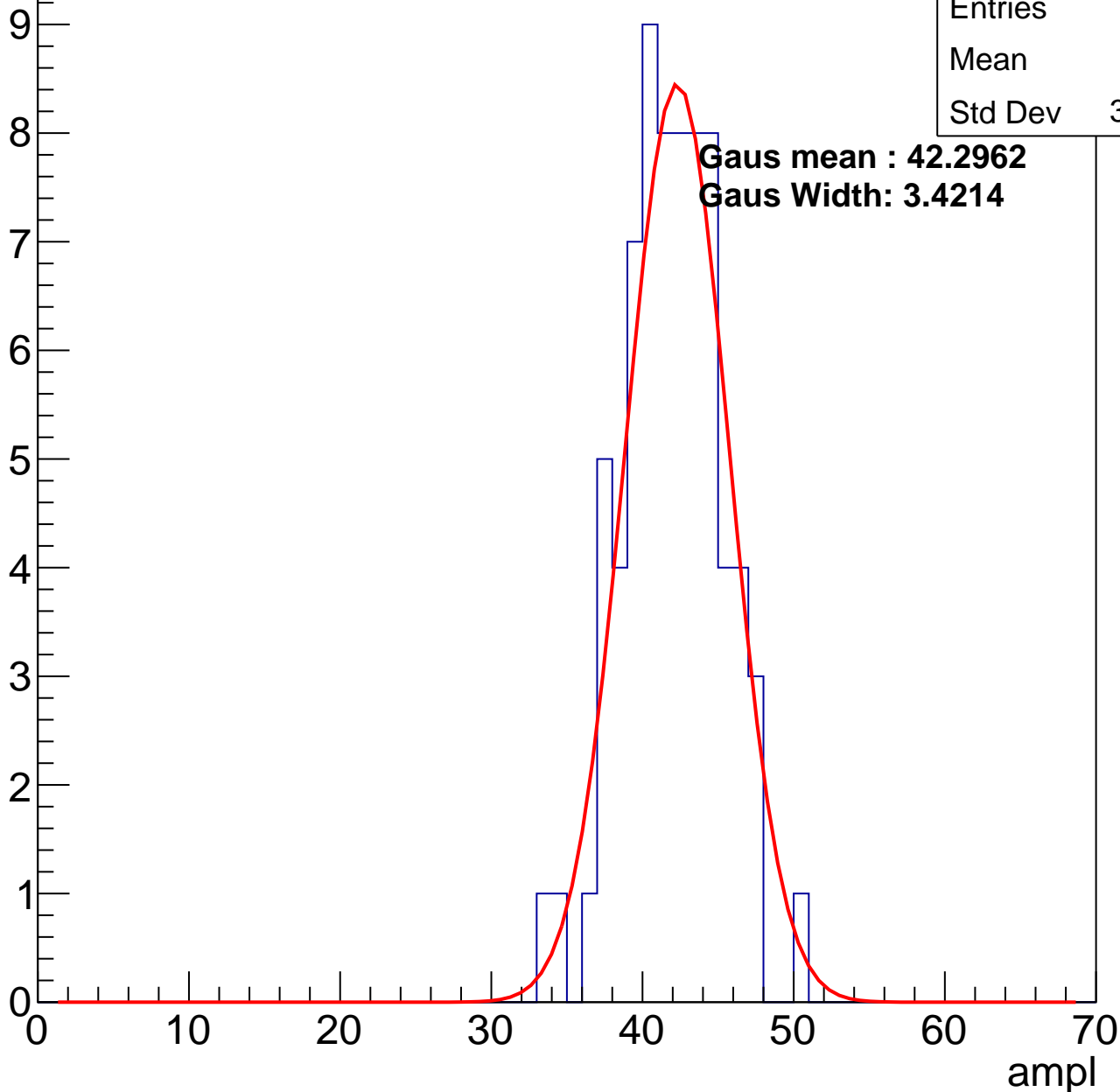
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 41.5 |
| Std Dev | 3.202 |

Gaus mean : 42.2962

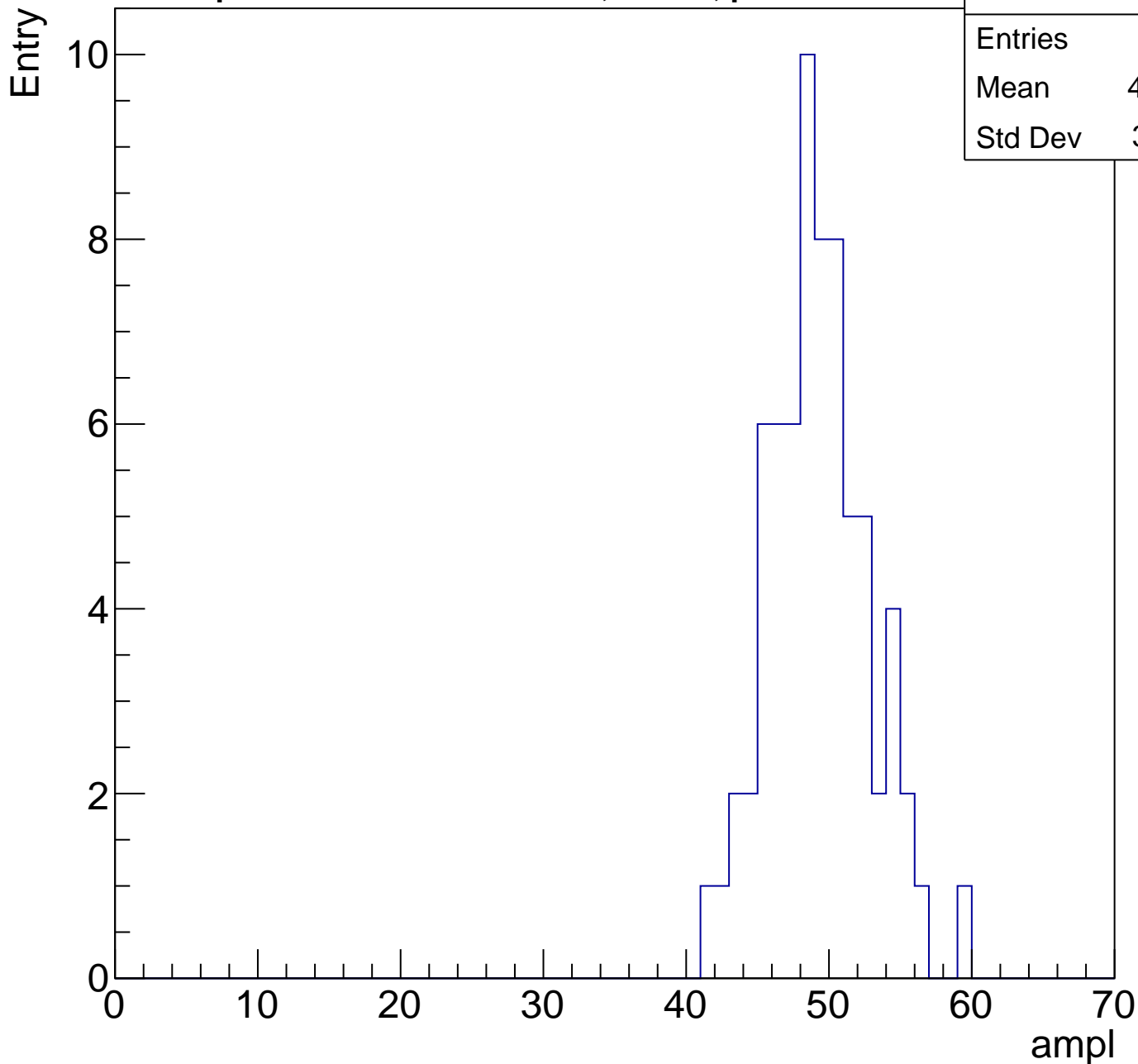
Gaus Width: 3.4214



B0L102S, U20-ch69, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 48.84 |
| Std Dev | 3.471 |

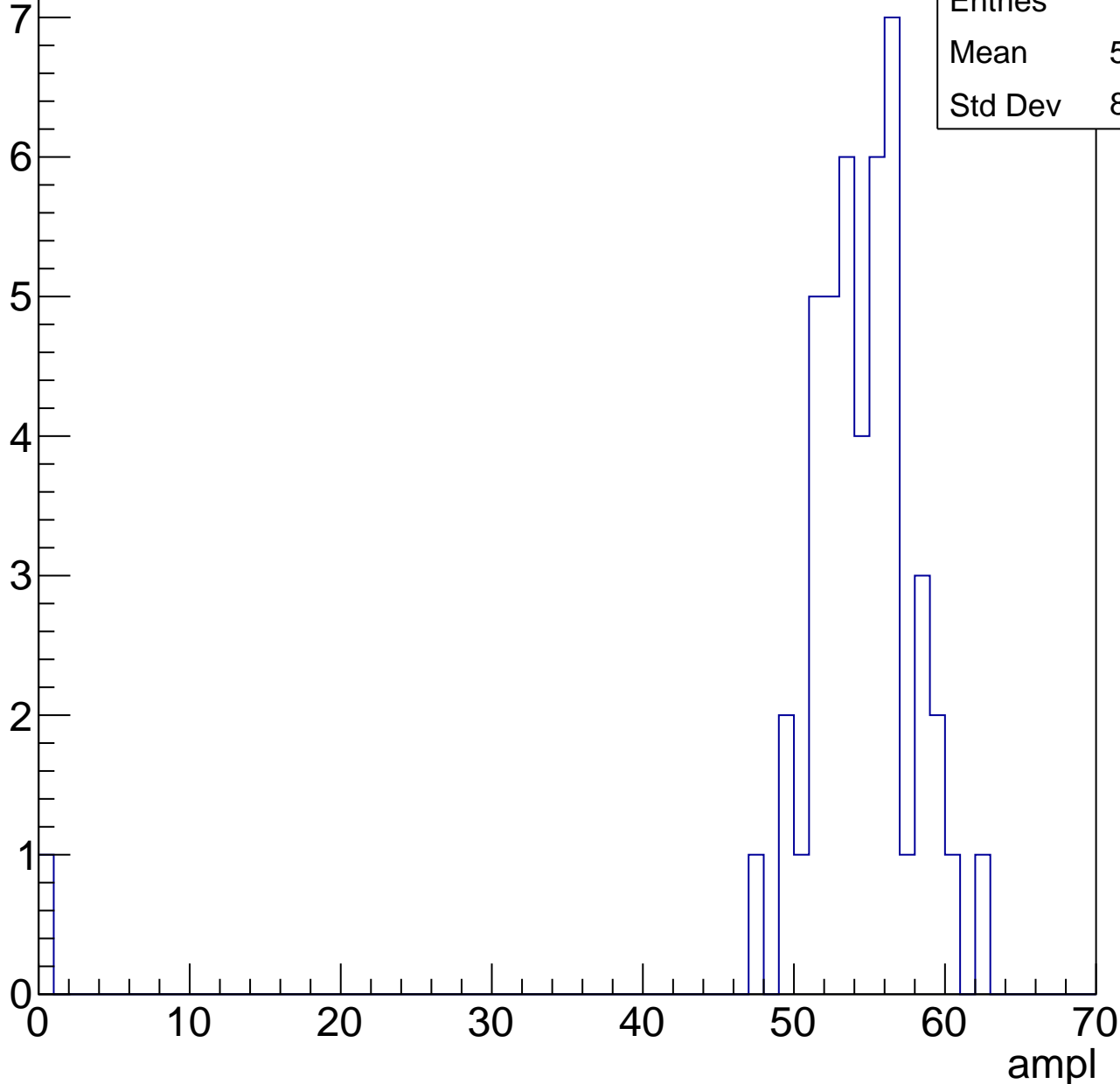


B0L102S, U20-ch69, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

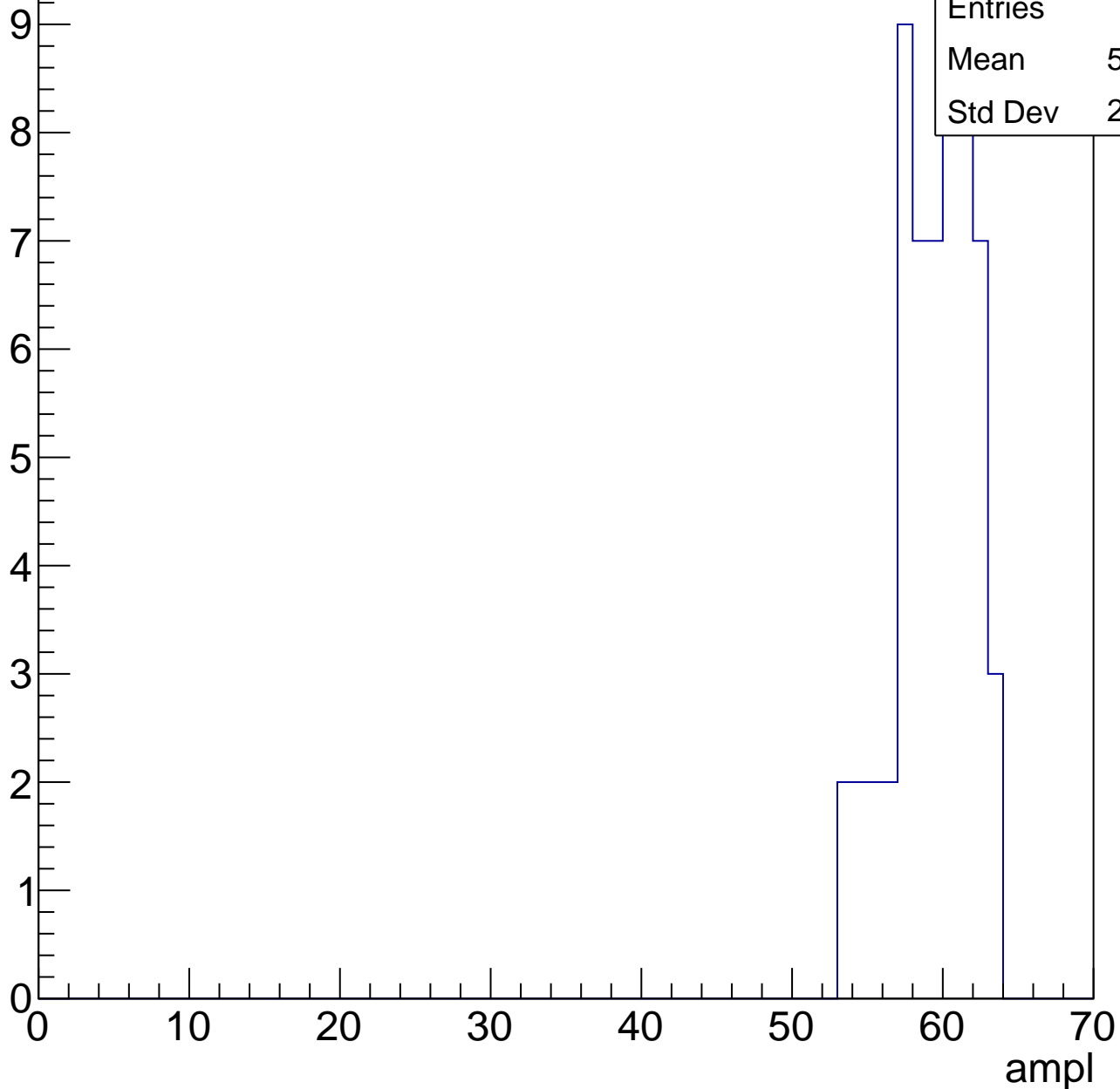
| | |
|---------|-------|
| Entries | 46 |
| Mean | 52.98 |
| Std Dev | 8.463 |



B0L102S, U20-ch69, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



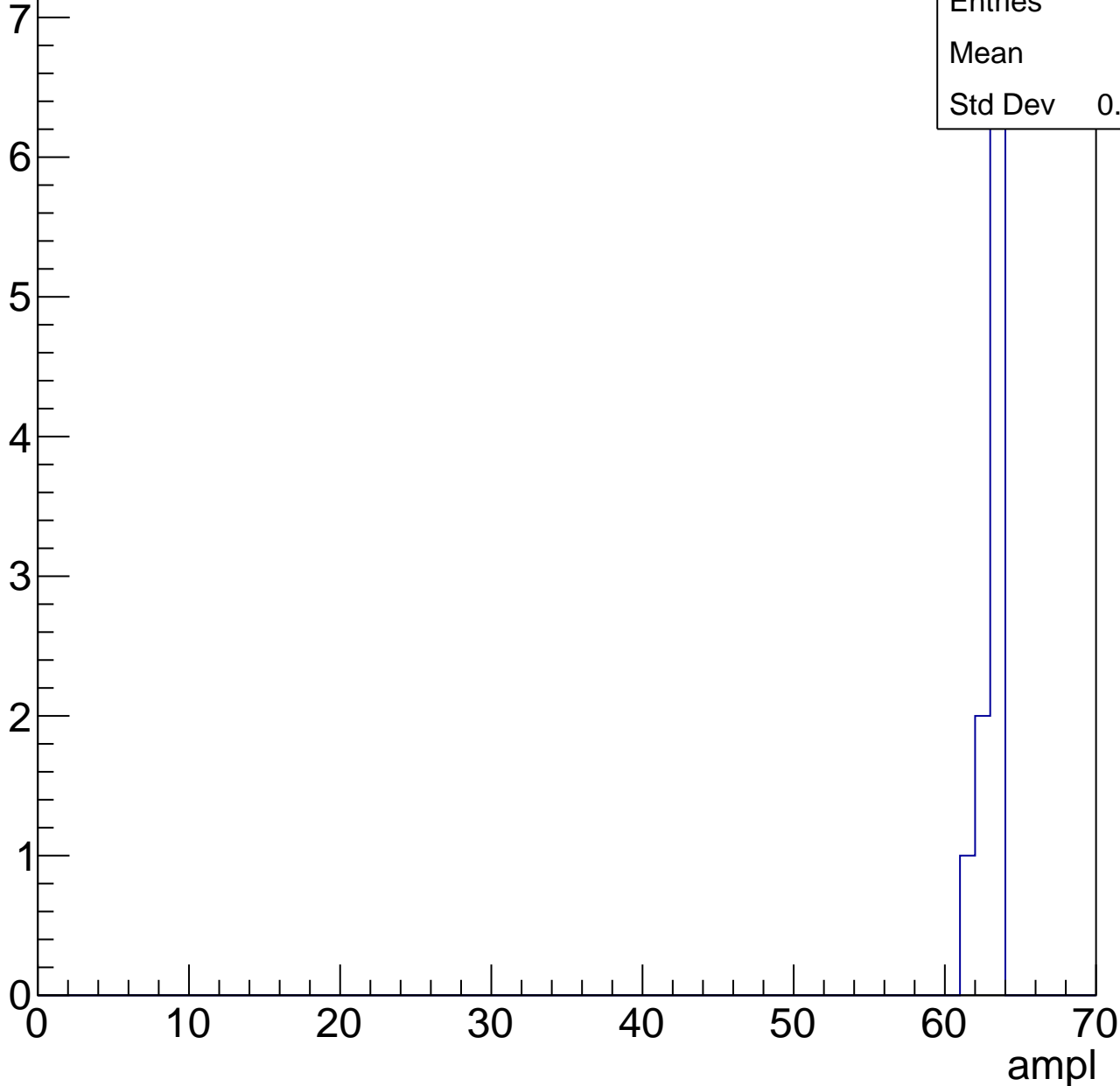
| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.98 |
| Std Dev | 2.508 |

B0L102S, U20-ch69, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|--------|
| Entries | 10 |
| Mean | 62.6 |
| Std Dev | 0.6633 |



B0L102S, U20-ch69, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U20-ch70, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 66 |
| Mean | 27.71 |
| Std Dev | 3.379 |

Gaus mean : 27.9936

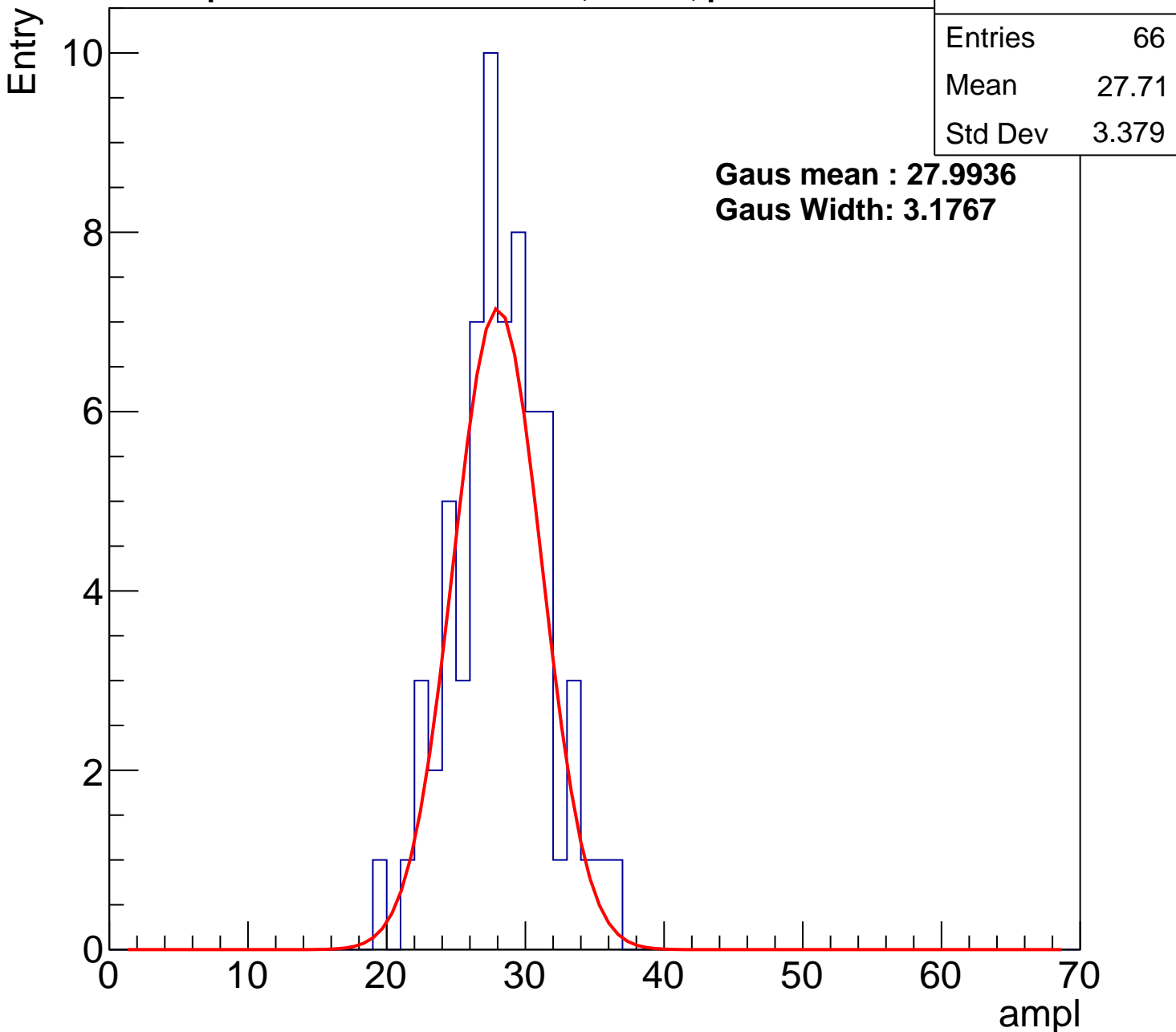
Gaus Width: 3.1767

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch70, adc1

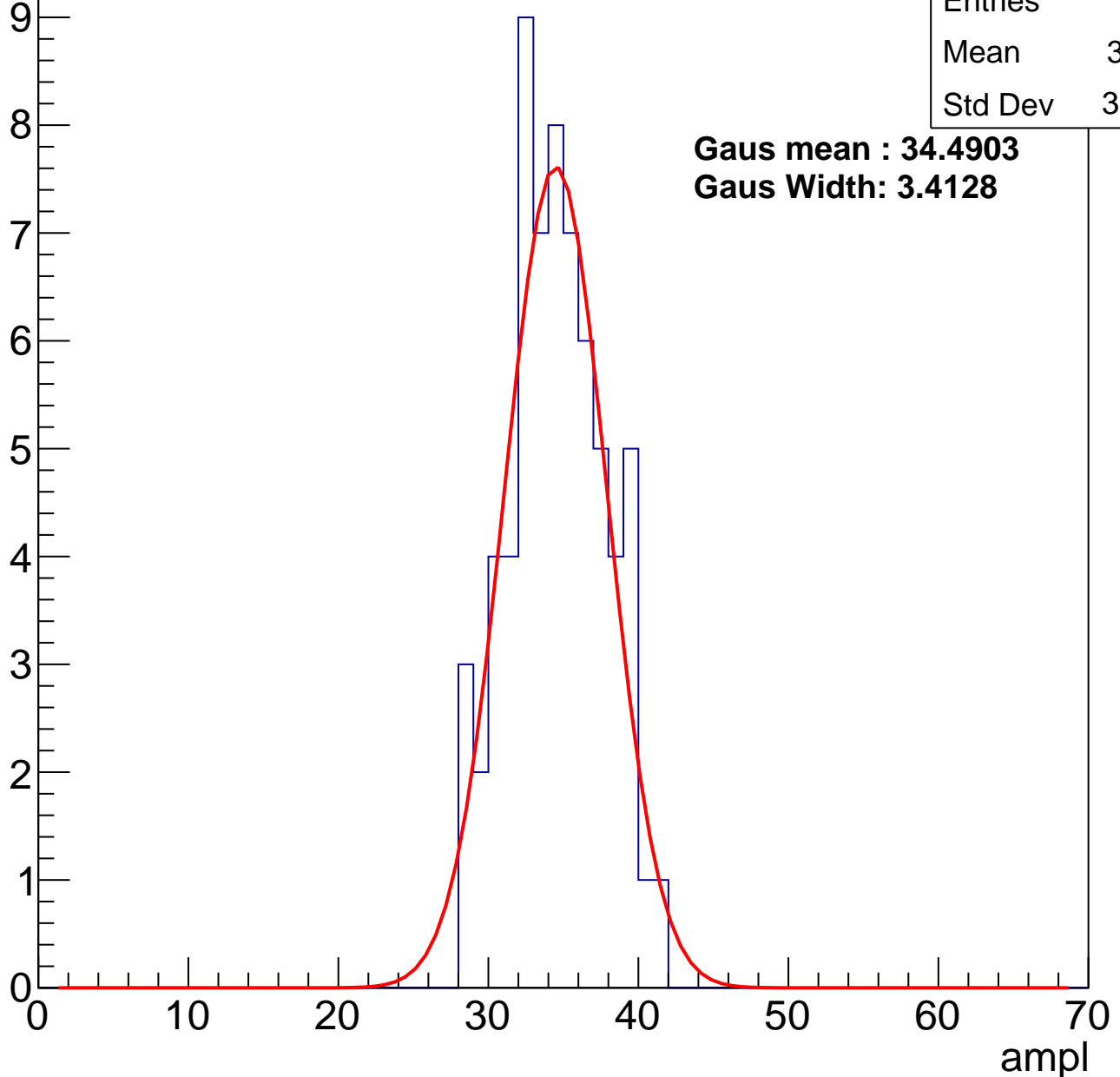
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 34.11 |
| Std Dev | 3.139 |

Gaus mean : 34.4903

Gaus Width: 3.4128



B0L102S, U20-ch70, adc2

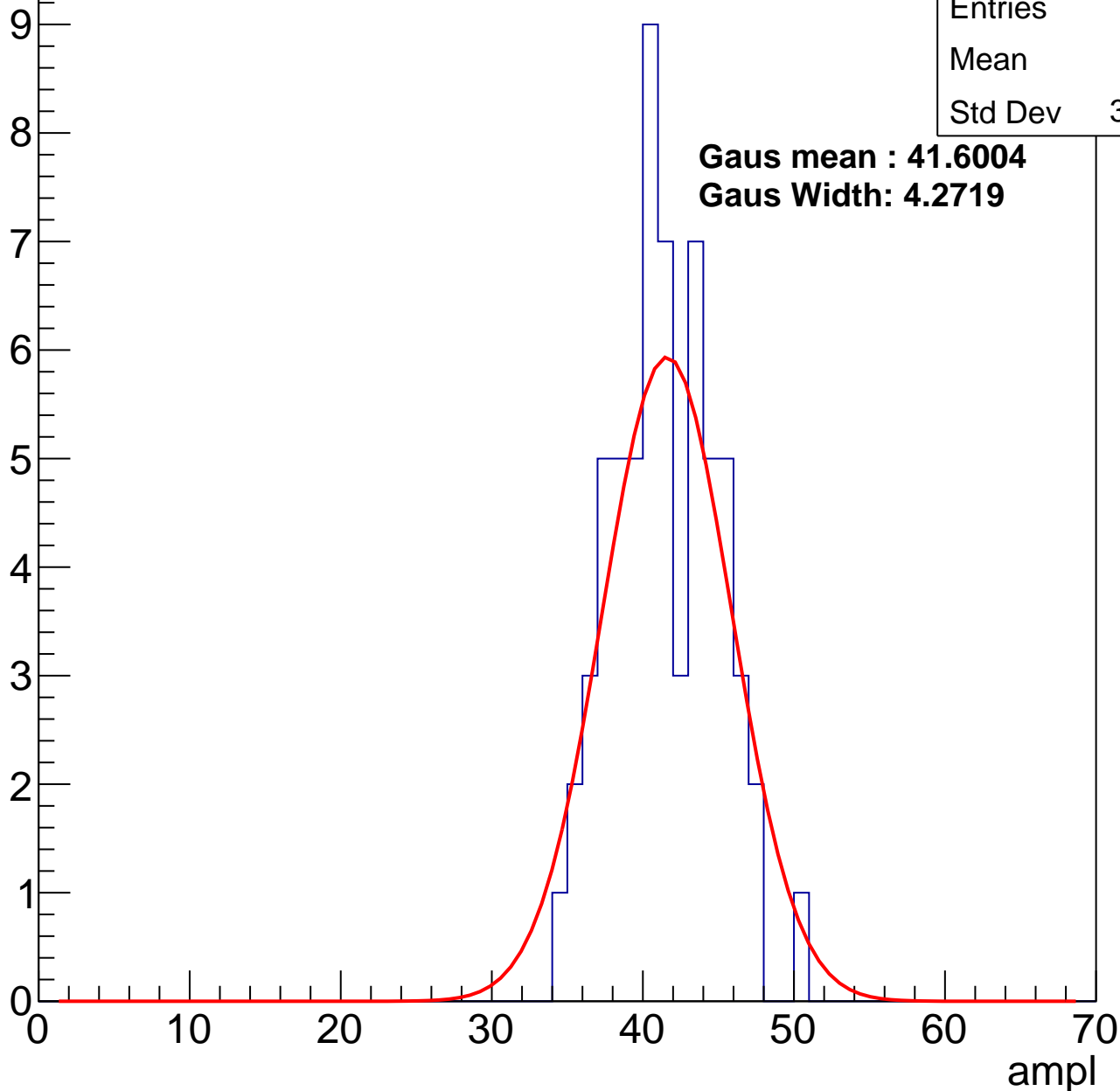
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41 |
| Std Dev | 3.413 |

Gaus mean : 41.6004

Gaus Width: 4.2719

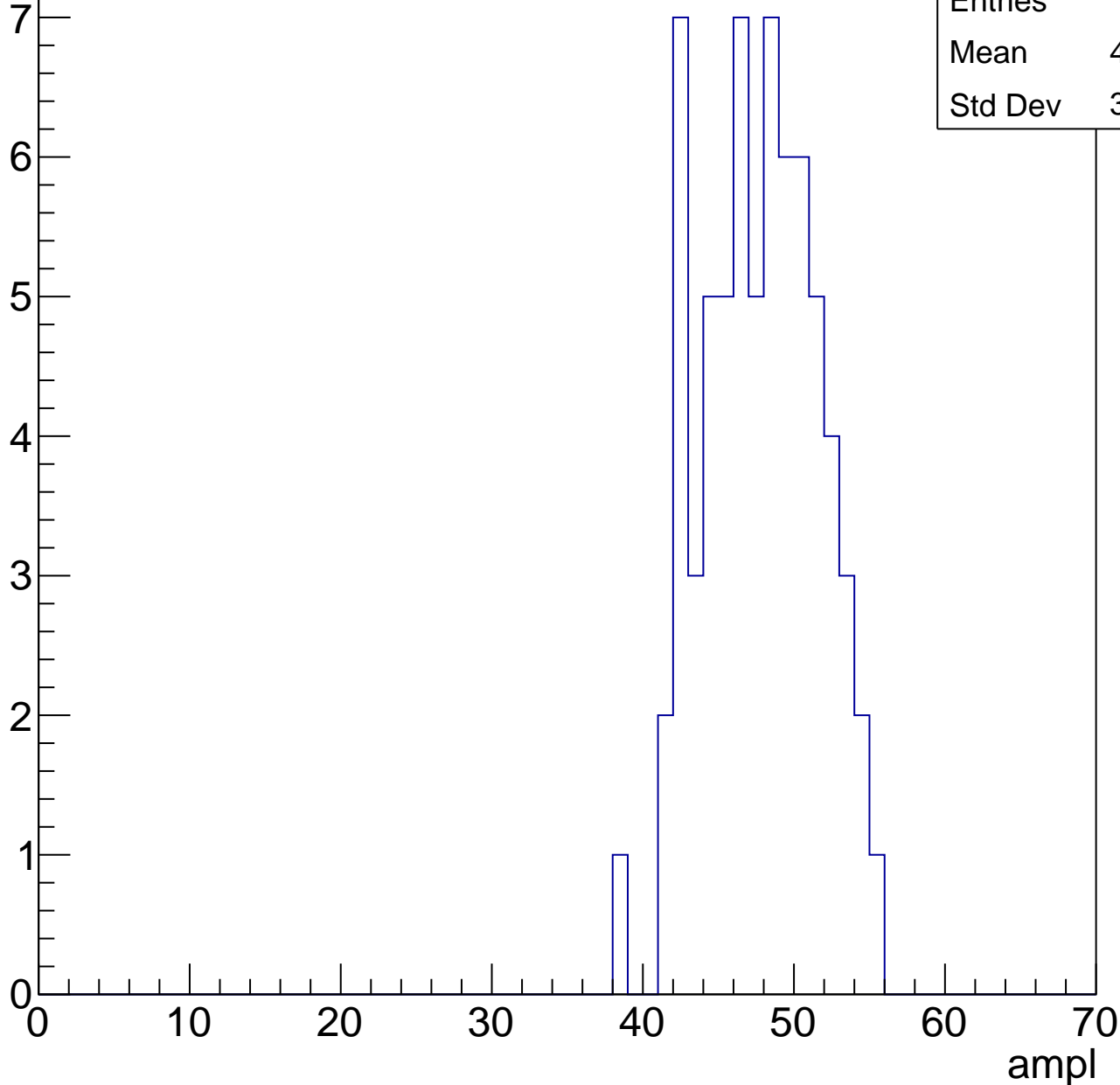


B0L102S, U20-ch70, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 47.25 |
| Std Dev | 3.766 |

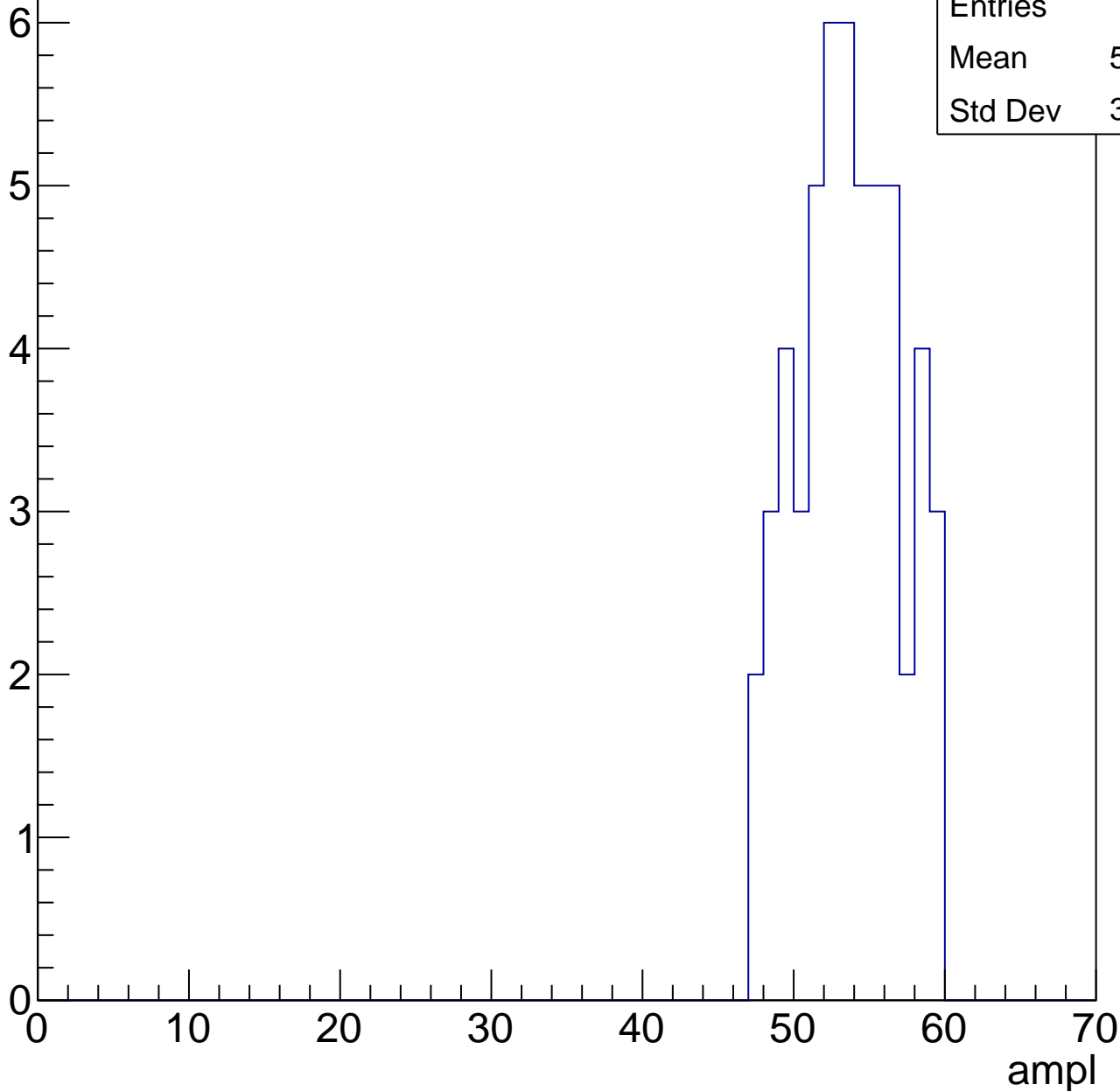


B0L102S, U20-ch70, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 53.15 |
| Std Dev | 3.287 |

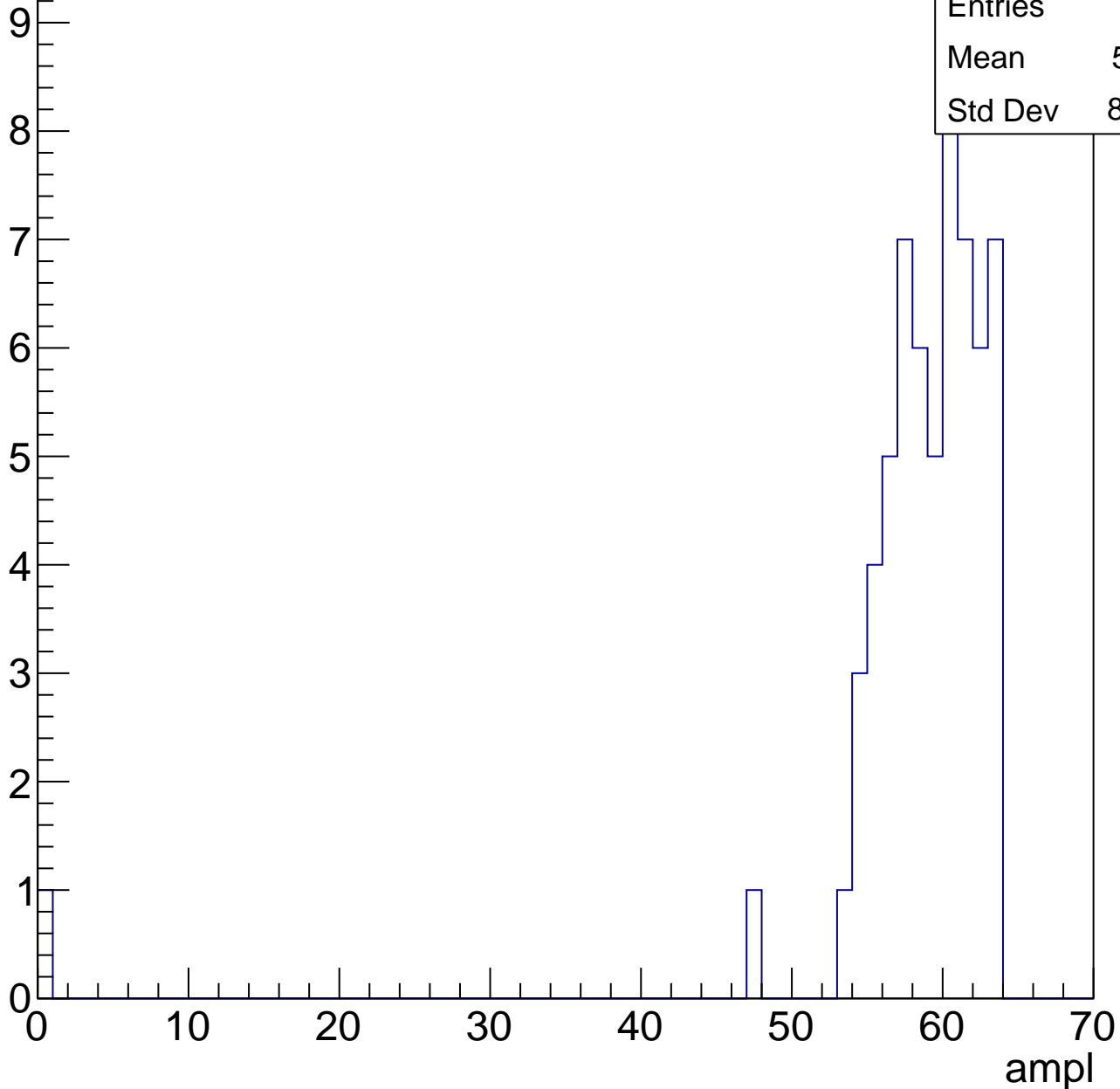


B0L102S, U20-ch70, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

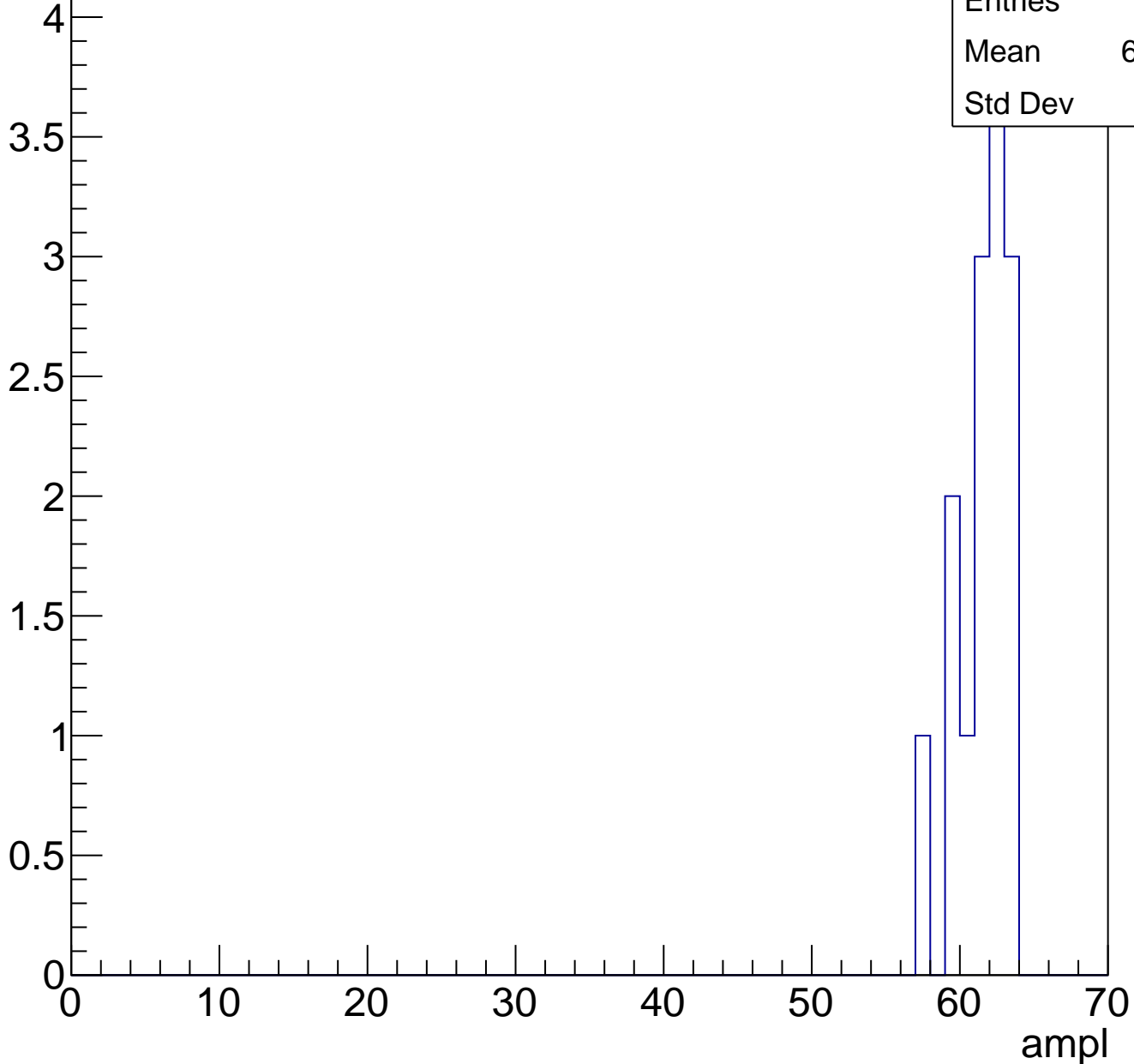
| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.81 |
| Std Dev | 8.026 |



B0L102S, U20-ch70, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch70, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch71, adc0

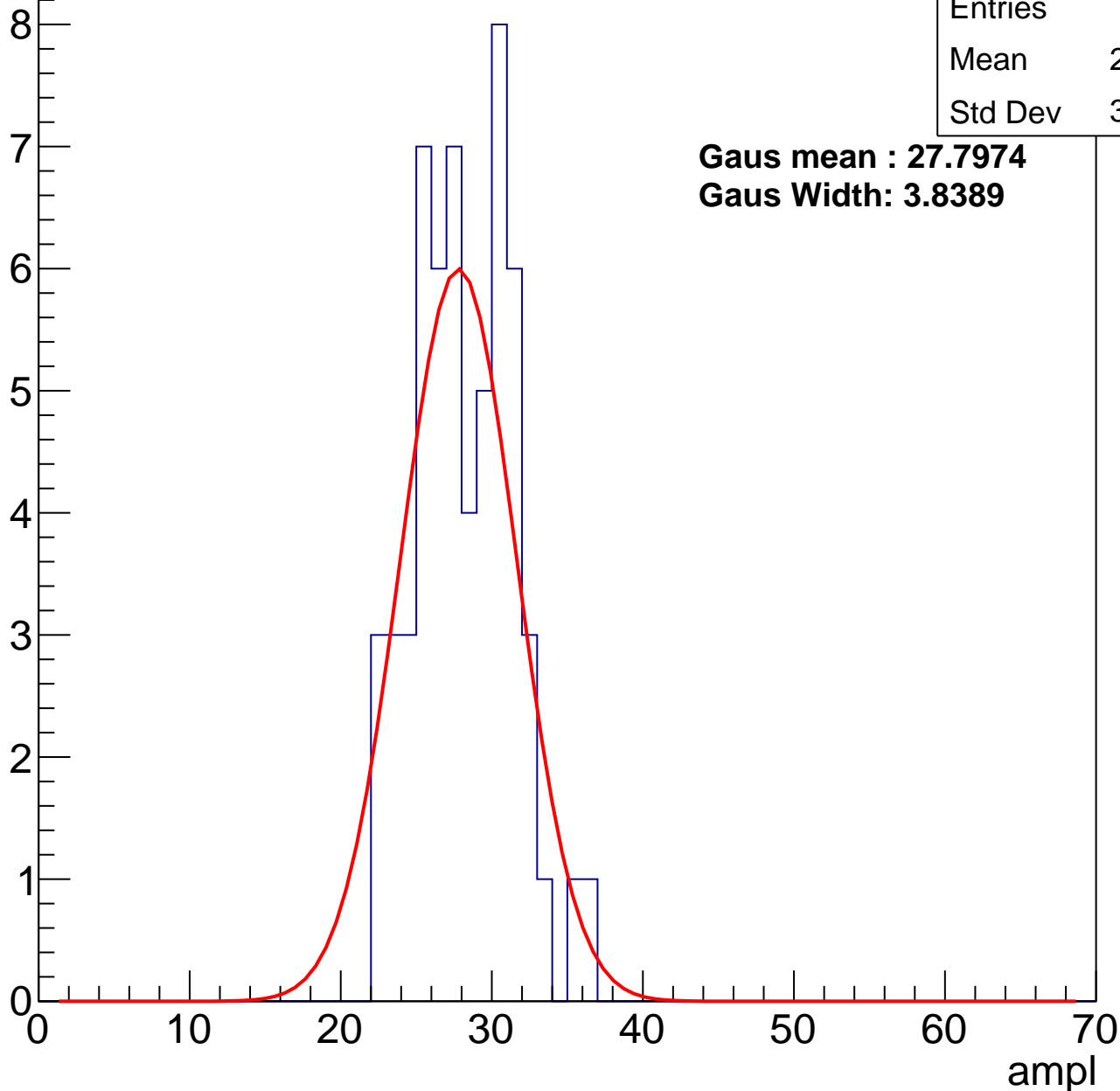
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 27.76 |
| Std Dev | 3.213 |

Gaus mean : 27.7974

Gaus Width: 3.8389



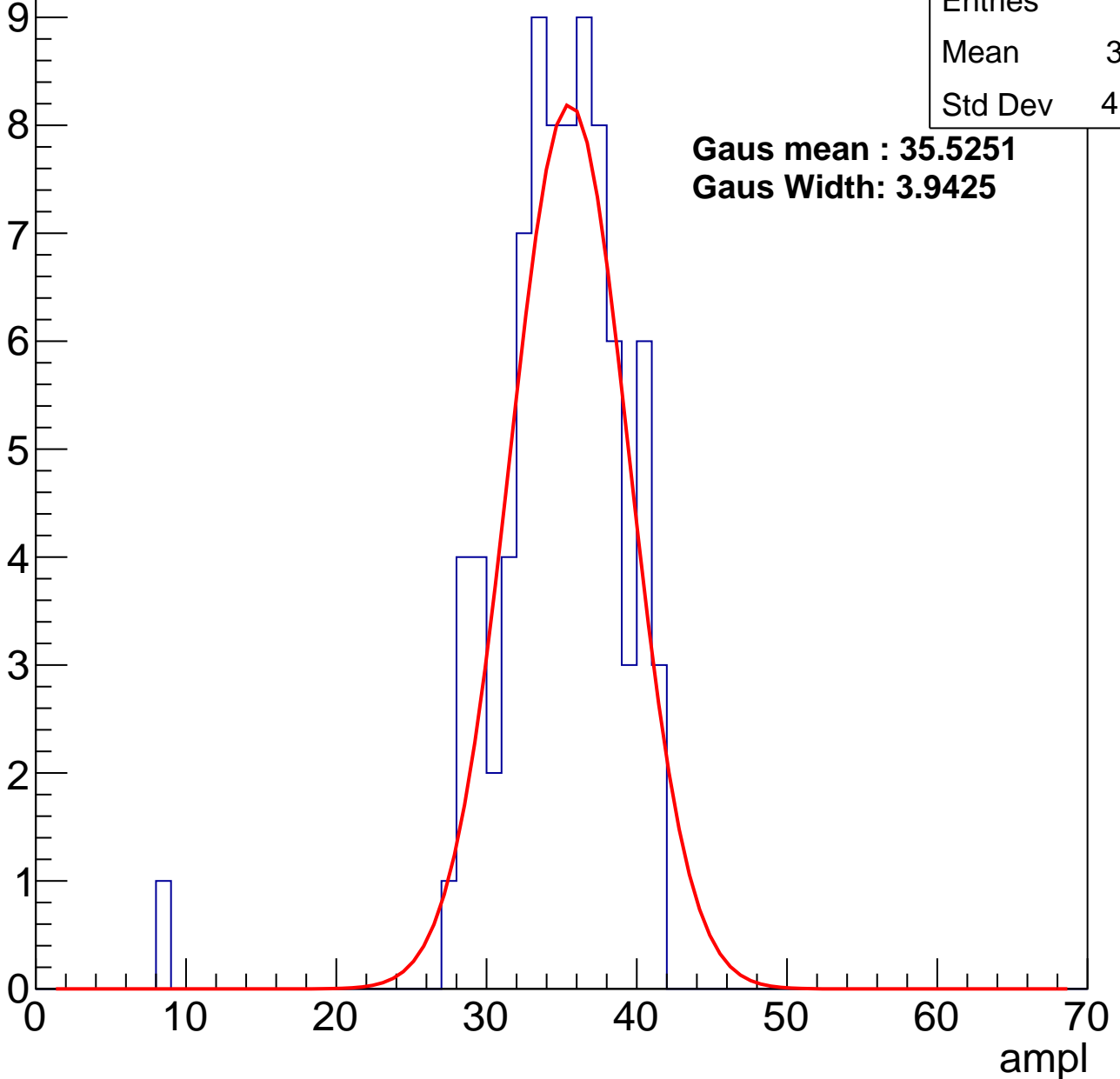
B0L102S, U20-ch71, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 34.31 |
| Std Dev | 4.547 |

Gaus mean : 35.5251
Gaus Width: 3.9425



B0L102S, U20-ch71, adc2

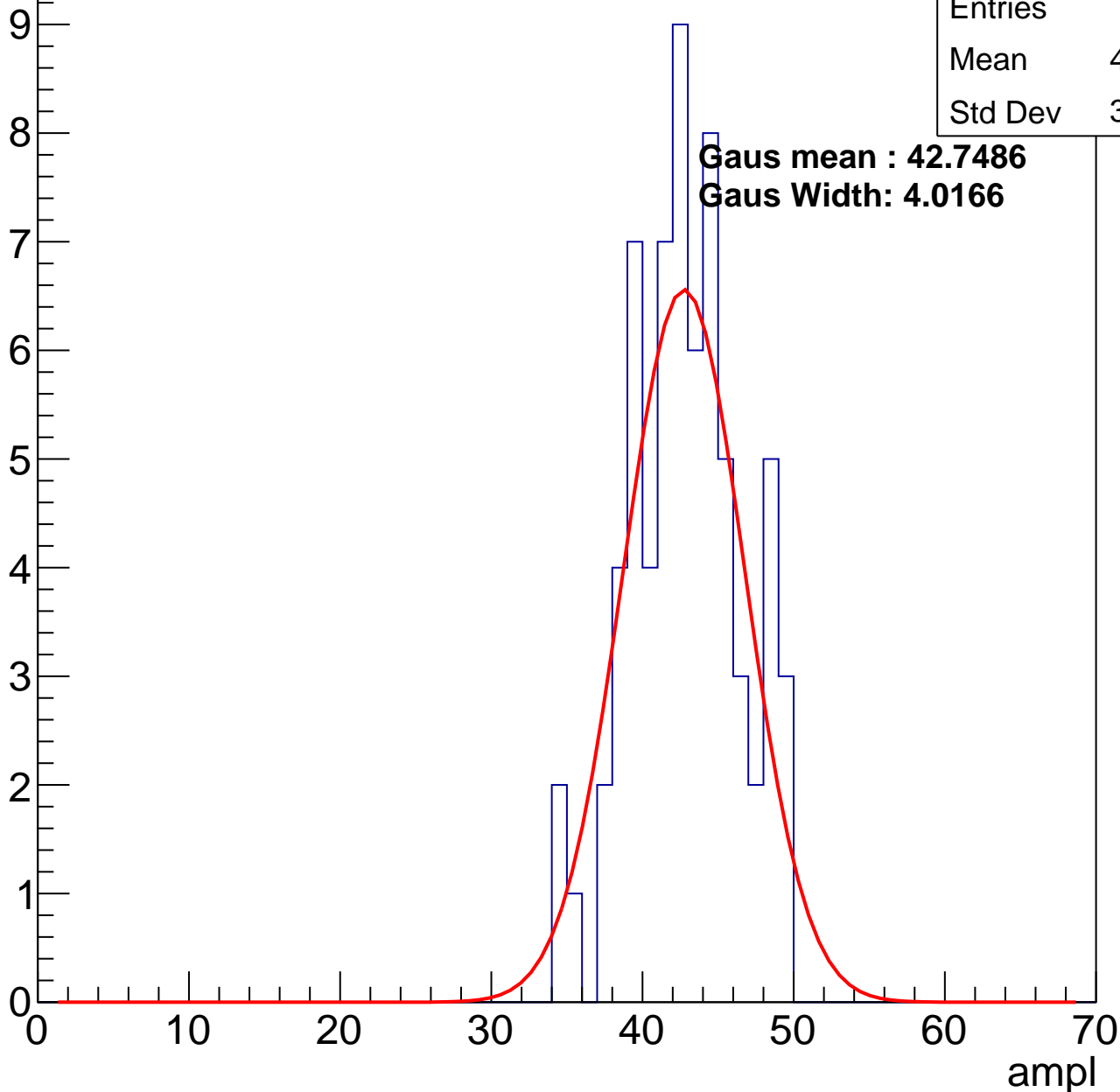
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.37 |
| Std Dev | 3.589 |

Gaus mean : 42.7486

Gaus Width: 4.0166

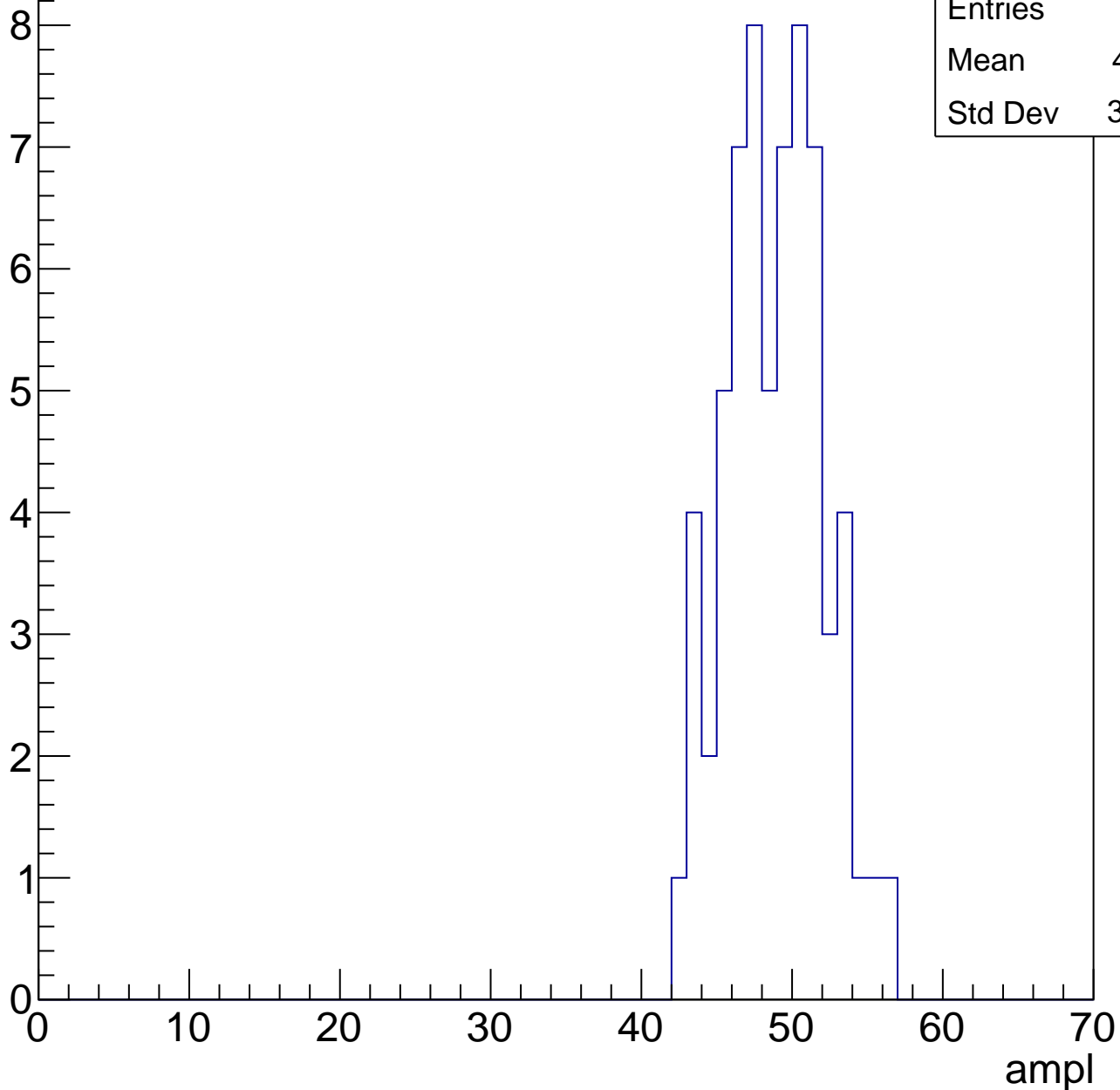


B0L102S, U20-ch71, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.41 |
| Std Dev | 3.156 |

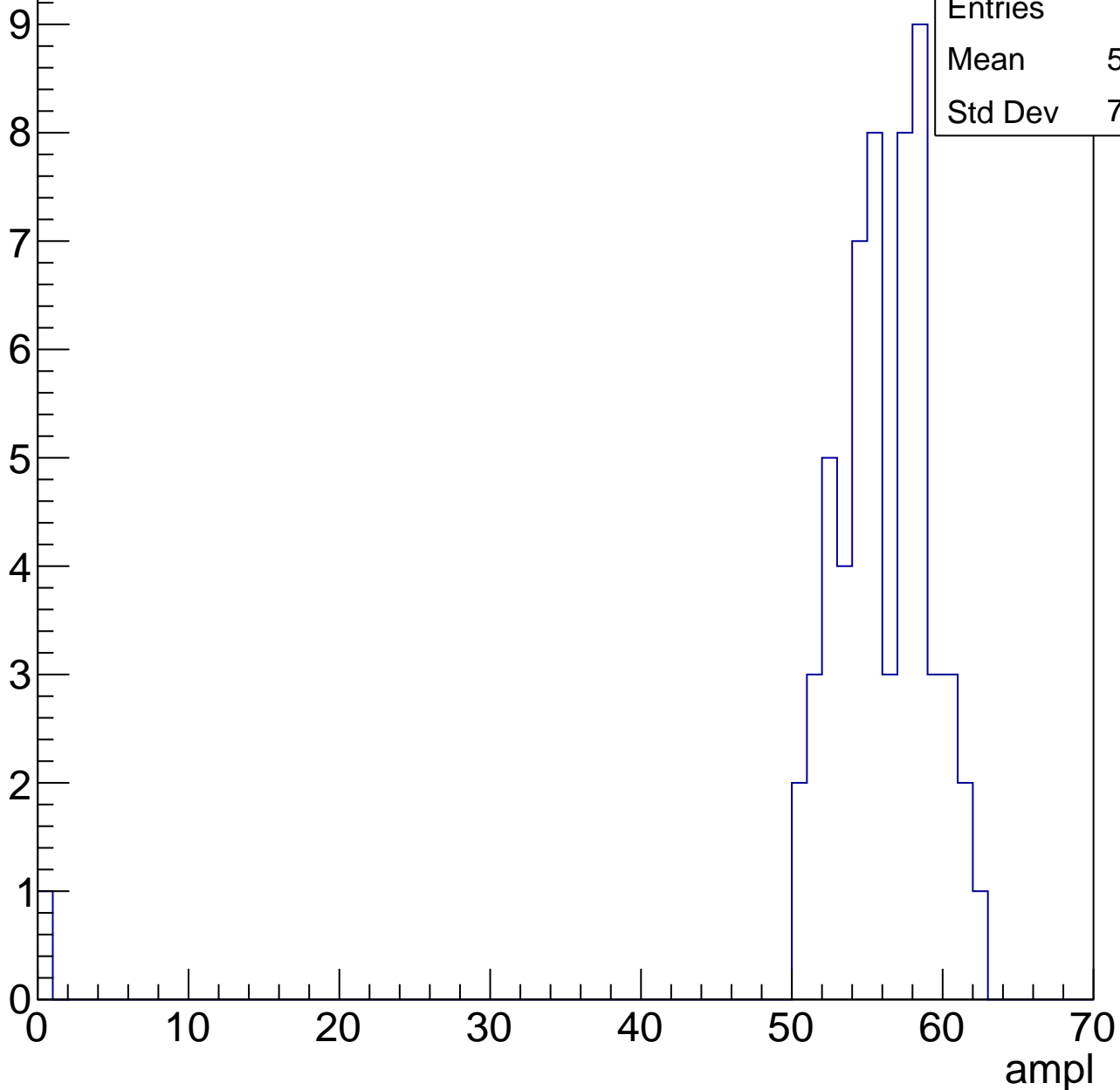


B0L102S, U20-ch71, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.75 |
| Std Dev | 7.754 |

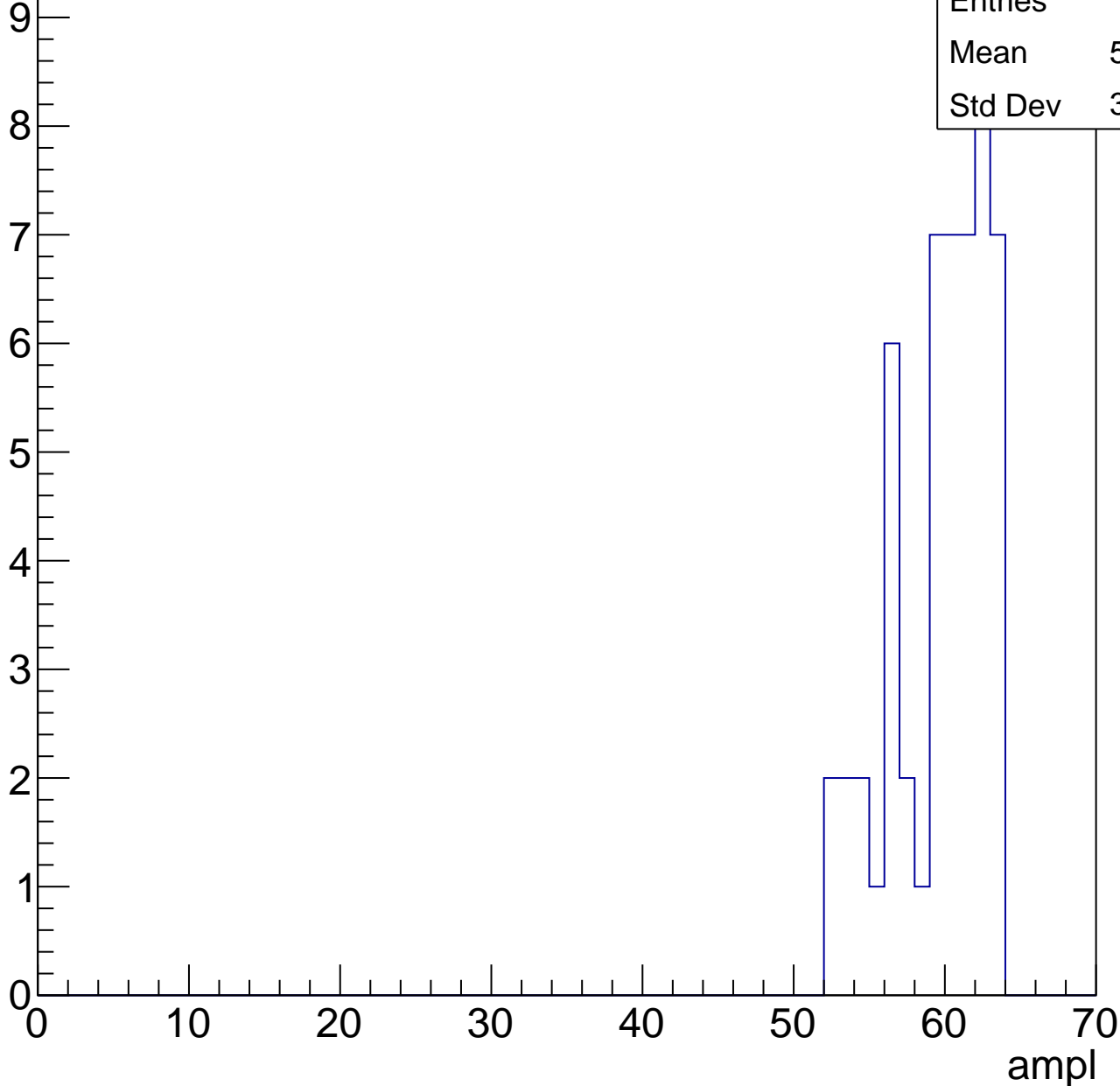


B0L102S, U20-ch71, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.25 |
| Std Dev | 3.144 |



B0L102S, U20-ch71, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch71, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch72, adc0

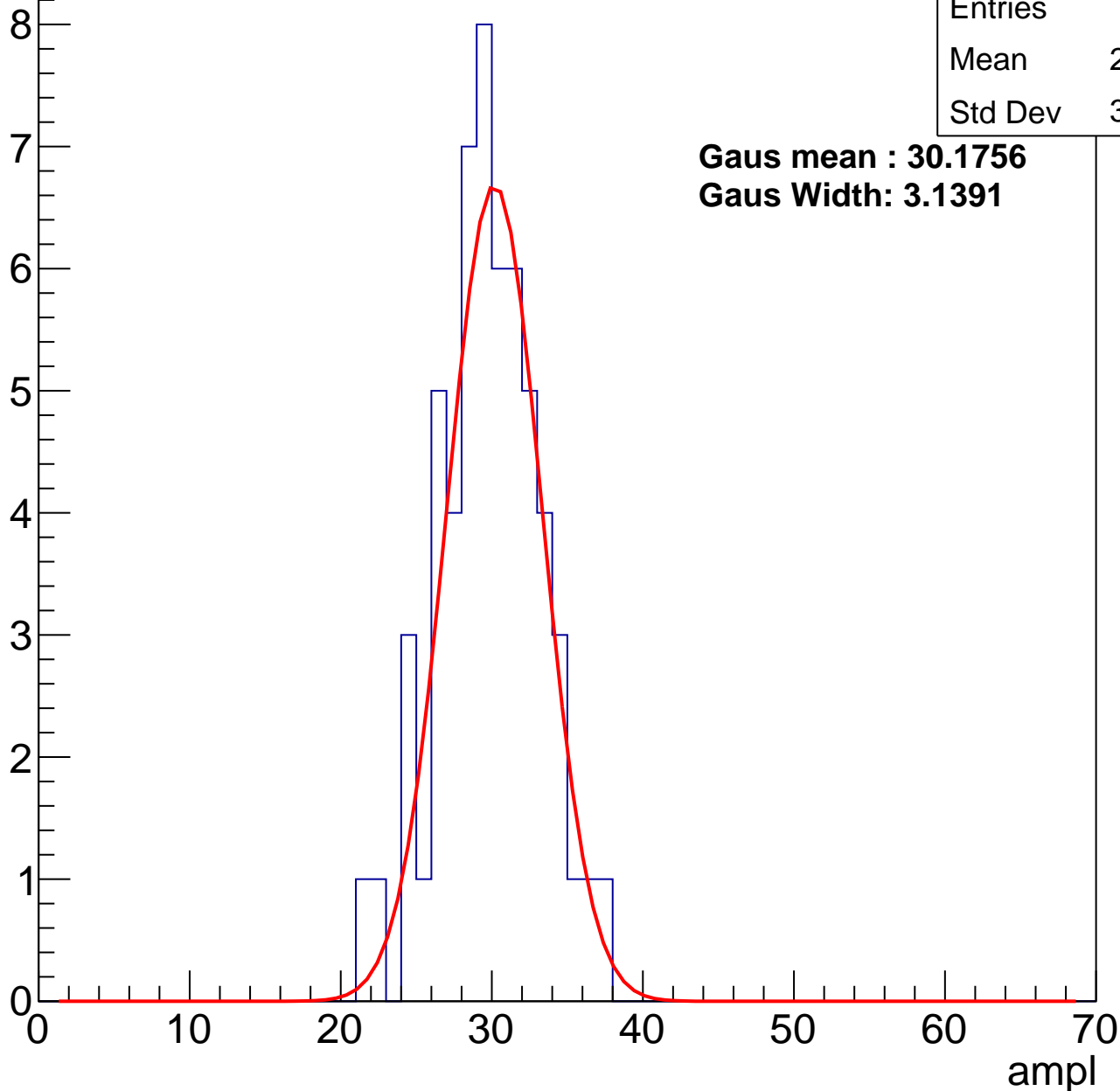
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 29.37 |
| Std Dev | 3.317 |

Gaus mean : 30.1756

Gaus Width: 3.1391



B0L102S, U20-ch72, adc1

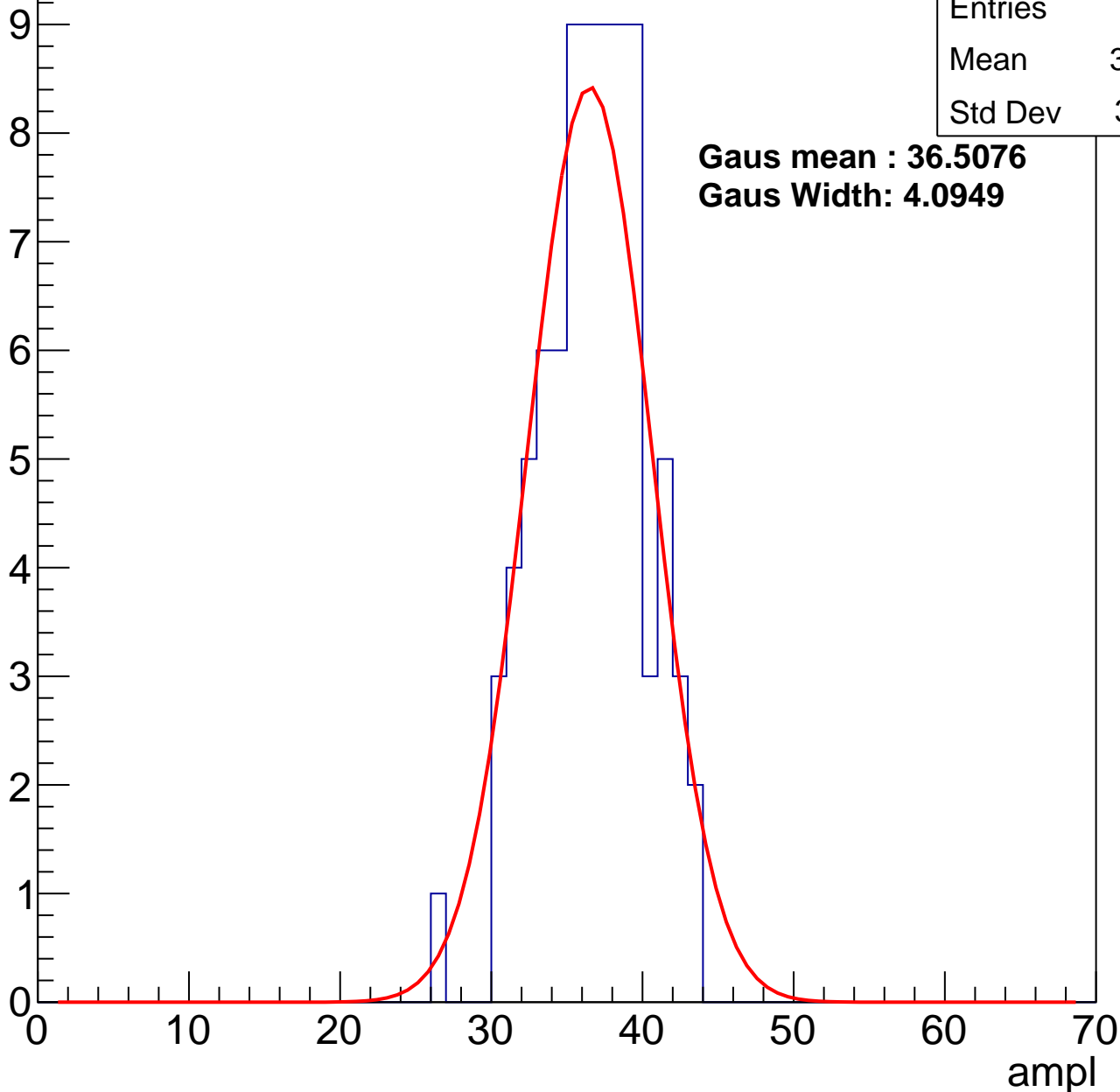
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 36.19 |
| Std Dev | 3.431 |

Gaus mean : 36.5076

Gaus Width: 4.0949



B0L102S, U20-ch72, adc2

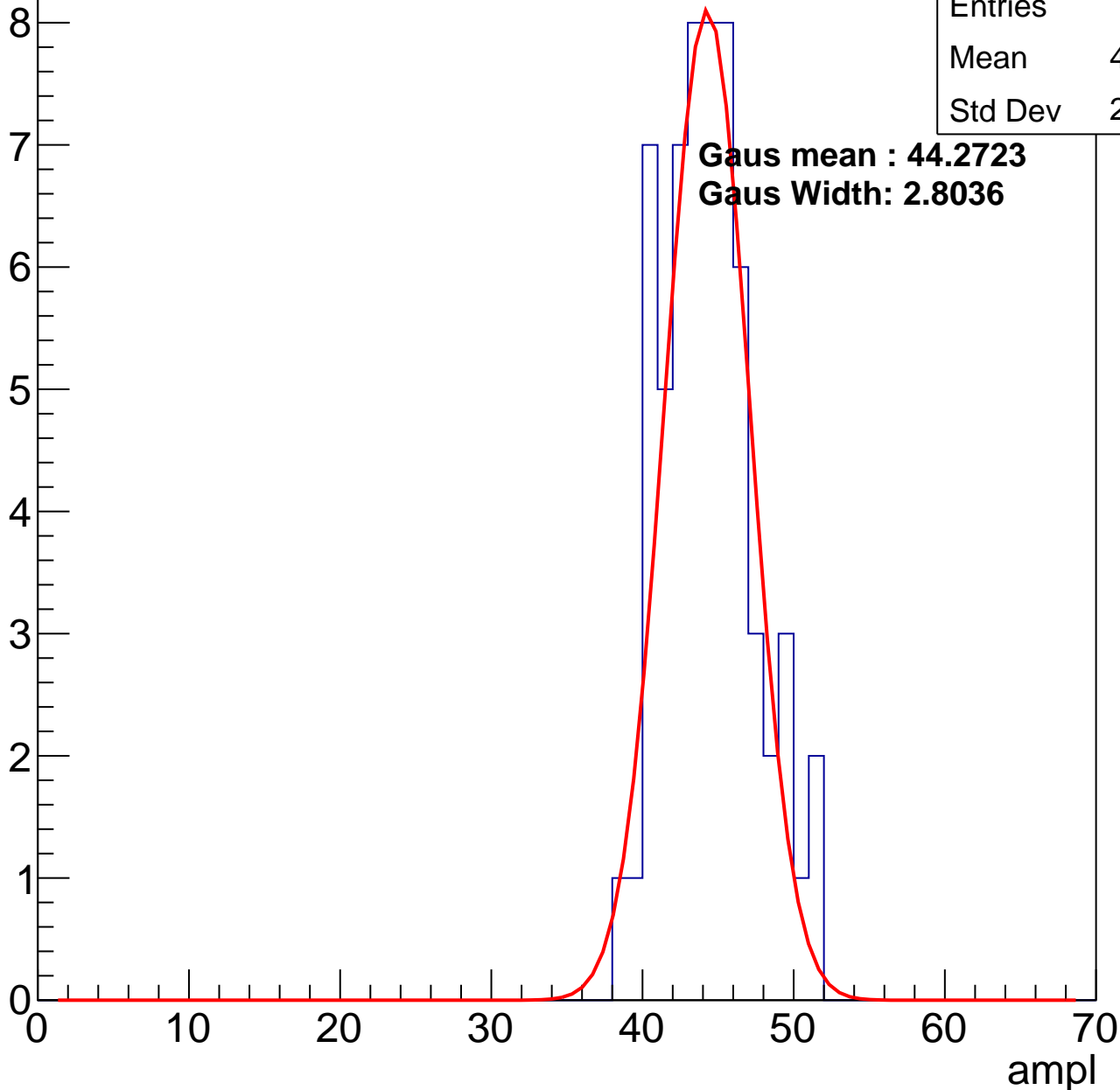
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.94 |
| Std Dev | 2.989 |

Gaus mean : 44.2723

Gaus Width: 2.8036

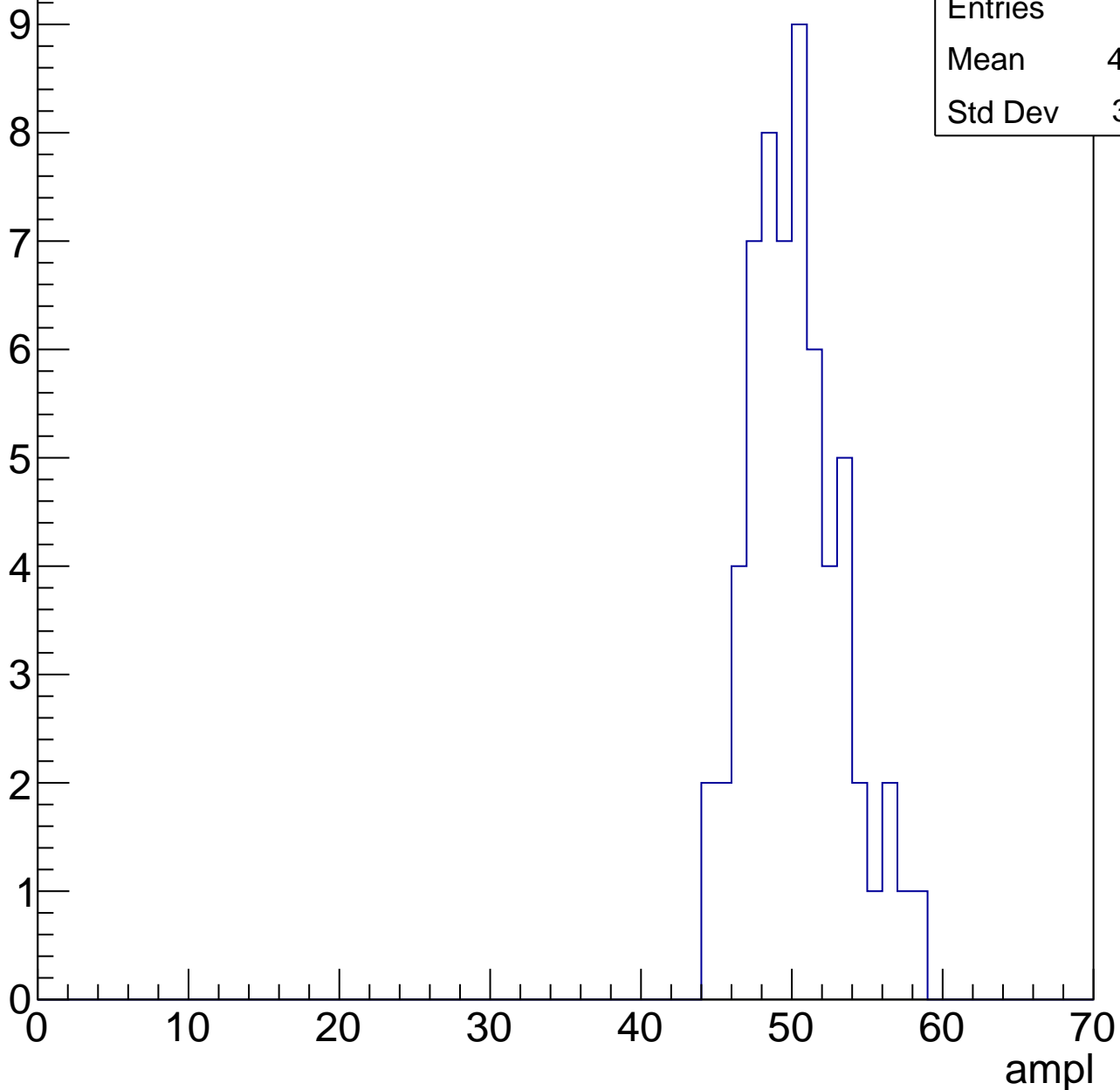


B0L102S, U20-ch72, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.79 |
| Std Dev | 3.121 |

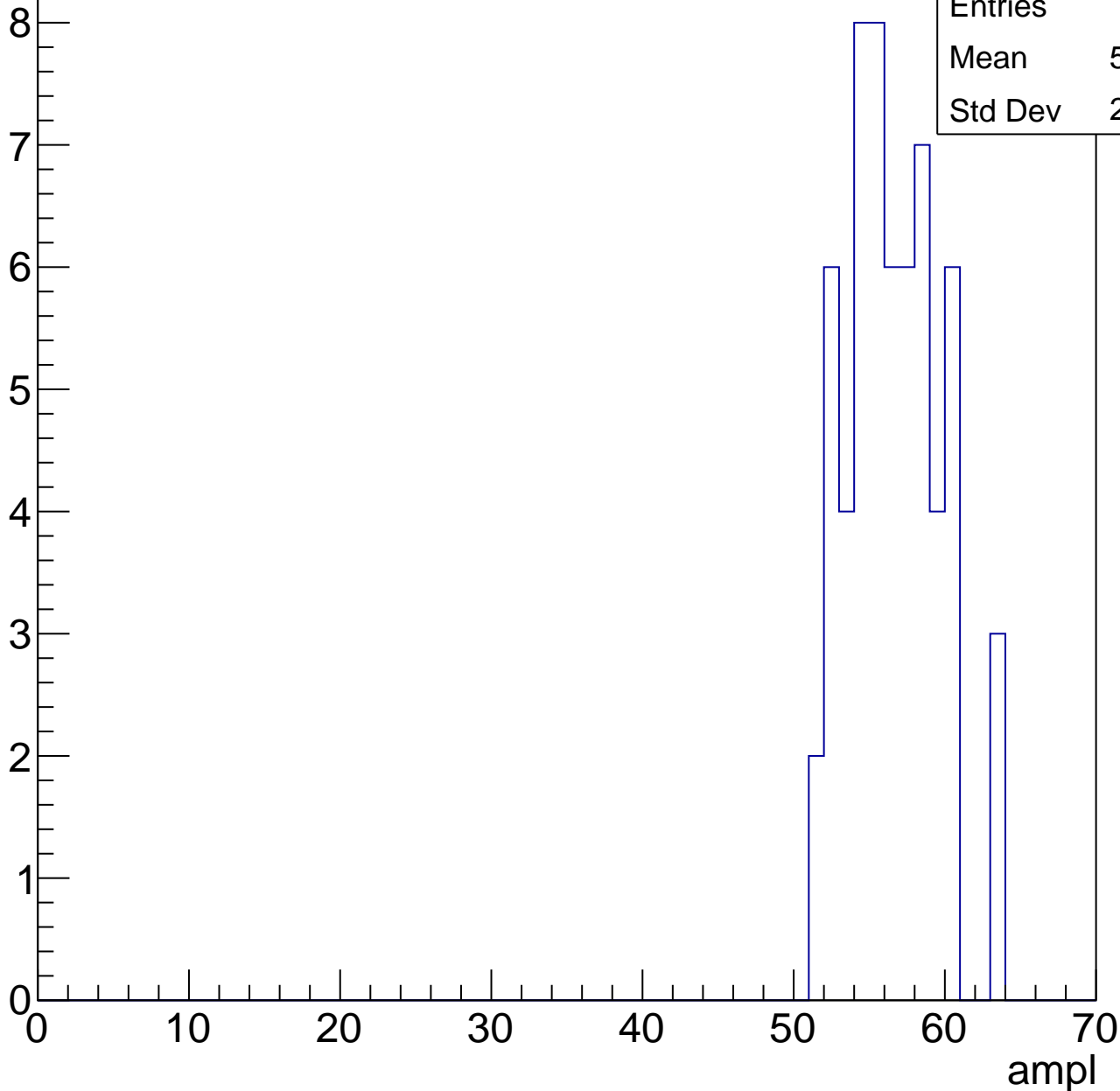


B0L102S, U20-ch72, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 56.12 |
| Std Dev | 2.984 |

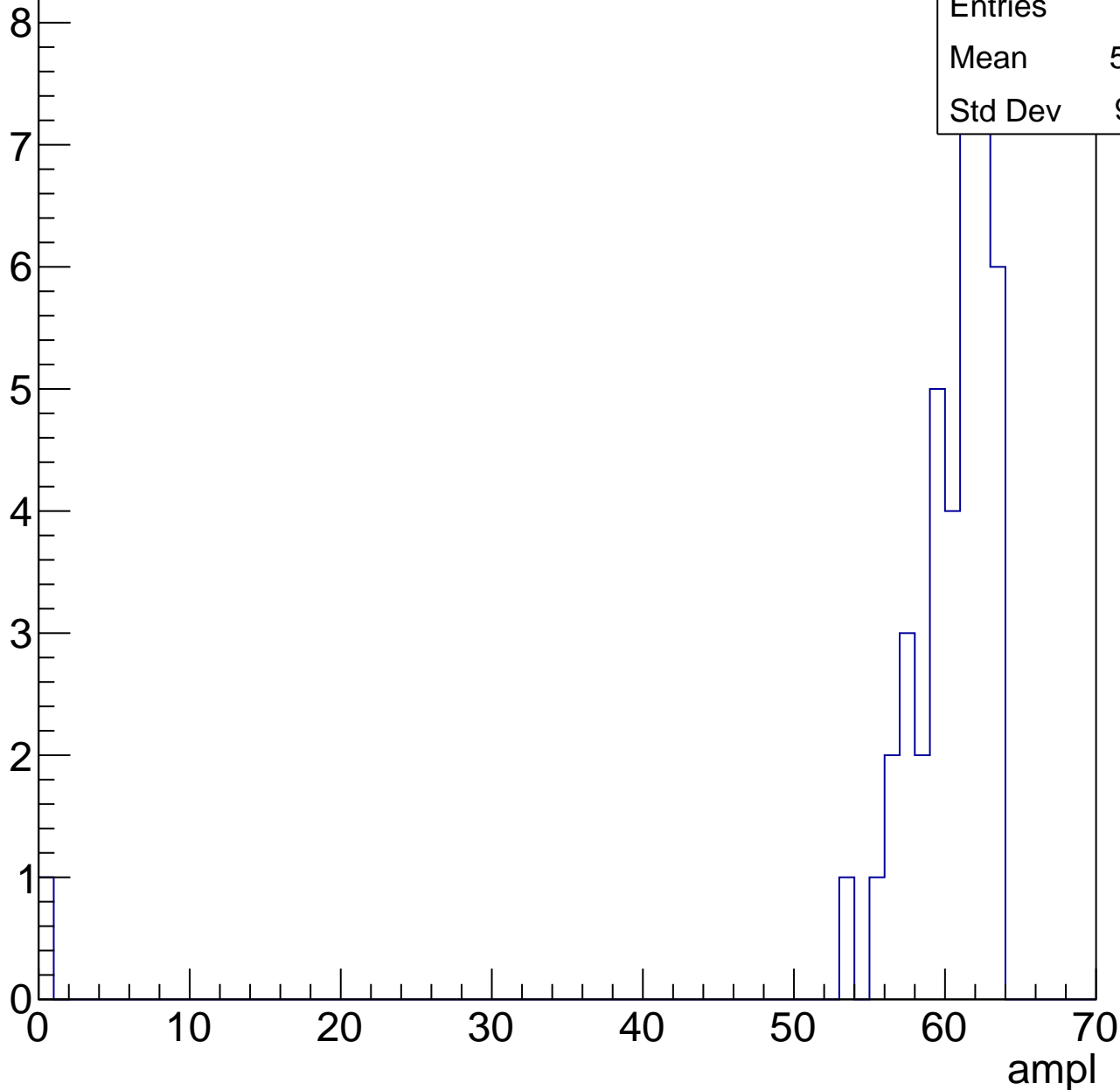


B0L102S, U20-ch72, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

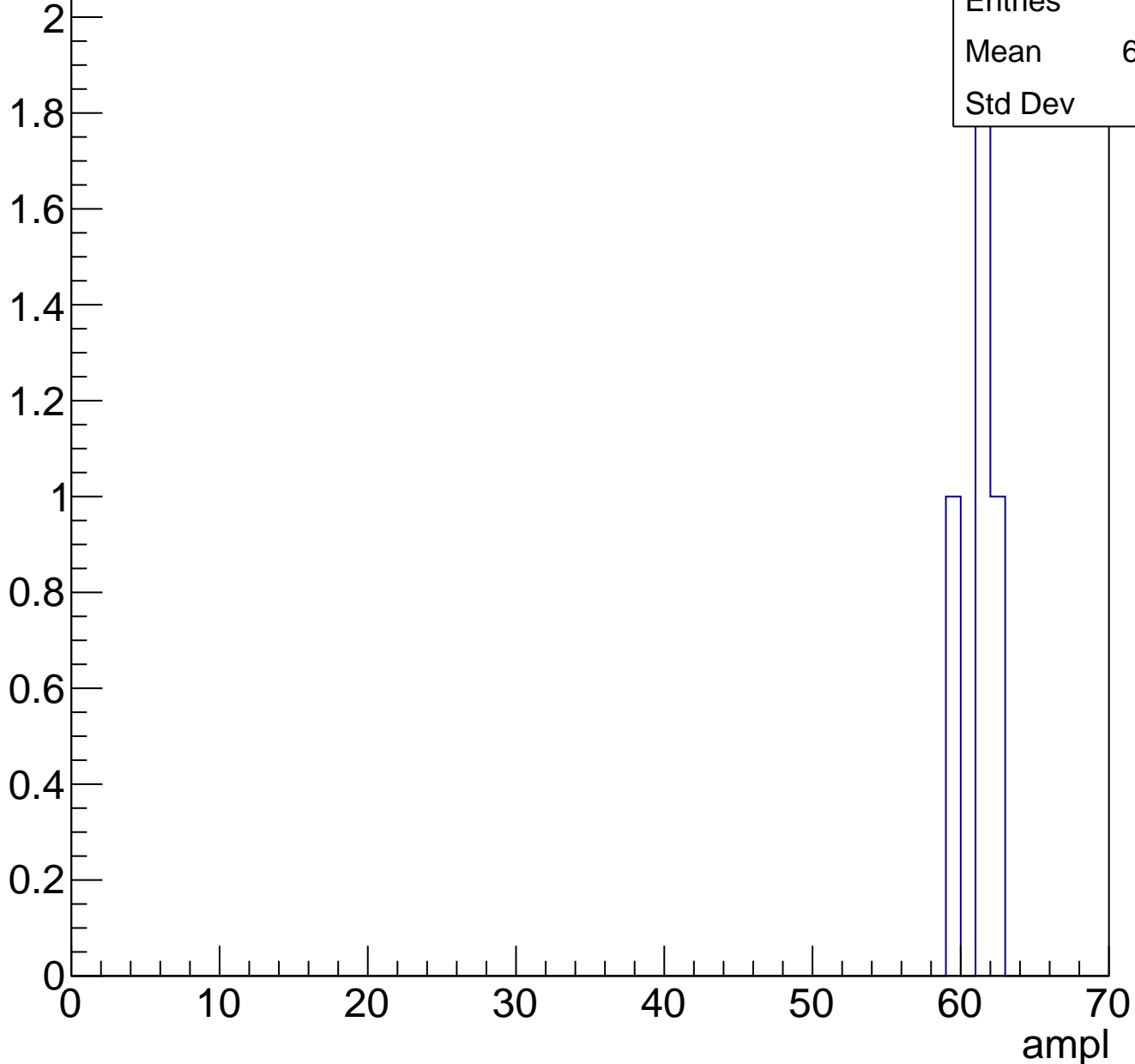
| | |
|---------|-------|
| Entries | 41 |
| Mean | 58.63 |
| Std Dev | 9.581 |



B0L102S, U20-ch72, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch72, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch73, adc0

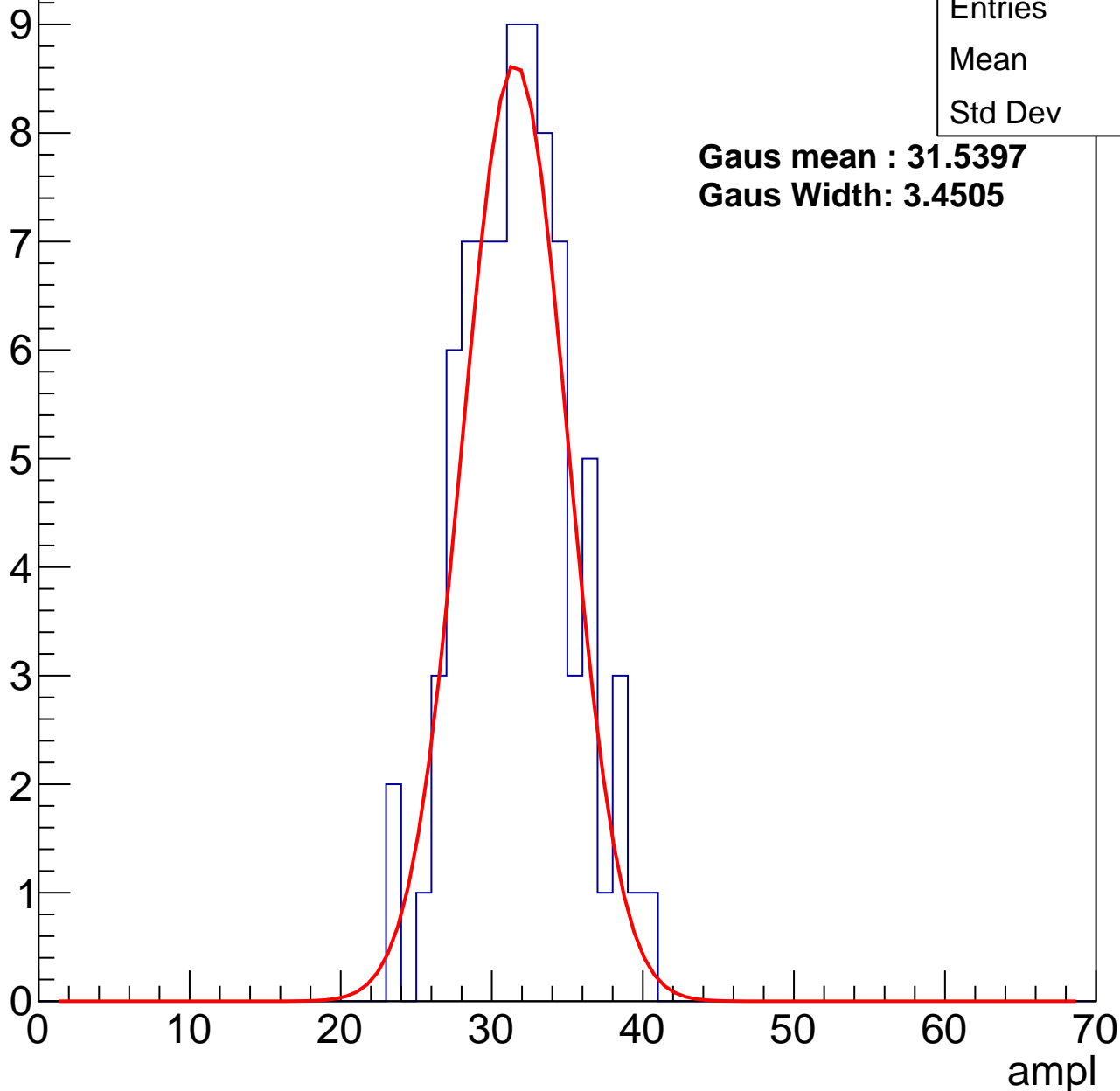
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 80 |
| Mean | 31.3 |
| Std Dev | 3.59 |

Gaus mean : 31.5397

Gaus Width: 3.4505



B0L102S, U20-ch73, adc1

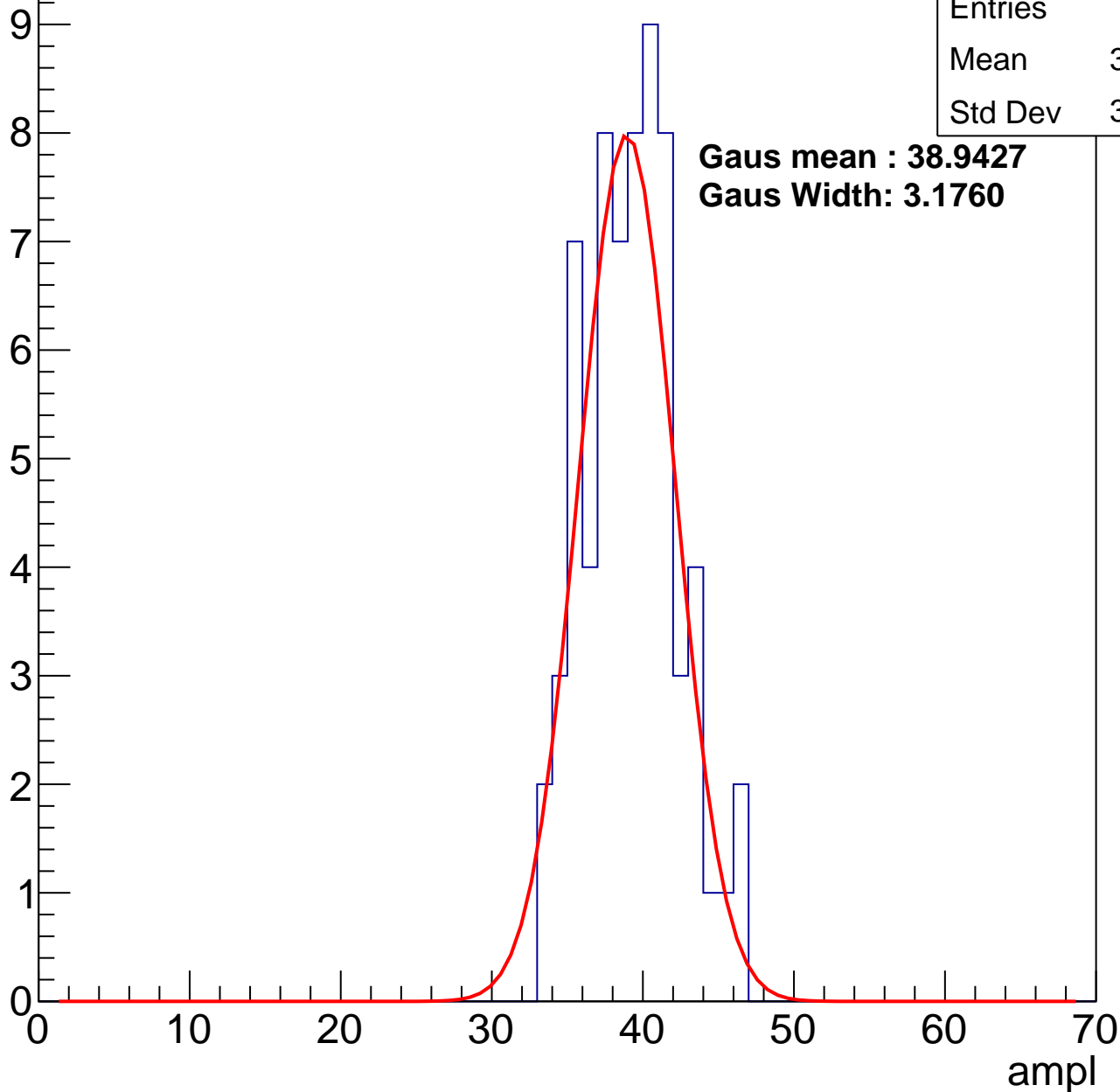
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 38.78 |
| Std Dev | 3.046 |

Gaus mean : 38.9427

Gaus Width: 3.1760



B0L102S, U20-ch73, adc2

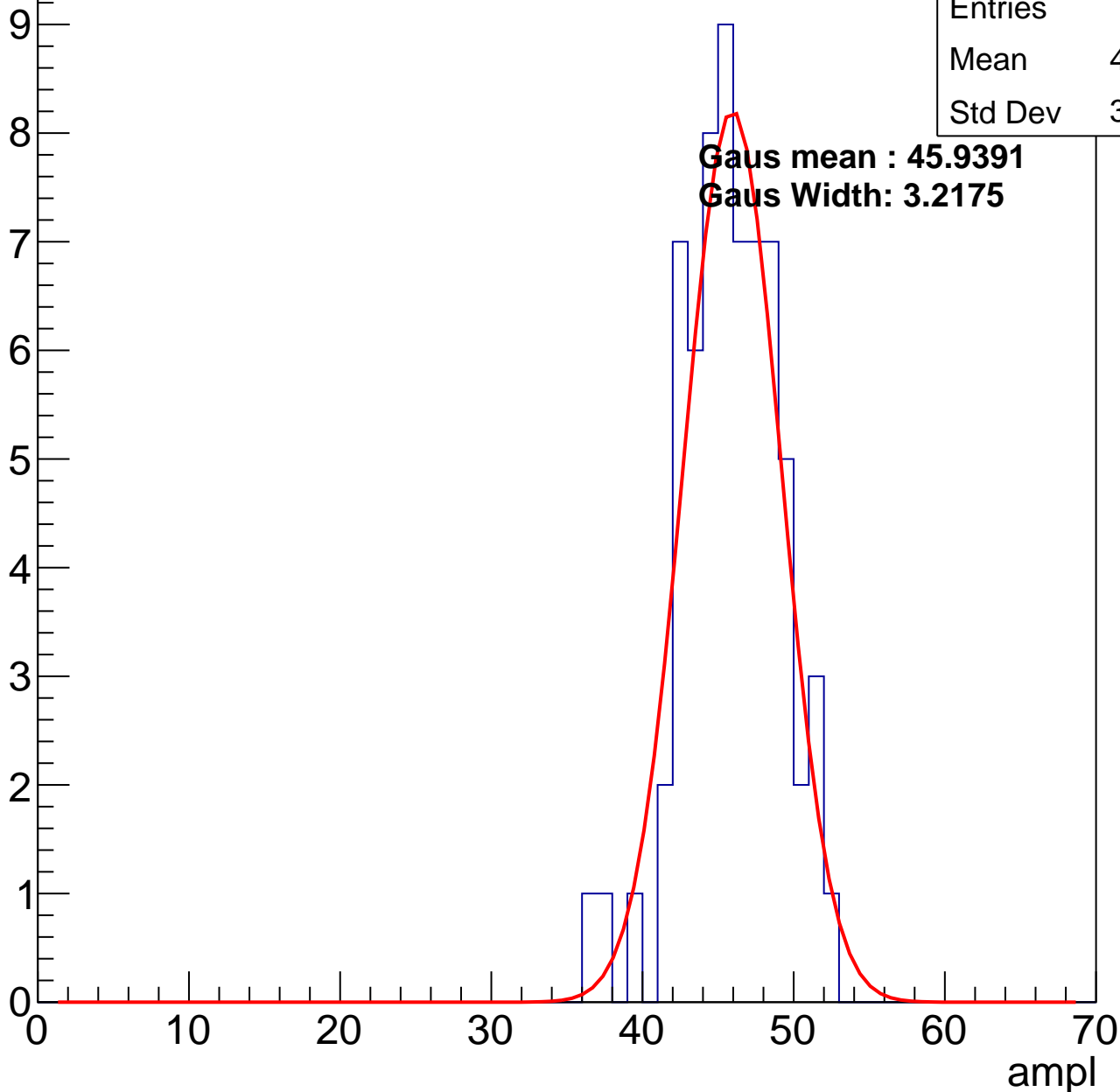
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 45.37 |
| Std Dev | 3.194 |

Gaus mean : 45.9391

Gaus Width: 3.2175

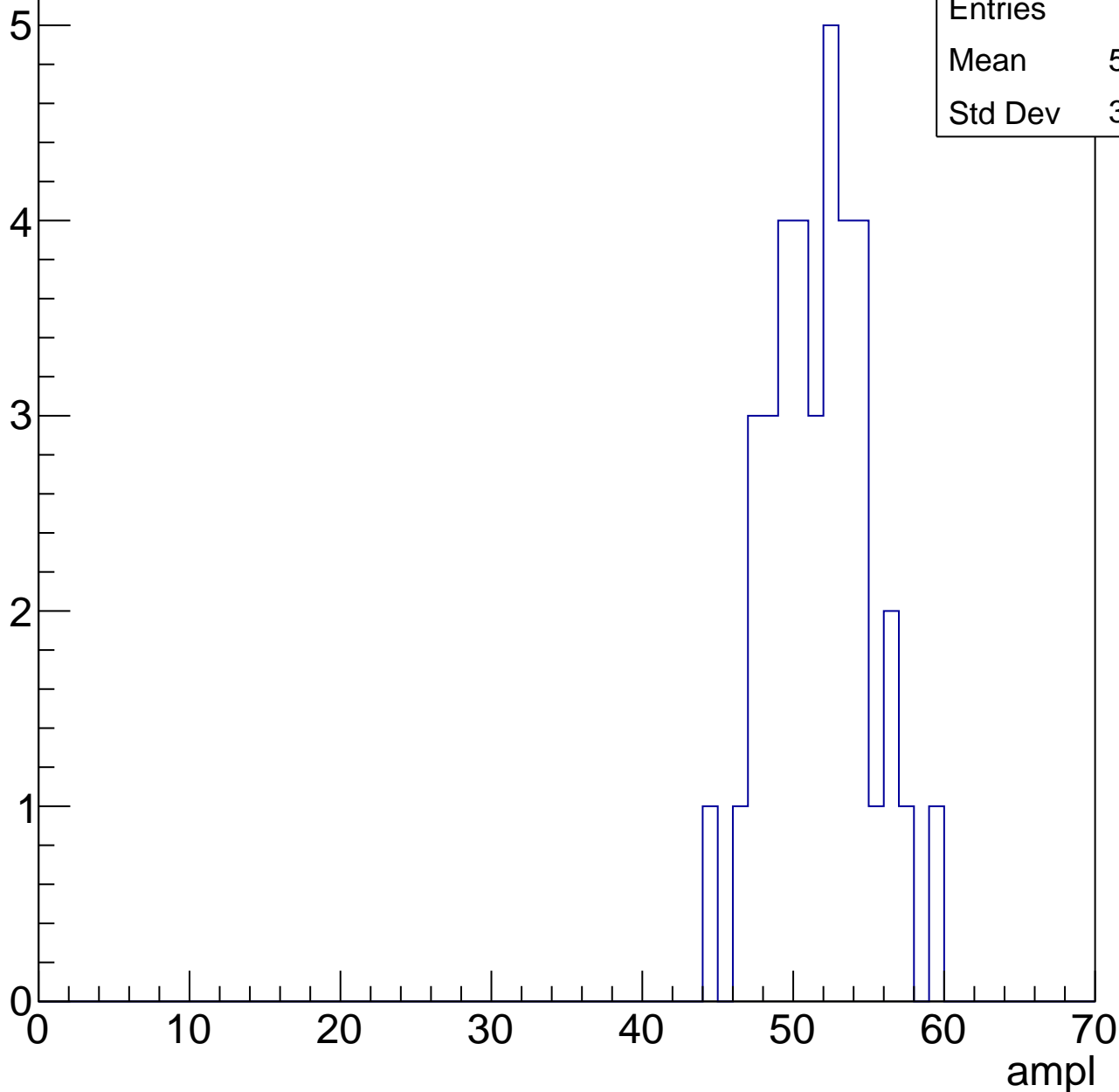


B0L102S, U20-ch73, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 51.22 |
| Std Dev | 3.248 |



B0L102S, U20-ch73, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

Entries 83

Mean 56.34

Std Dev 3.451

8

6

4

2

0

0

10

20

30

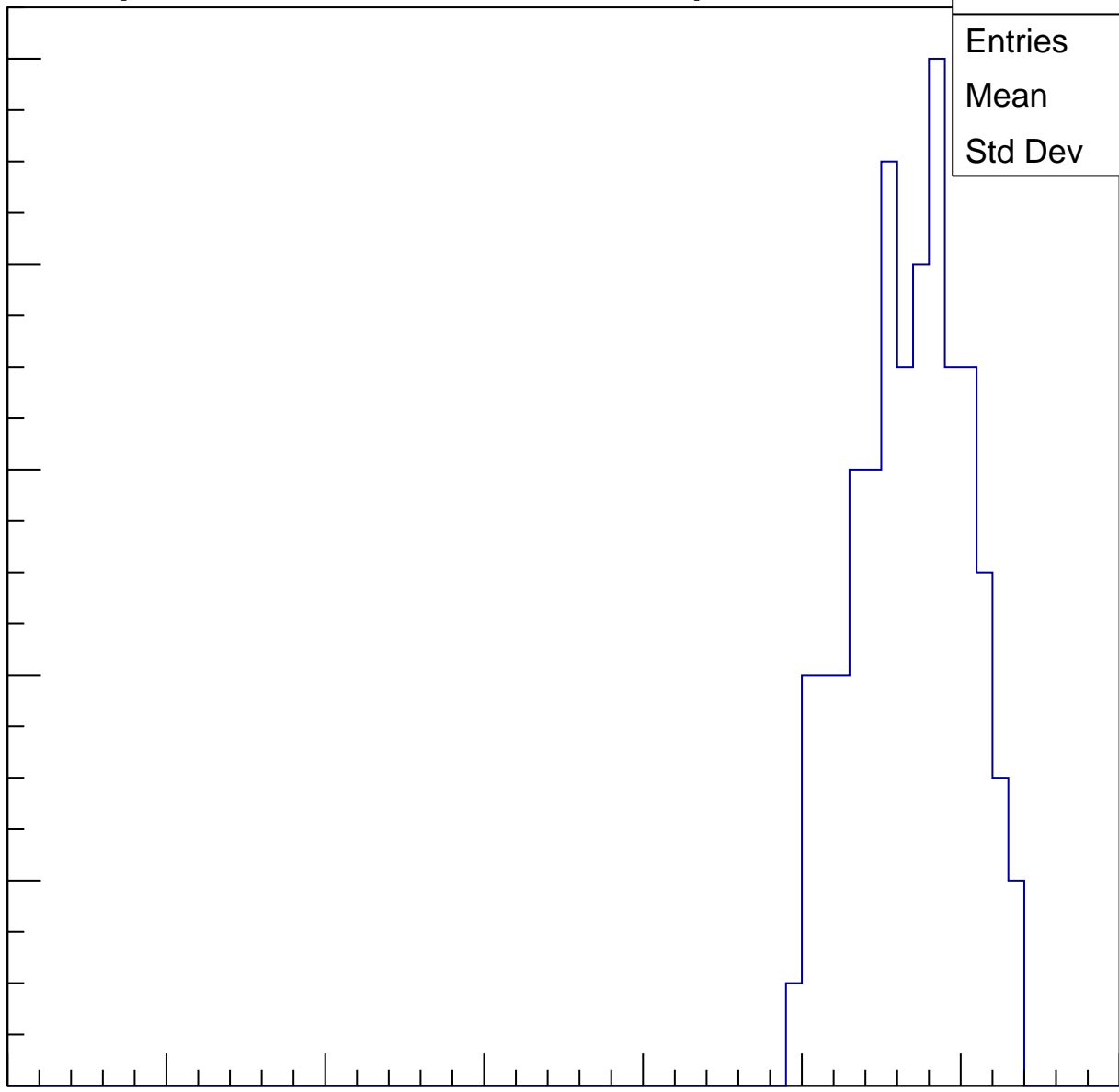
40

50

60

70

ampl

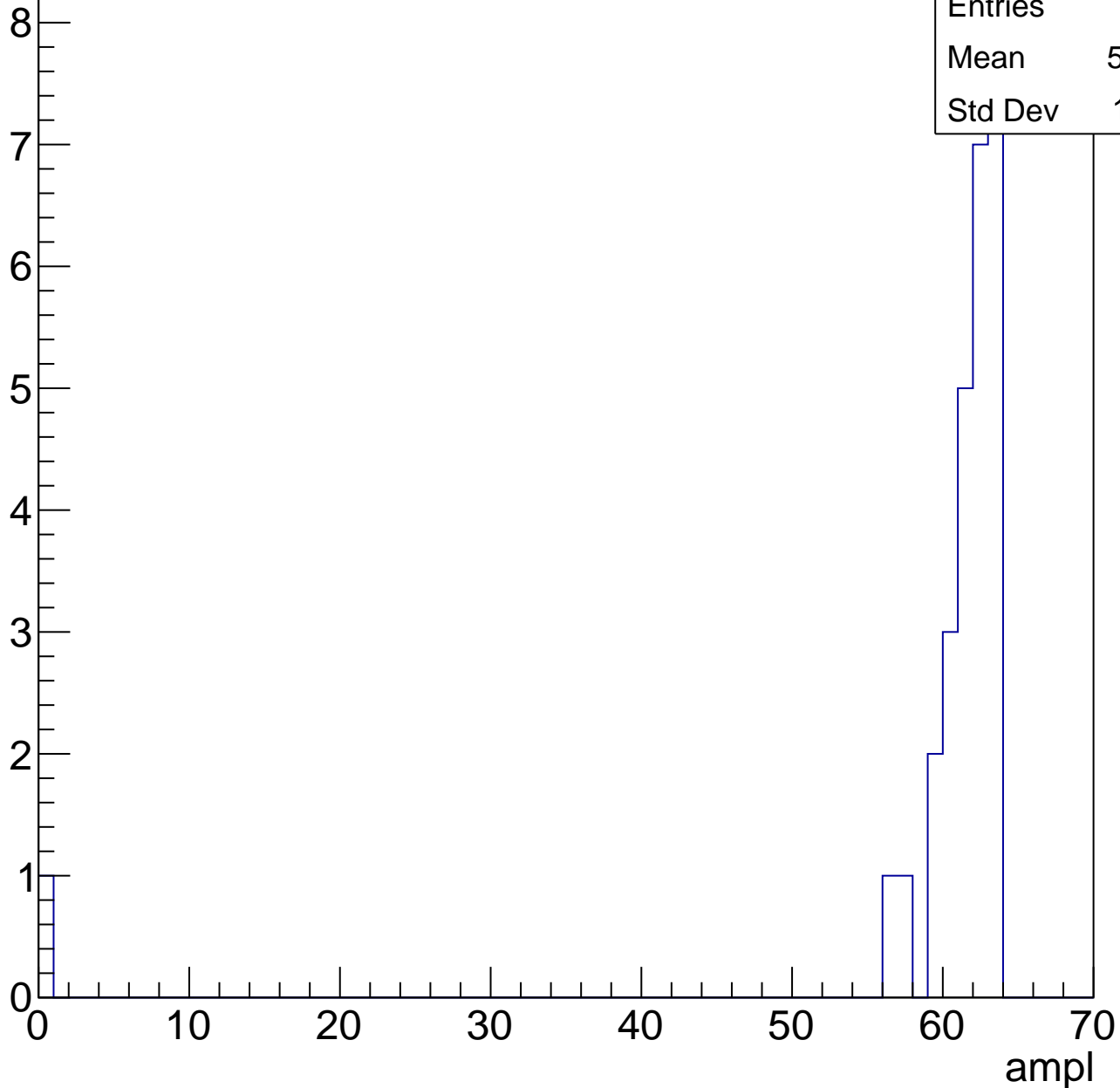


B0L102S, U20-ch73, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 59.07 |
| Std Dev | 11.51 |



B0L102S, U20-ch73, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch73, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L102S, U20-ch74, adc0

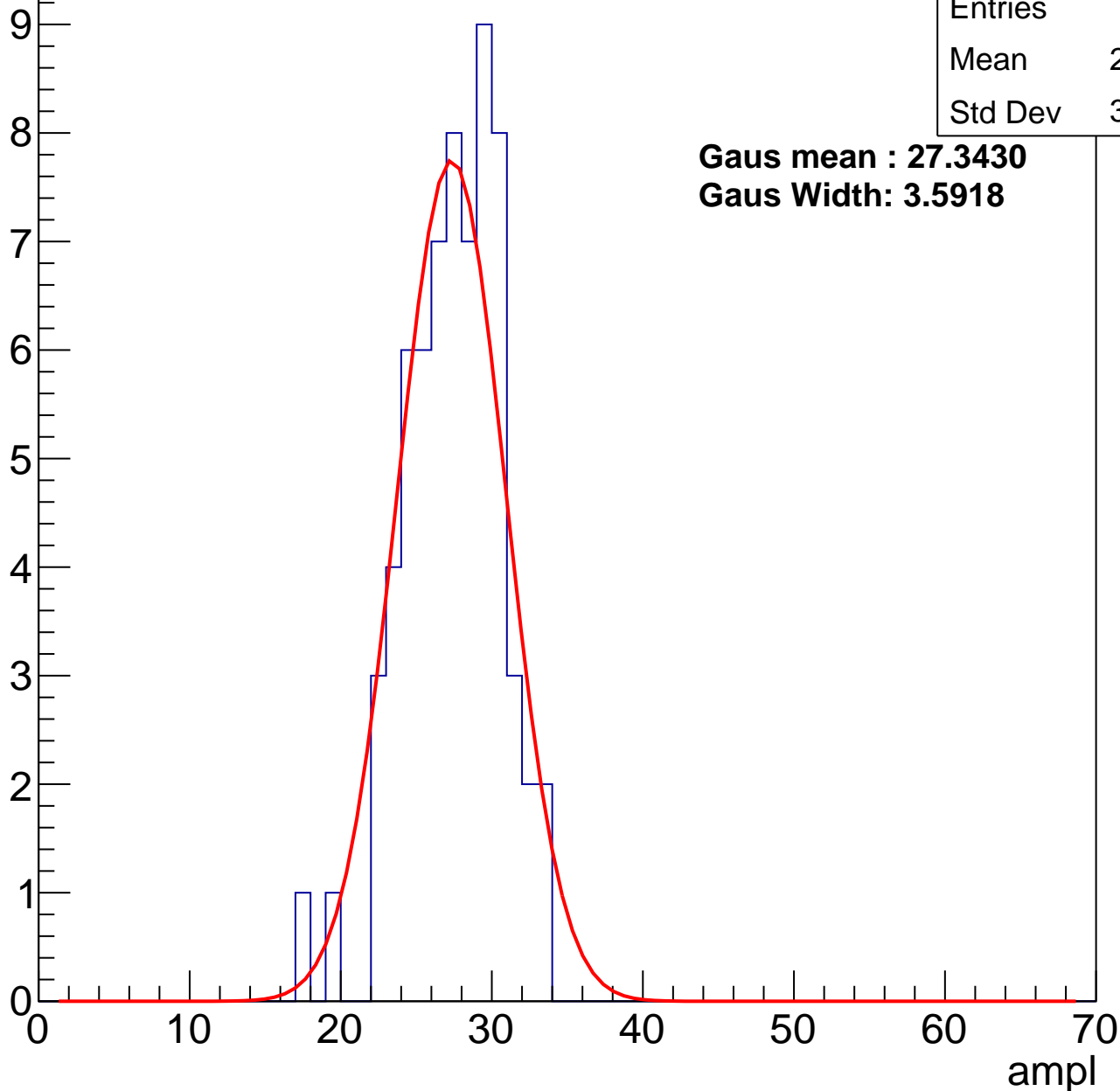
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 26.96 |
| Std Dev | 3.169 |

Gaus mean : 27.3430

Gaus Width: 3.5918



B0L102S, U20-ch74, adc1

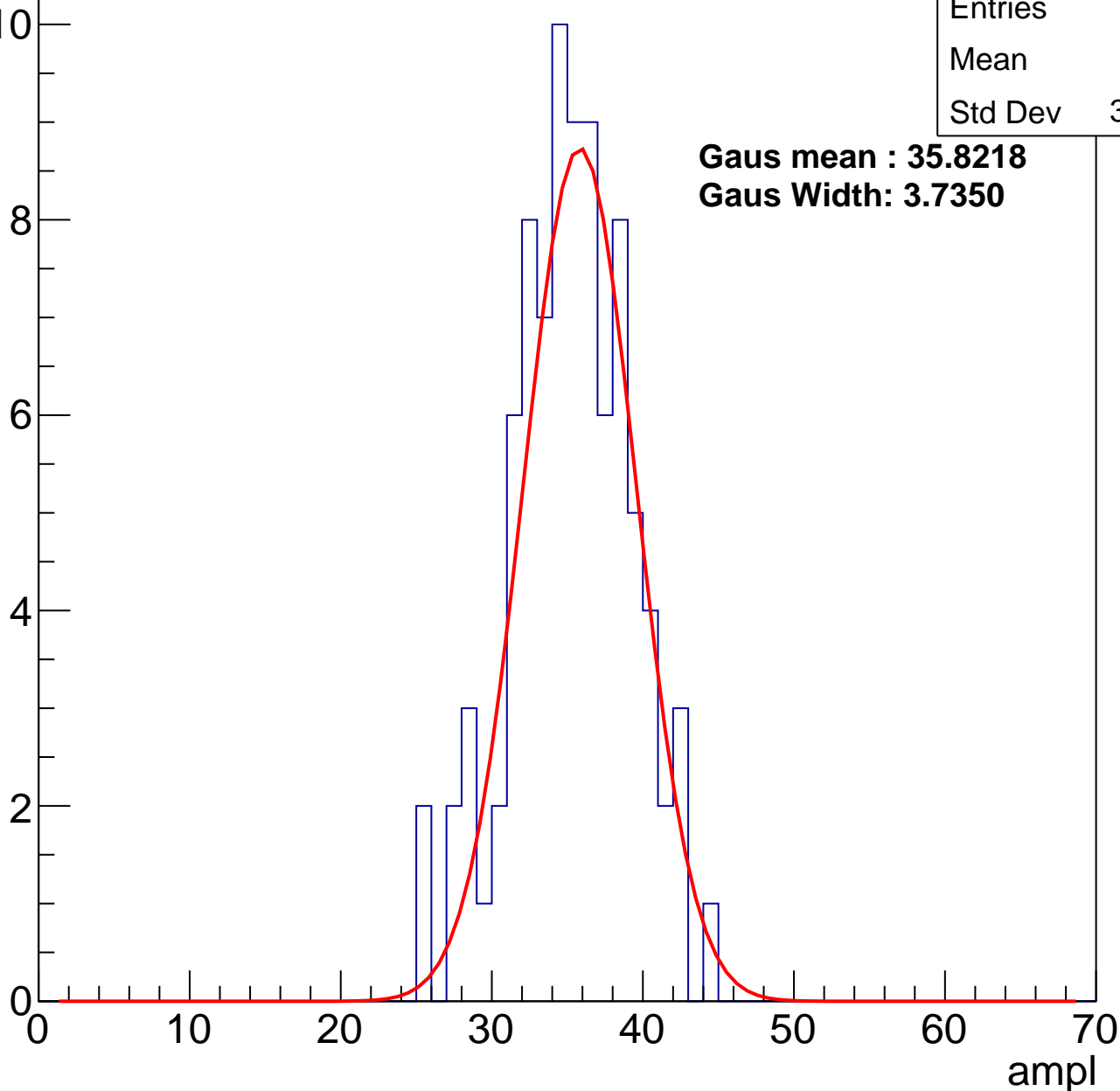
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 34.8 |
| Std Dev | 3.914 |

Gaus mean : 35.8218

Gaus Width: 3.7350



B0L102S, U20-ch74, adc2

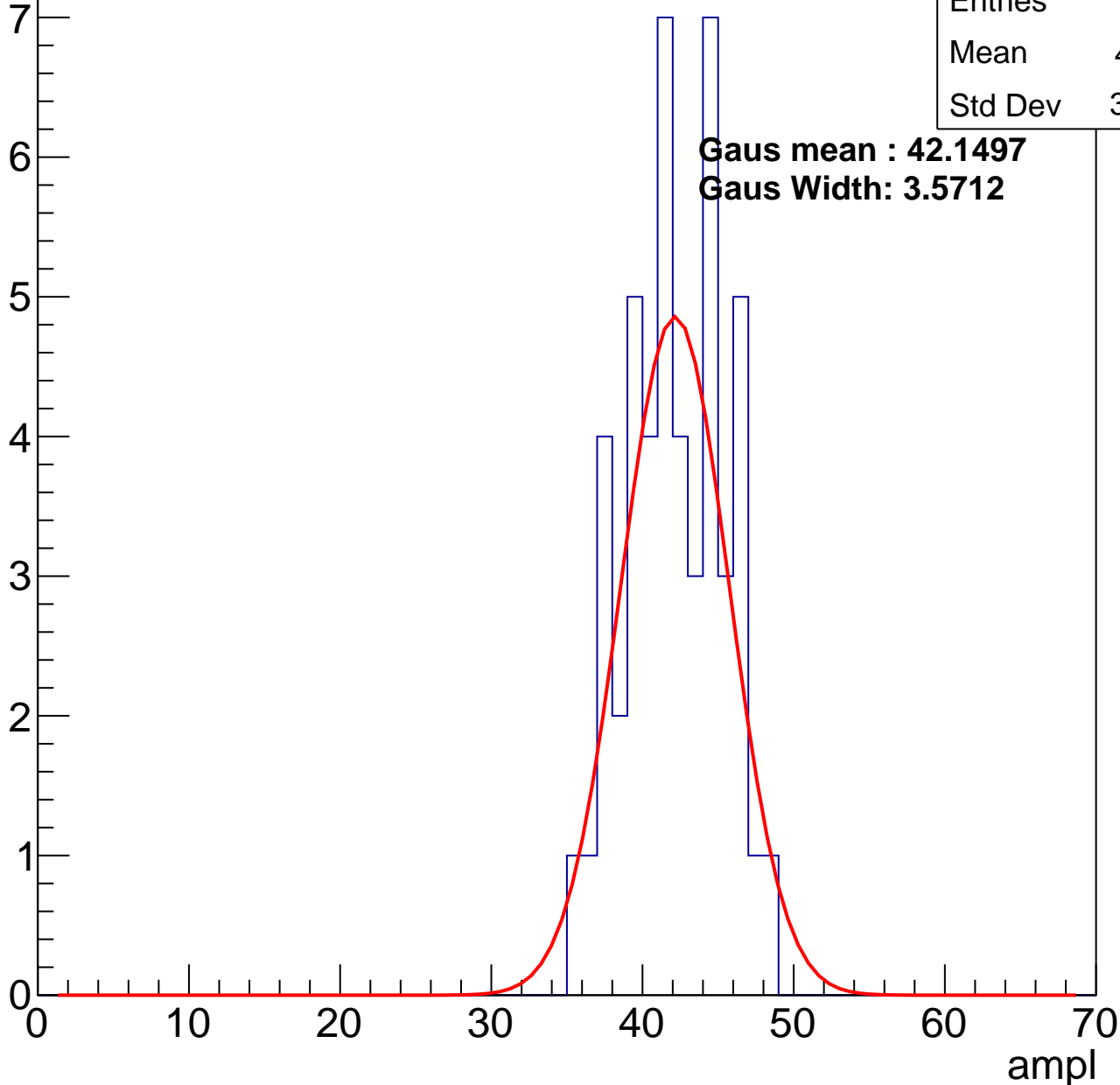
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 41.71 |
| Std Dev | 3.162 |

Gaus mean : 42.1497

Gaus Width: 3.5712

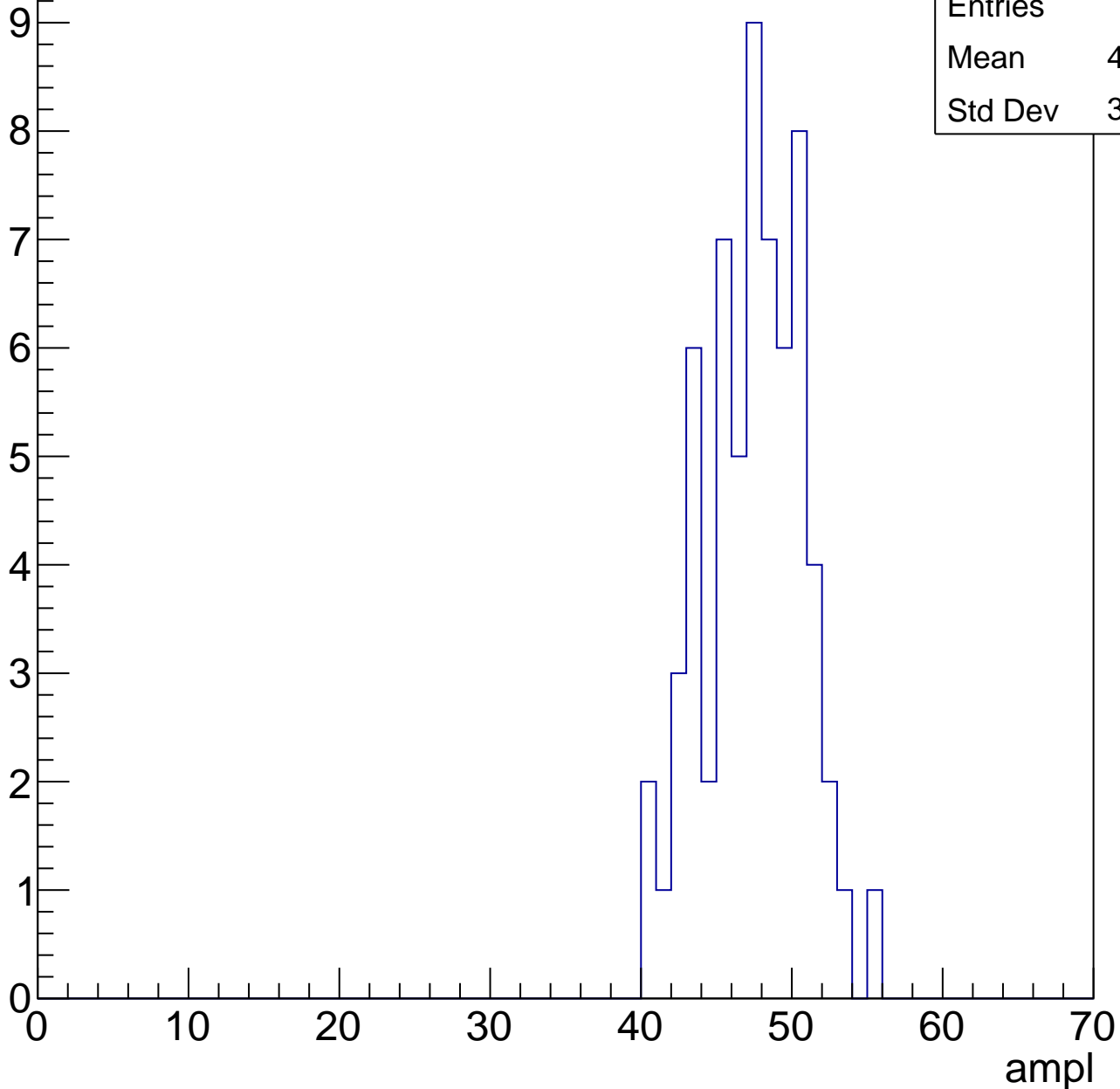


B0L102S, U20-ch74, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 46.98 |
| Std Dev | 3.243 |

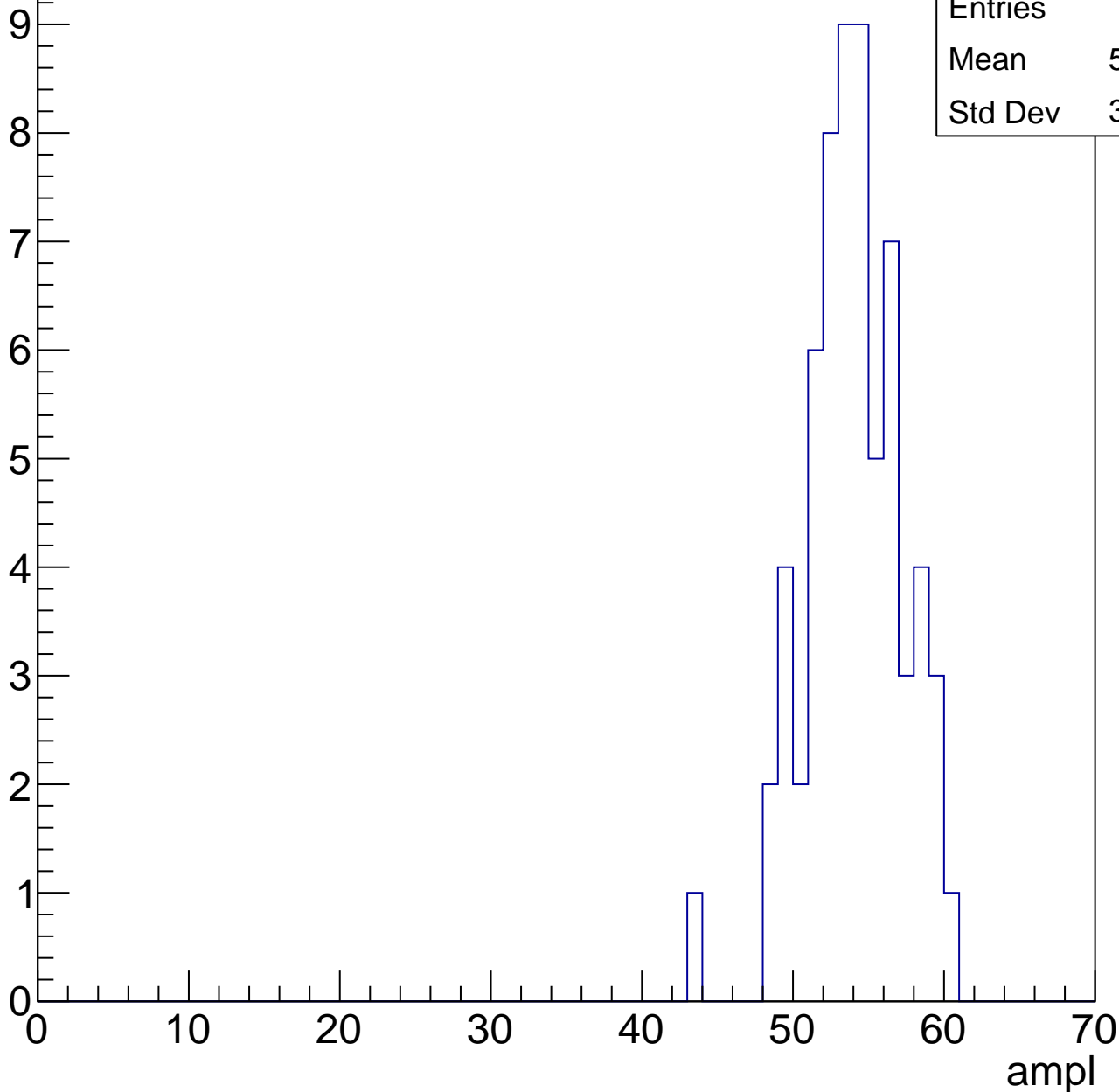


B0L102S, U20-ch74, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 53.55 |
| Std Dev | 3.167 |

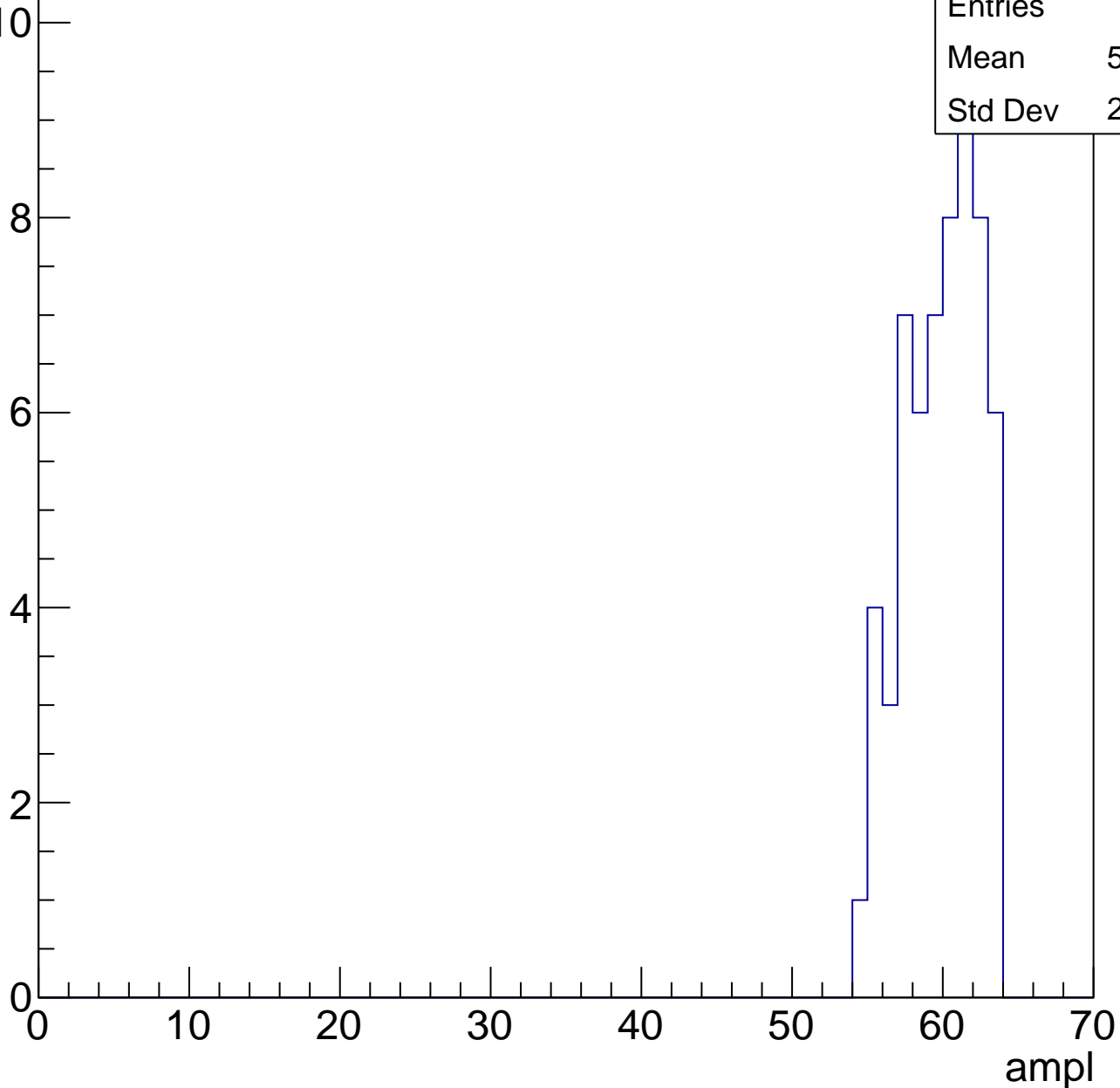


B0L102S, U20-ch74, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

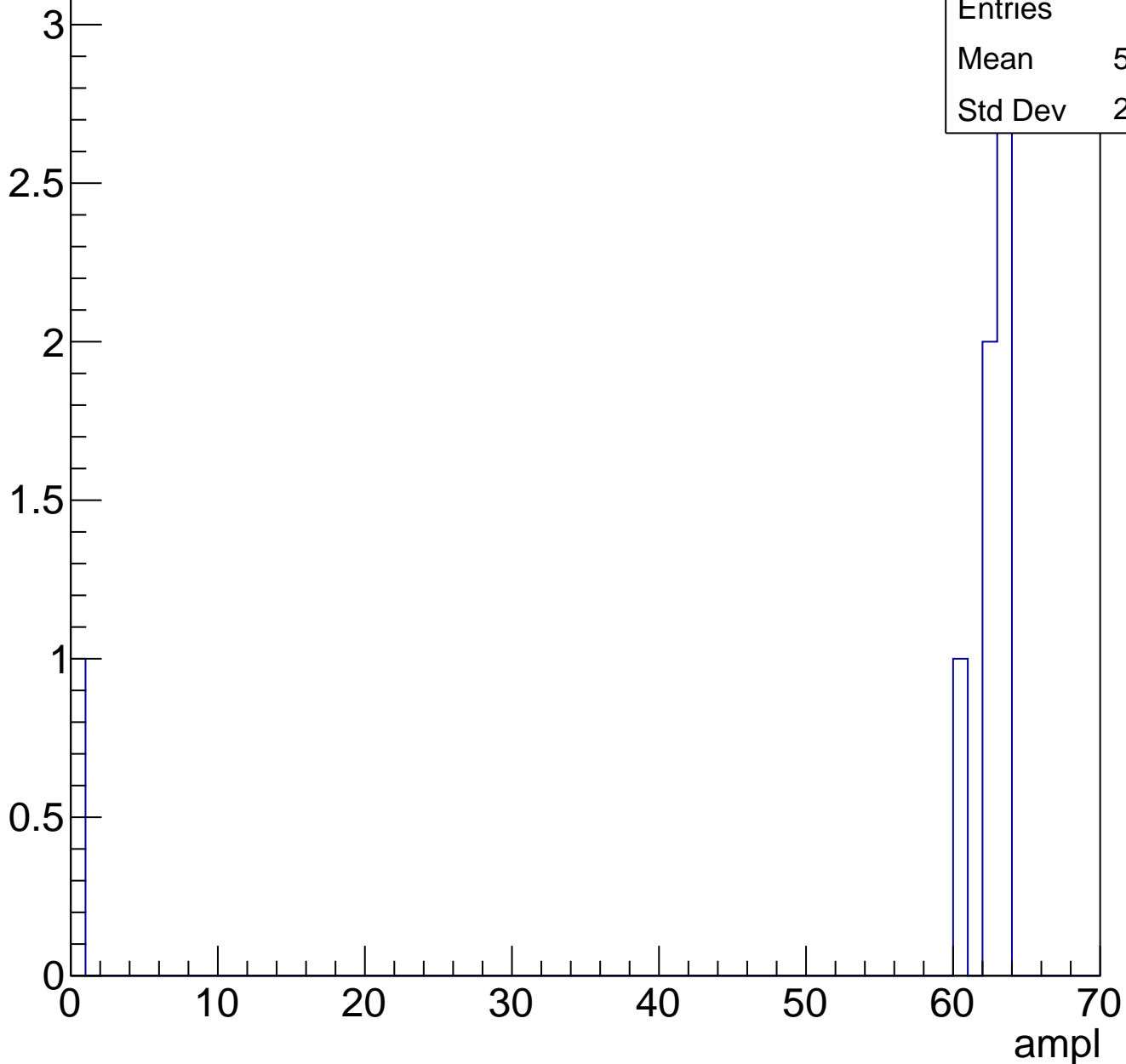
| | |
|---------|-------|
| Entries | 60 |
| Mean | 59.43 |
| Std Dev | 2.432 |



B0L102S, U20-ch74, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch74, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch75, adc0

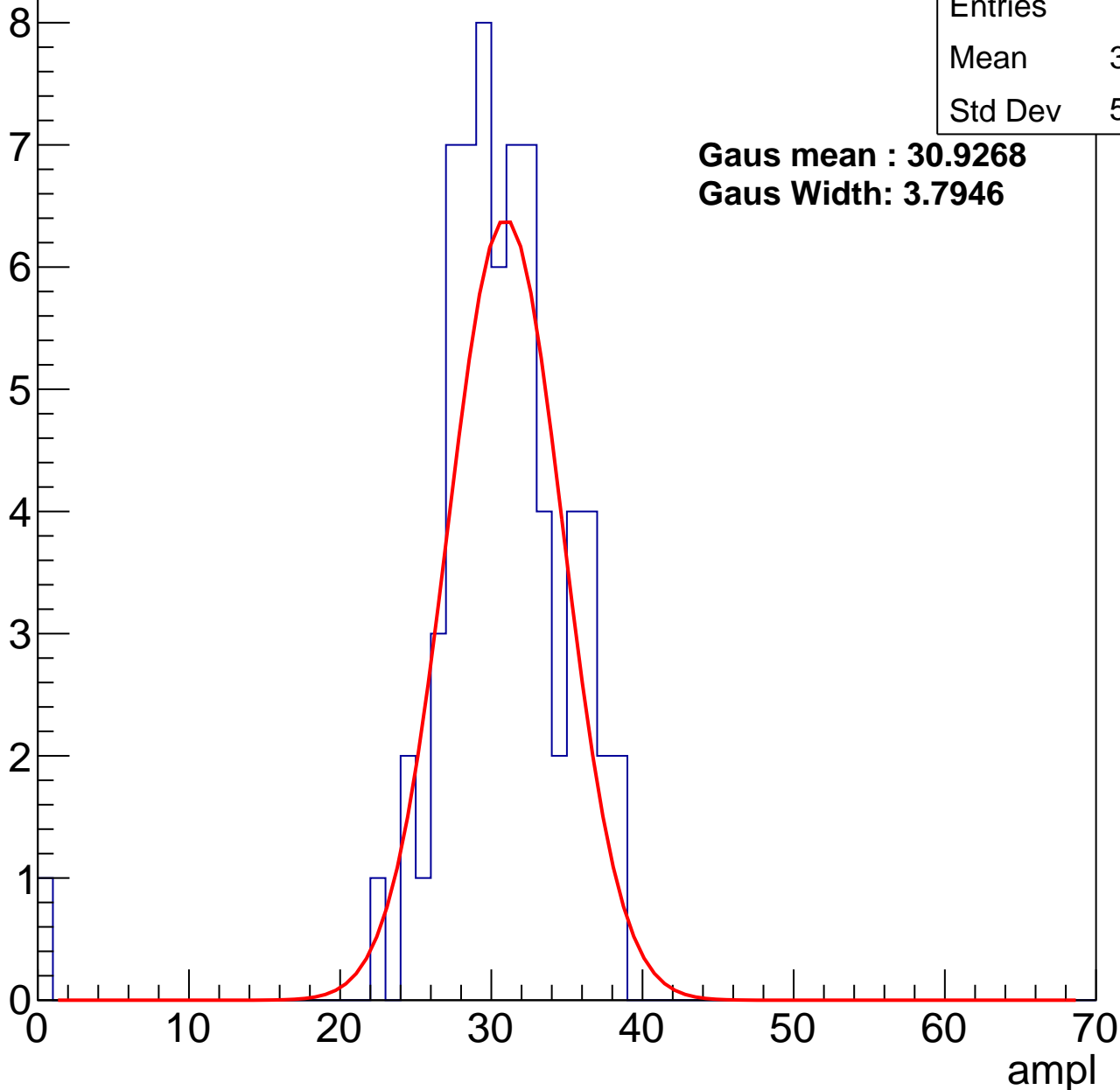
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 30.07 |
| Std Dev | 5.117 |

Gaus mean : 30.9268

Gaus Width: 3.7946



B0L102S, U20-ch75, adc1

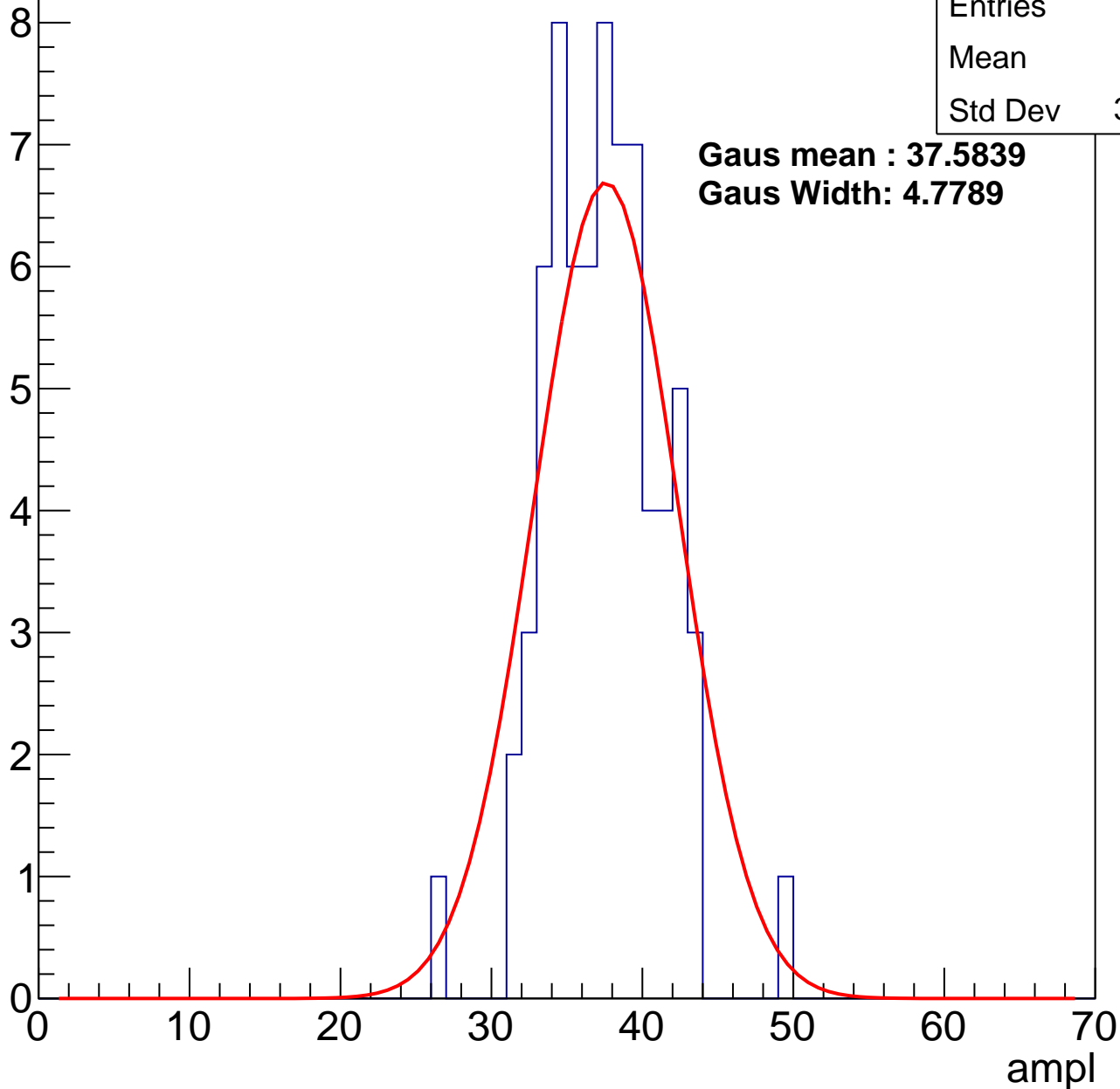
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37 |
| Std Dev | 3.711 |

Gaus mean : 37.5839

Gaus Width: 4.7789



B0L102S, U20-ch75, adc2

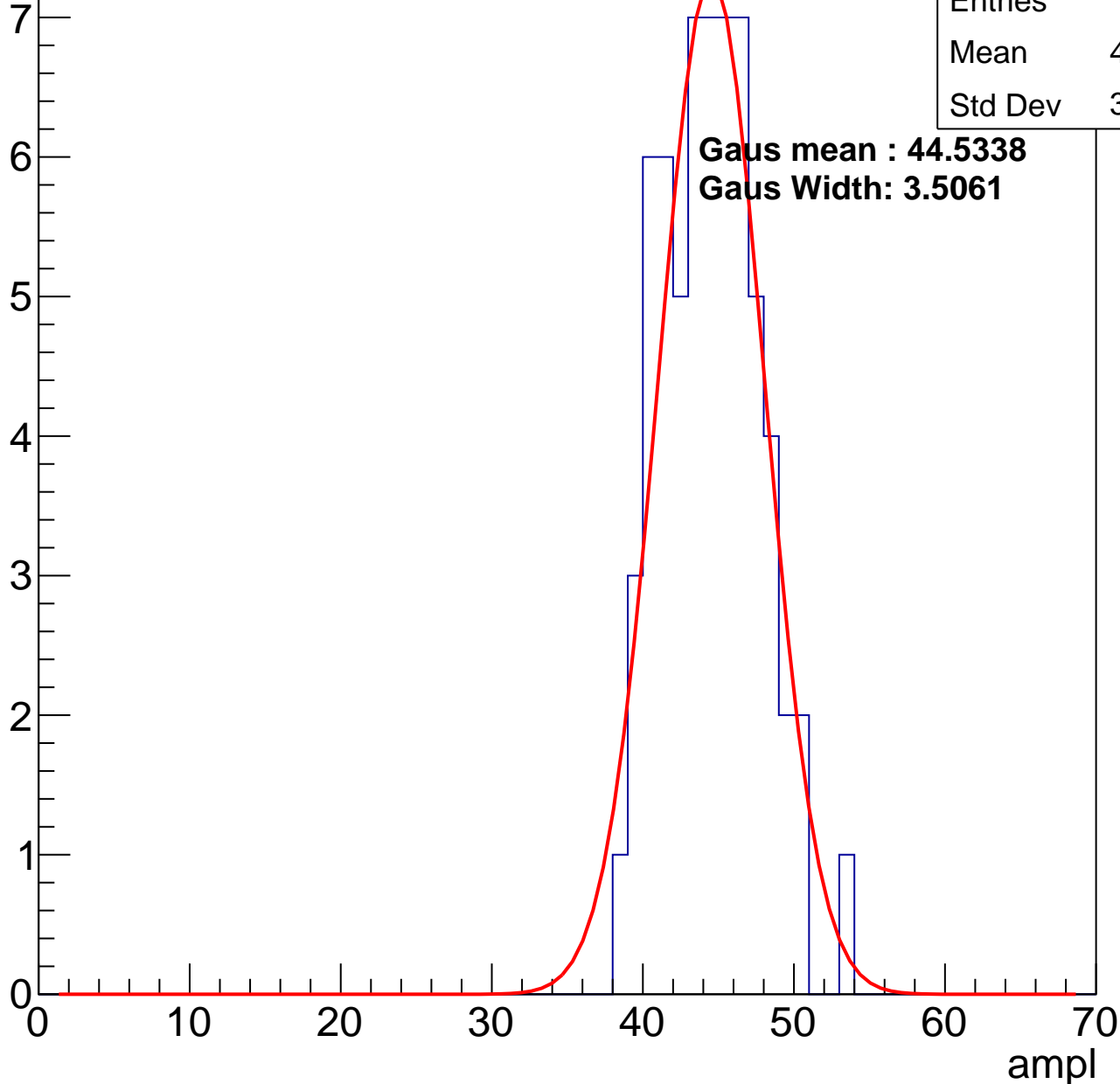
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 44.05 |
| Std Dev | 3.174 |

Gaus mean : 44.5338

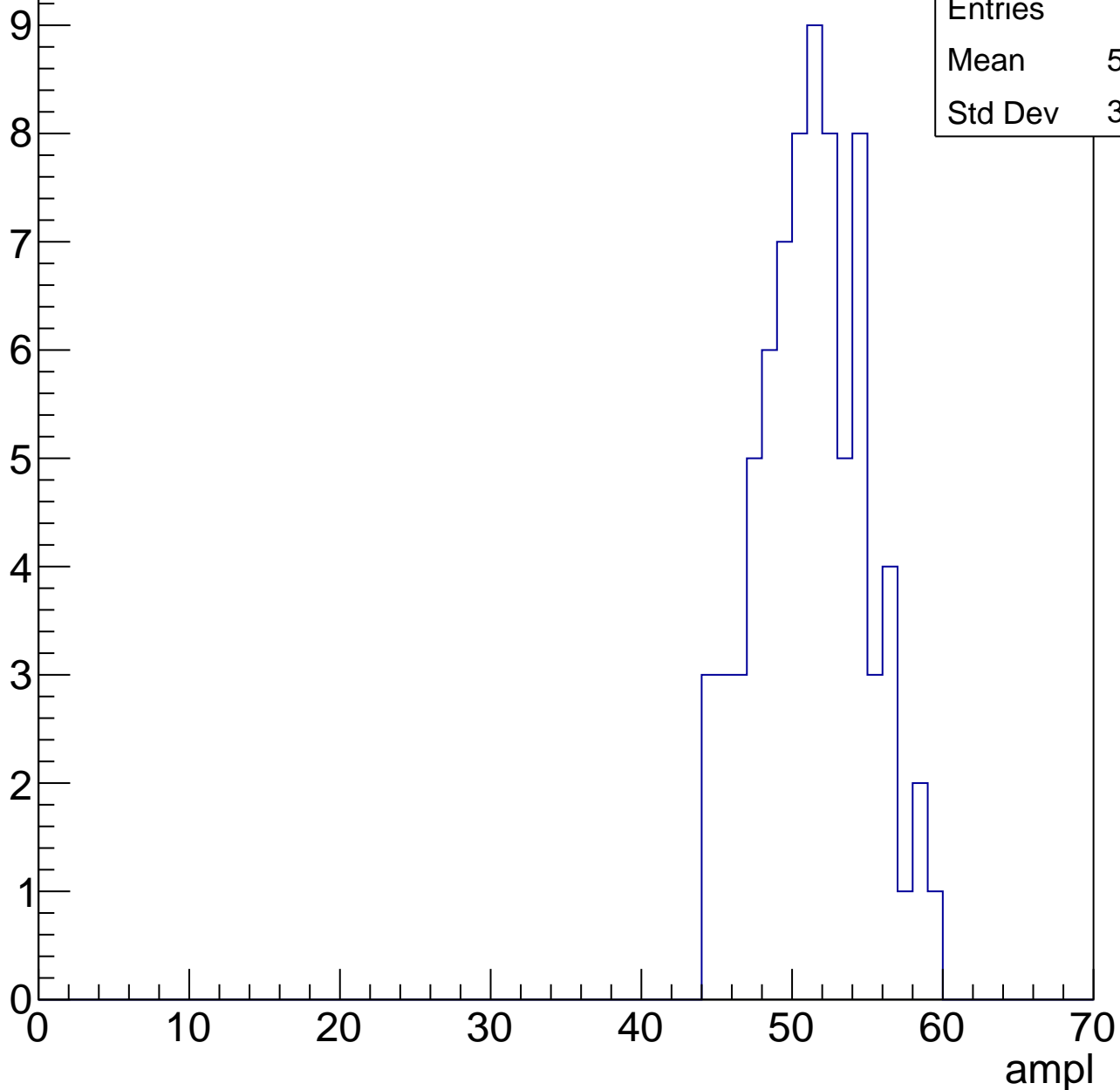
Gaus Width: 3.5061



B0L102S, U20-ch75, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

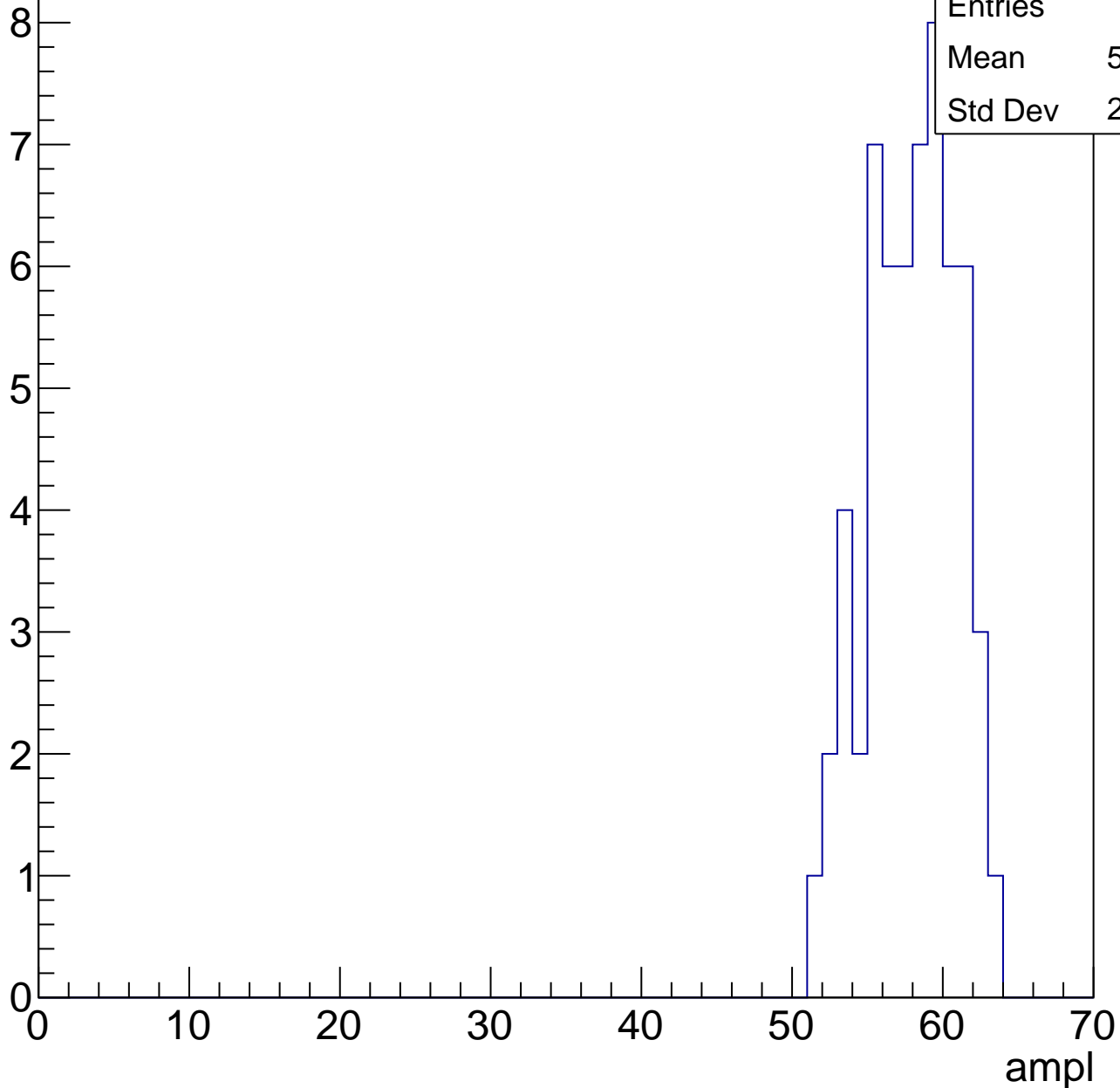


B0L102S, U20-ch75, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 57.47 |
| Std Dev | 2.878 |

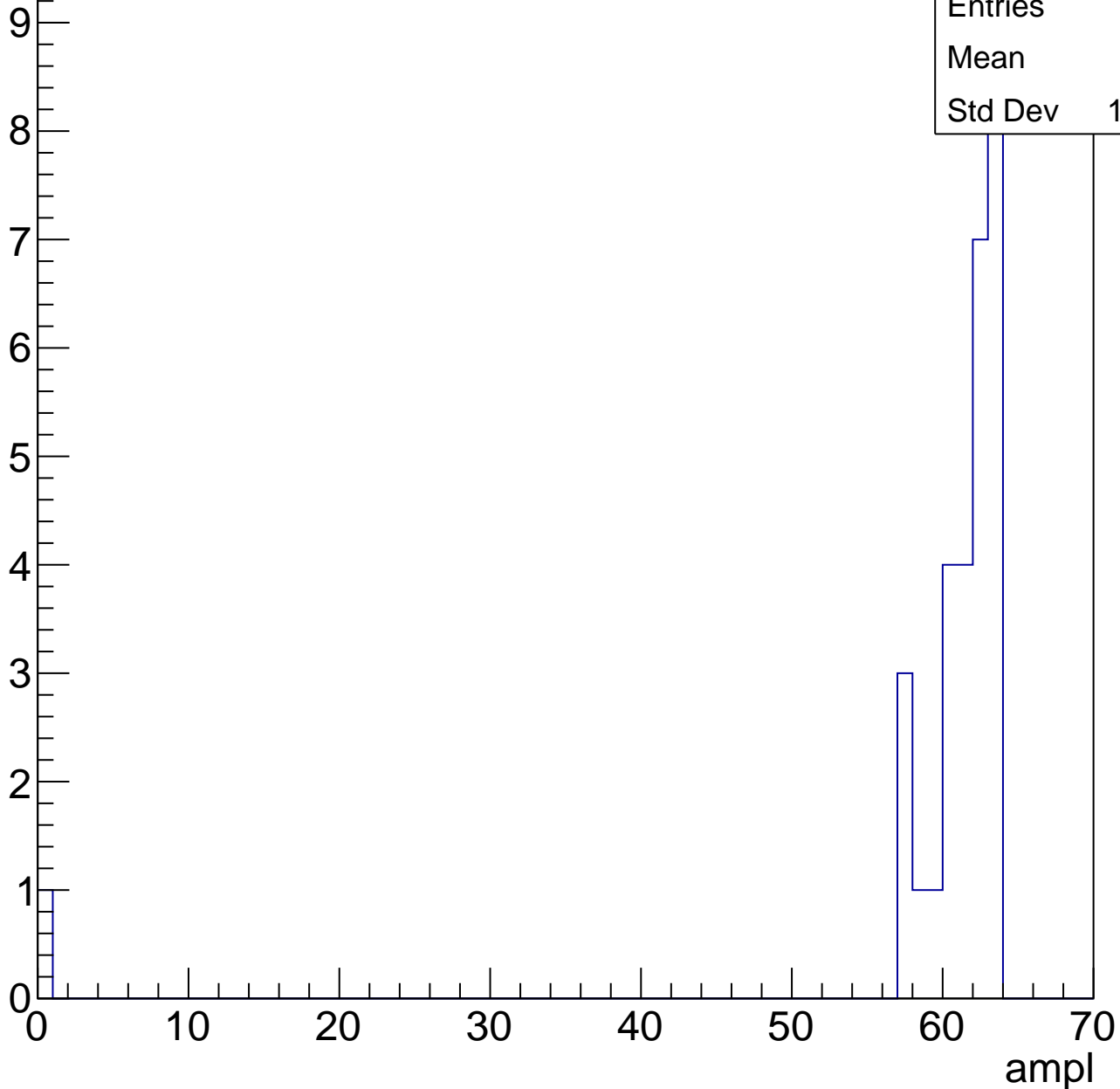


B0L102S, U20-ch75, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 30 |
| Mean | 59.1 |
| Std Dev | 11.14 |



B0L102S, U20-ch75, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch75, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch76, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.53 |
| Std Dev | 3.424 |

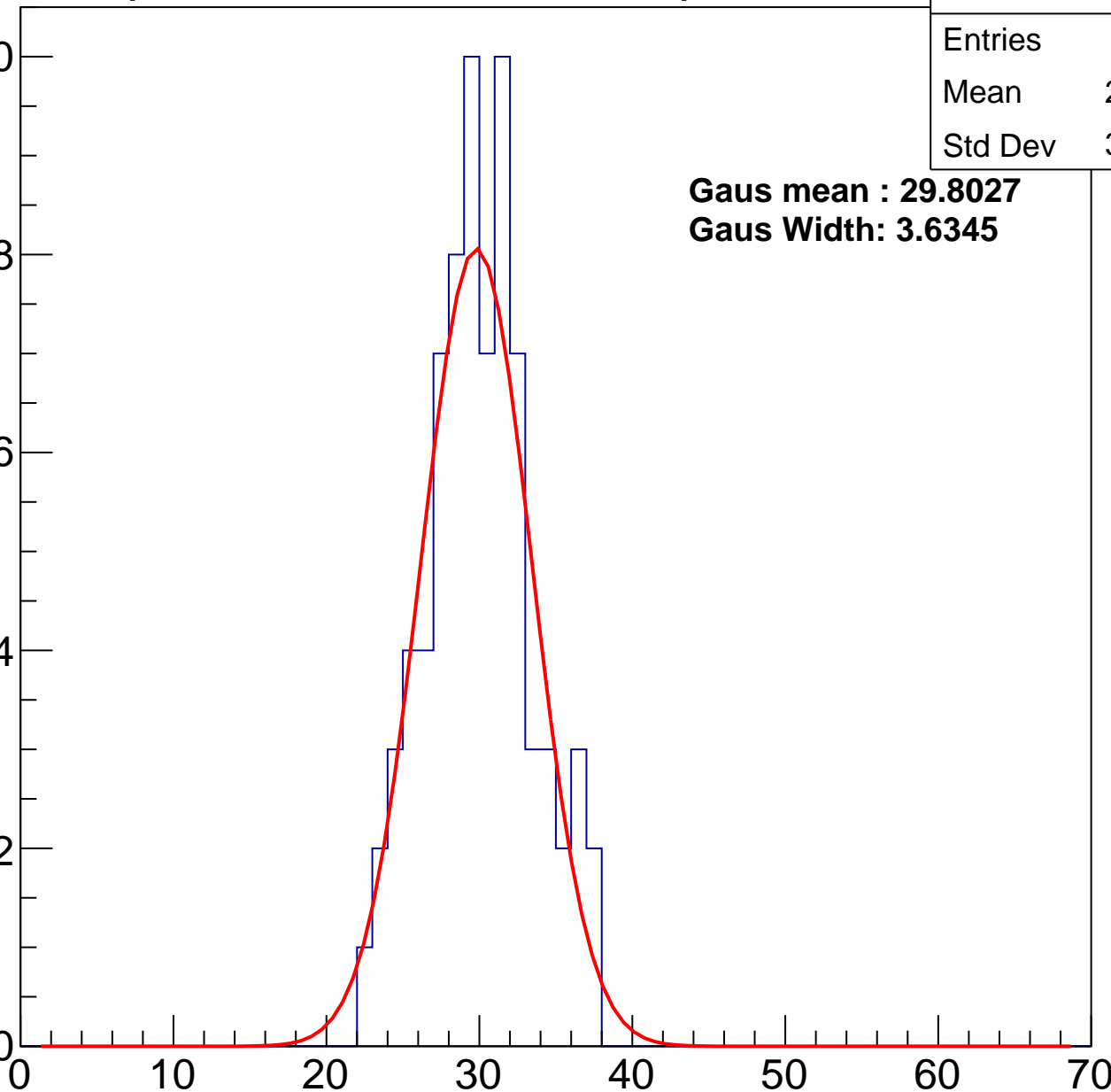
Gaus mean : 29.8027

Gaus Width: 3.6345

Entry

10
8
6
4
2
0

ampl



B0L102S, U20-ch76, adc1

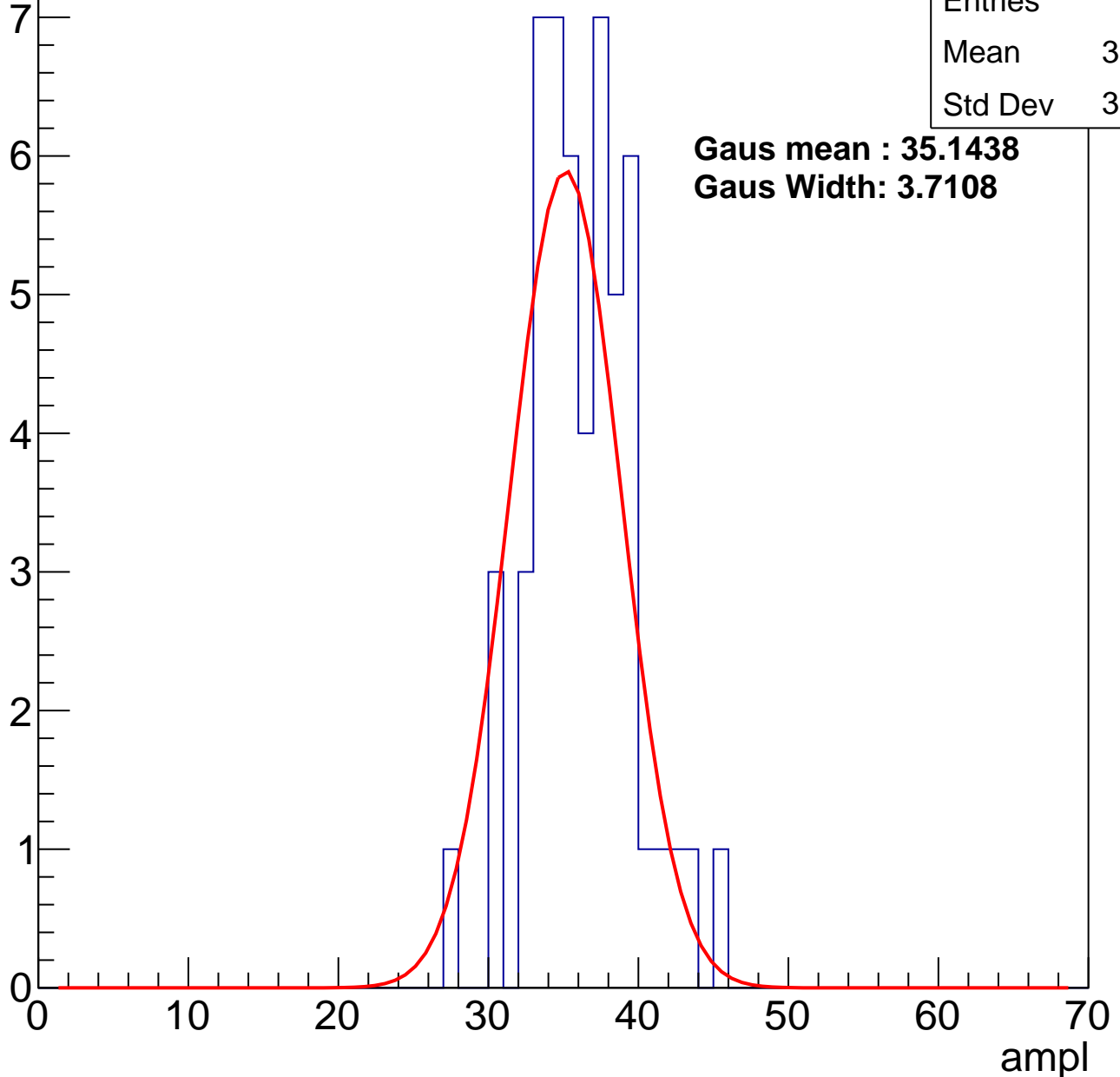
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 35.74 |
| Std Dev | 3.384 |

Gaus mean : 35.1438

Gaus Width: 3.7108



B0L102S, U20-ch76, adc2

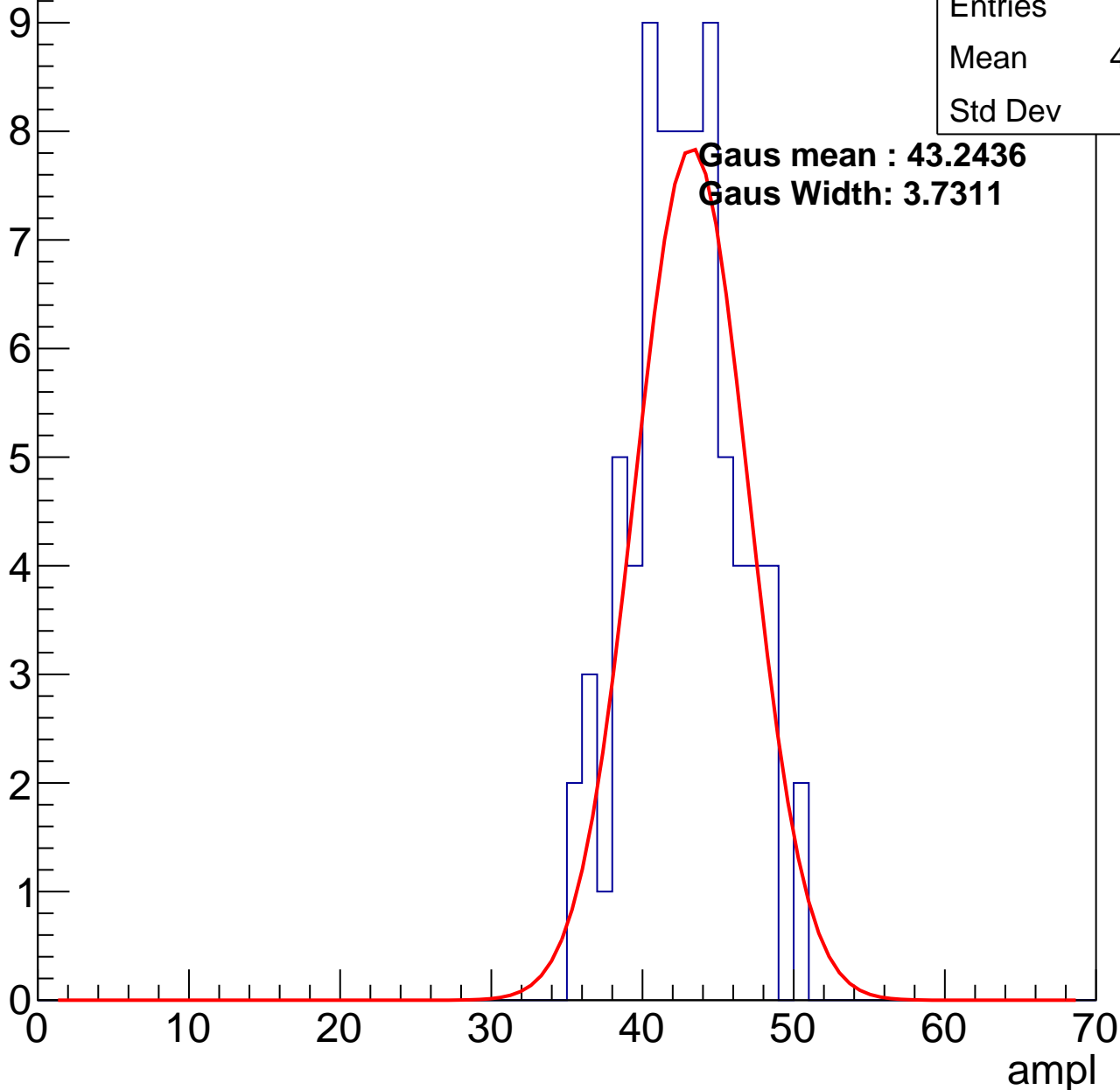
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.29 |
| Std Dev | 3.46 |

Gaus mean : 43.2436

Gaus Width: 3.7311



B0L102S, U20-ch76, adc3

calib_packv5_042523_0143.root, FC#12, port B1

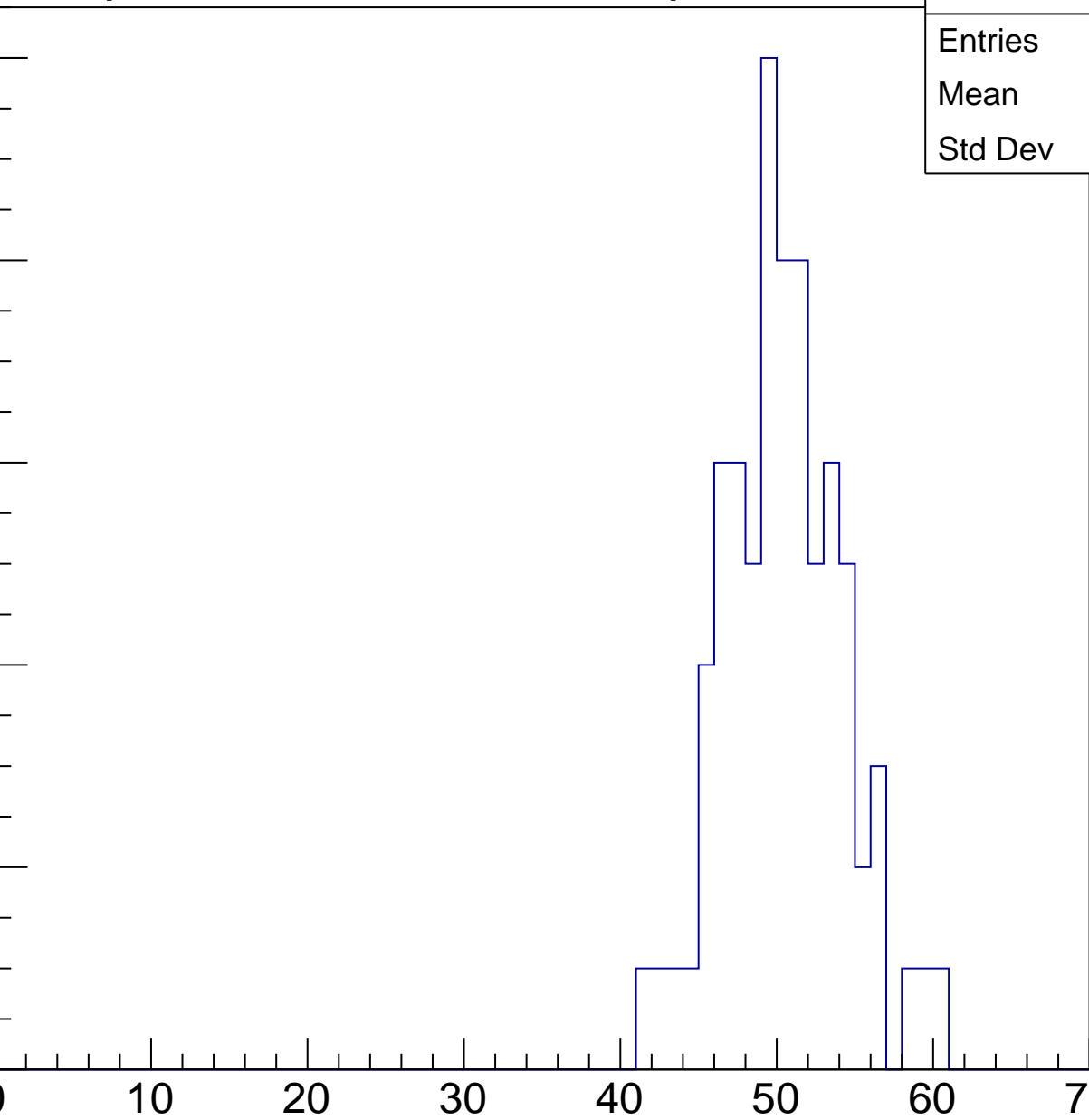
| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.99 |
| Std Dev | 3.786 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

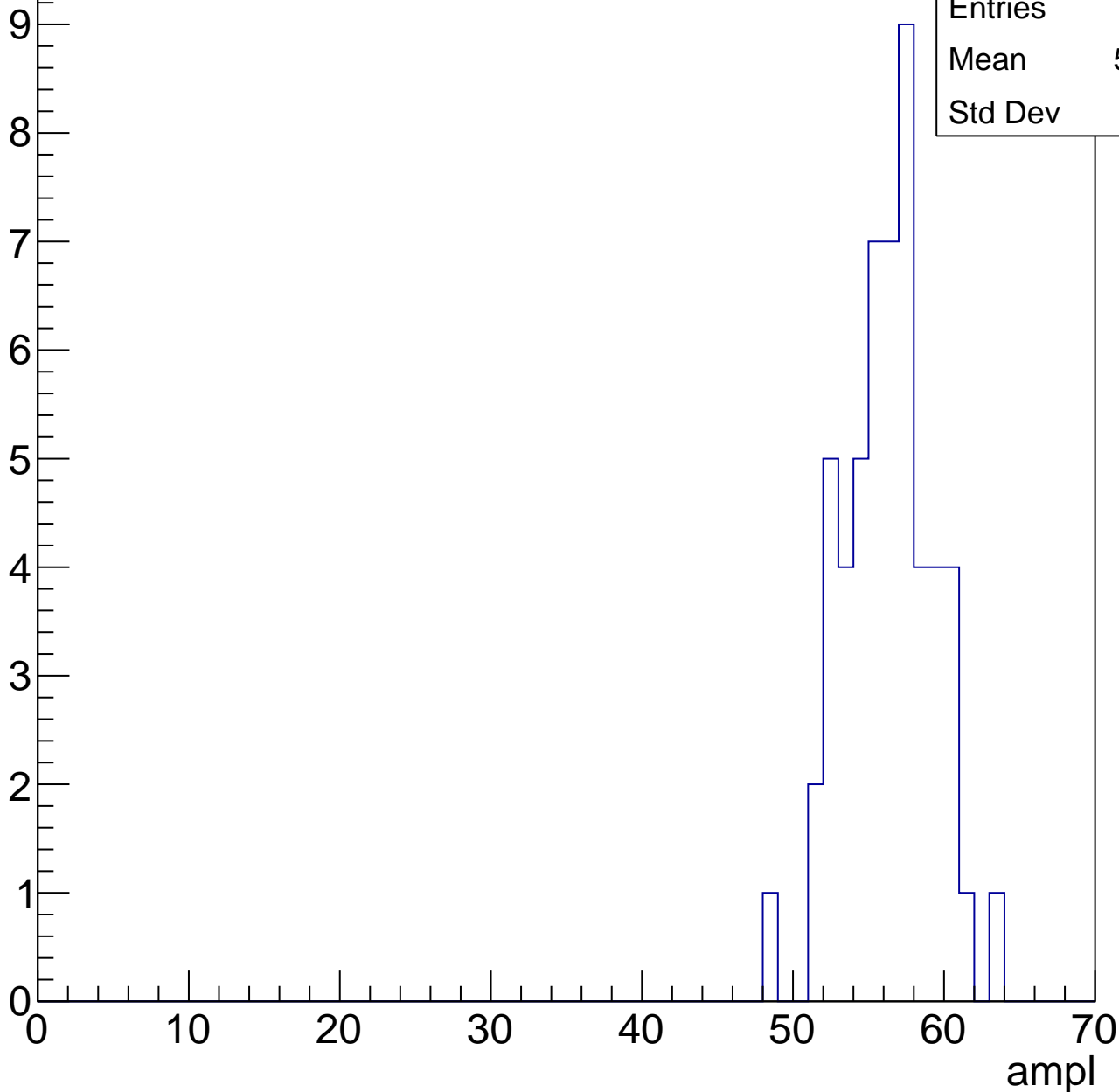


B0L102S, U20-ch76, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 55.81 |
| Std Dev | 2.9 |

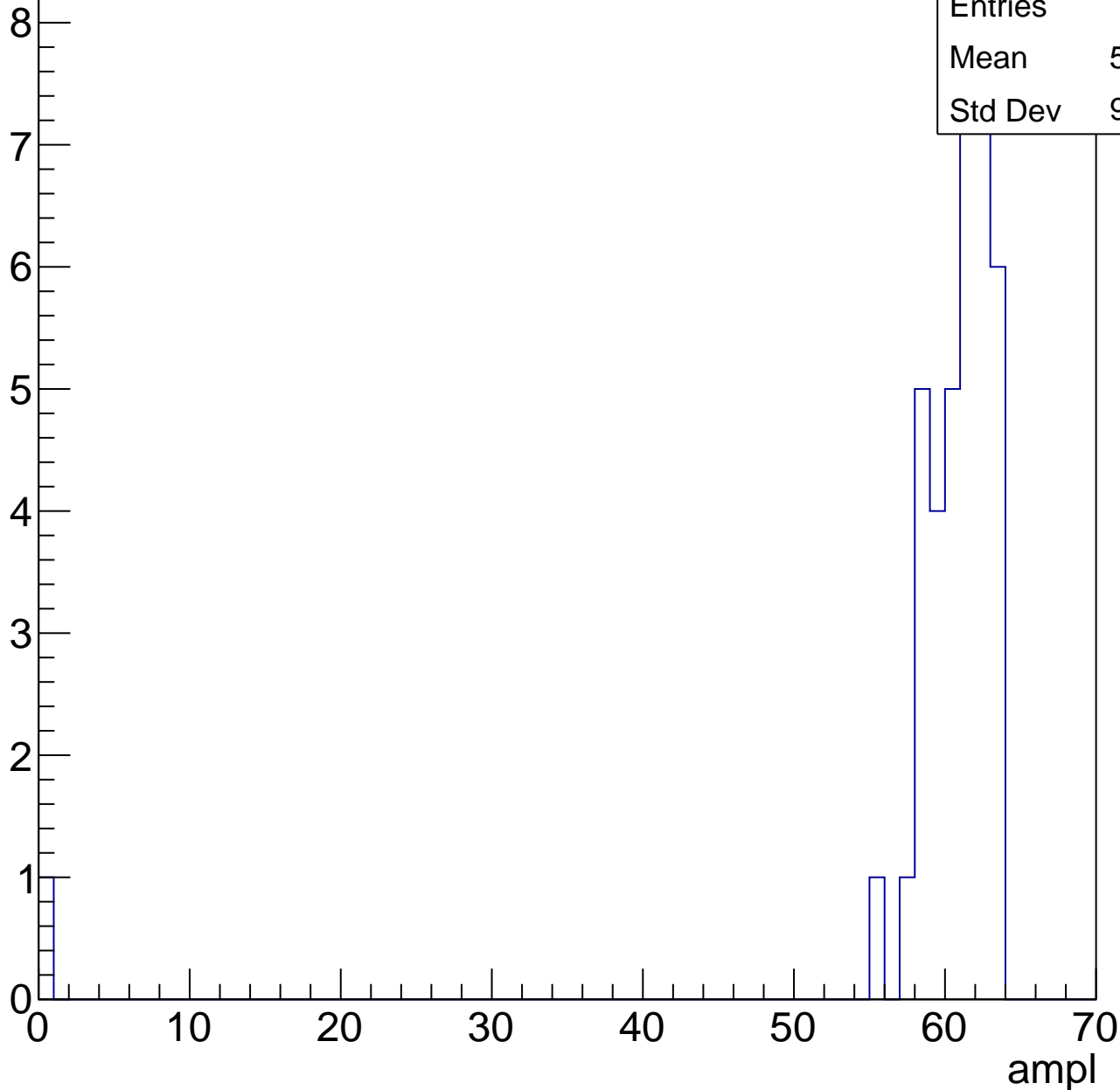


B0L102S, U20-ch76, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.97 |
| Std Dev | 9.755 |



B0L102S, U20-ch76, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch76, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch77, adc0

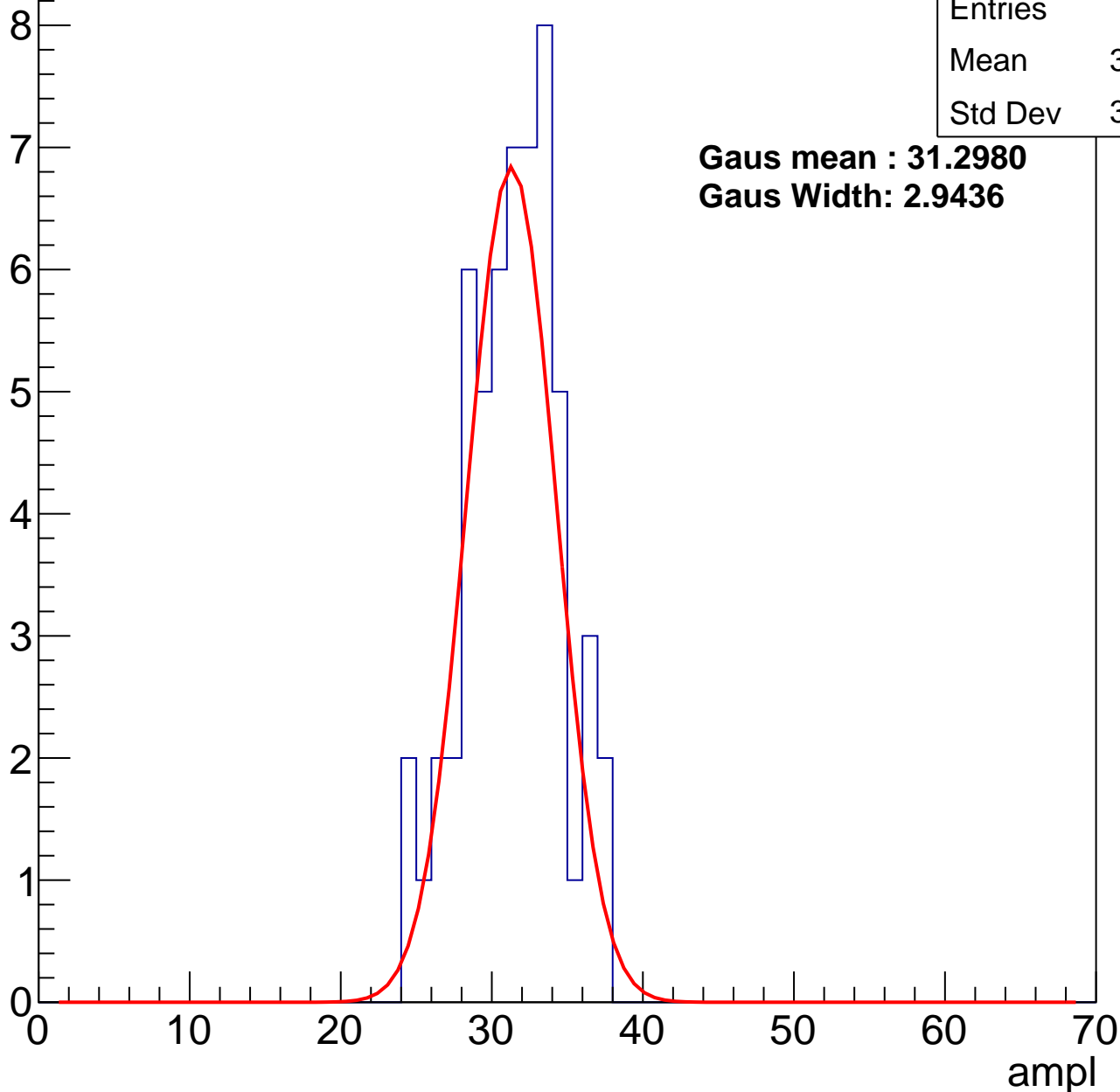
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 30.95 |
| Std Dev | 3.086 |

Gaus mean : 31.2980

Gaus Width: 2.9436



B0L102S, U20-ch77, adc1

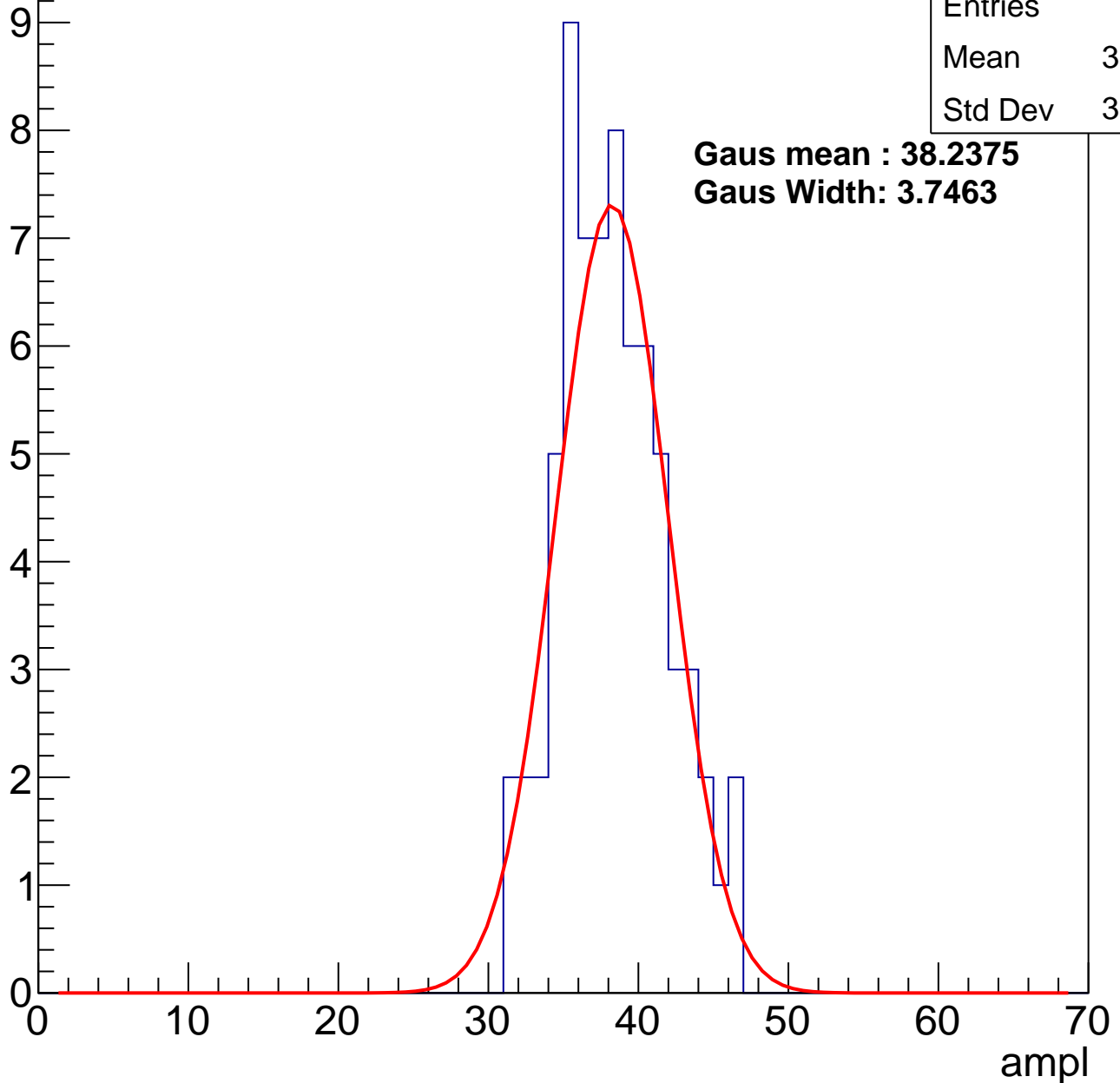
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.87 |
| Std Dev | 3.509 |

Gaus mean : 38.2375

Gaus Width: 3.7463



B0L102S, U20-ch77, adc2

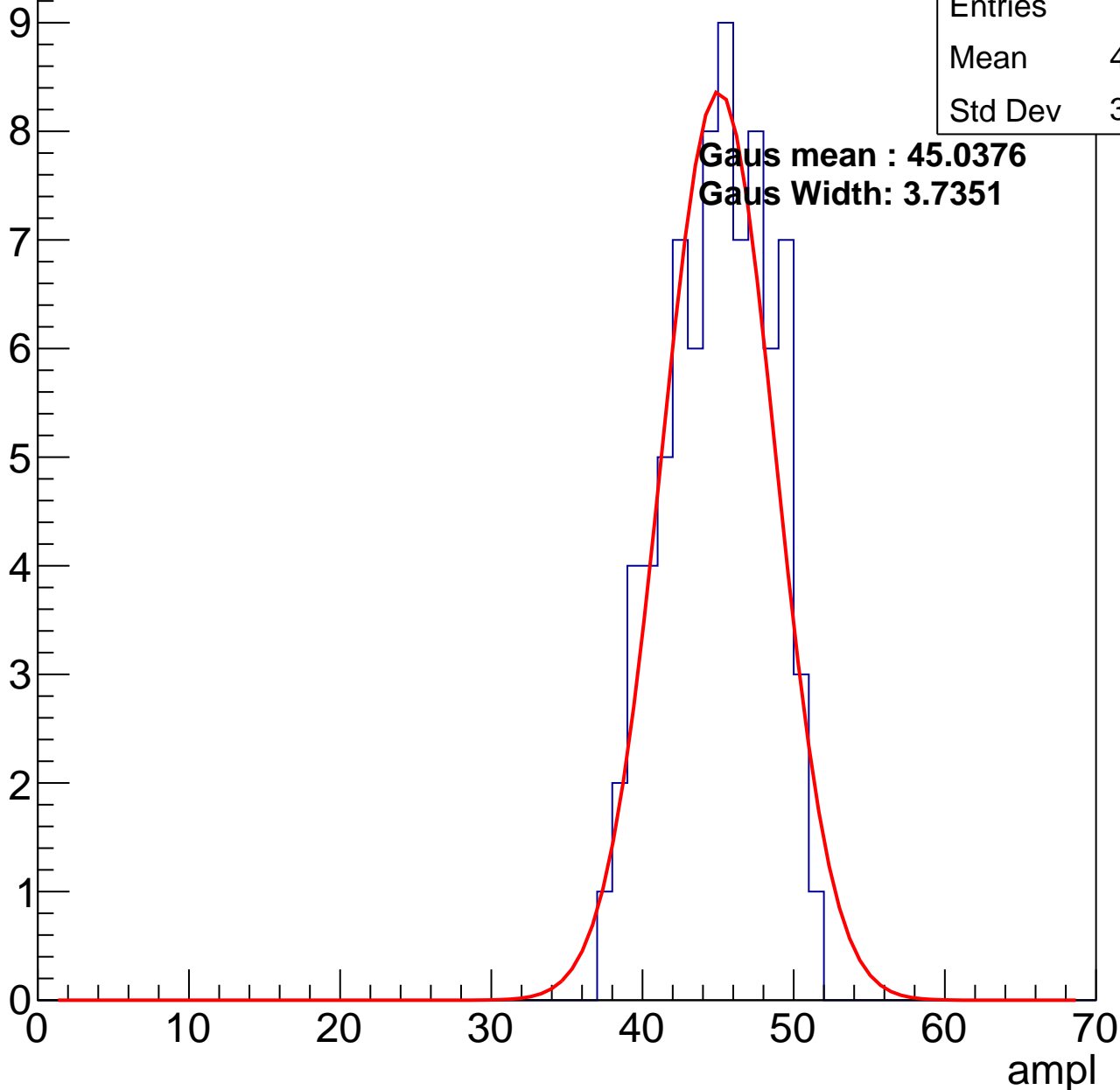
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 44.53 |
| Std Dev | 3.358 |

Gaus mean : 45.0376

Gaus Width: 3.7351

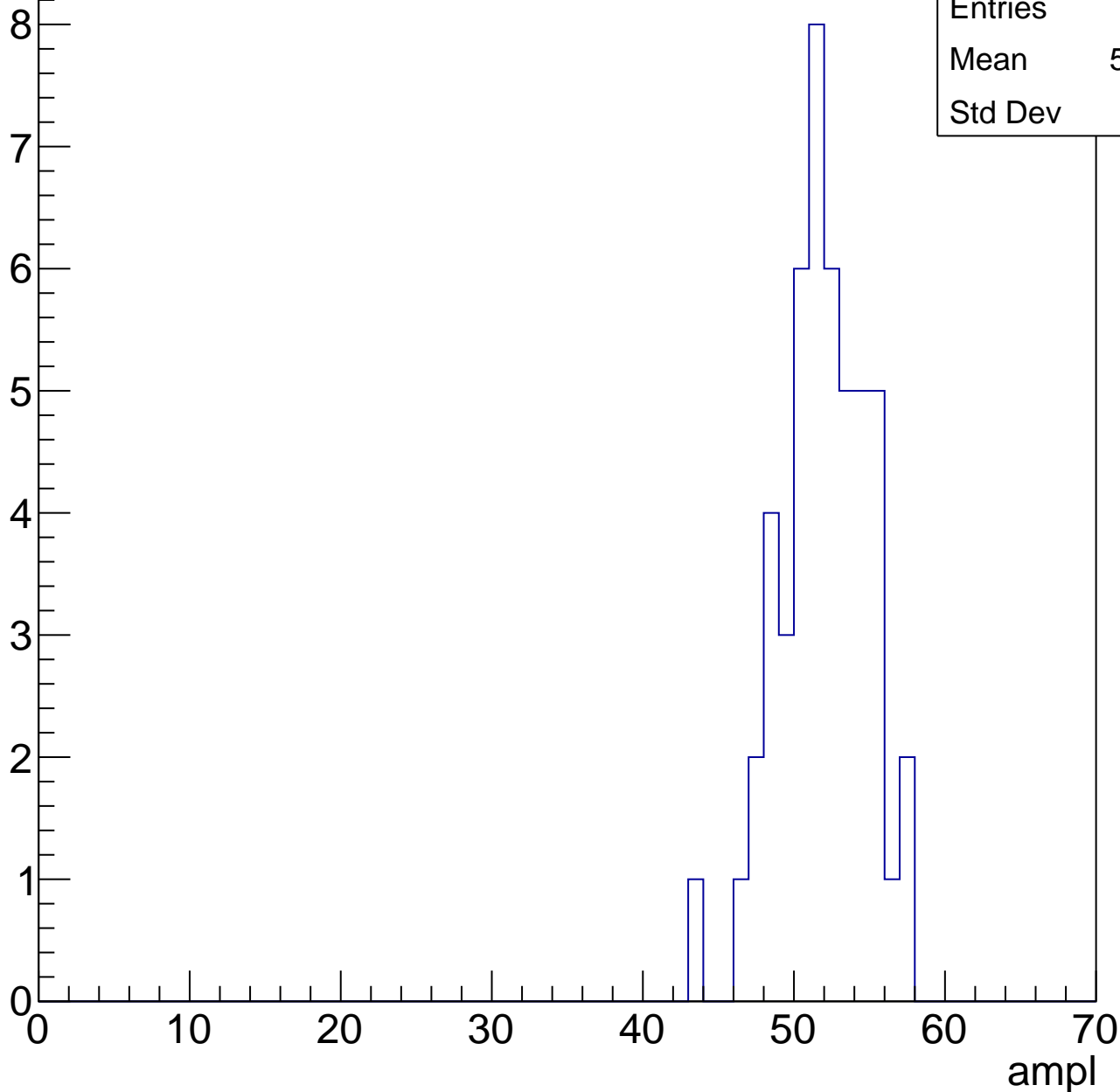


B0L102S, U20-ch77, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 51.47 |
| Std Dev | 2.9 |

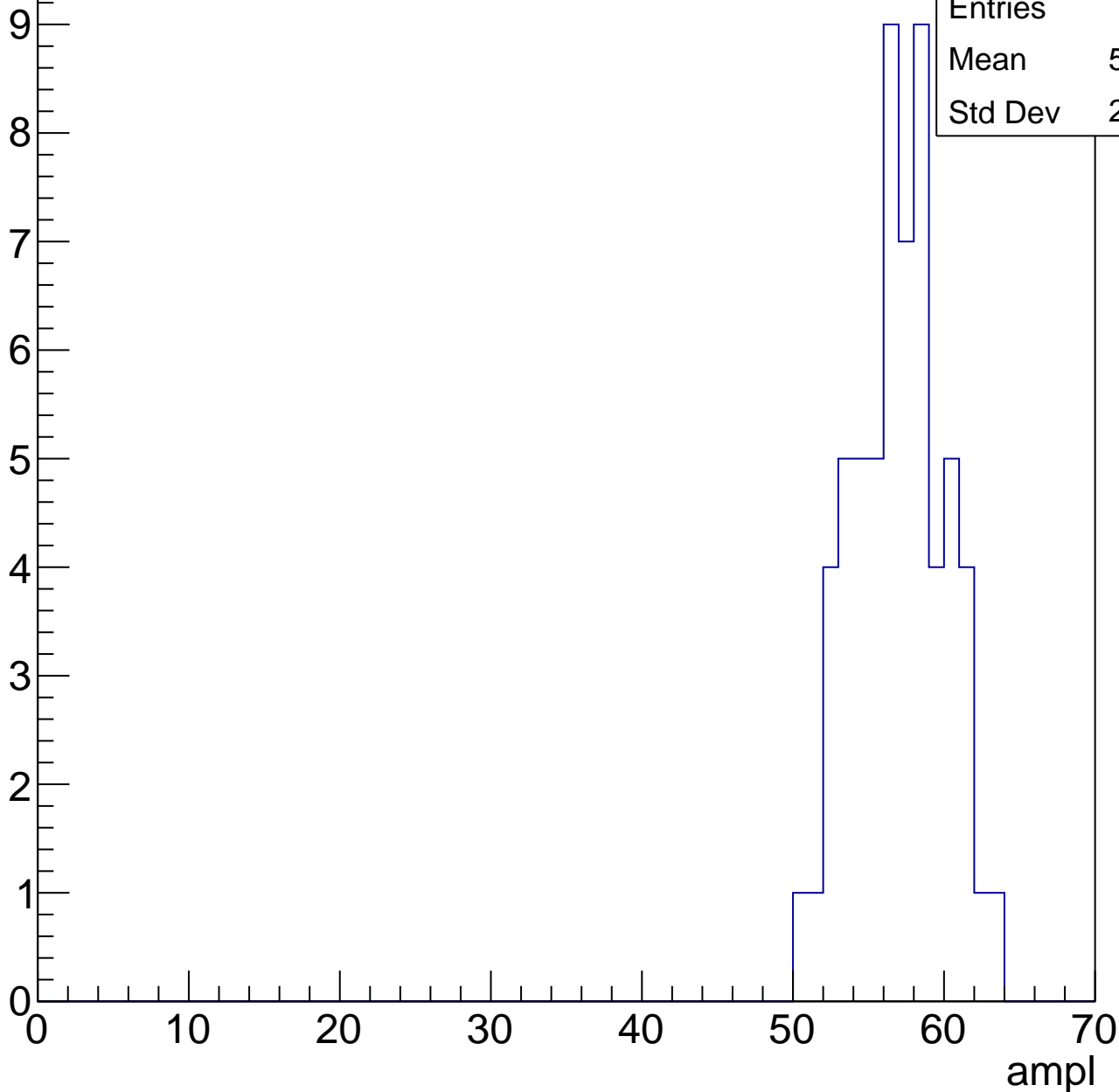


B0L102S, U20-ch77, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 56.54 |
| Std Dev | 2.923 |

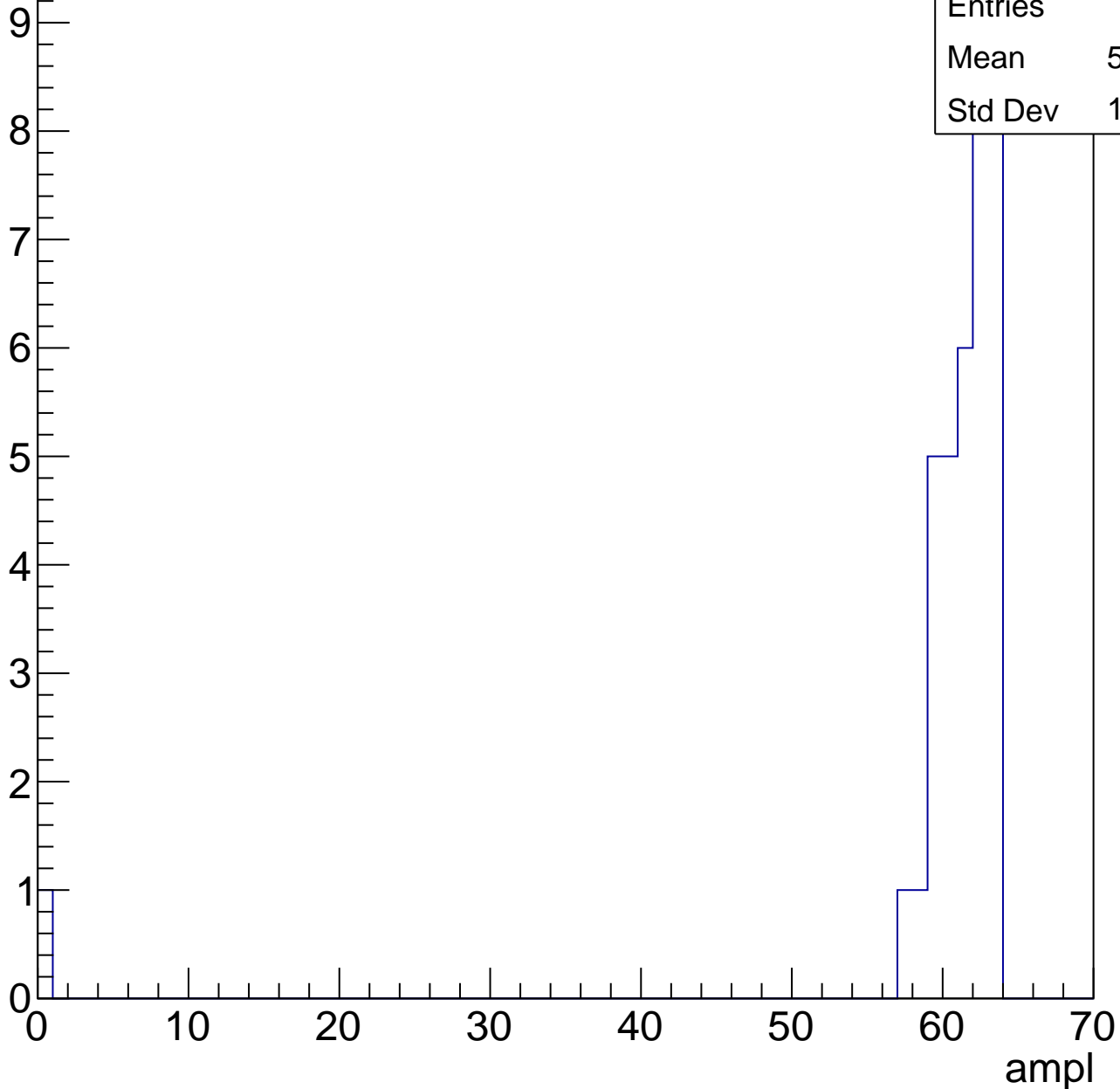


B0L102S, U20-ch77, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

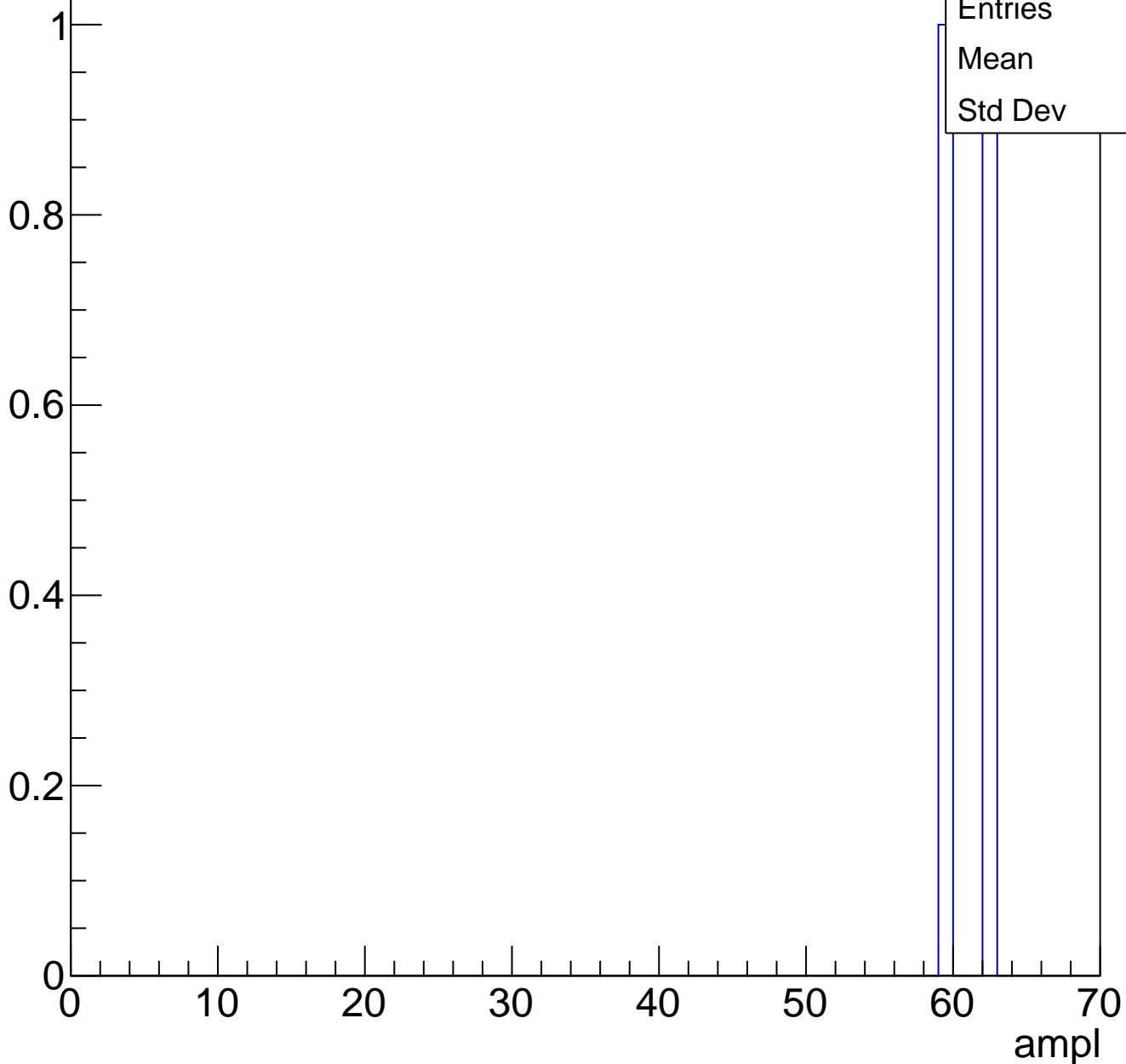
| | |
|---------|-------|
| Entries | 36 |
| Mean | 59.42 |
| Std Dev | 10.17 |



B0L102S, U20-ch77, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch77, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B0L102S, U20-ch78, adc0

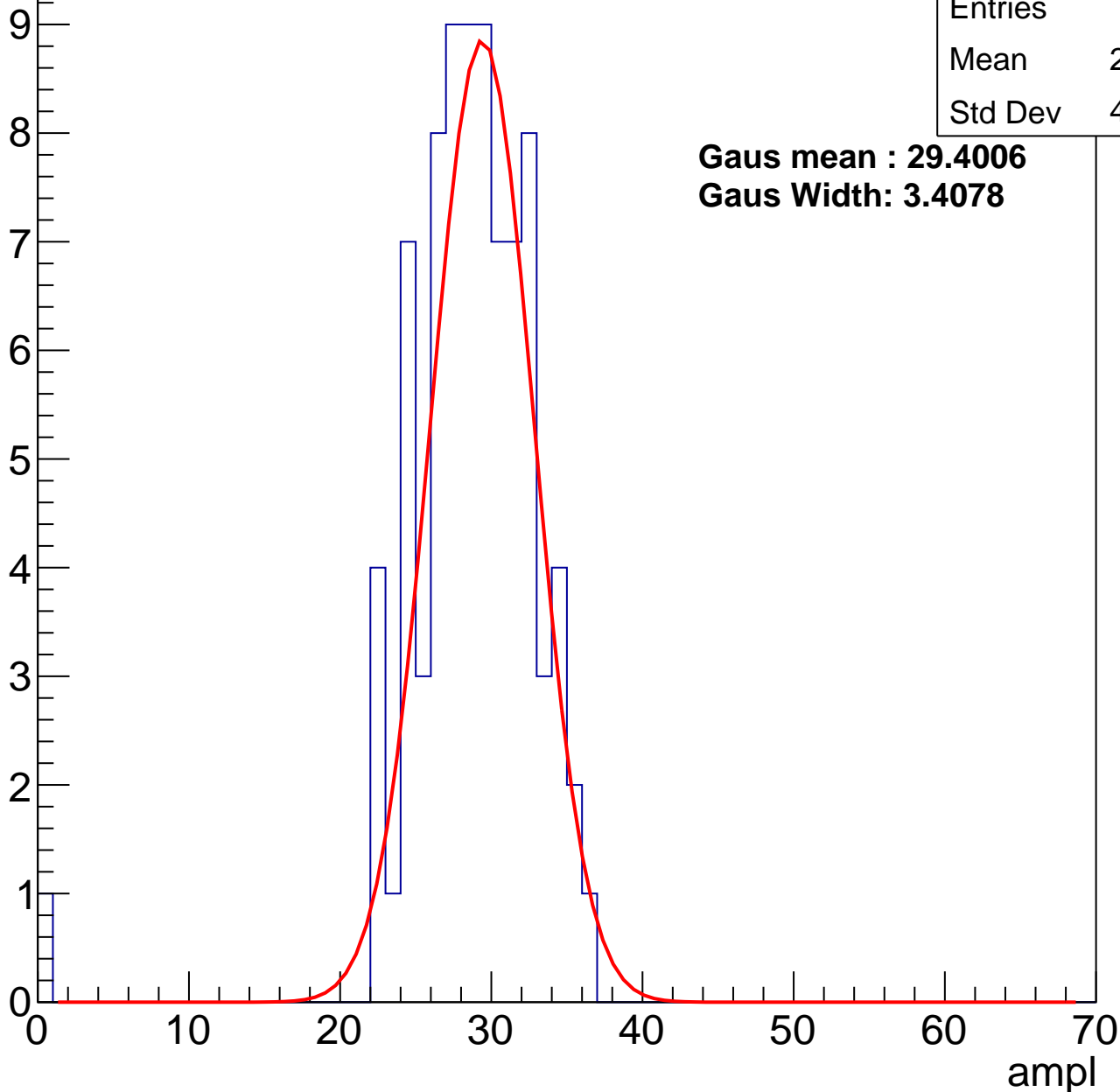
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 28.22 |
| Std Dev | 4.576 |

Gaus mean : 29.4006

Gaus Width: 3.4078



B0L102S, U20-ch78, adc1

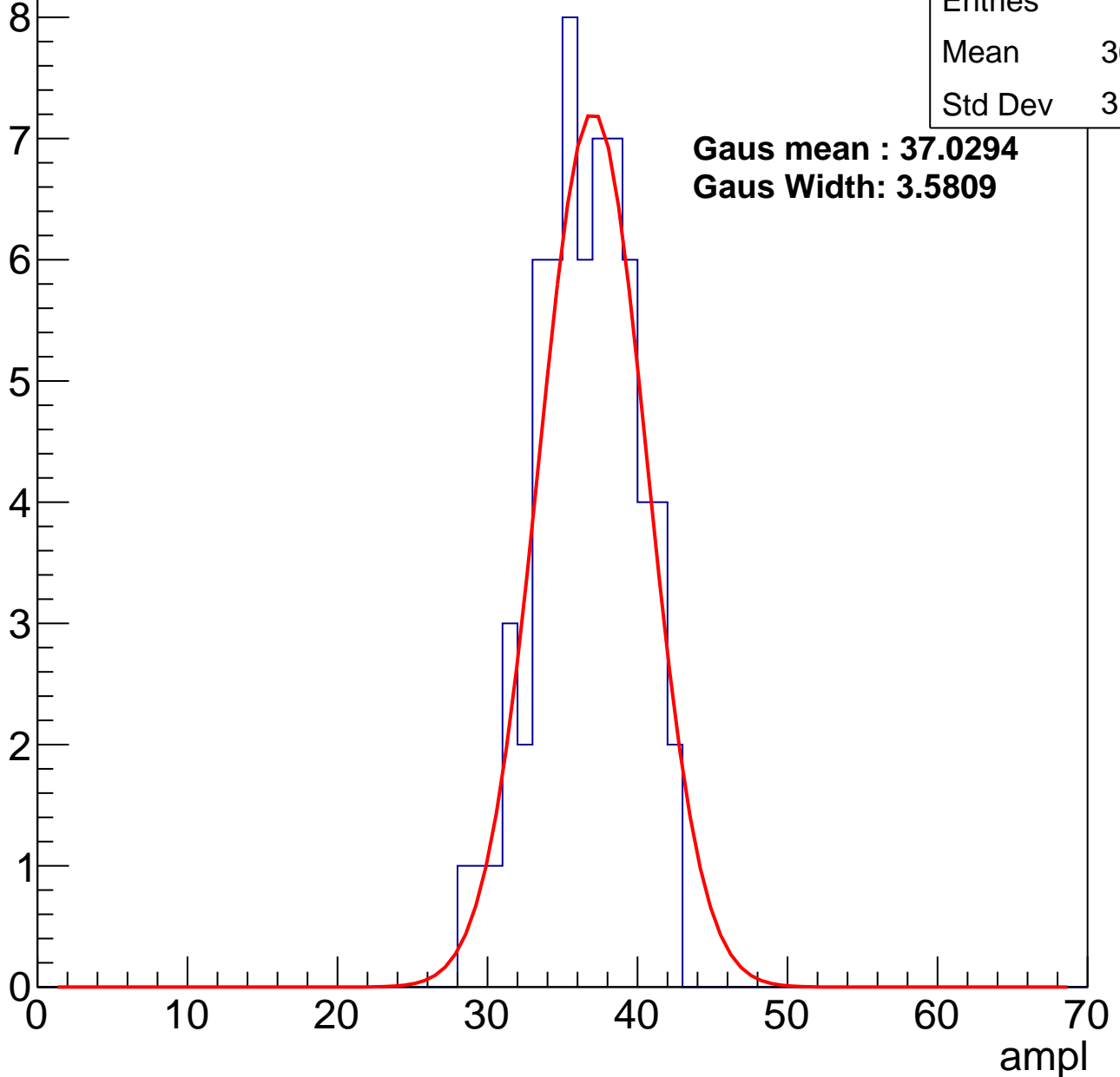
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.08 |
| Std Dev | 3.227 |

Gaus mean : 37.0294

Gaus Width: 3.5809



B0L102S, U20-ch78, adc2

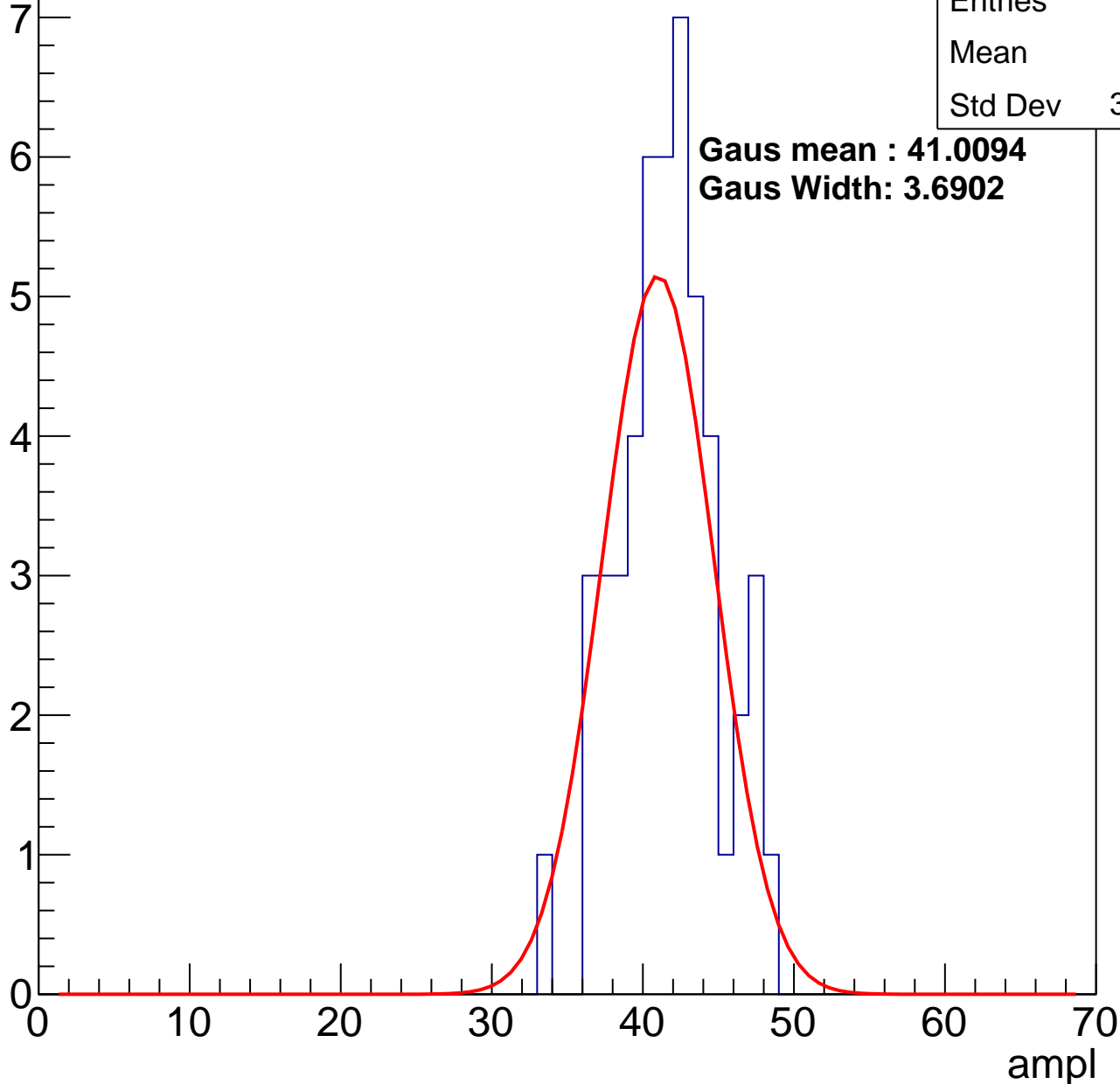
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 41.2 |
| Std Dev | 3.258 |

Gaus mean : 41.0094

Gaus Width: 3.6902

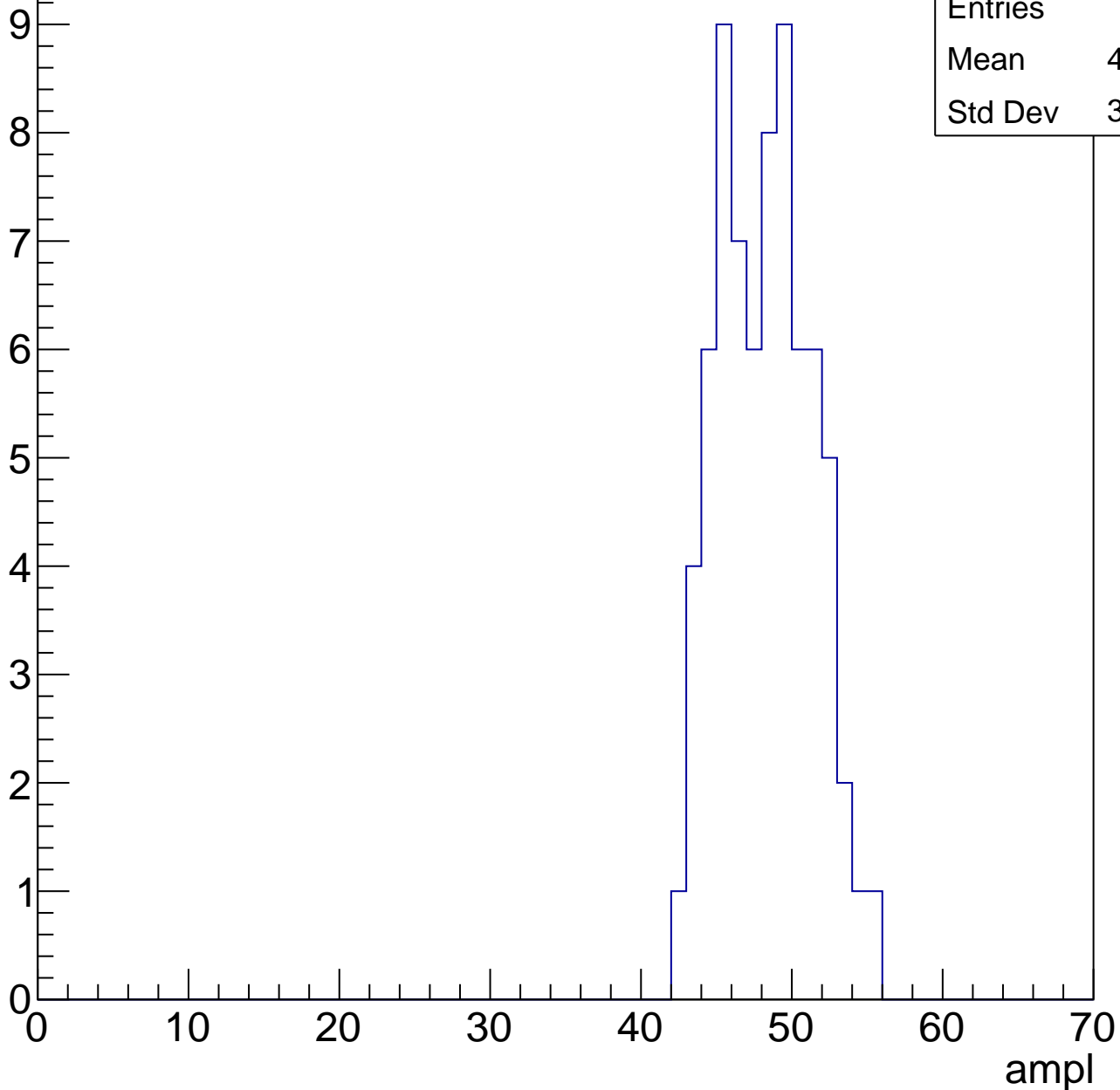


B0L102S, U20-ch78, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 47.79 |
| Std Dev | 3.016 |

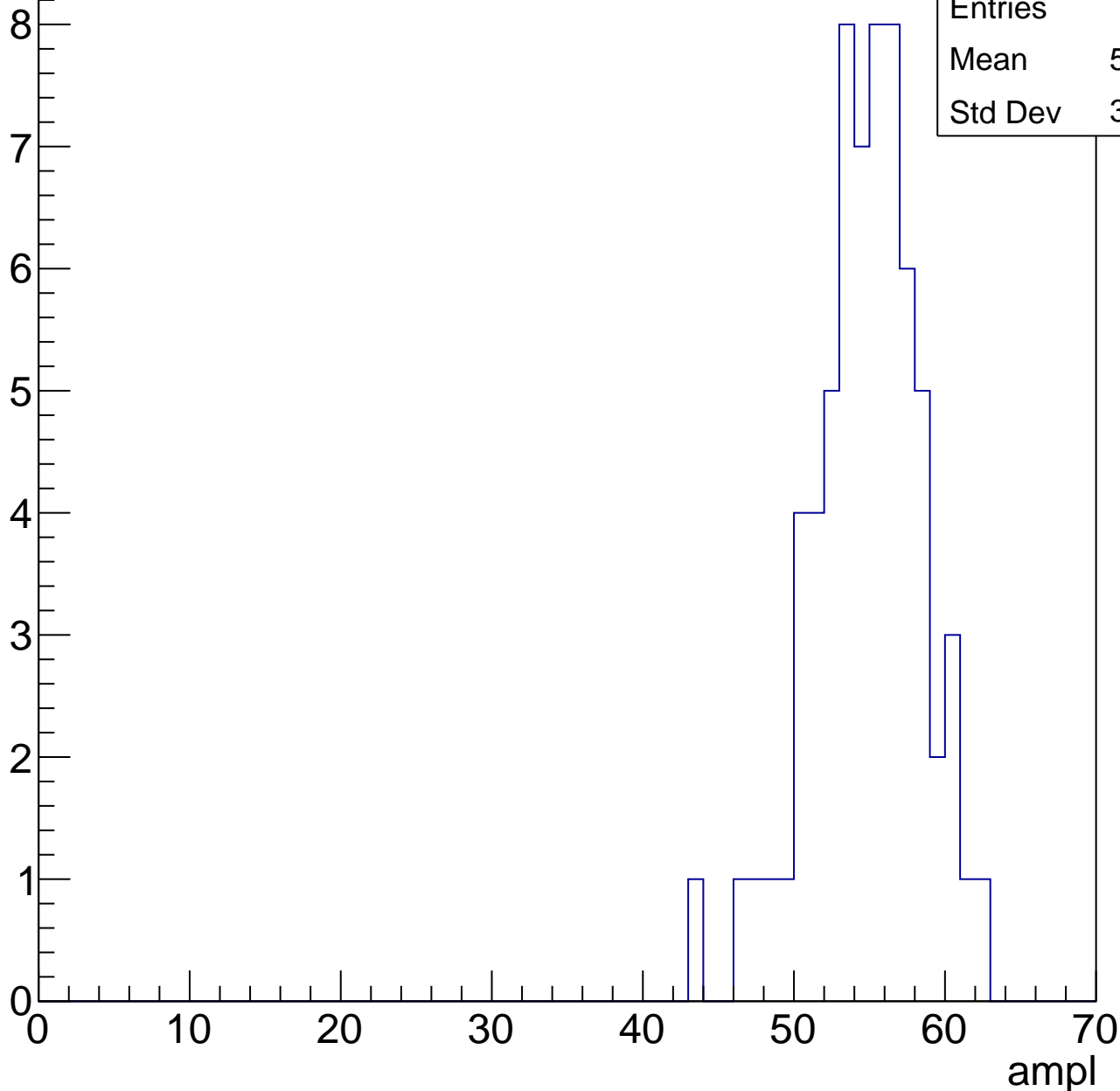


B0L102S, U20-ch78, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 54.33 |
| Std Dev | 3.584 |

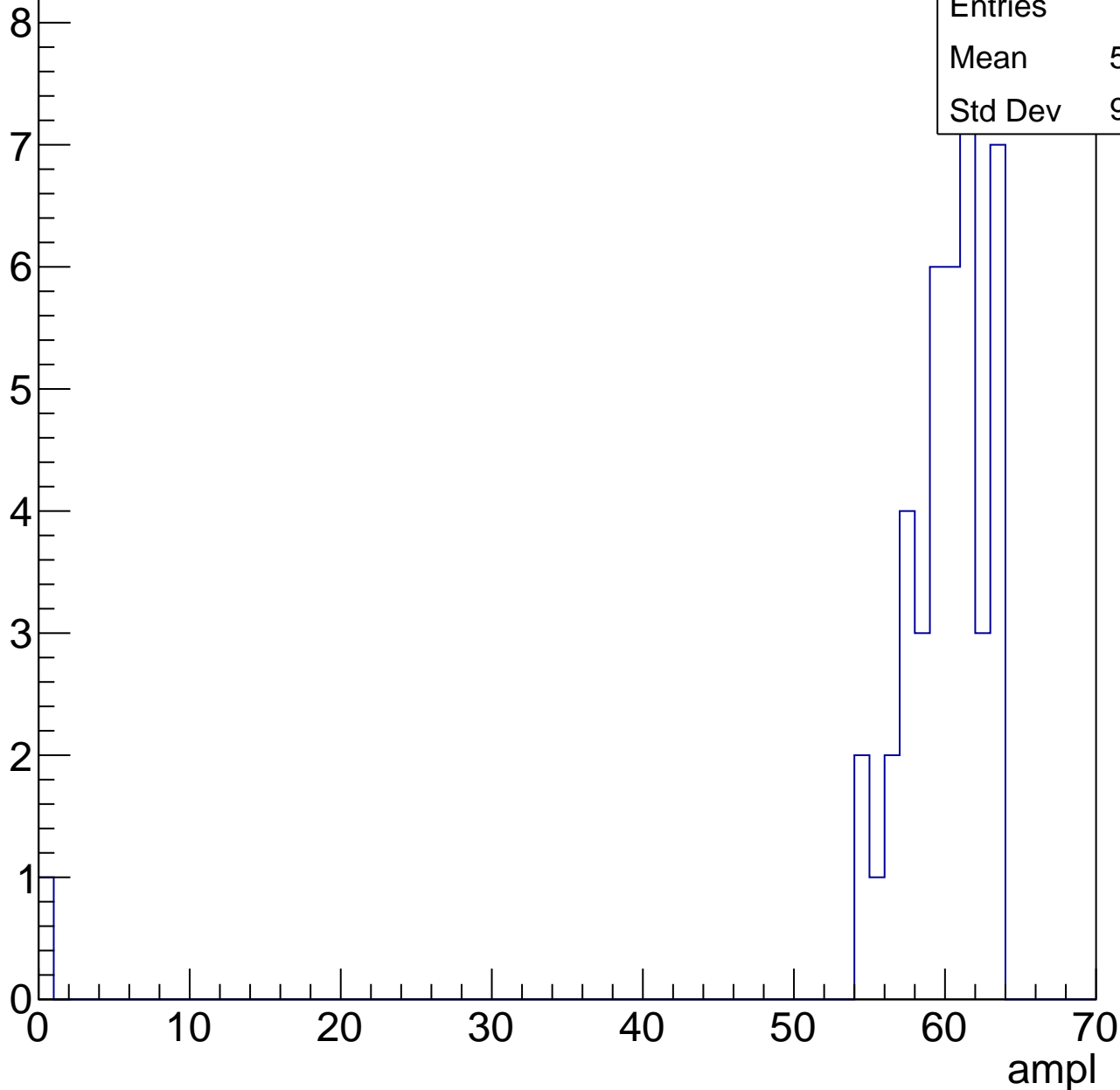


B0L102S, U20-ch78, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.28 |
| Std Dev | 9.324 |

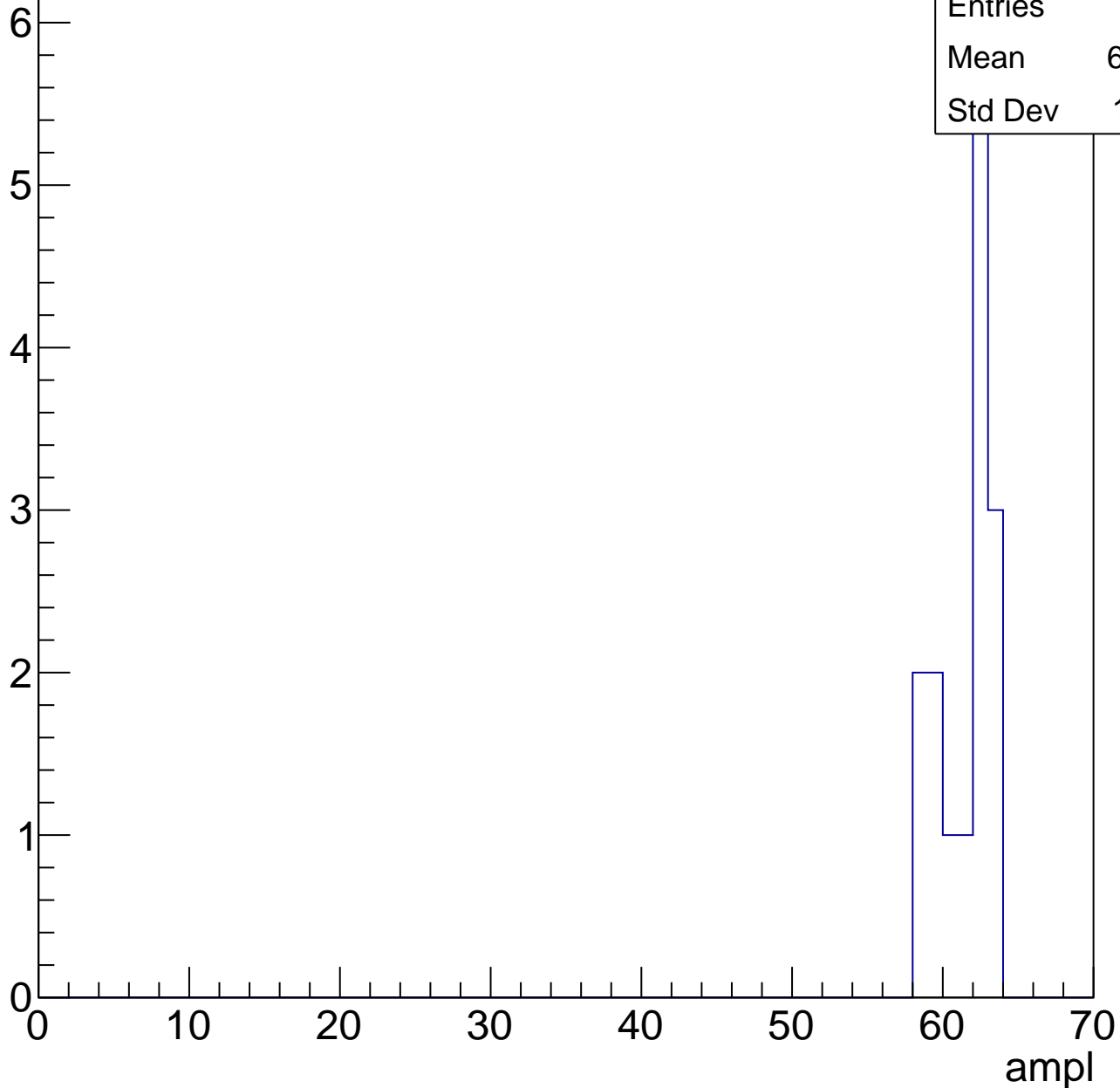


B0L102S, U20-ch78, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.07 |
| Std Dev | 1.731 |



B0L102S, U20-ch78, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch79, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 90 |
| Mean | 28.63 |
| Std Dev | 3.677 |

Gaus mean : 29.4908

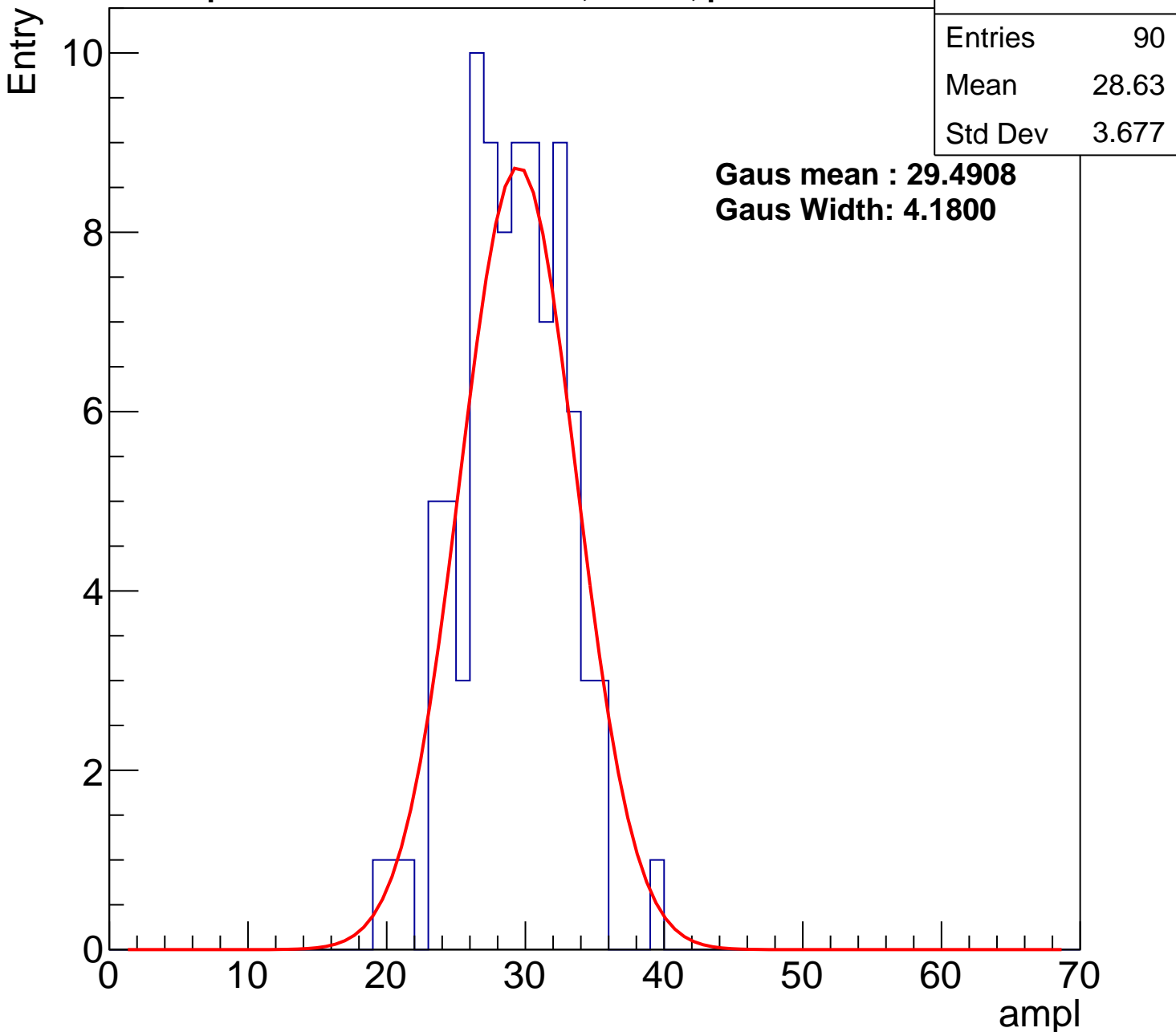
Gaus Width: 4.1800

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch79, adc1

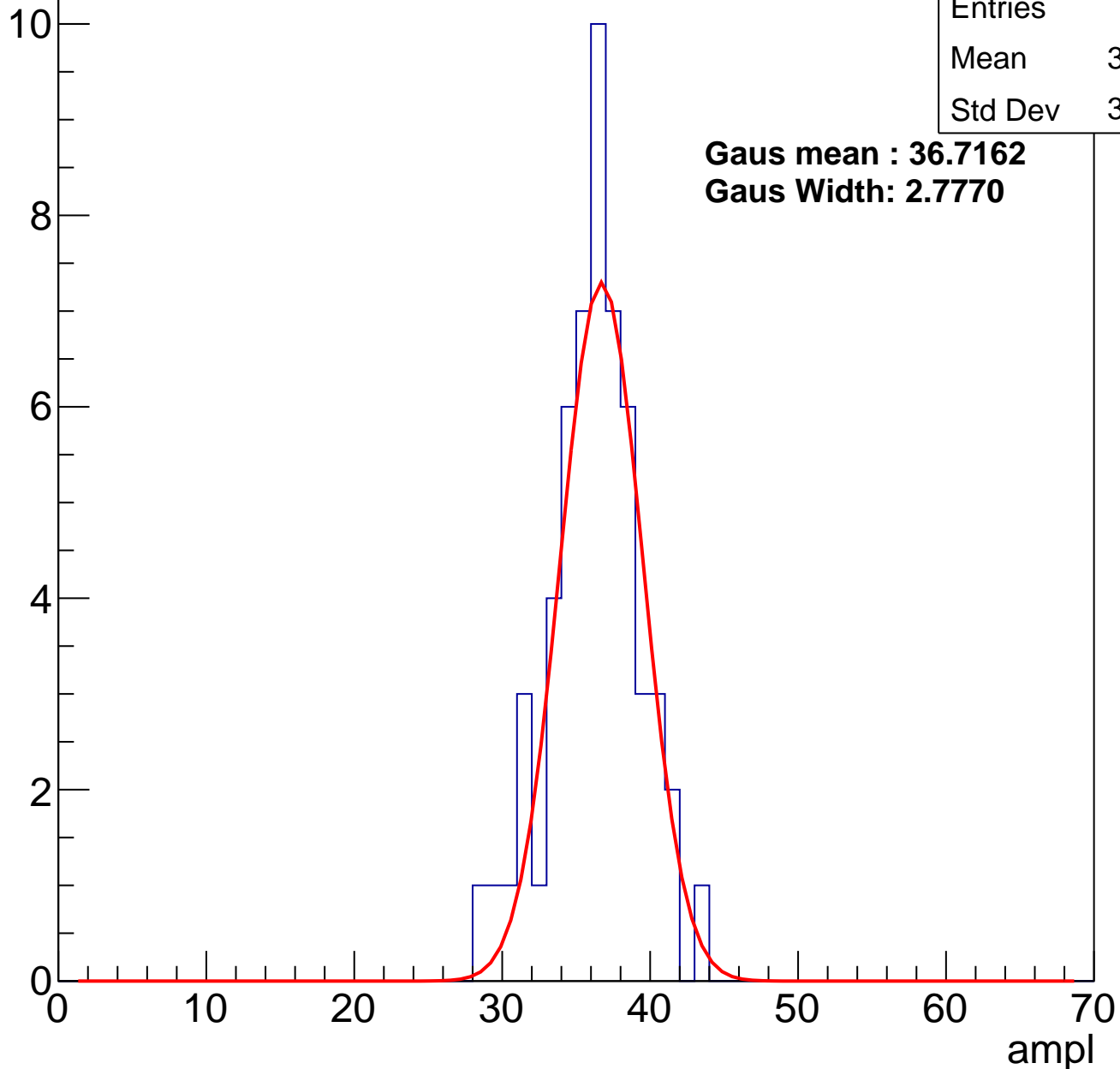
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 56 |
| Mean | 35.75 |
| Std Dev | 3.013 |

Gaus mean : 36.7162

Gaus Width: 2.7770

Entry



B0L102S, U20-ch79, adc2

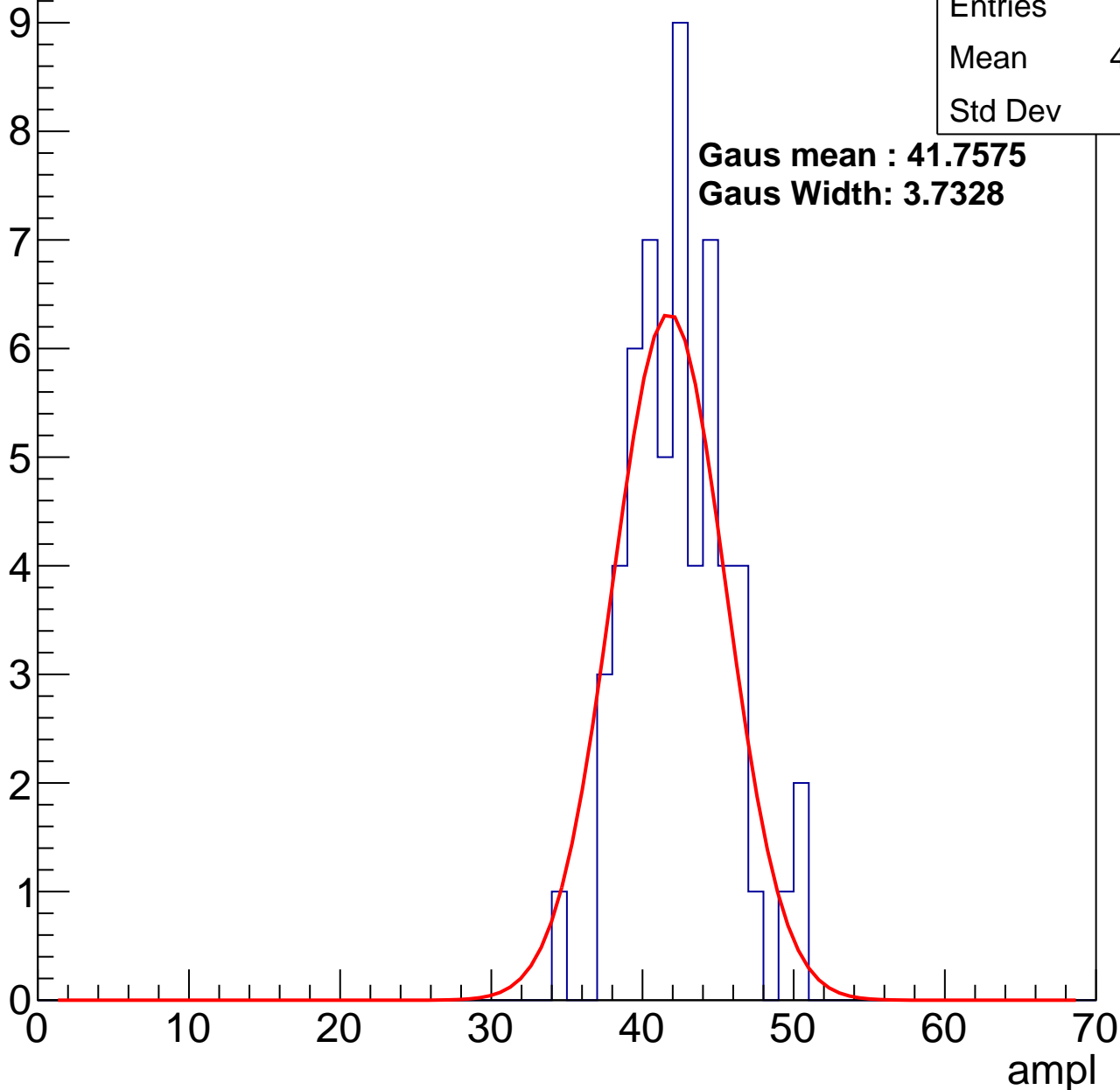
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 41.97 |
| Std Dev | 3.28 |

Gaus mean : 41.7575

Gaus Width: 3.7328

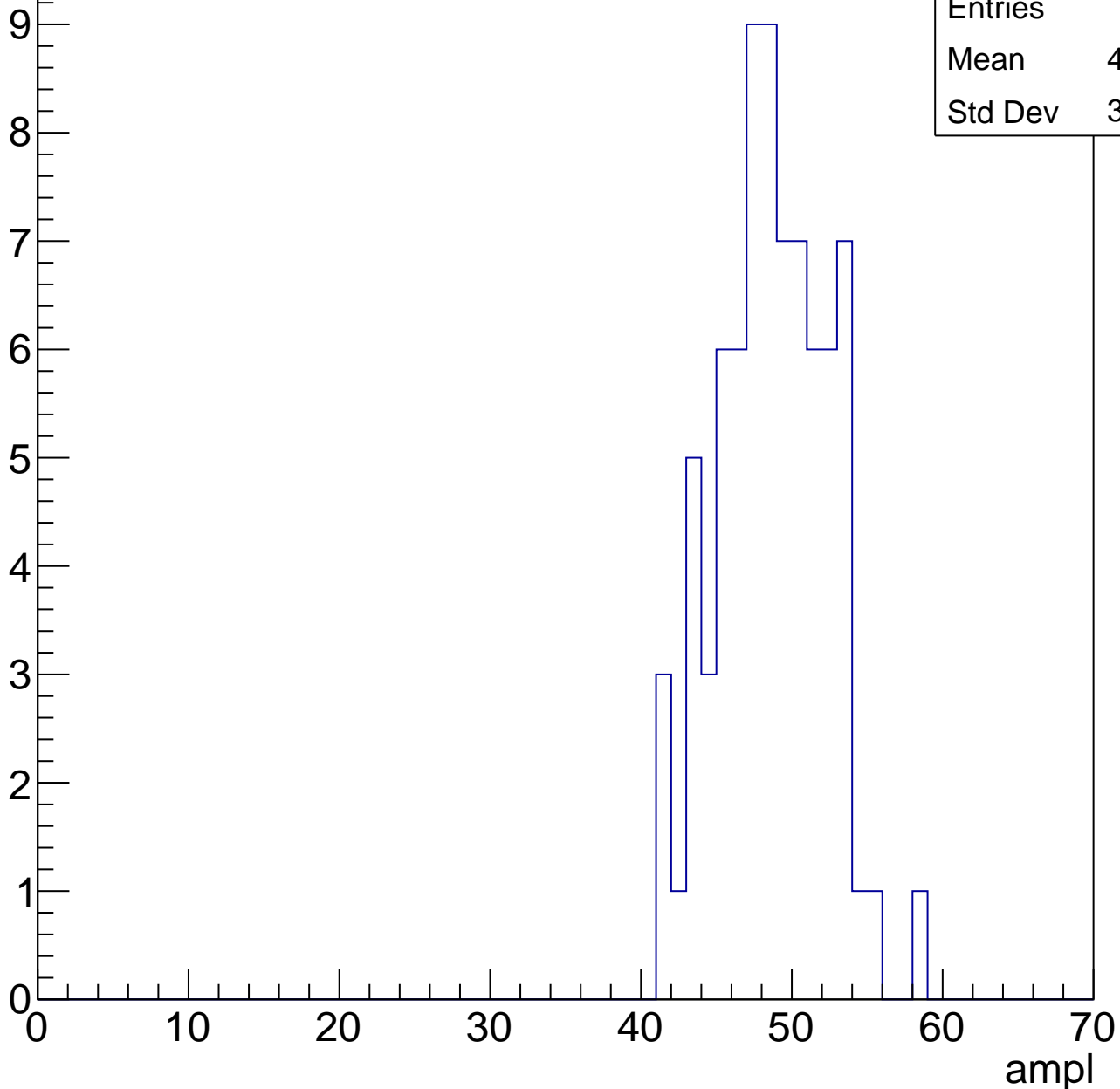


B0L102S, U20-ch79, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 48.23 |
| Std Dev | 3.552 |

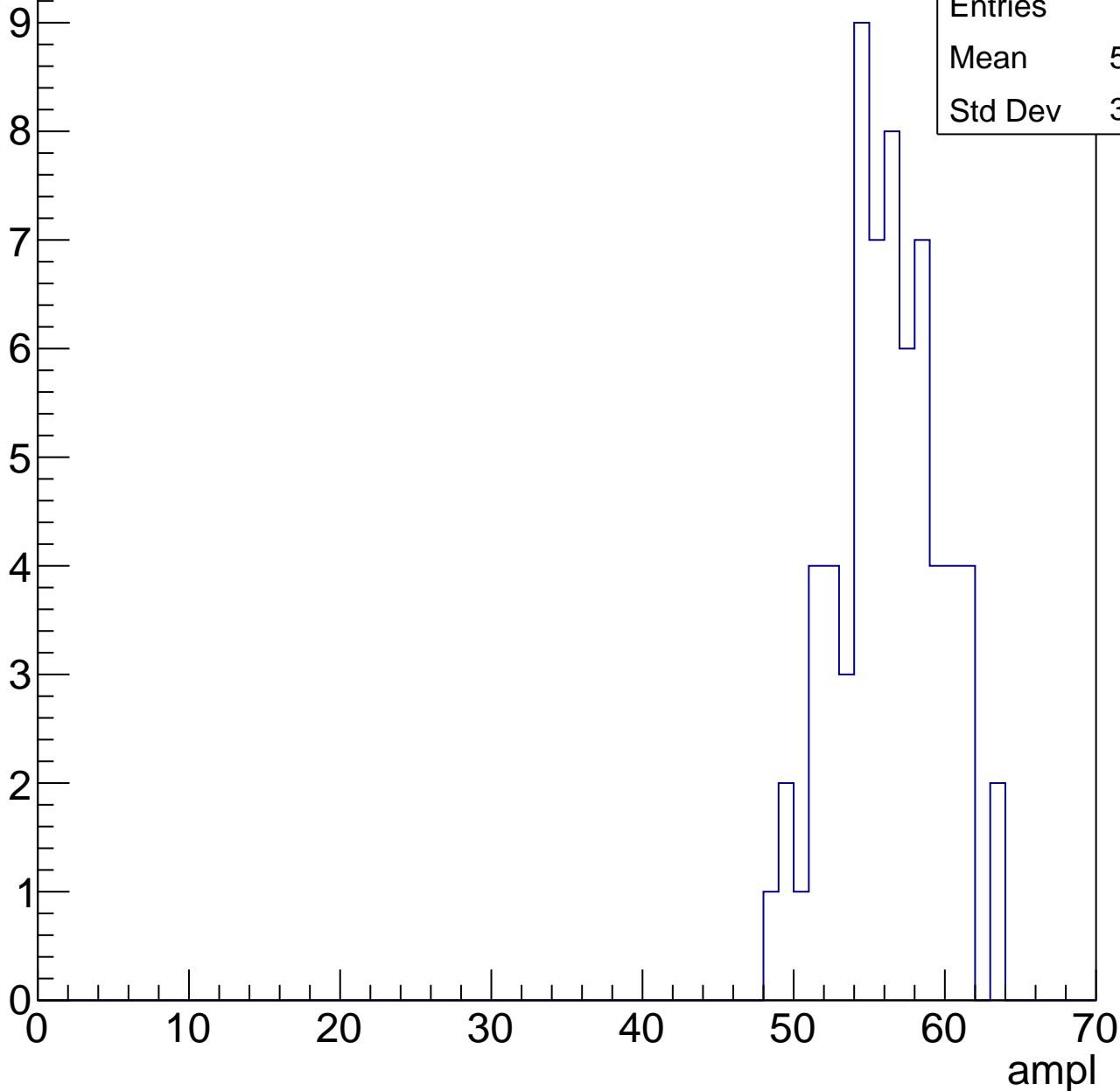


B0L102S, U20-ch79, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

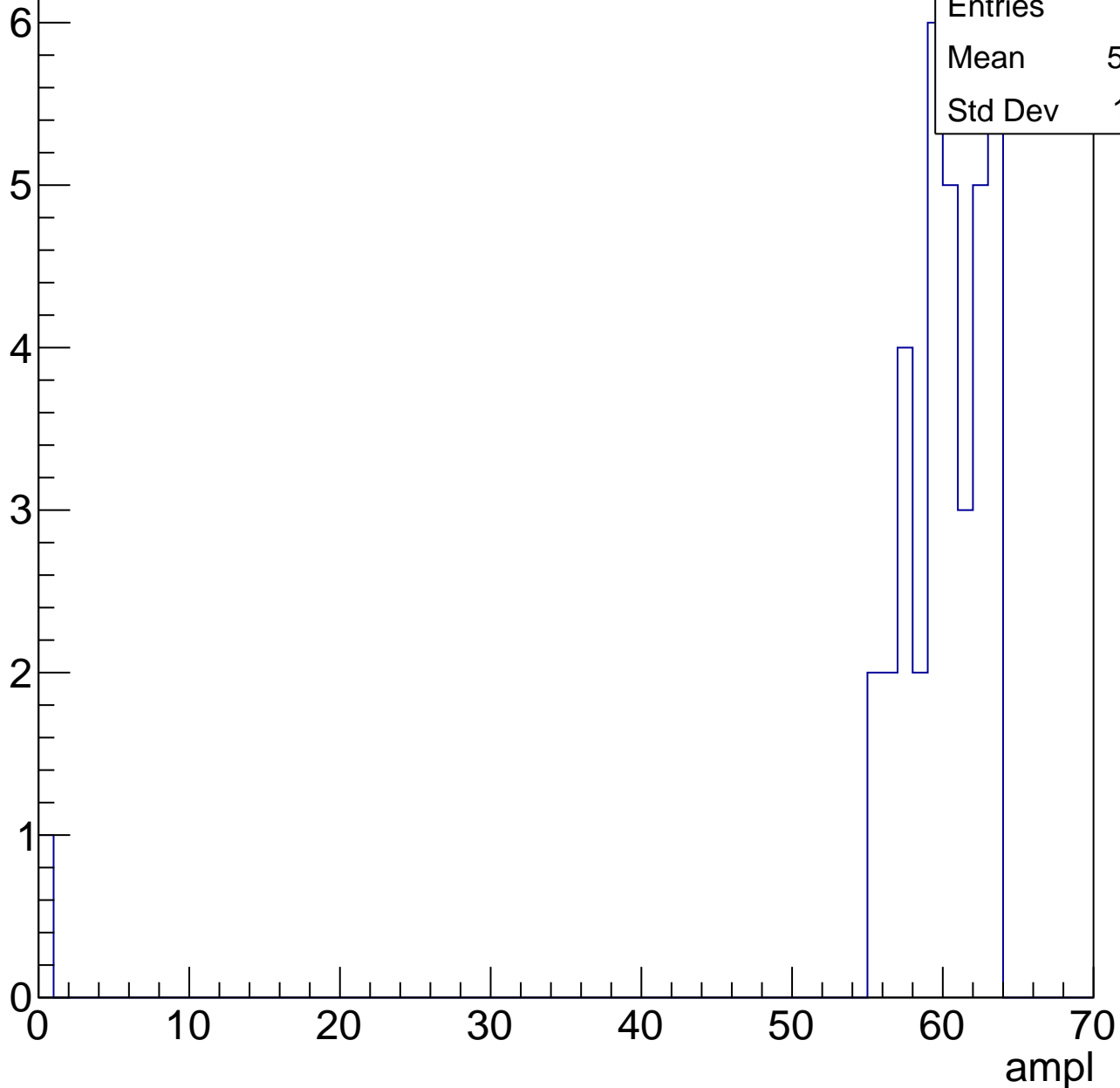
| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.76 |
| Std Dev | 3.394 |



B0L102S, U20-ch79, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

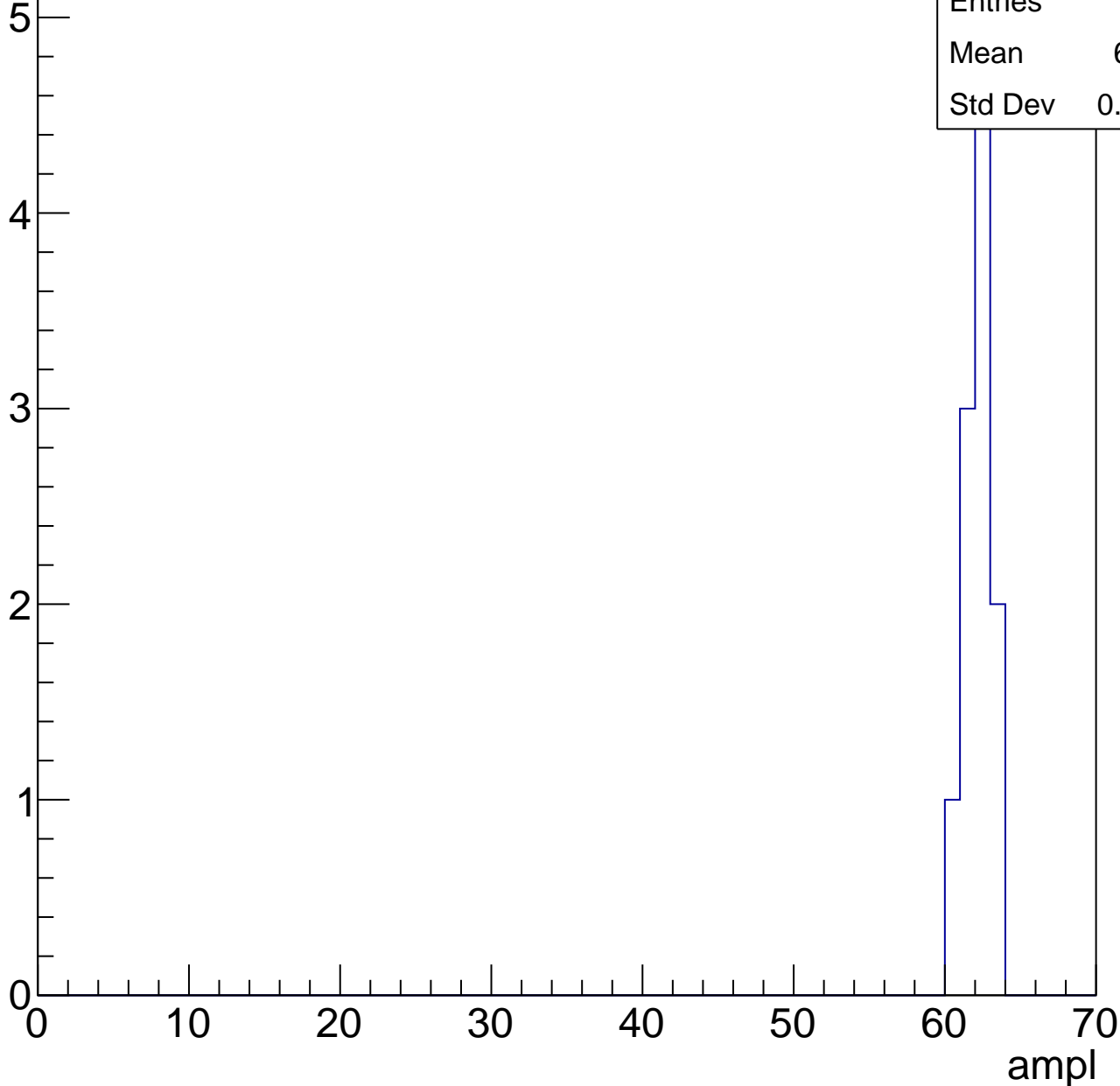


B0L102S, U20-ch79, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|--------|
| Entries | 11 |
| Mean | 61.73 |
| Std Dev | 0.8624 |



B0L102S, U20-ch79, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch80, adc0

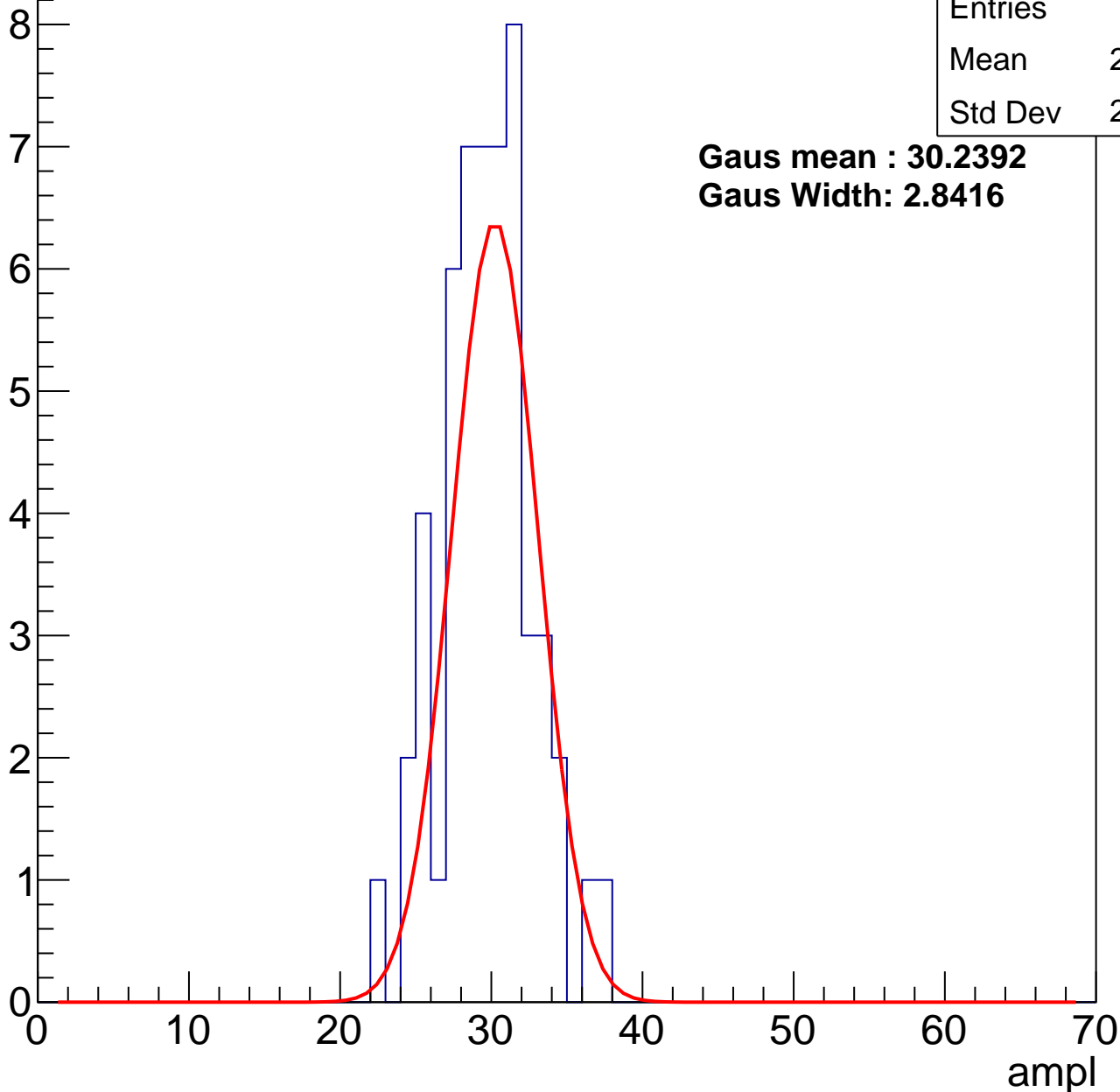
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.26 |
| Std Dev | 2.998 |

Gaus mean : 30.2392

Gaus Width: 2.8416



B0L102S, U20-ch80, adc1

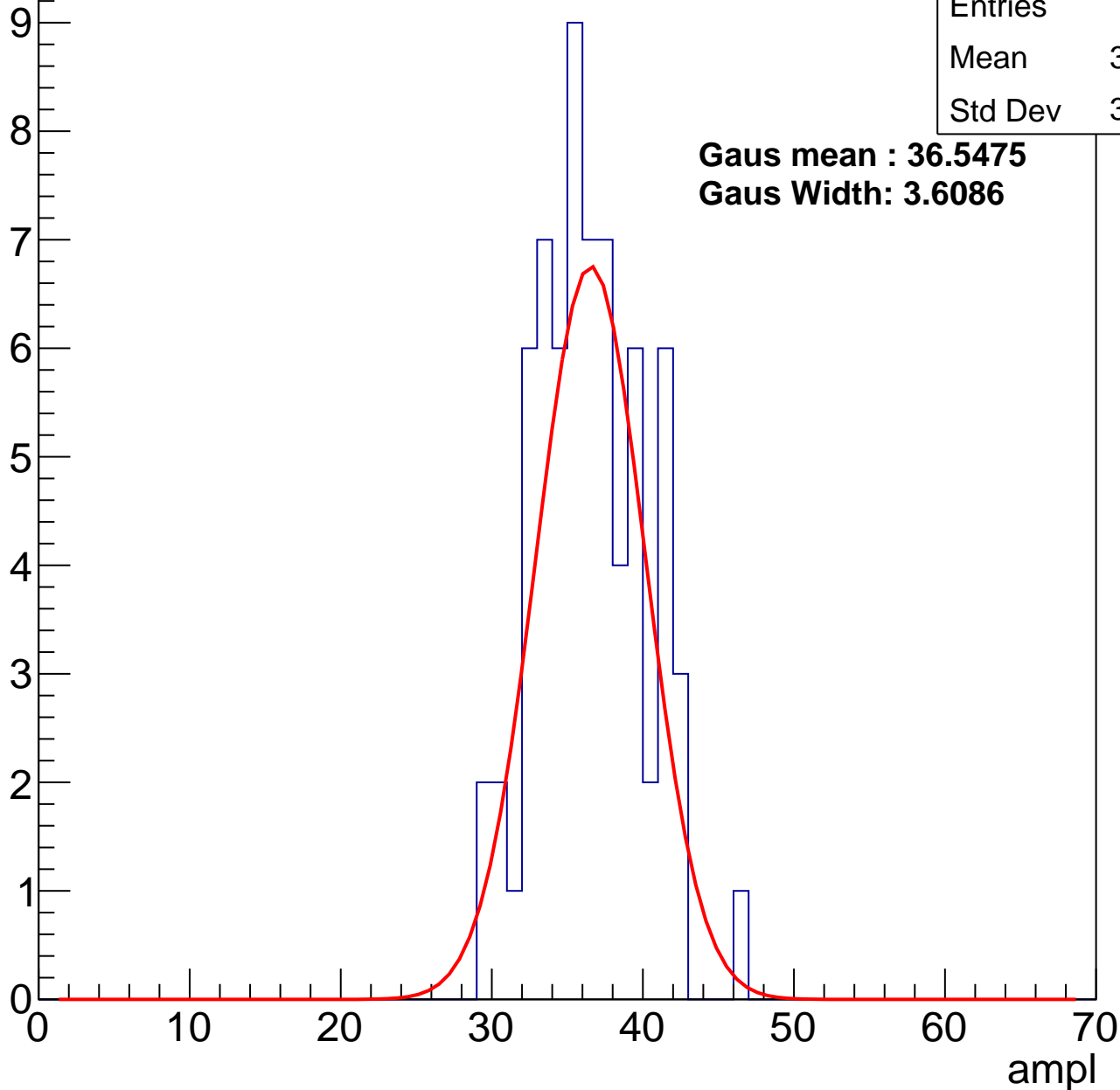
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.03 |
| Std Dev | 3.518 |

Gaus mean : 36.5475

Gaus Width: 3.6086



B0L102S, U20-ch80, adc2

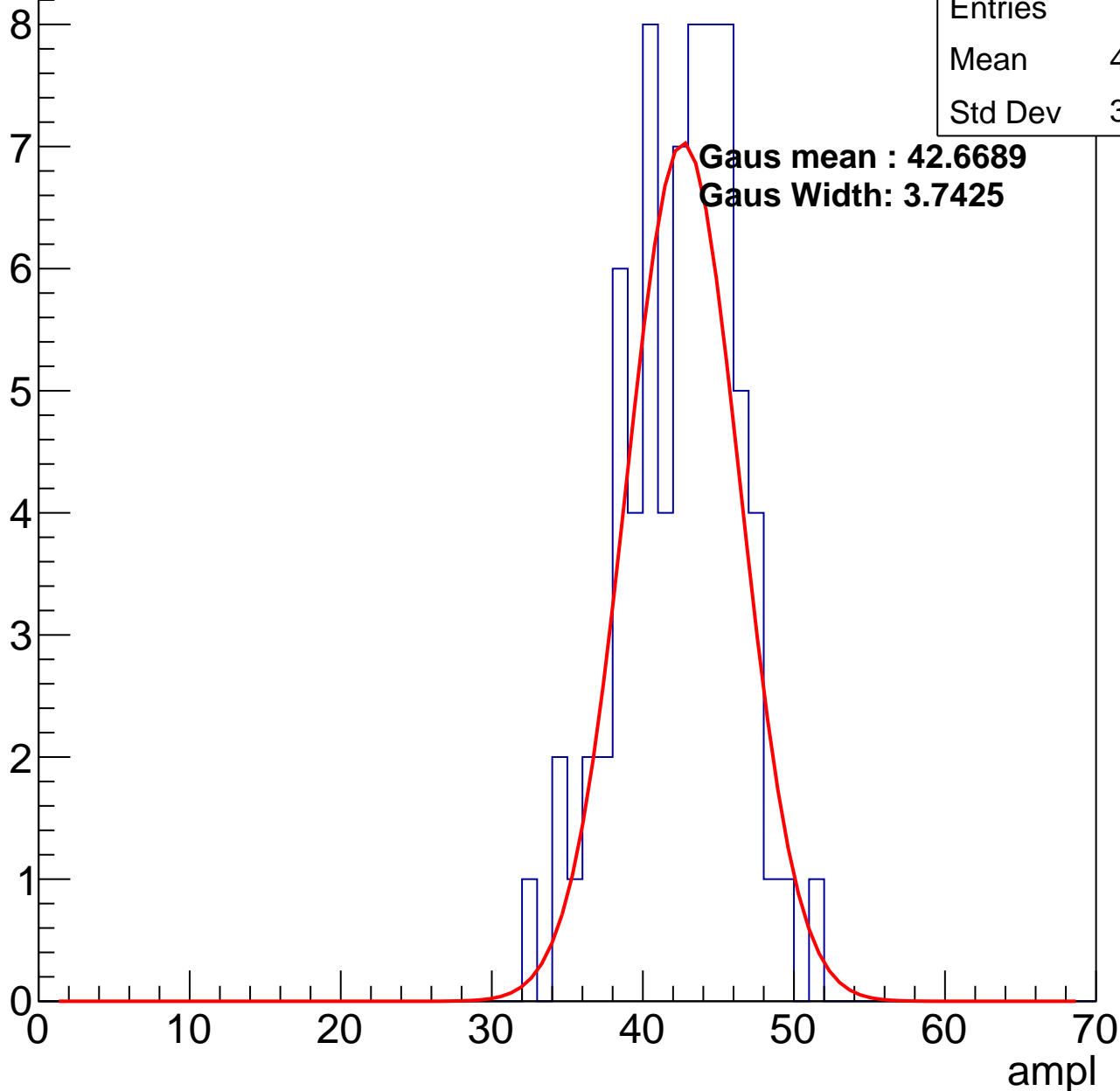
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 41.99 |
| Std Dev | 3.747 |

Gaus mean : 42.6689

Gaus Width: 3.7425

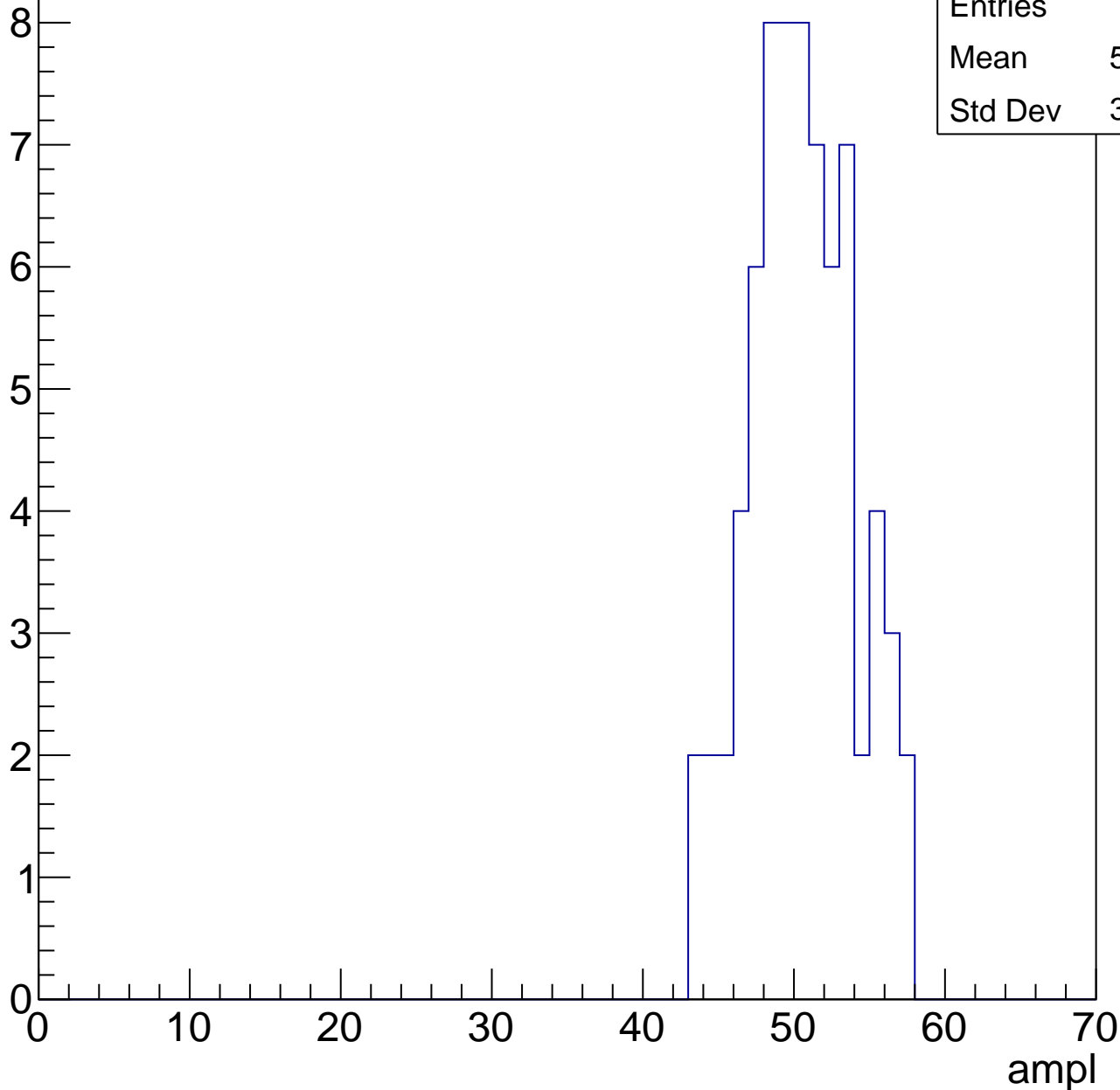


B0L102S, U20-ch80, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 50.08 |
| Std Dev | 3.377 |

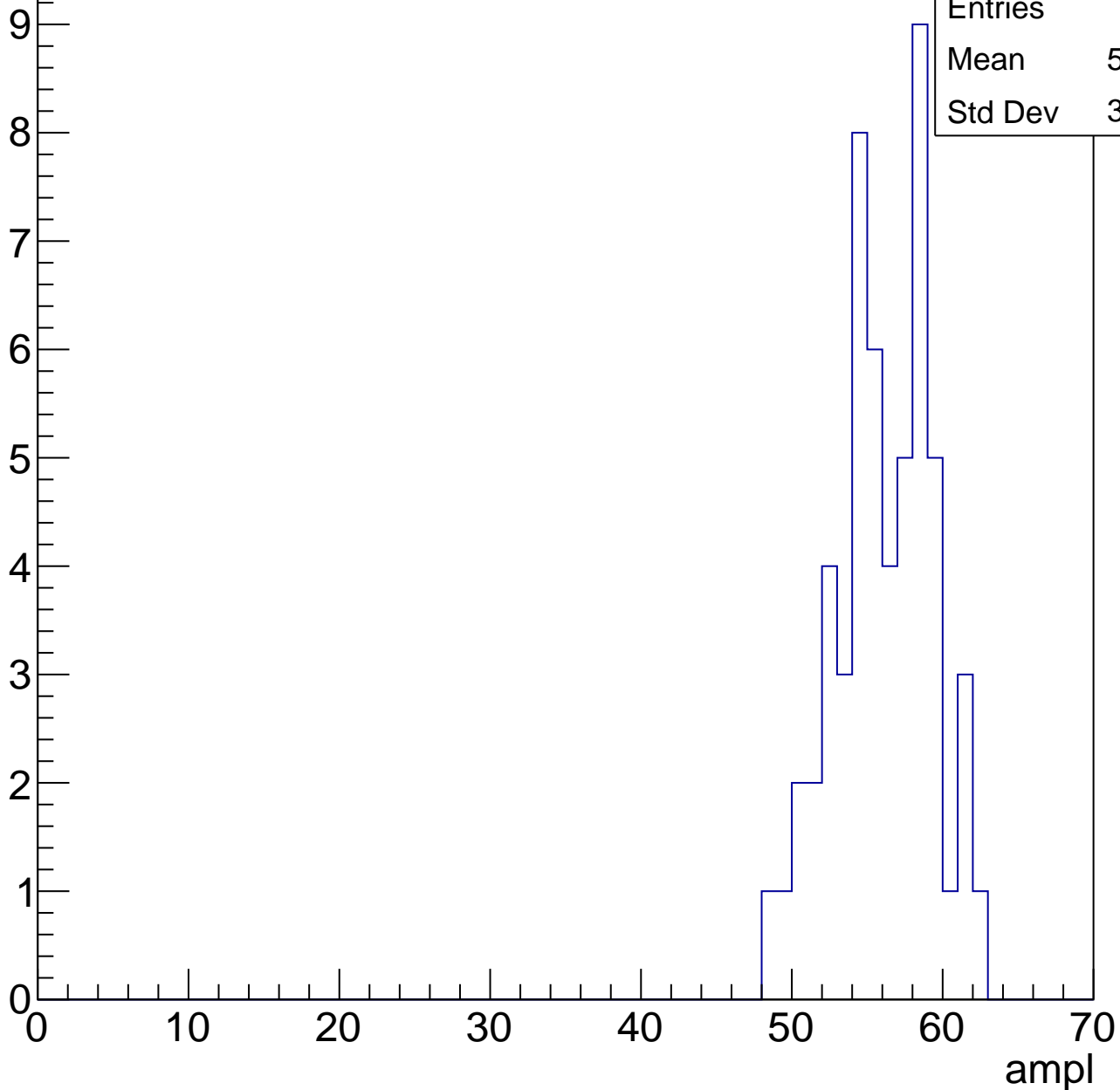


B0L102S, U20-ch80, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

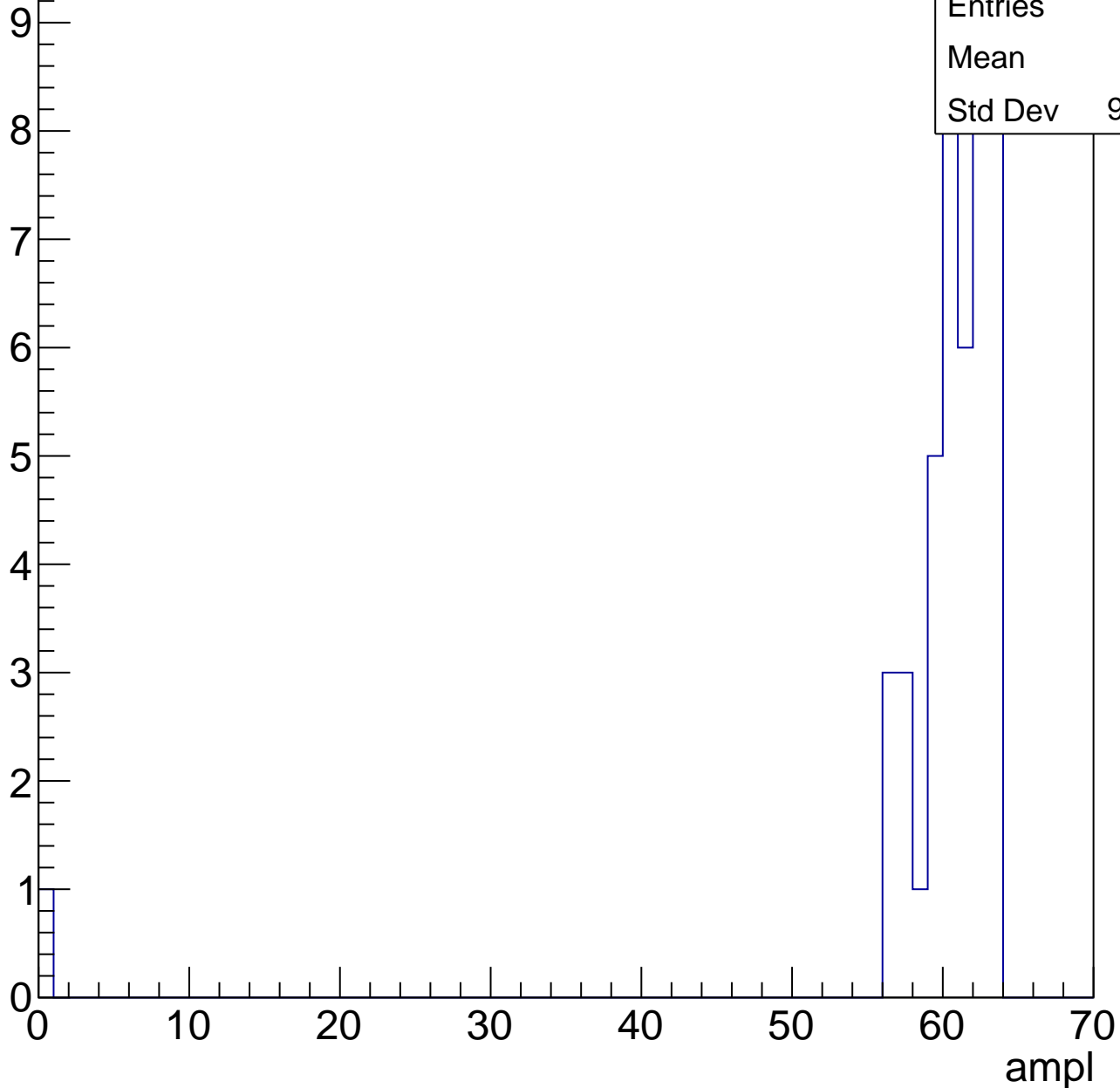
| | |
|---------|-------|
| Entries | 55 |
| Mean | 55.62 |
| Std Dev | 3.216 |



B0L102S, U20-ch80, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch80, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch80, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch81, adc0

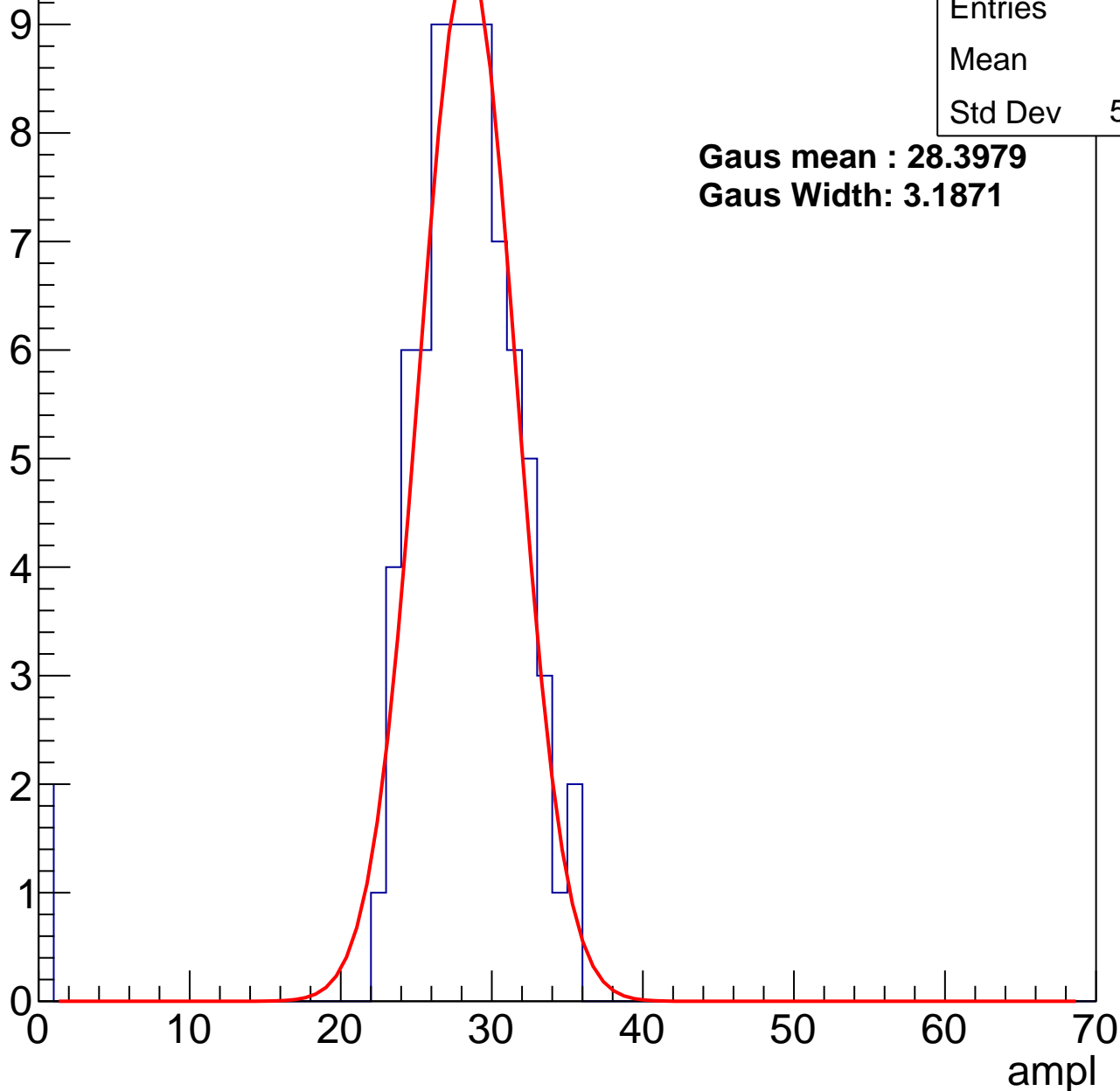
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 27.3 |
| Std Dev | 5.326 |

Gaus mean : 28.3979

Gaus Width: 3.1871



B0L102S, U20-ch81, adc1

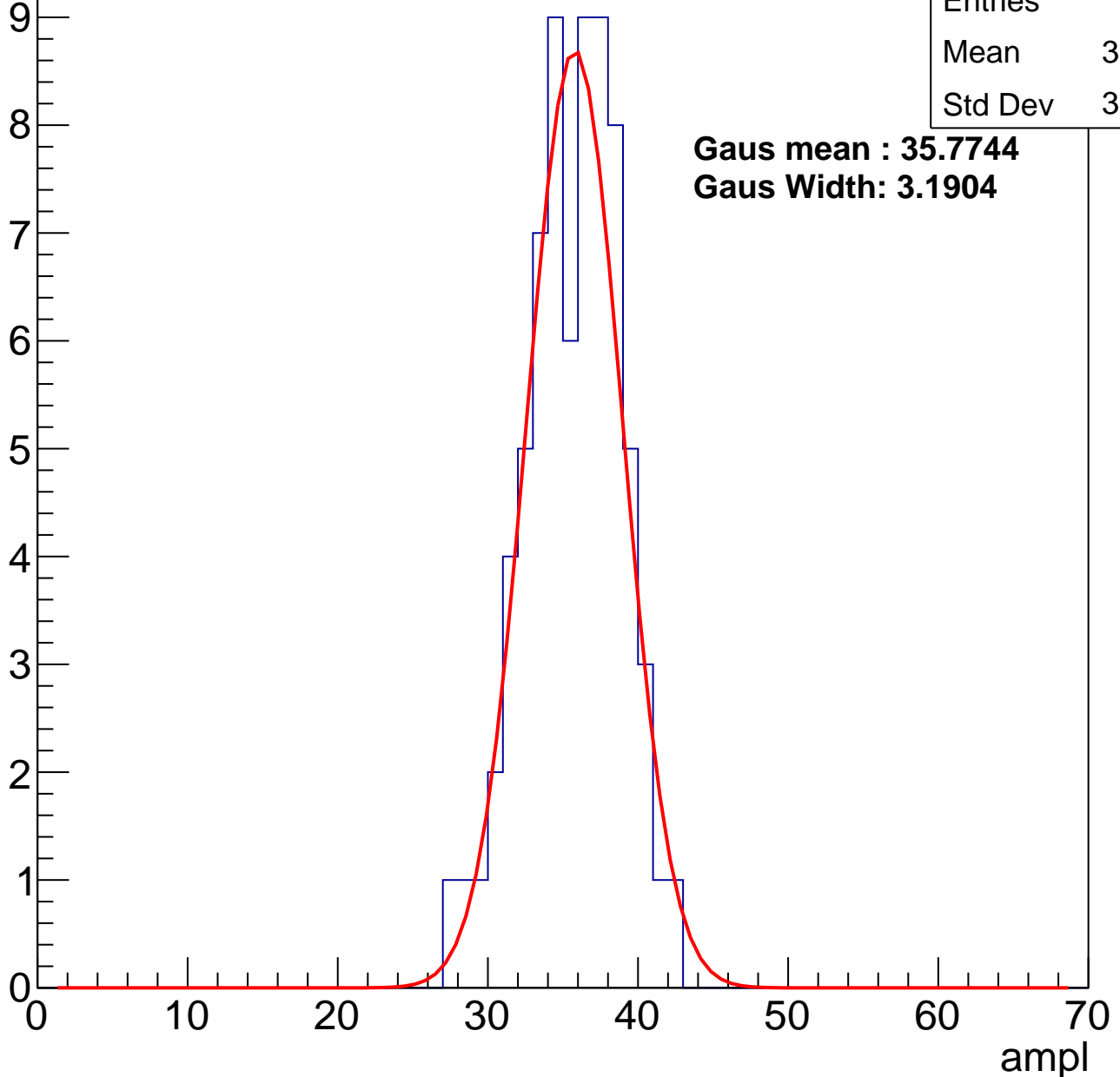
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.19 |
| Std Dev | 3.116 |

Gaus mean : 35.7744

Gaus Width: 3.1904



B0L102S, U20-ch81, adc2

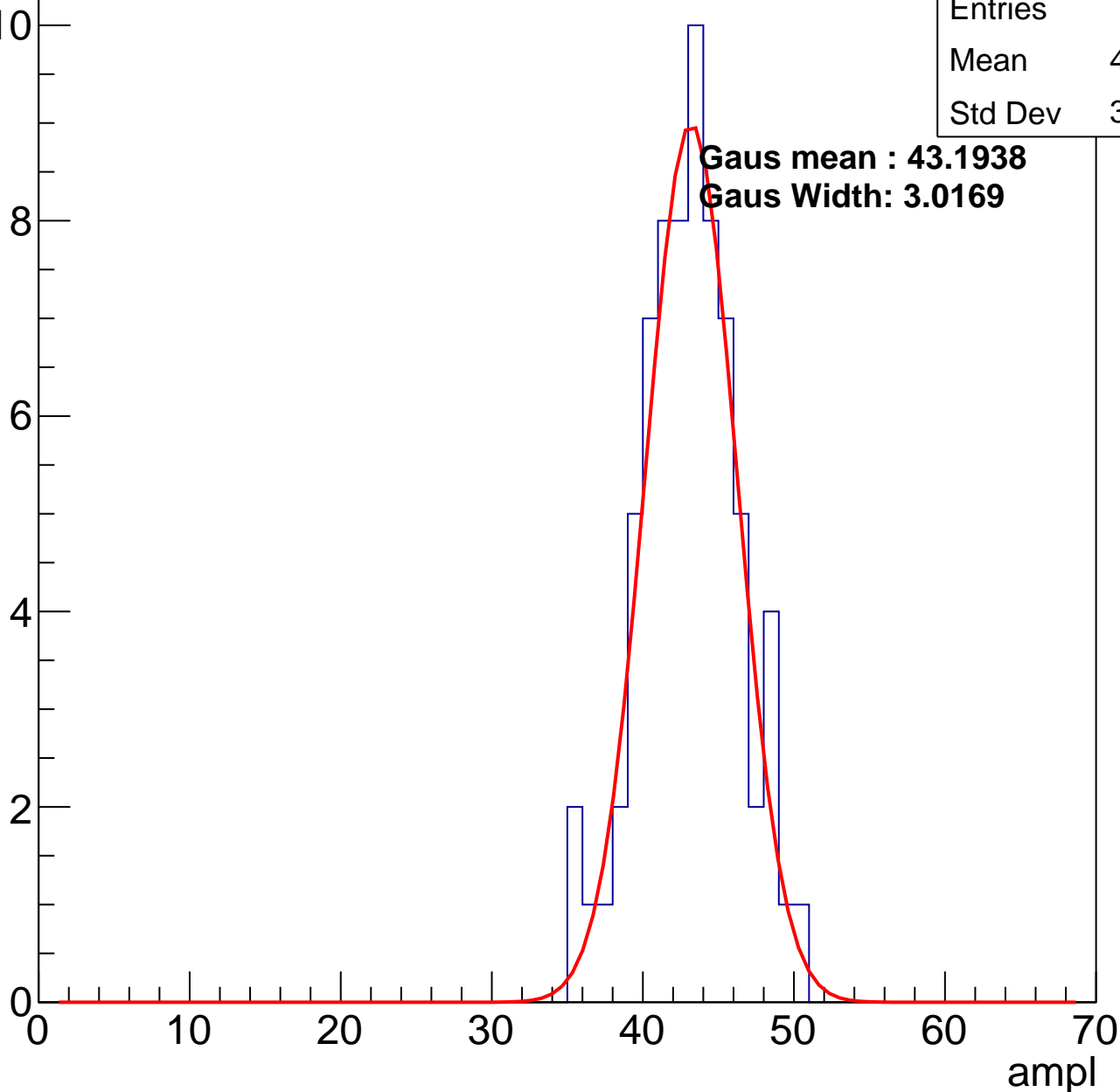
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 42.64 |
| Std Dev | 3.194 |

Gaus mean : 43.1938

Gaus Width: 3.0169

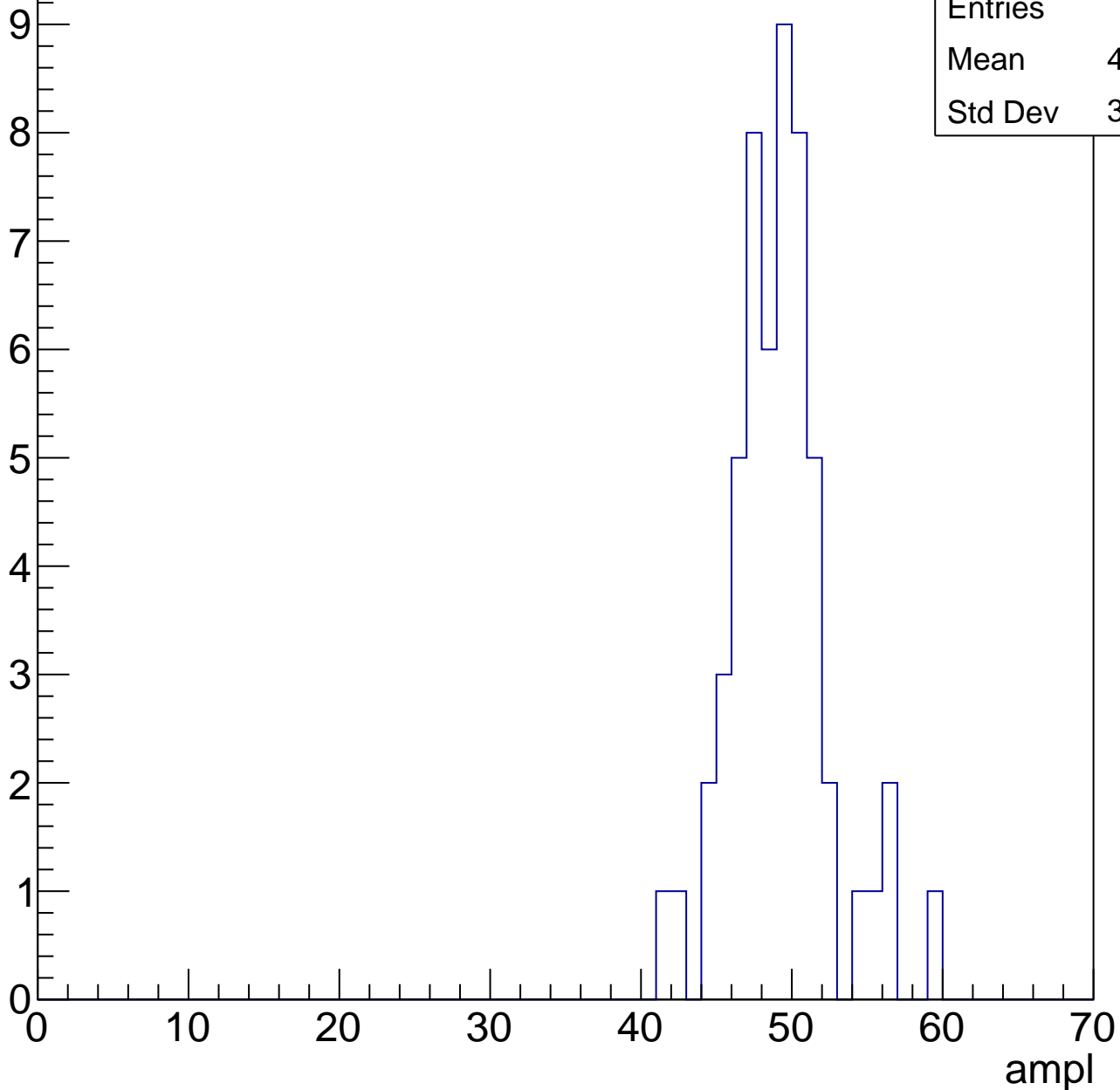


B0L102S, U20-ch81, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

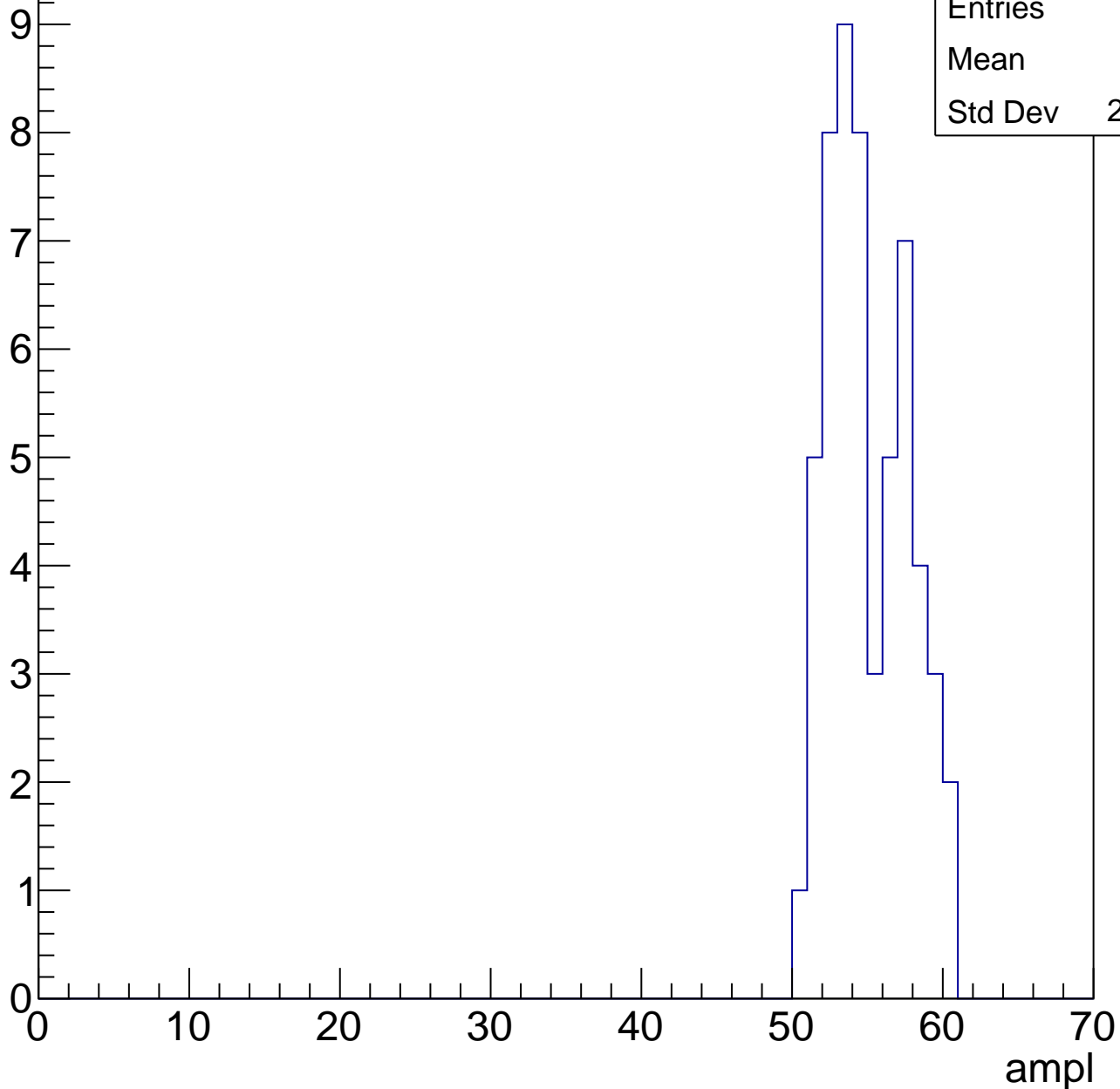
| | |
|---------|-------|
| Entries | 55 |
| Mean | 48.73 |
| Std Dev | 3.289 |



B0L102S, U20-ch81, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



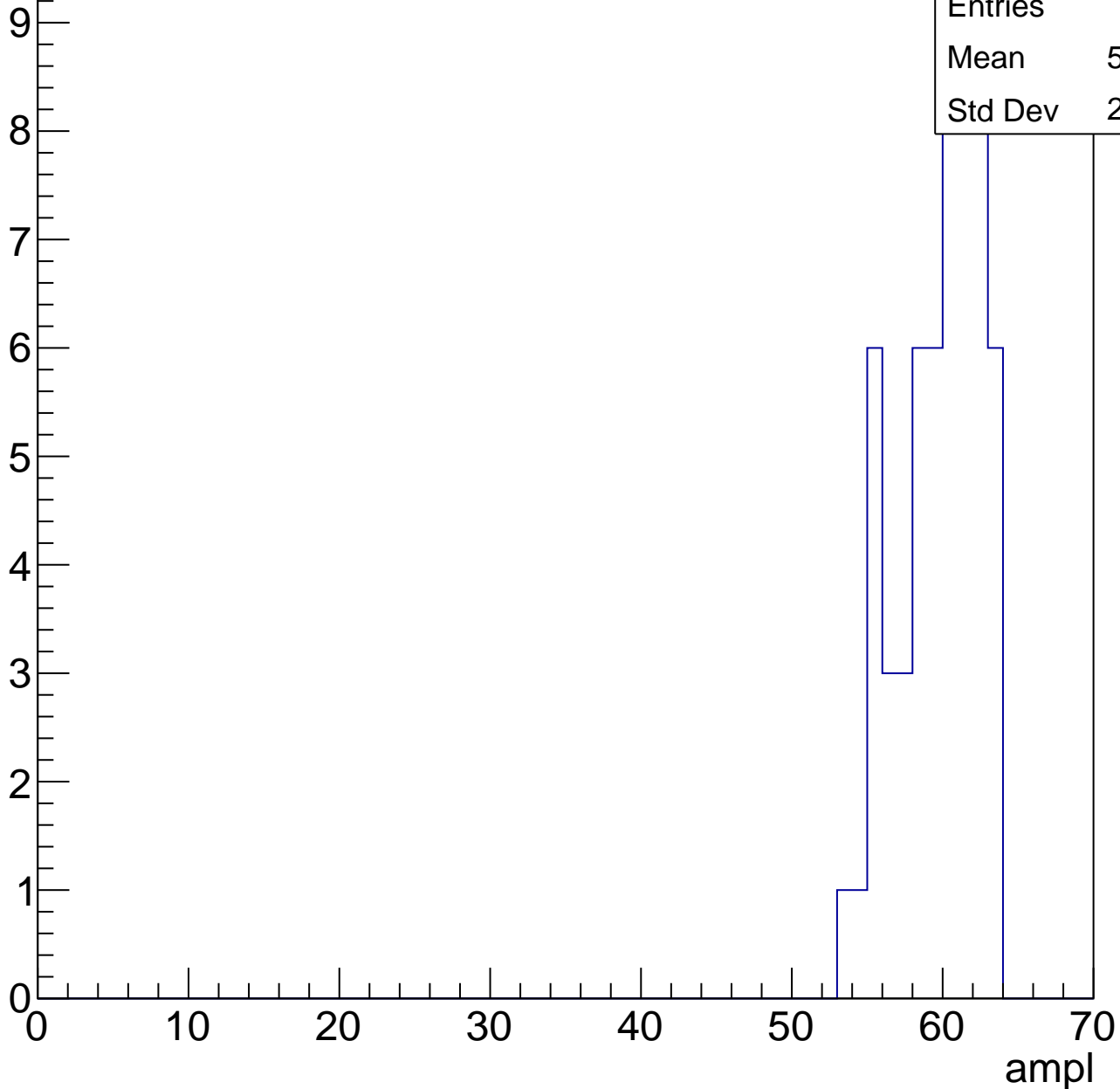
| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.6 |
| Std Dev | 2.626 |

B0L102S, U20-ch81, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 59.32 |
| Std Dev | 2.676 |



B0L102S, U20-ch81, adc6

calib_packv5_042523_0143.root, FC#12, port B1

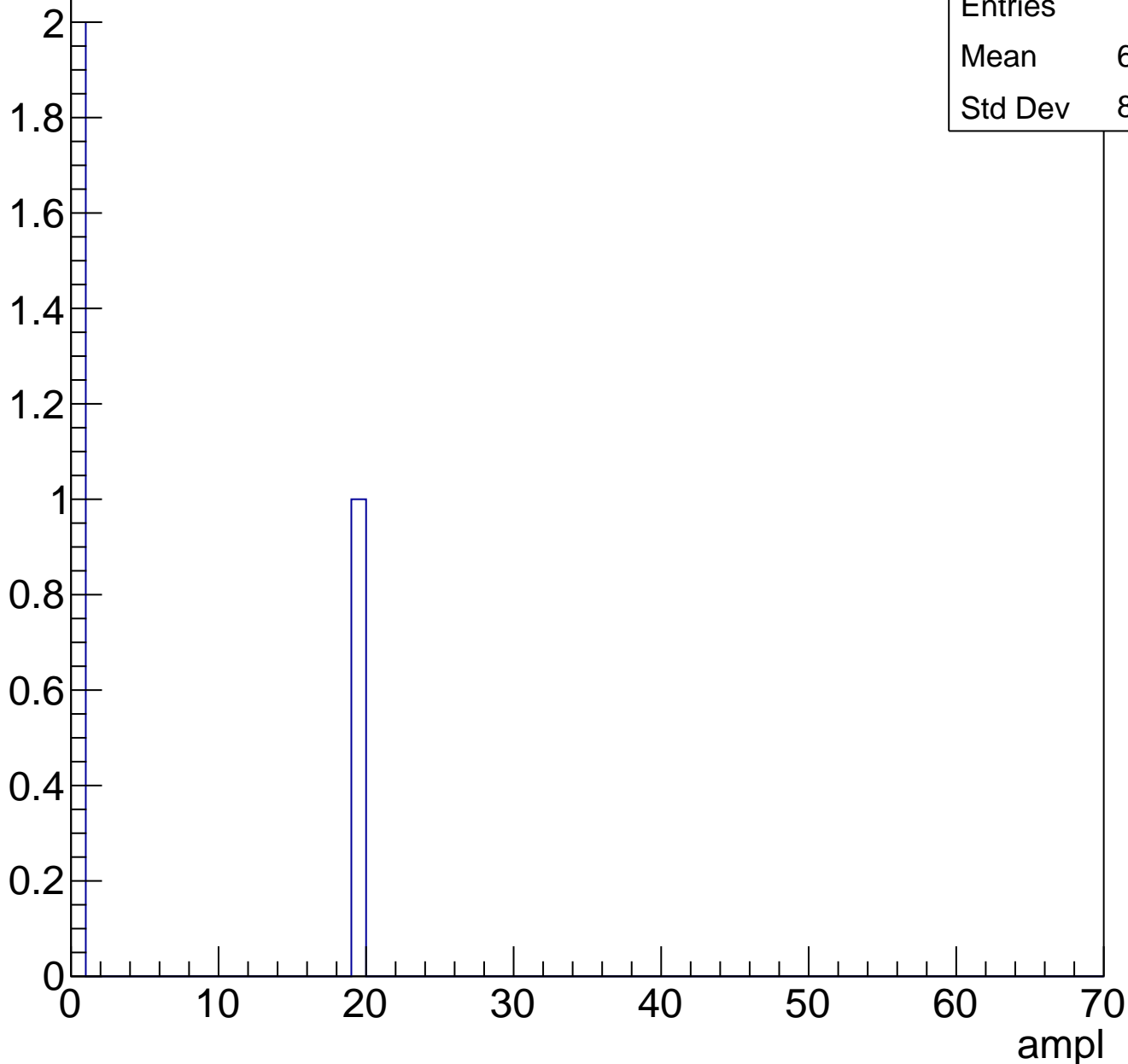
Entry



B0L102S, U20-ch81, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

B0L102S, U20-ch82, adc0

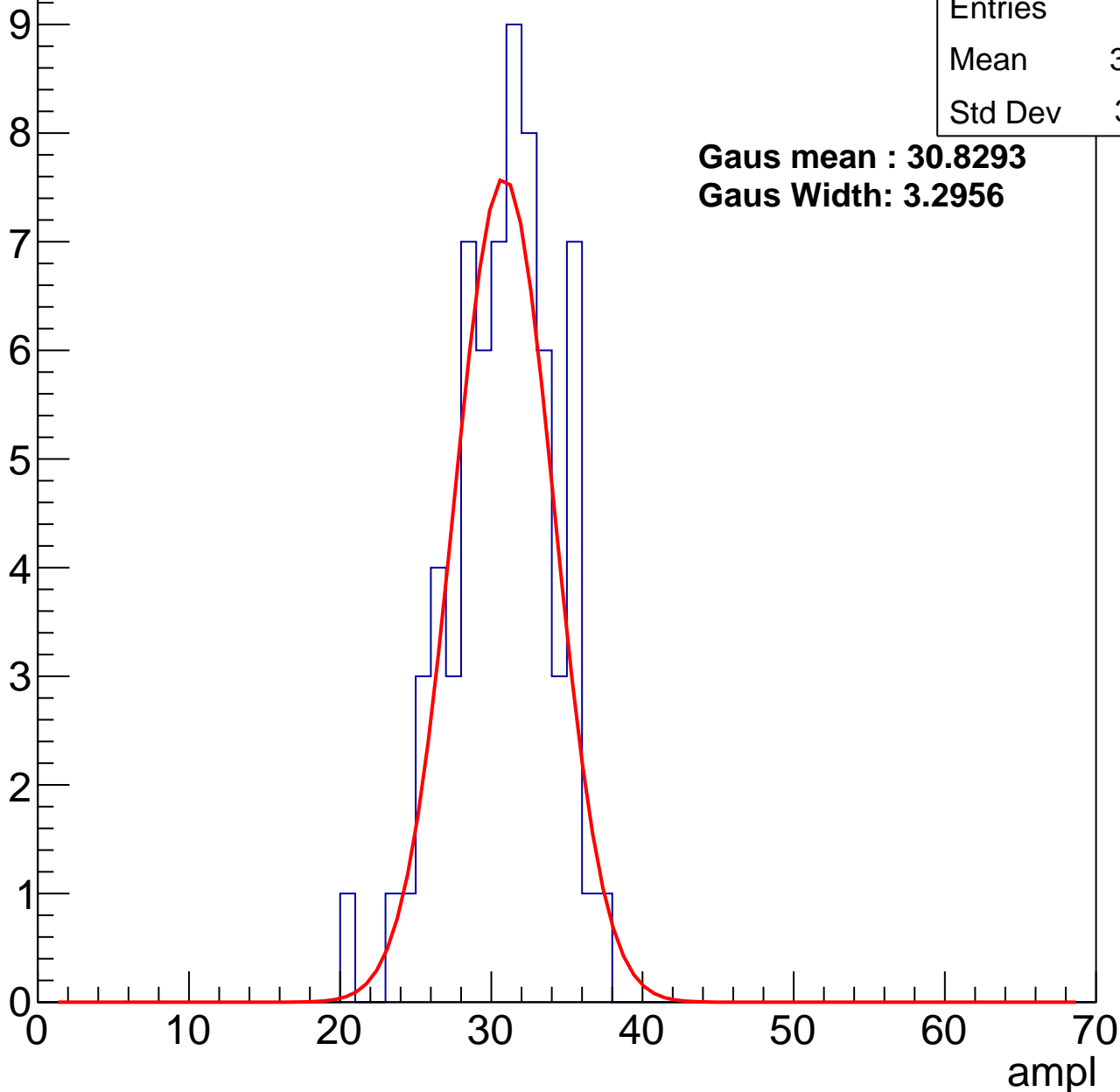
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 30.29 |
| Std Dev | 3.391 |

Gaus mean : 30.8293

Gaus Width: 3.2956



B0L102S, U20-ch82, adc1

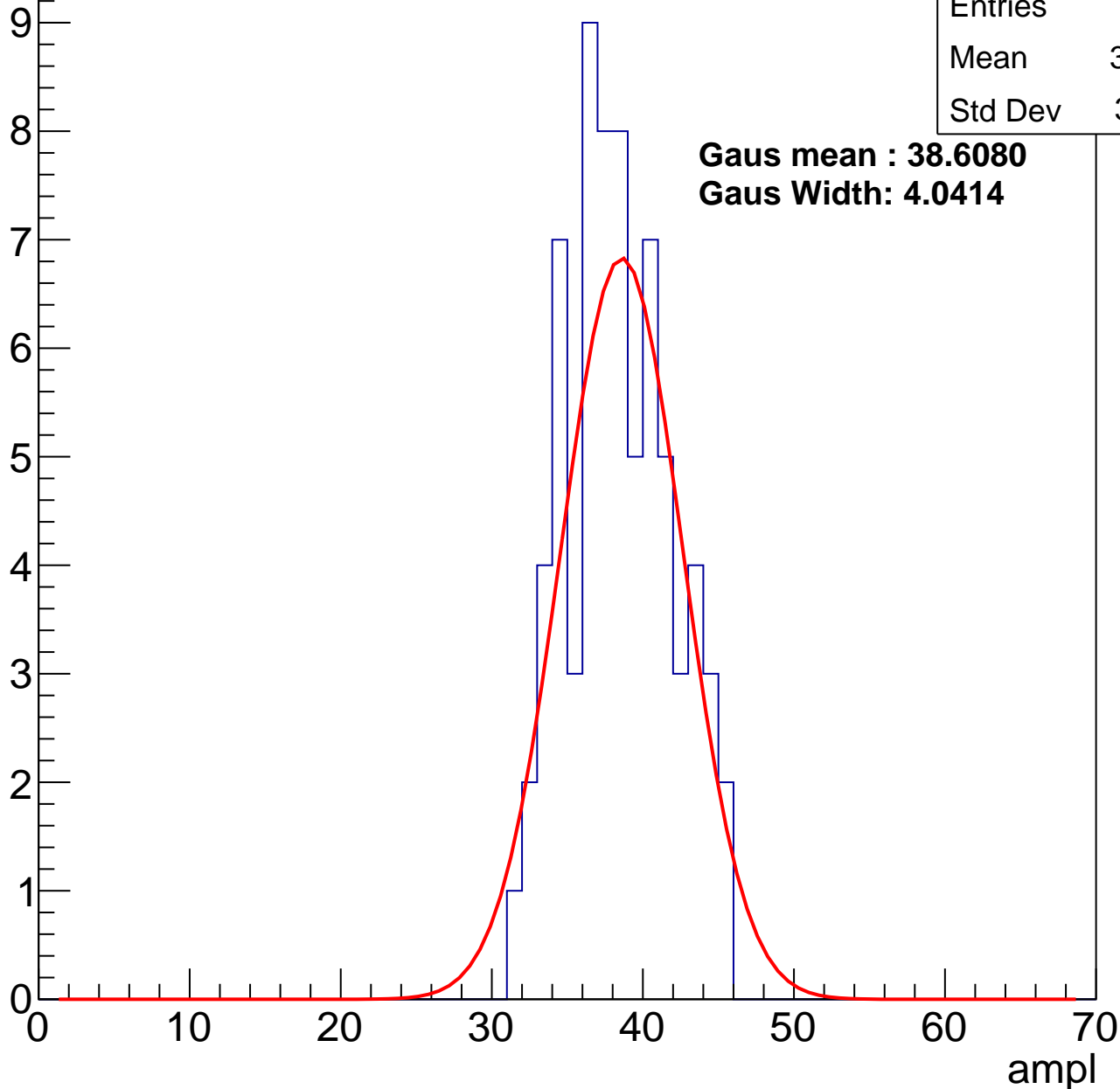
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.94 |
| Std Dev | 3.431 |

Gaus mean : 38.6080

Gaus Width: 4.0414



B0L102S, U20-ch82, adc2

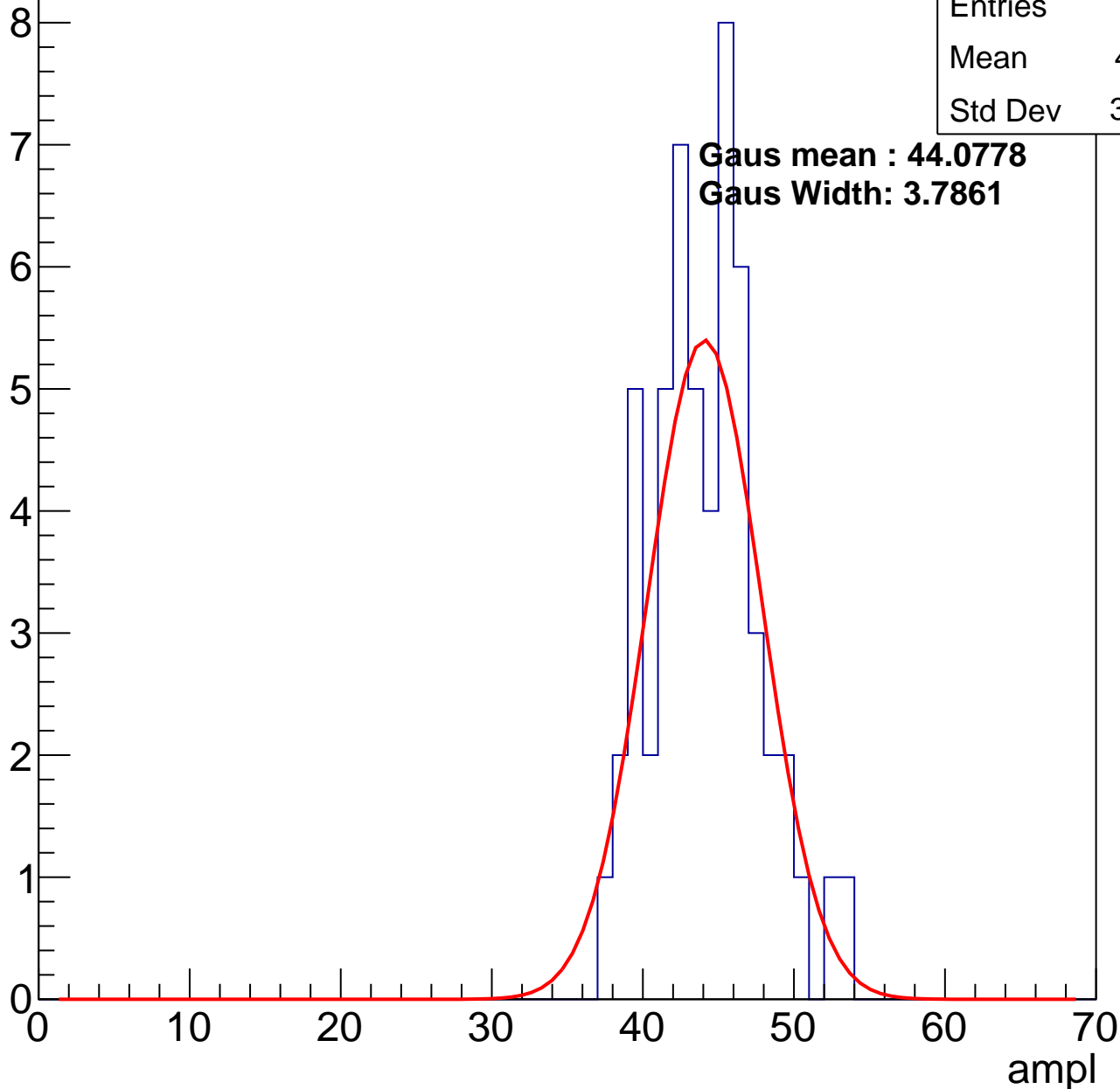
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.71 |
| Std Dev | 3.499 |

Gaus mean : 44.0778

Gaus Width: 3.7861

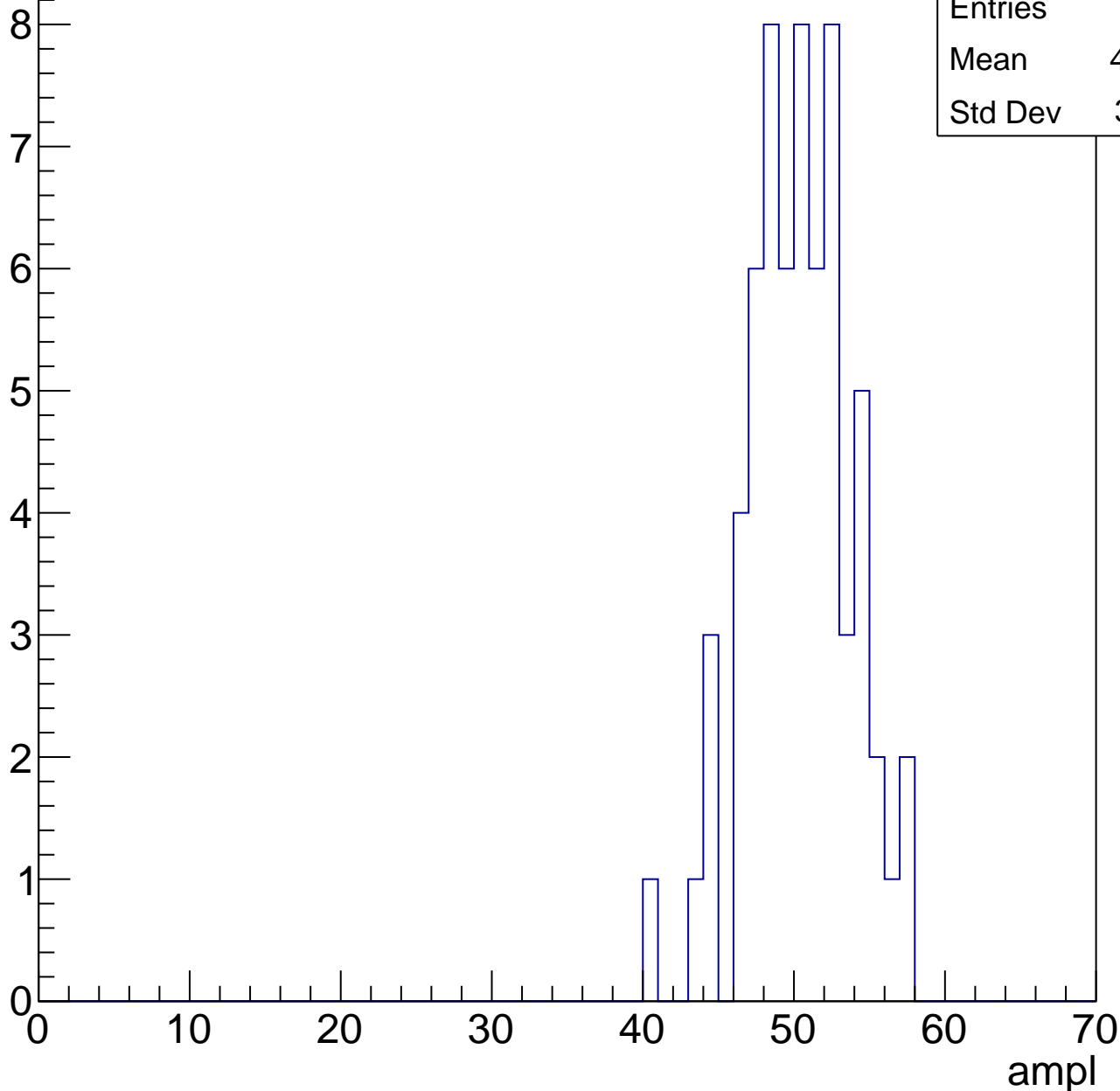


B0L102S, U20-ch82, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.84 |
| Std Dev | 3.401 |

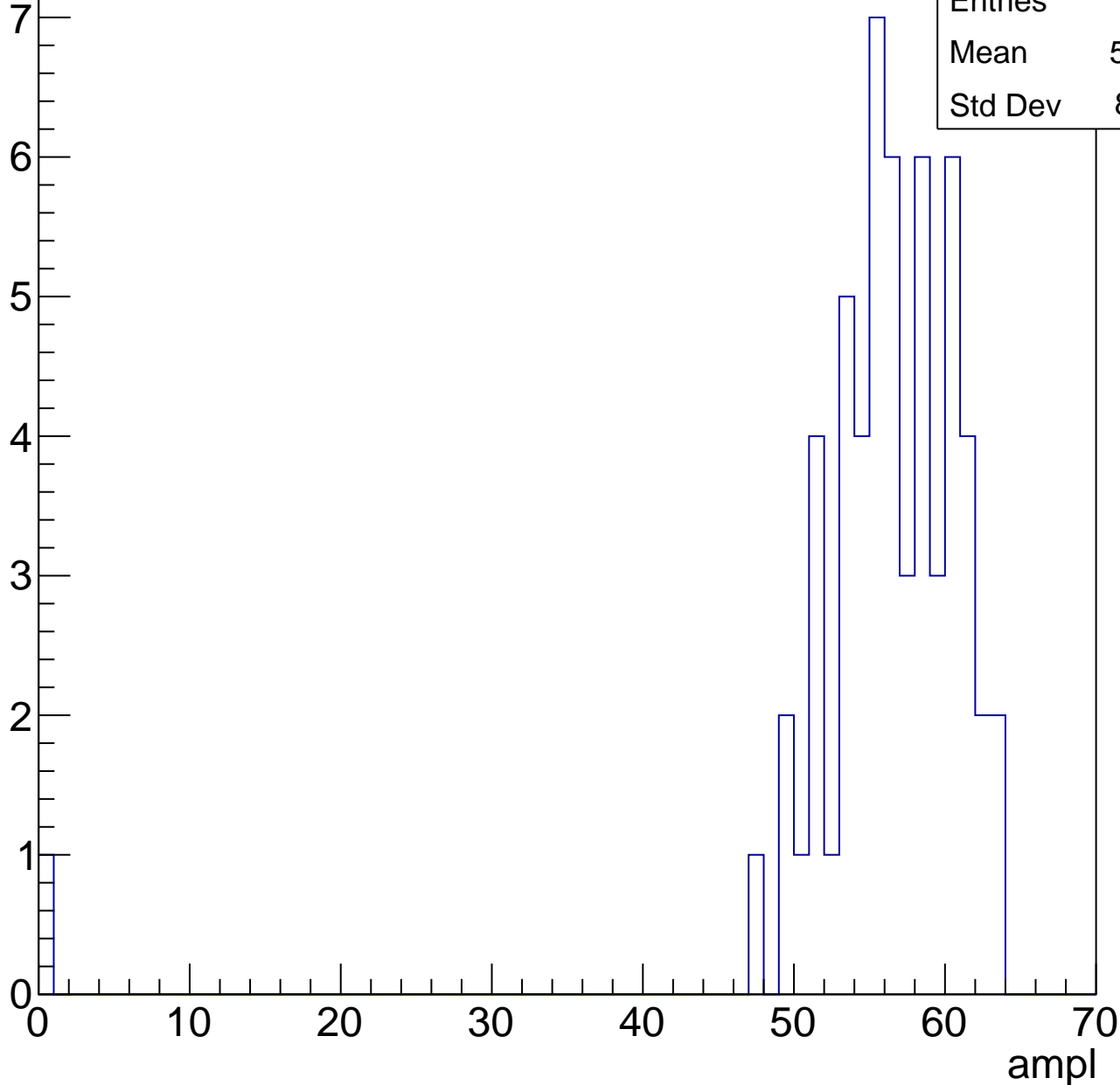


B0L102S, U20-ch82, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.22 |
| Std Dev | 8.221 |

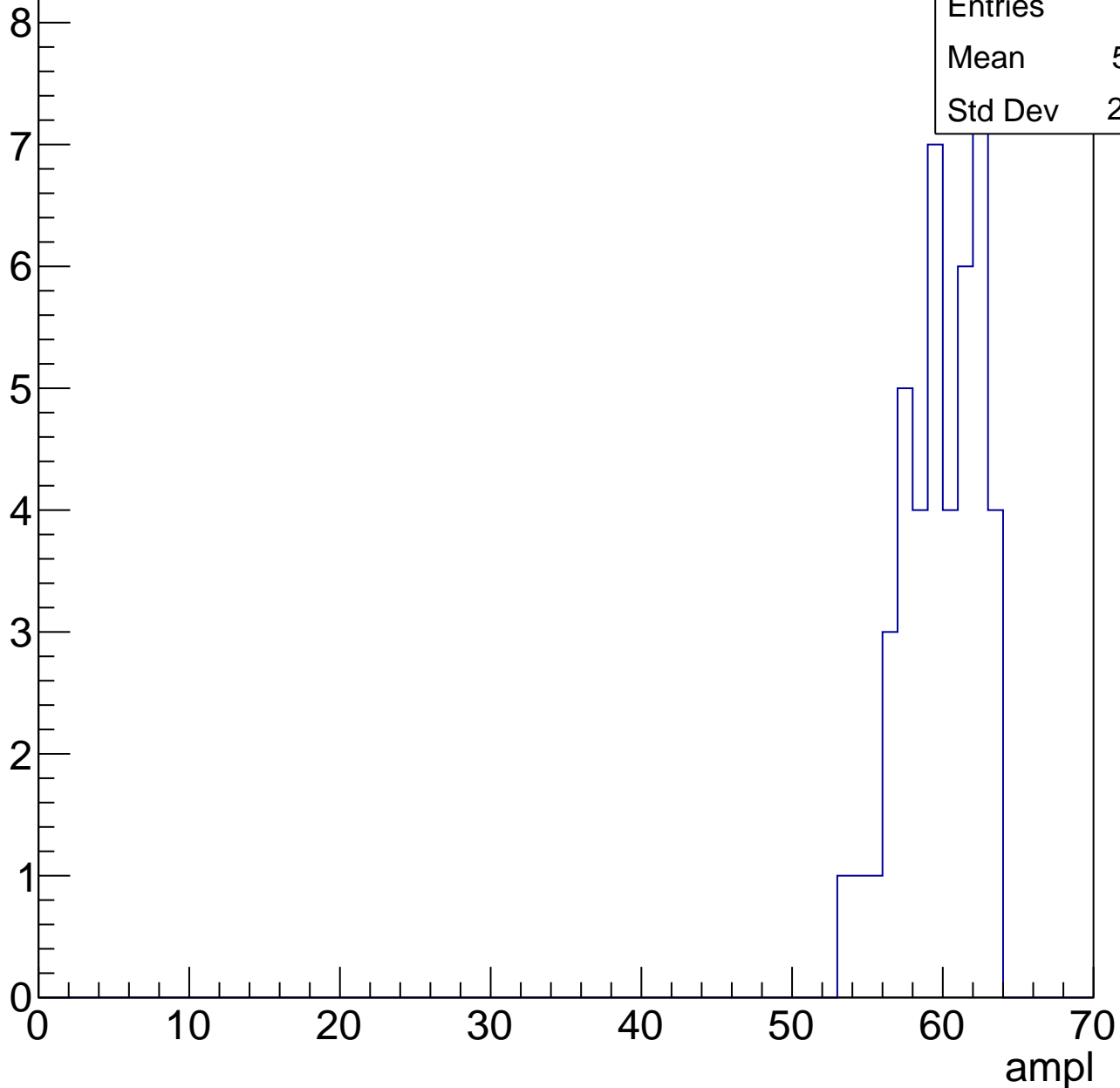


B0L102S, U20-ch82, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.41 |
| Std Dev | 2.543 |



B0L102S, U20-ch82, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch82, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch83, adc0

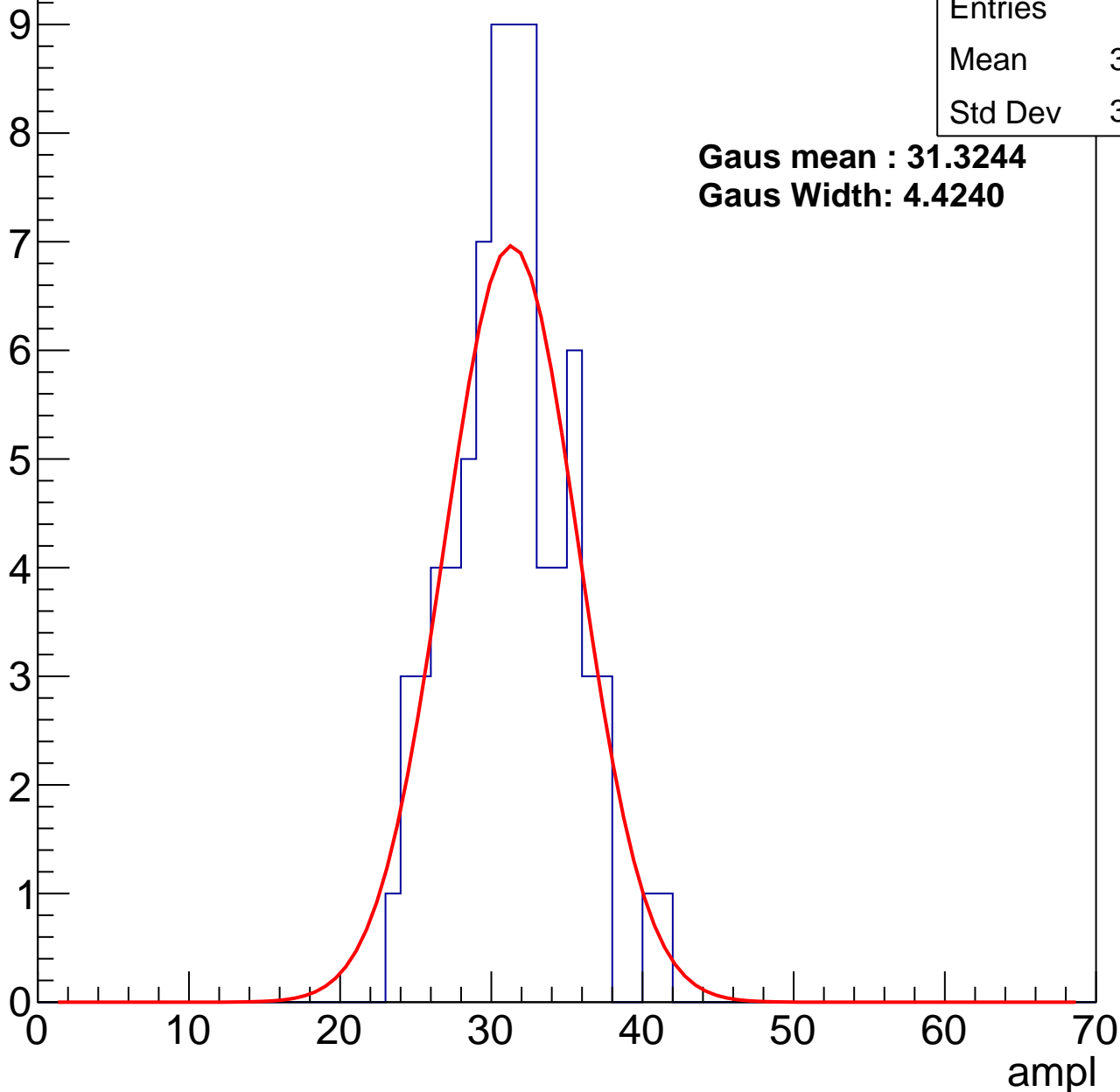
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.79 |
| Std Dev | 3.774 |

Gaus mean : 31.3244

Gaus Width: 4.4240



B0L102S, U20-ch83, adc1

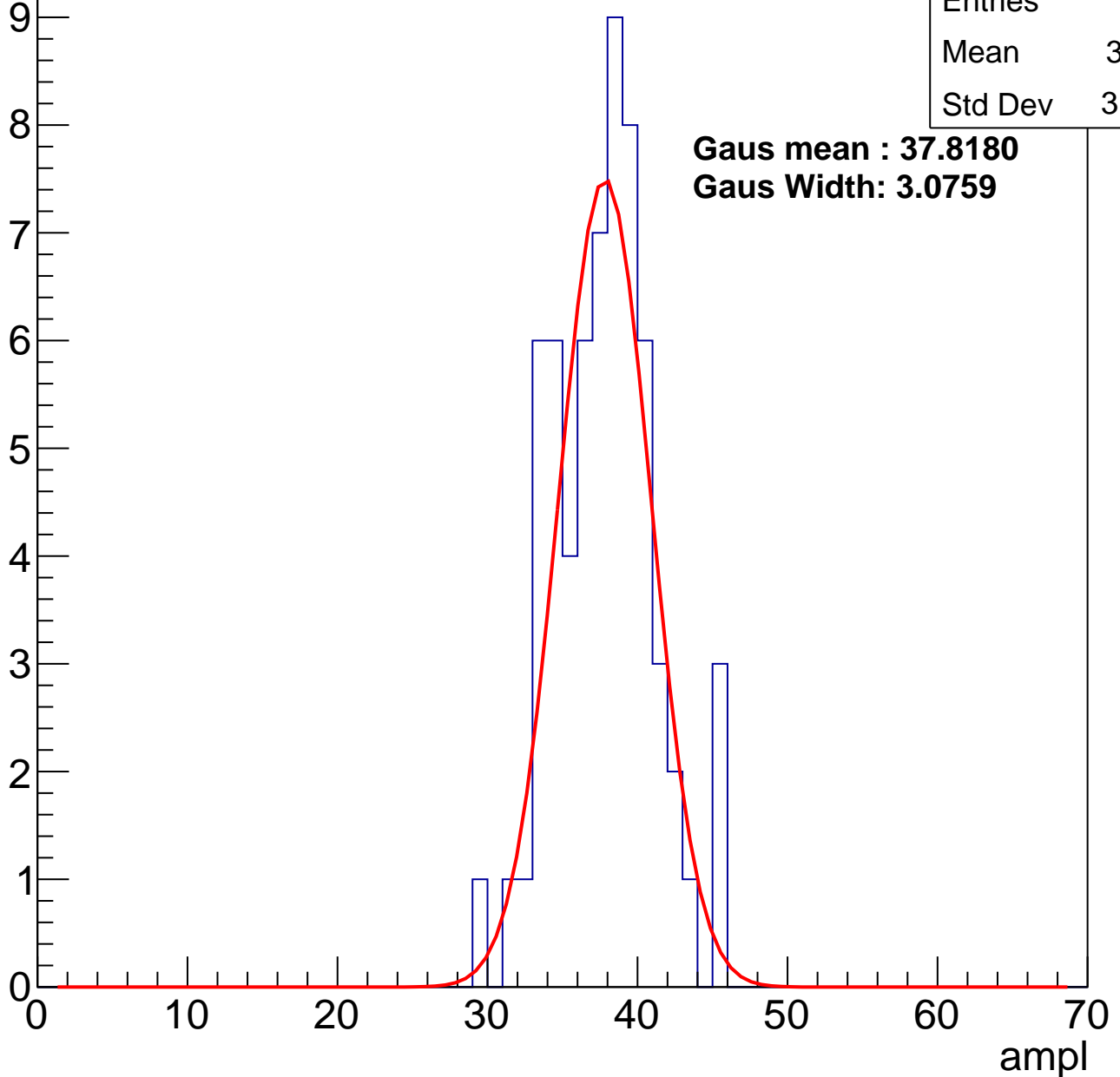
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37.31 |
| Std Dev | 3.335 |

Gaus mean : 37.8180

Gaus Width: 3.0759



B0L102S, U20-ch83, adc2

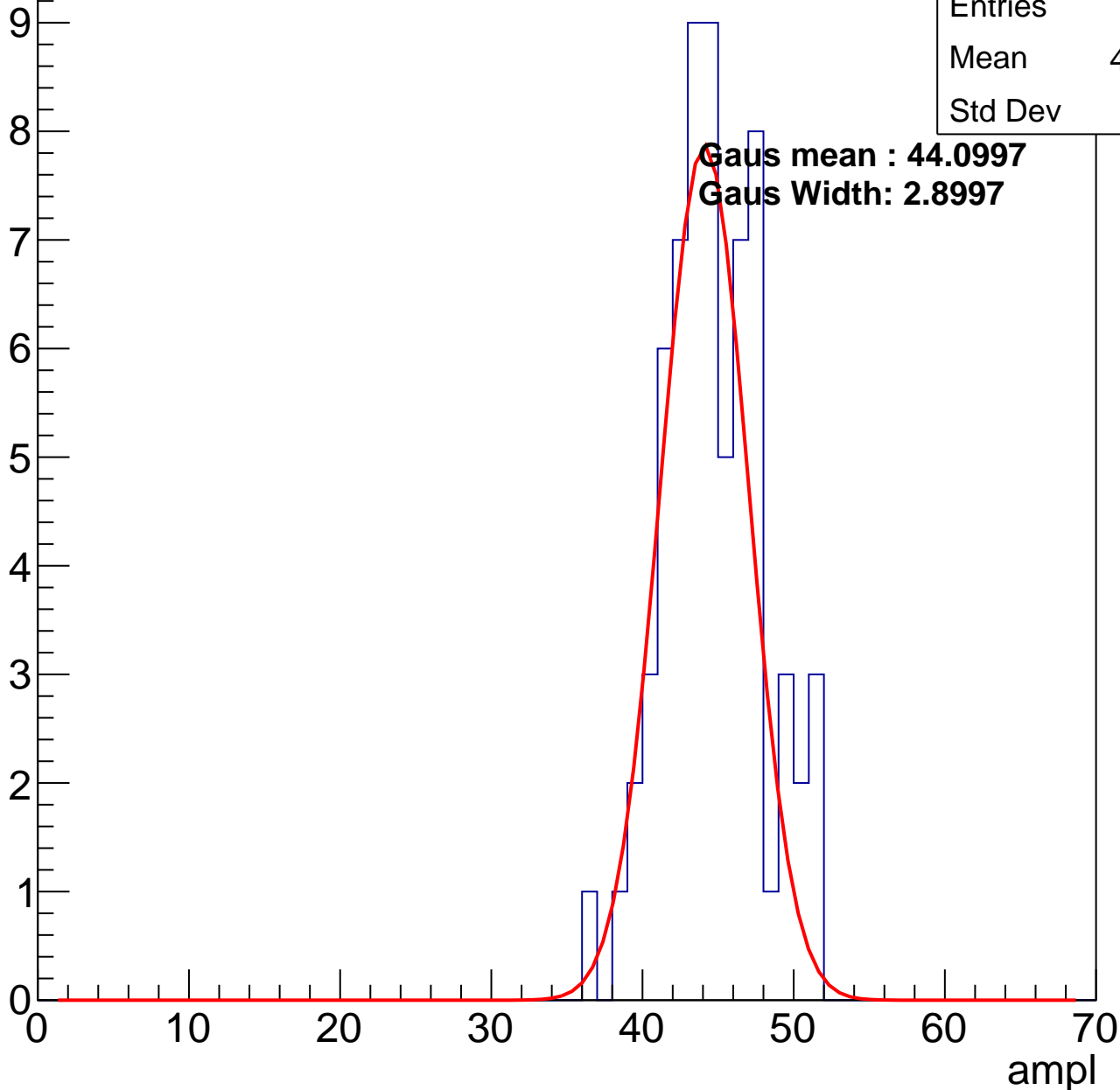
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.27 |
| Std Dev | 3.23 |

Gaus mean : 44.0997

Gaus Width: 2.8997

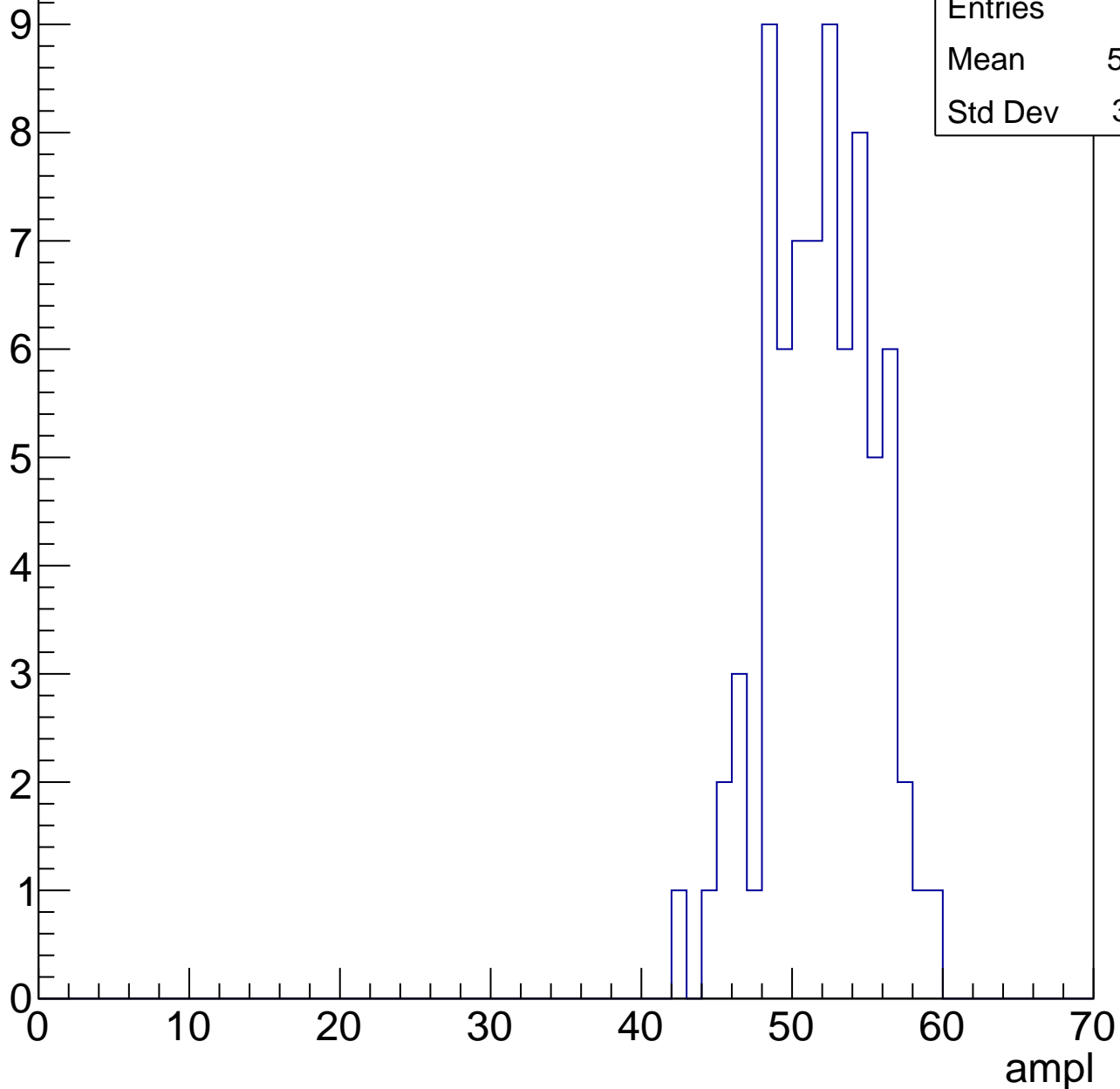


B0L102S, U20-ch83, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

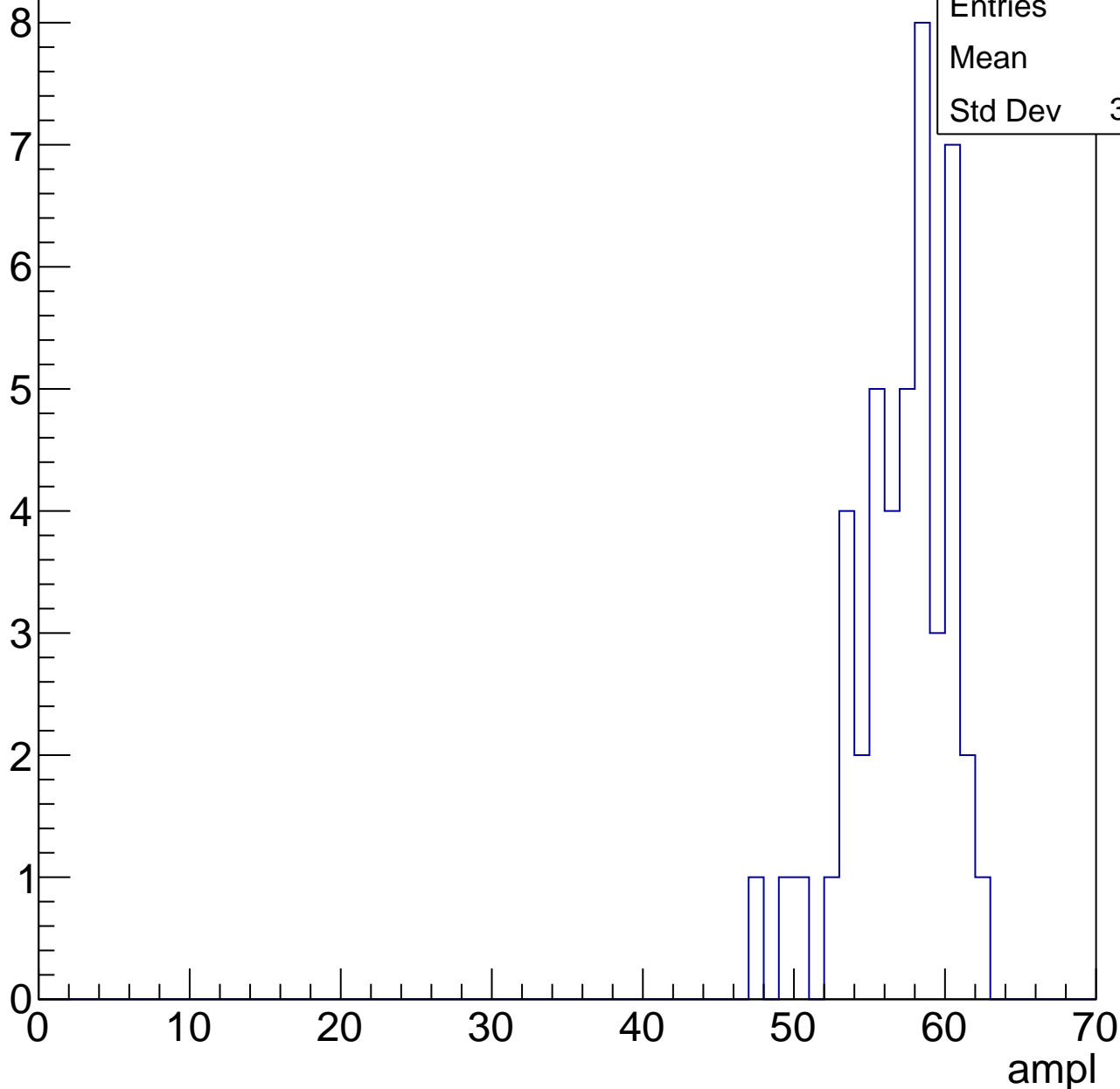
| | |
|---------|-------|
| Entries | 75 |
| Mean | 51.39 |
| Std Dev | 3.491 |



B0L102S, U20-ch83, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

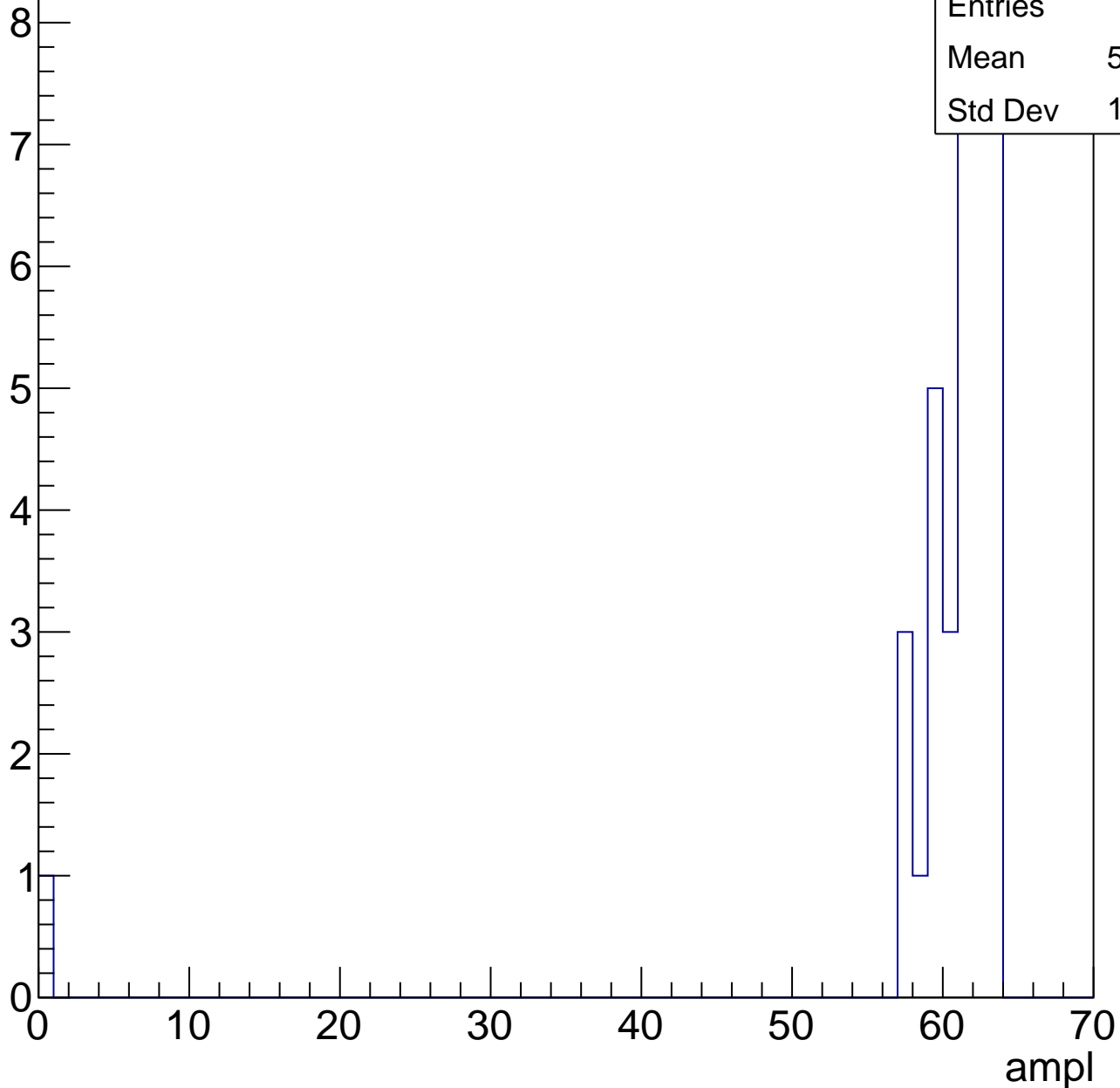


B0L102S, U20-ch83, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 59.24 |
| Std Dev | 10.04 |



B0L102S, U20-ch83, adc6

calib_packv5_042523_0143.root, FC#12, port B1

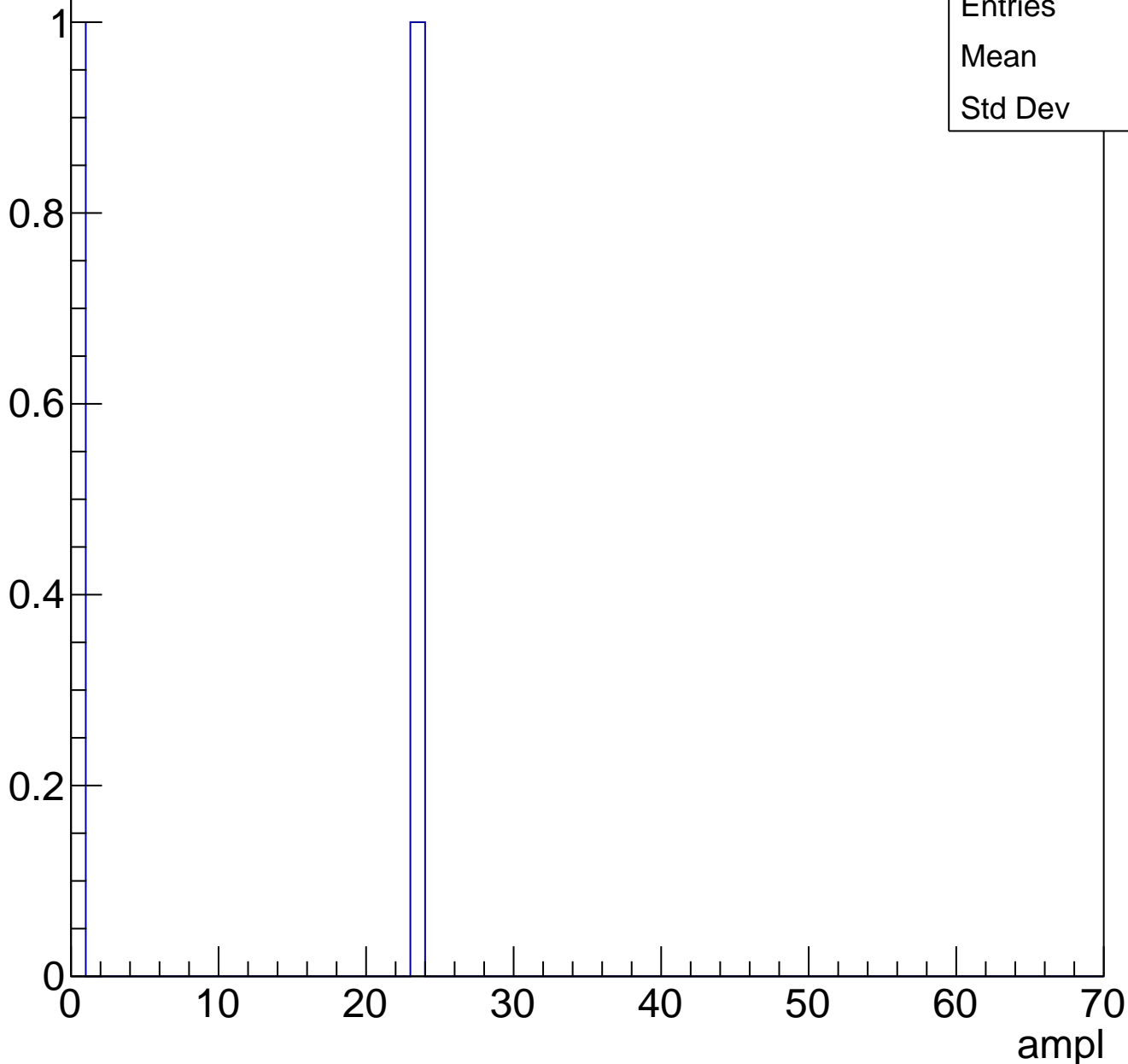
Entry



B0L102S, U20-ch83, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch84, adc0

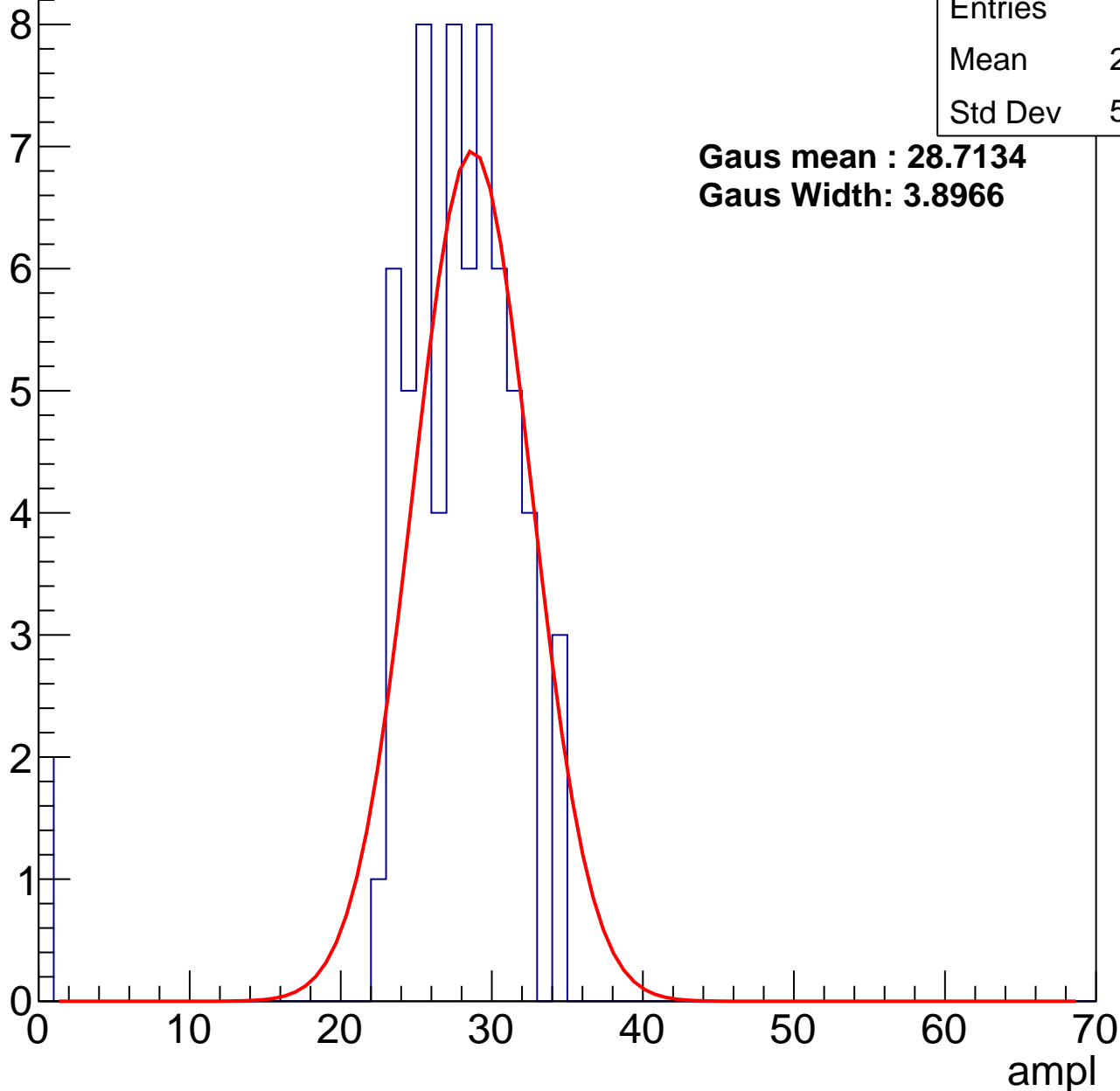
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 26.74 |
| Std Dev | 5.607 |

Gaus mean : 28.7134

Gaus Width: 3.8966



B0L102S, U20-ch84, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 64 |
| Mean | 34.11 |
| Std Dev | 3.068 |

Gaus mean : 34.6248

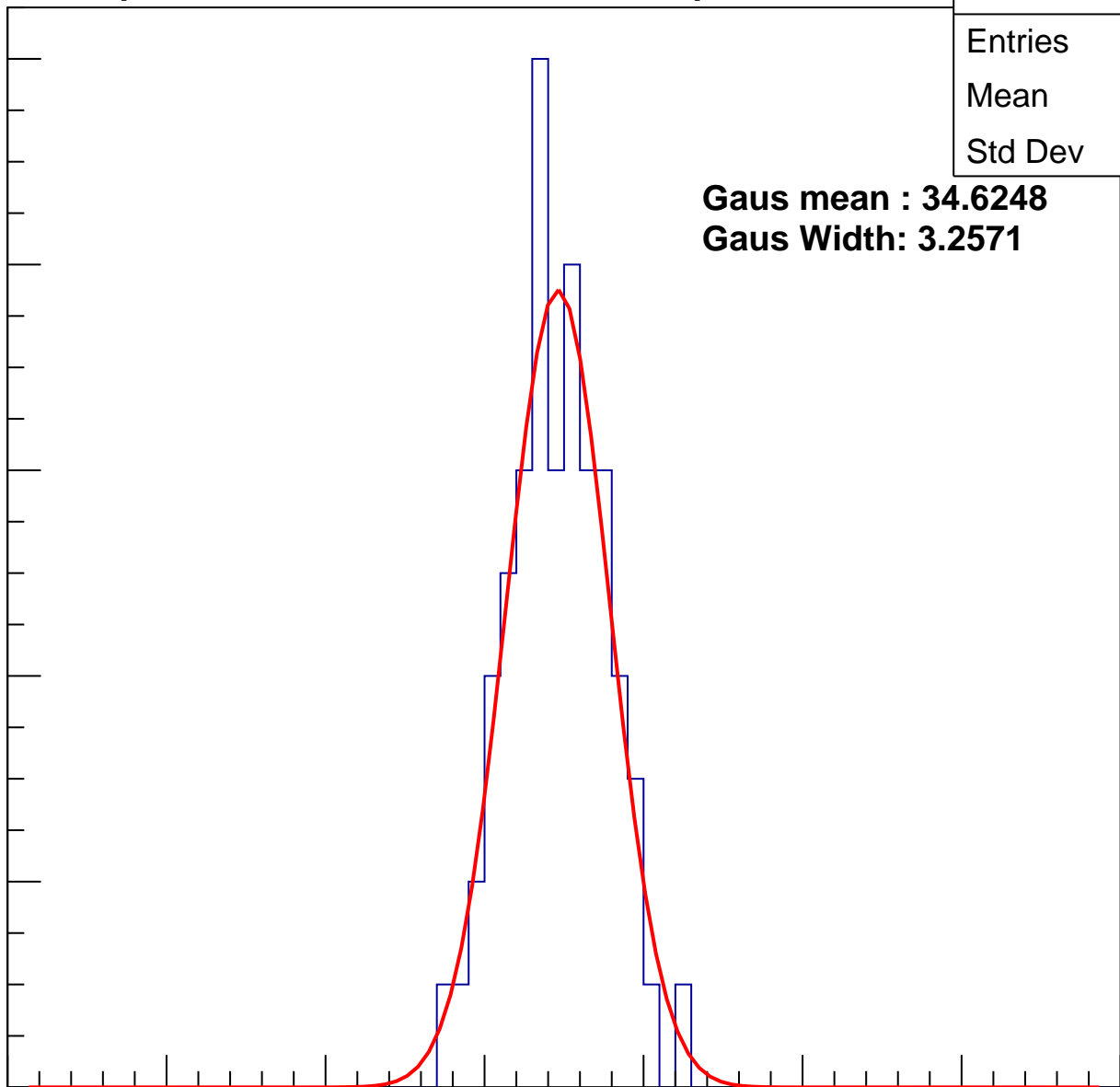
Gaus Width: 3.2571

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch84, adc2

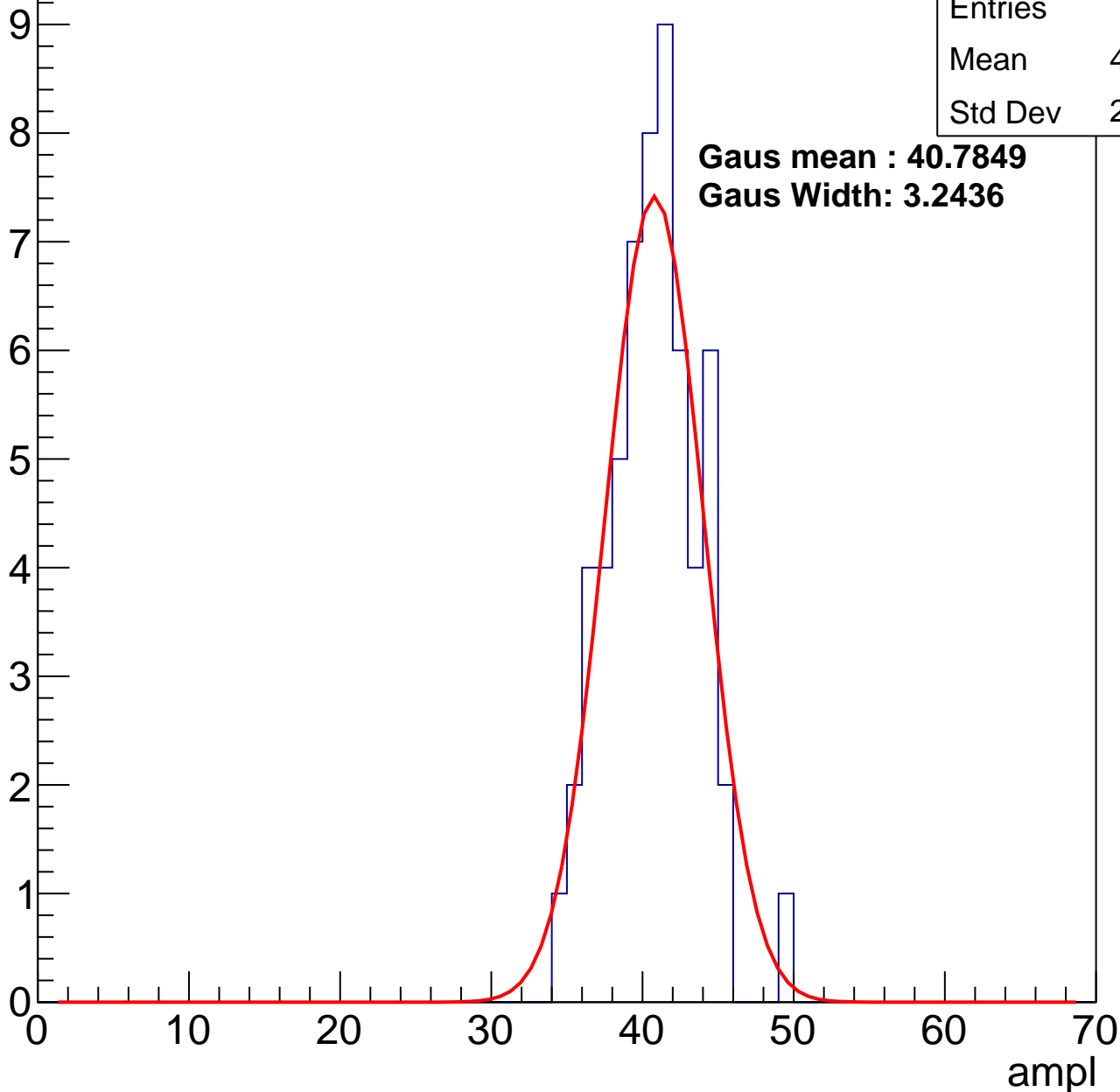
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 40.25 |
| Std Dev | 2.926 |

Gaus mean : 40.7849

Gaus Width: 3.2436

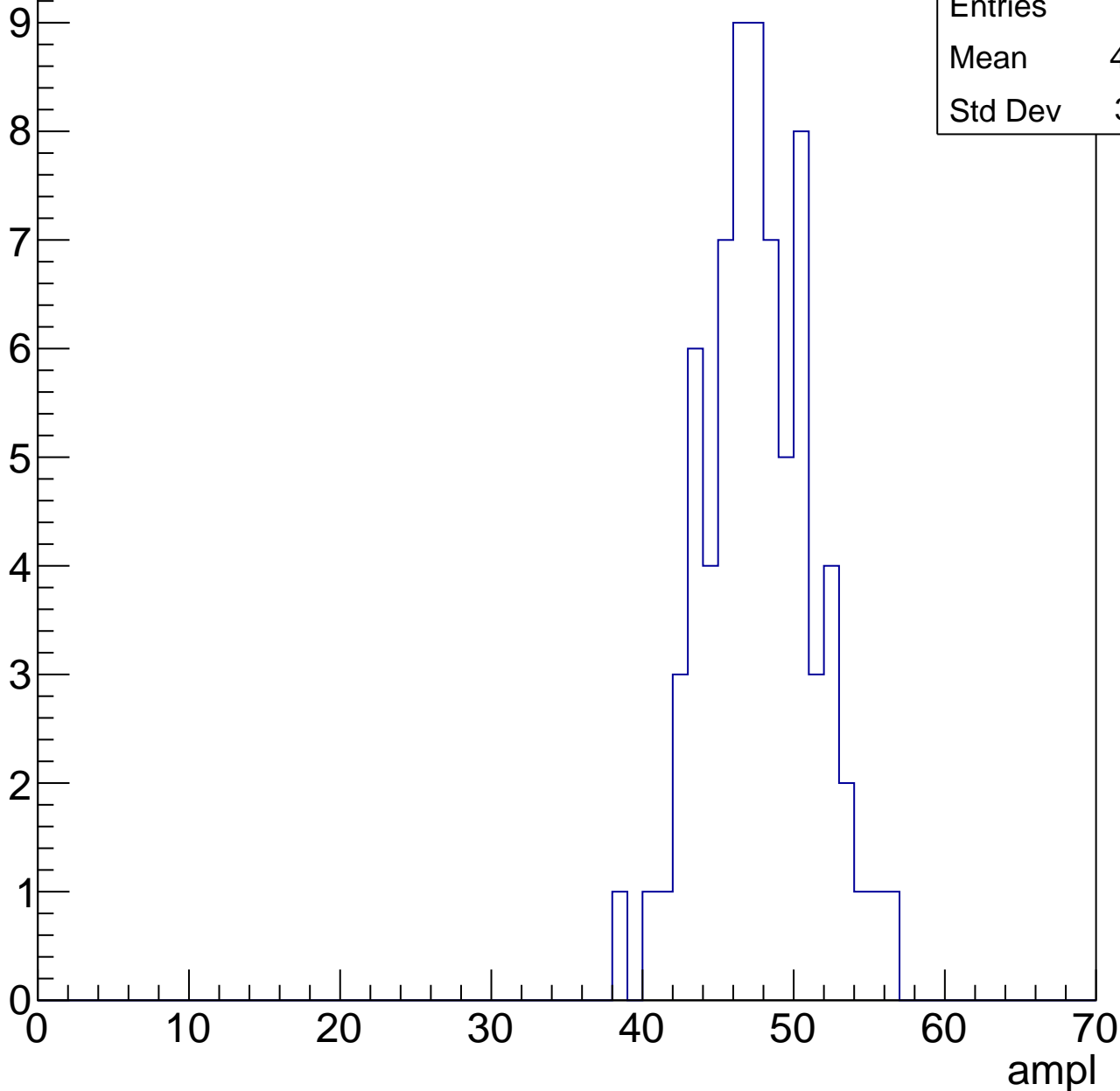


B0L102S, U20-ch84, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 47.18 |
| Std Dev | 3.551 |

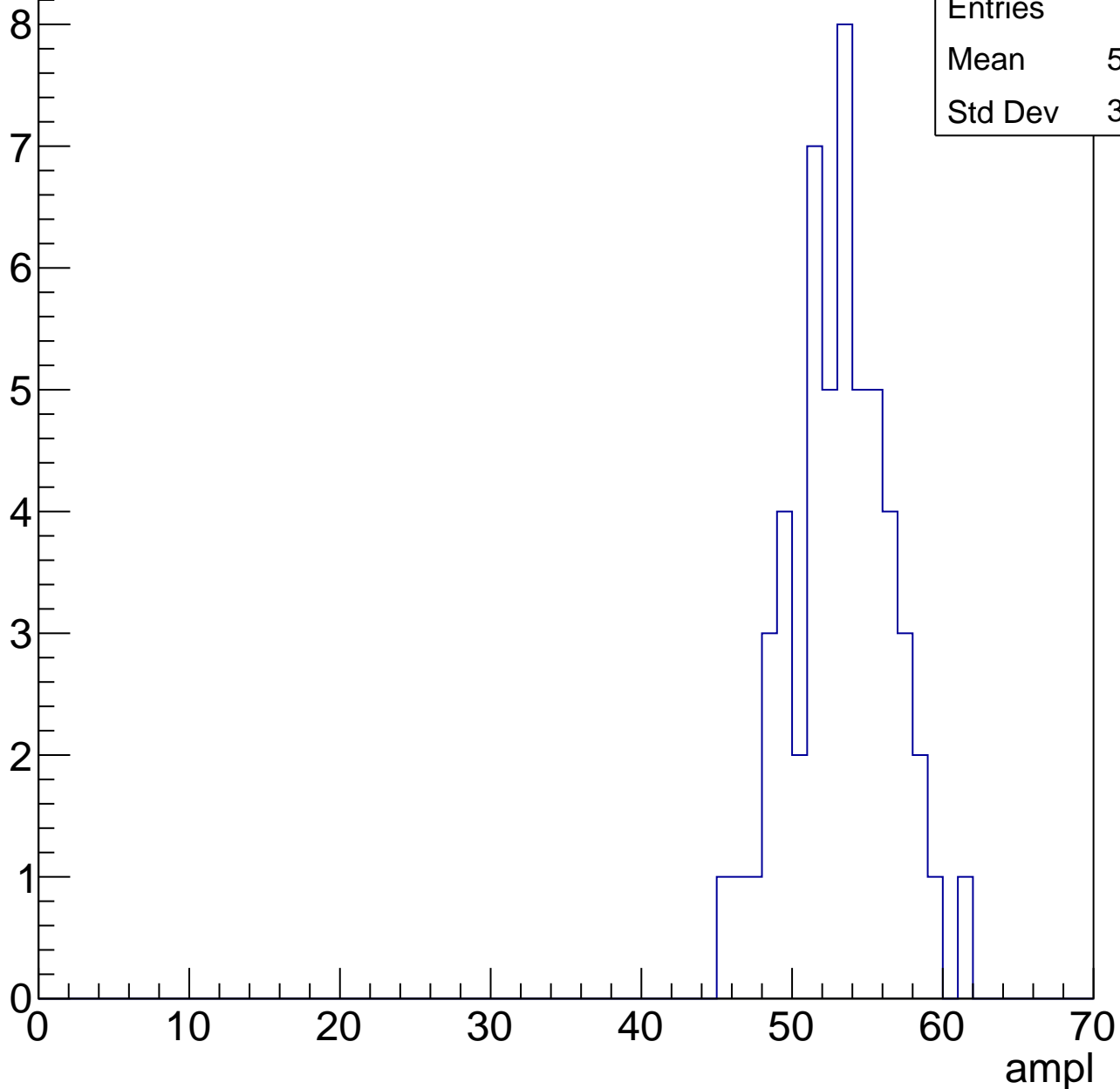


B0L102S, U20-ch84, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 52.74 |
| Std Dev | 3.349 |

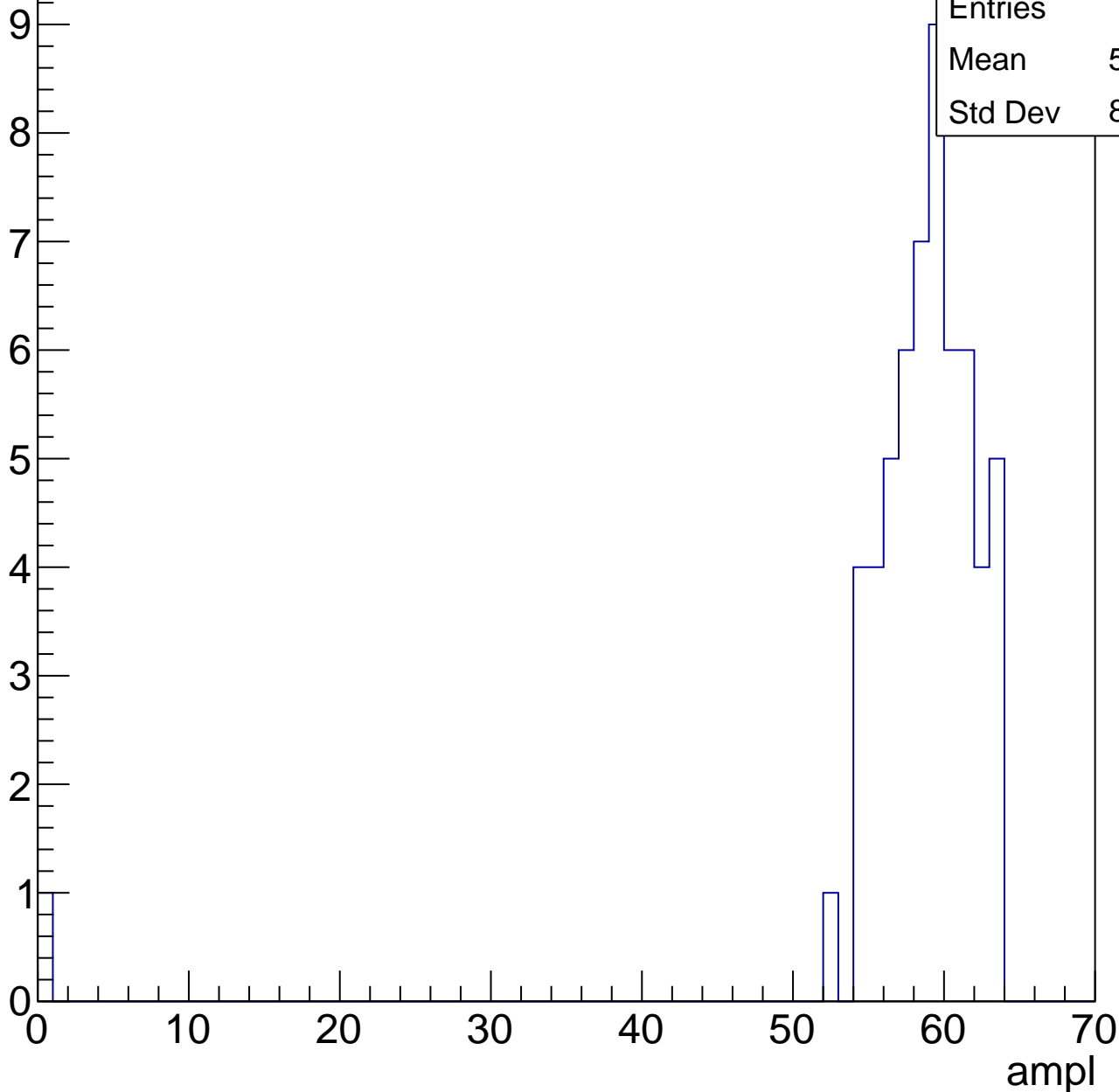


B0L102S, U20-ch84, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 57.52 |
| Std Dev | 8.082 |

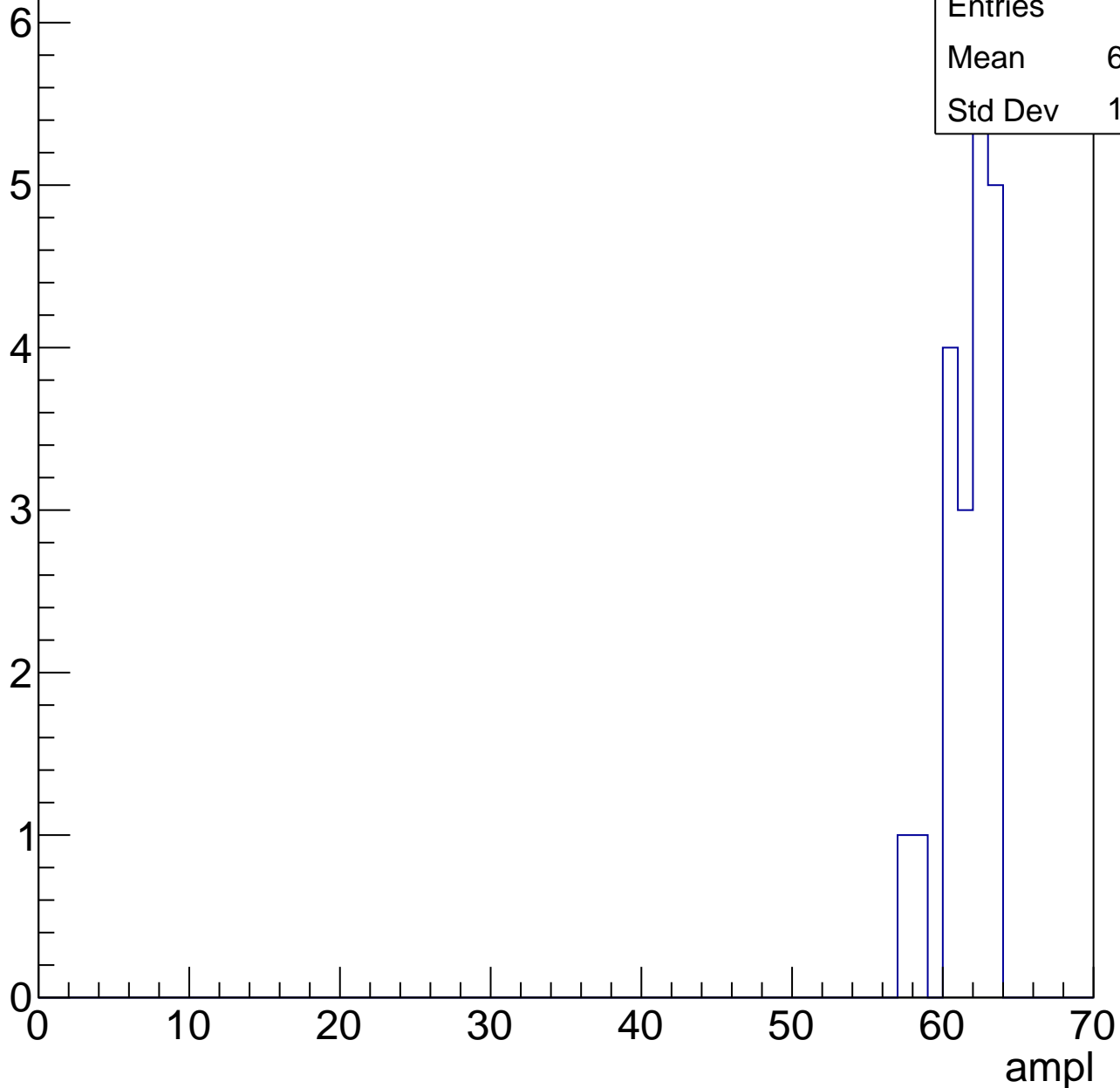


B0L102S, U20-ch84, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 61.25 |
| Std Dev | 1.639 |



B0L102S, U20-ch84, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch85, adc0

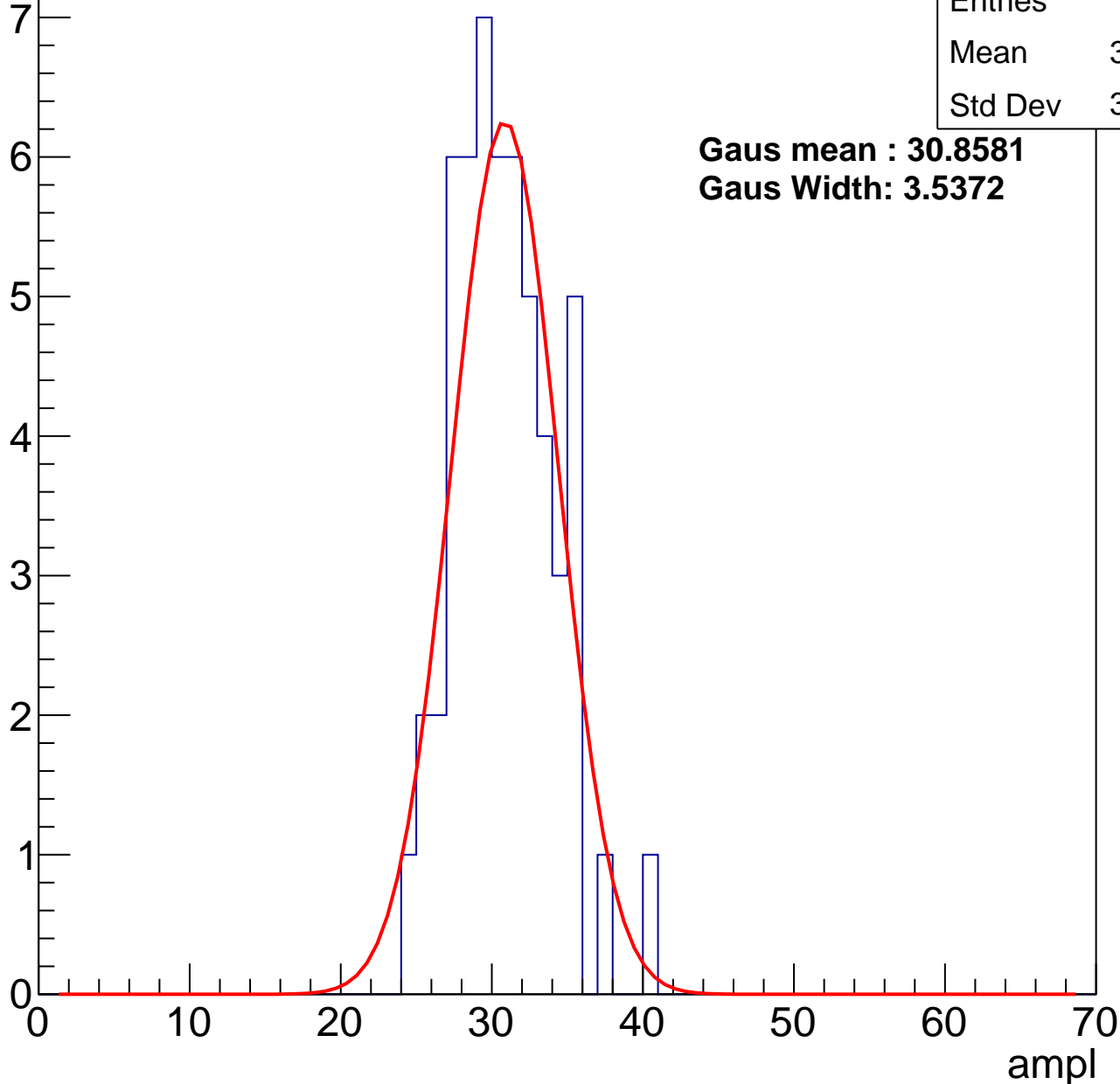
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 30.38 |
| Std Dev | 3.245 |

Gaus mean : 30.8581

Gaus Width: 3.5372



B0L102S, U20-ch85, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 37.31 |
| Std Dev | 3.601 |

Gaus mean : 37.9500

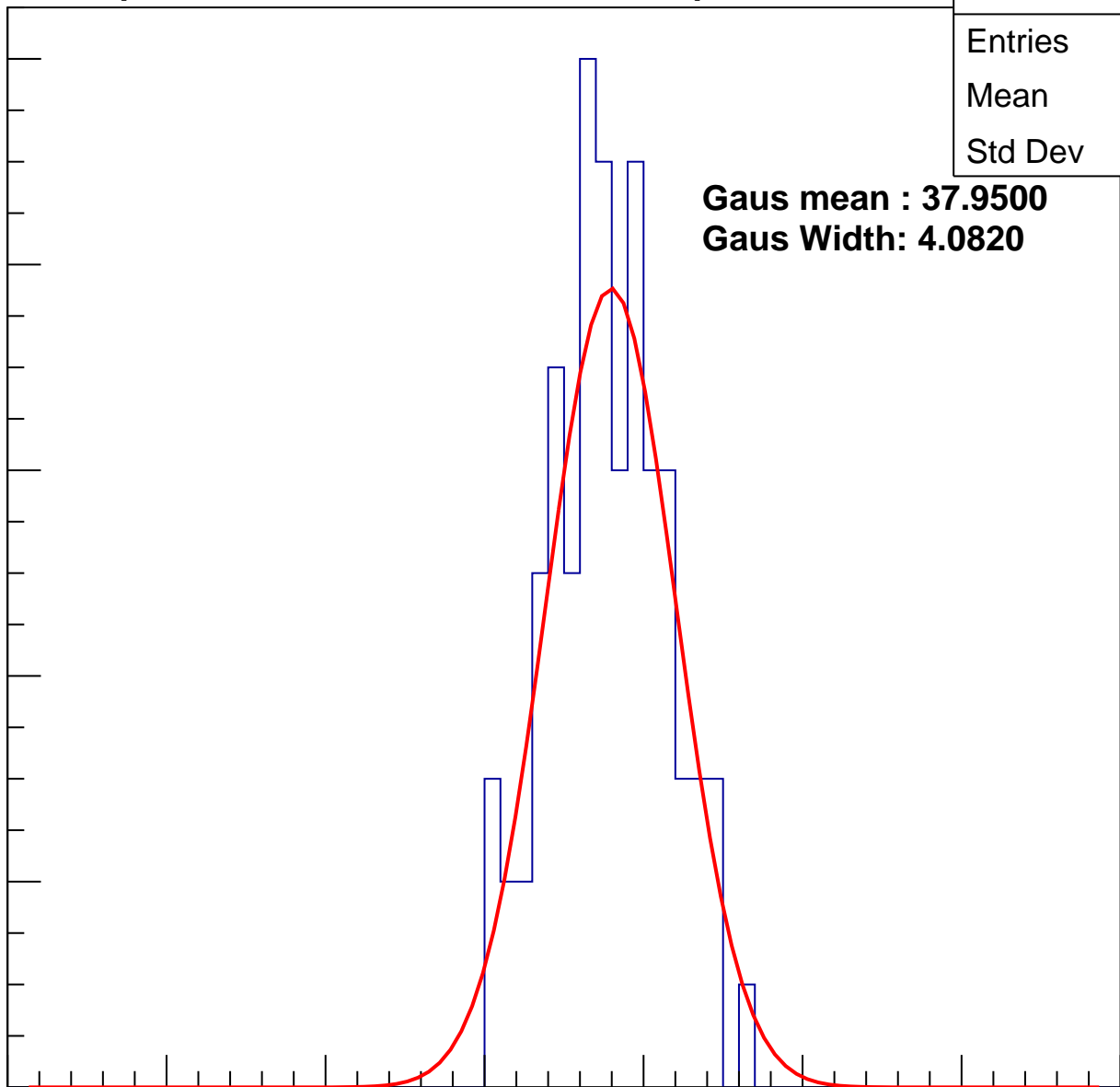
Gaus Width: 4.0820

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch85, adc2

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.66 |
| Std Dev | 3.281 |

Gaus mean : 45.4792

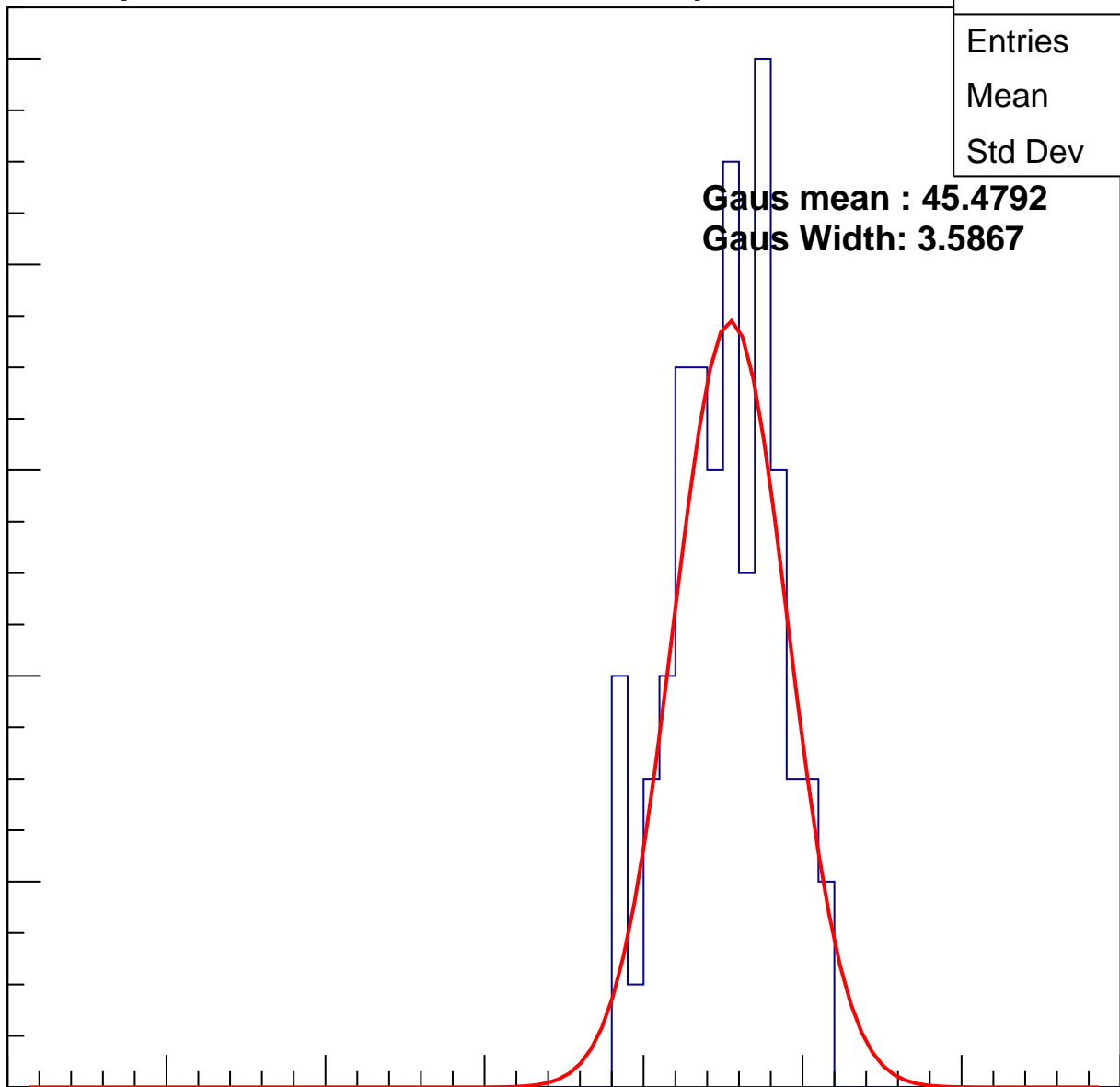
Gaus Width: 3.5867

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

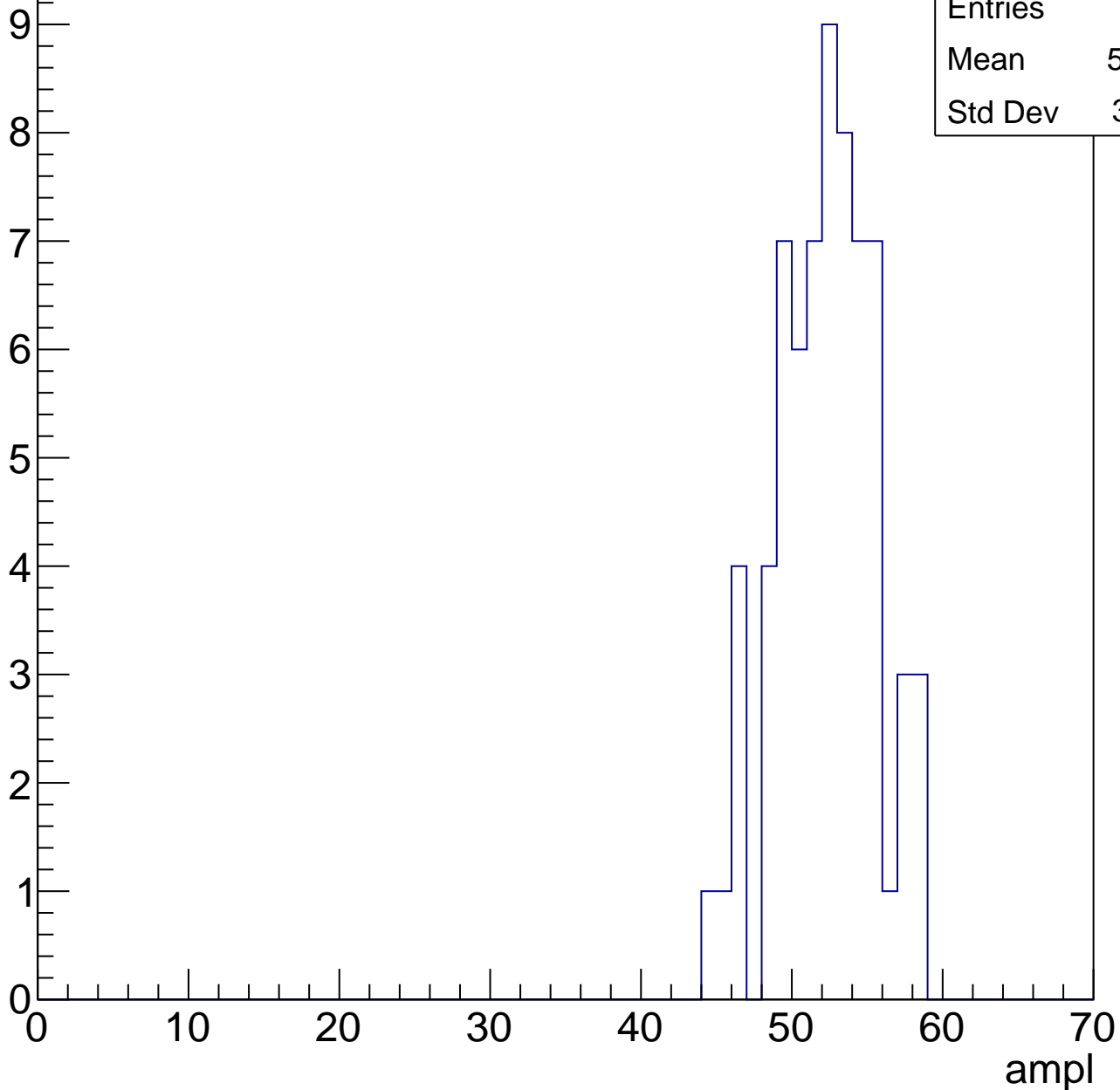


B0L102S, U20-ch85, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 51.78 |
| Std Dev | 3.231 |

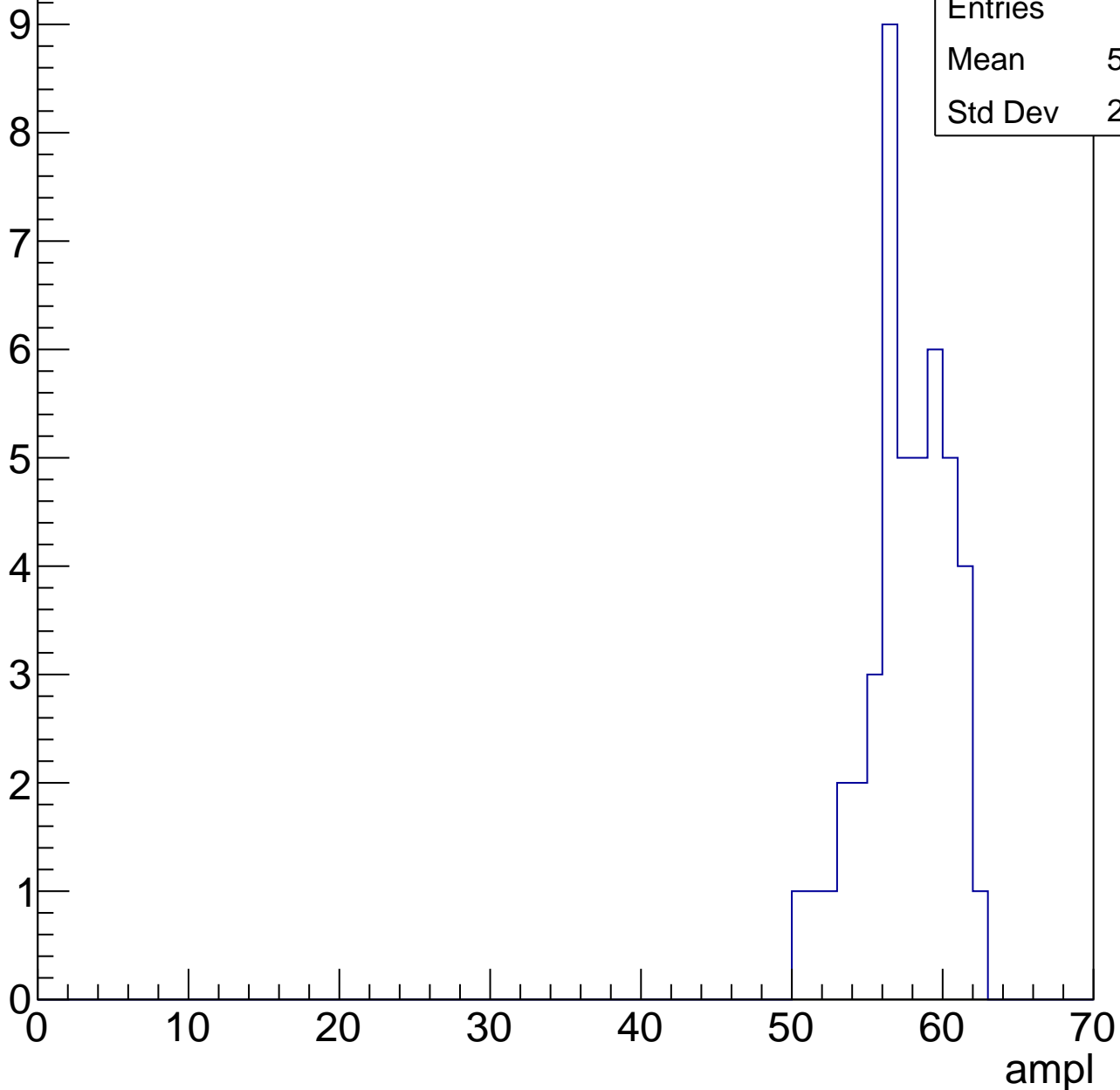


B0L102S, U20-ch85, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 57.13 |
| Std Dev | 2.762 |

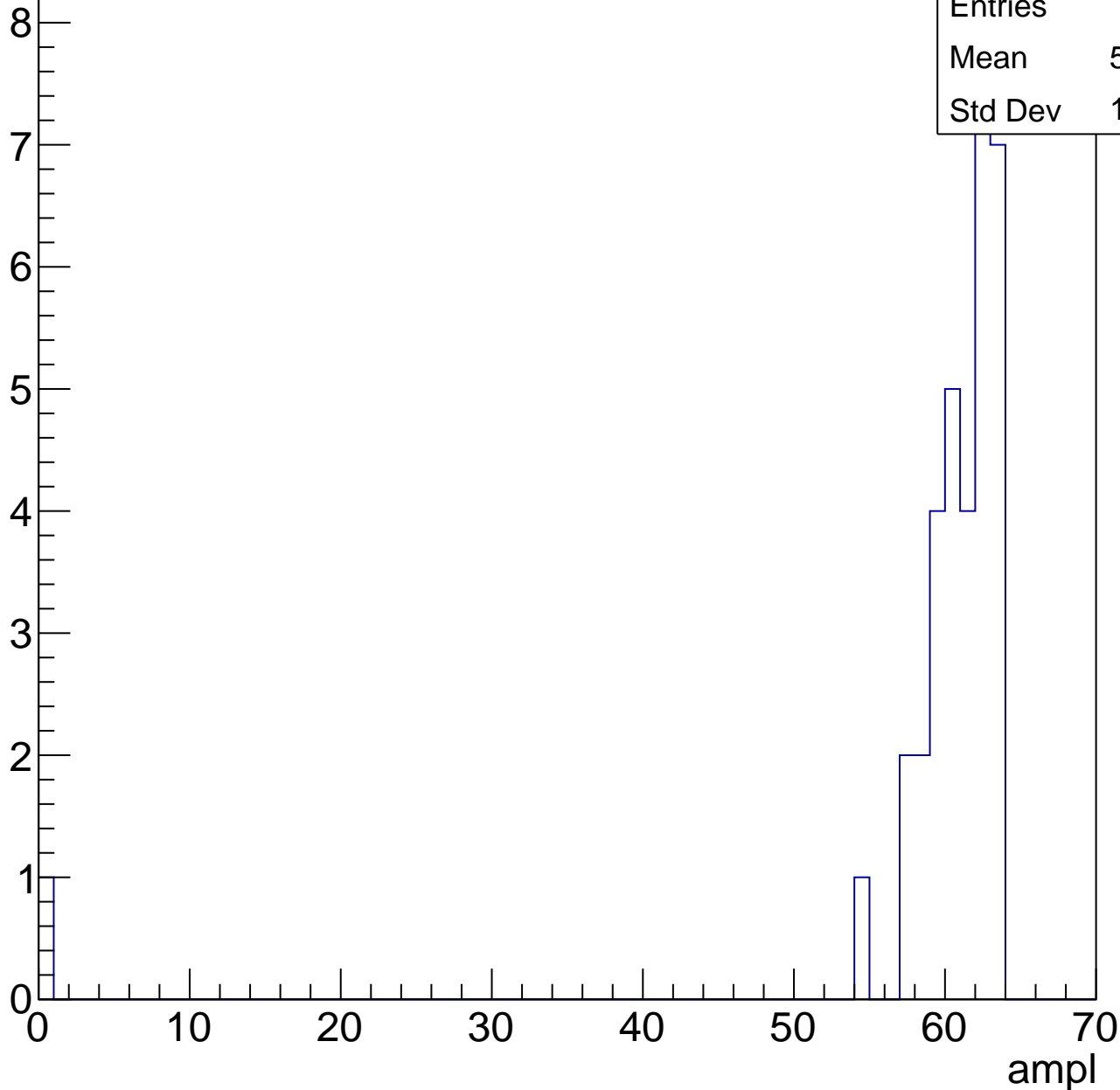


B0L102S, U20-ch85, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 58.85 |
| Std Dev | 10.46 |



B0L102S, U20-ch85, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch85, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B0L102S, U20-ch86, adc0

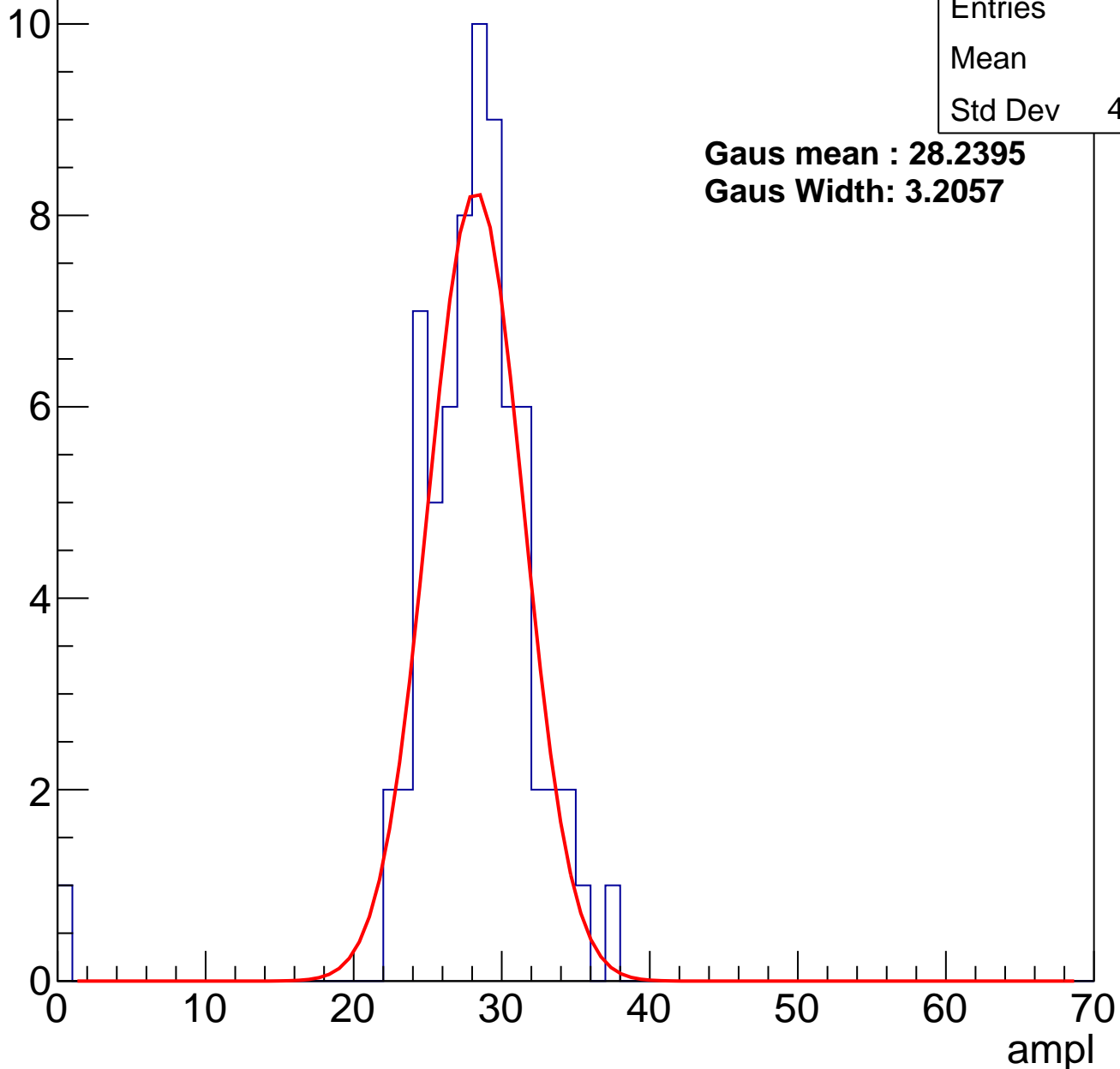
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 27.6 |
| Std Dev | 4.559 |

Gaus mean : 28.2395

Gaus Width: 3.2057

Entry



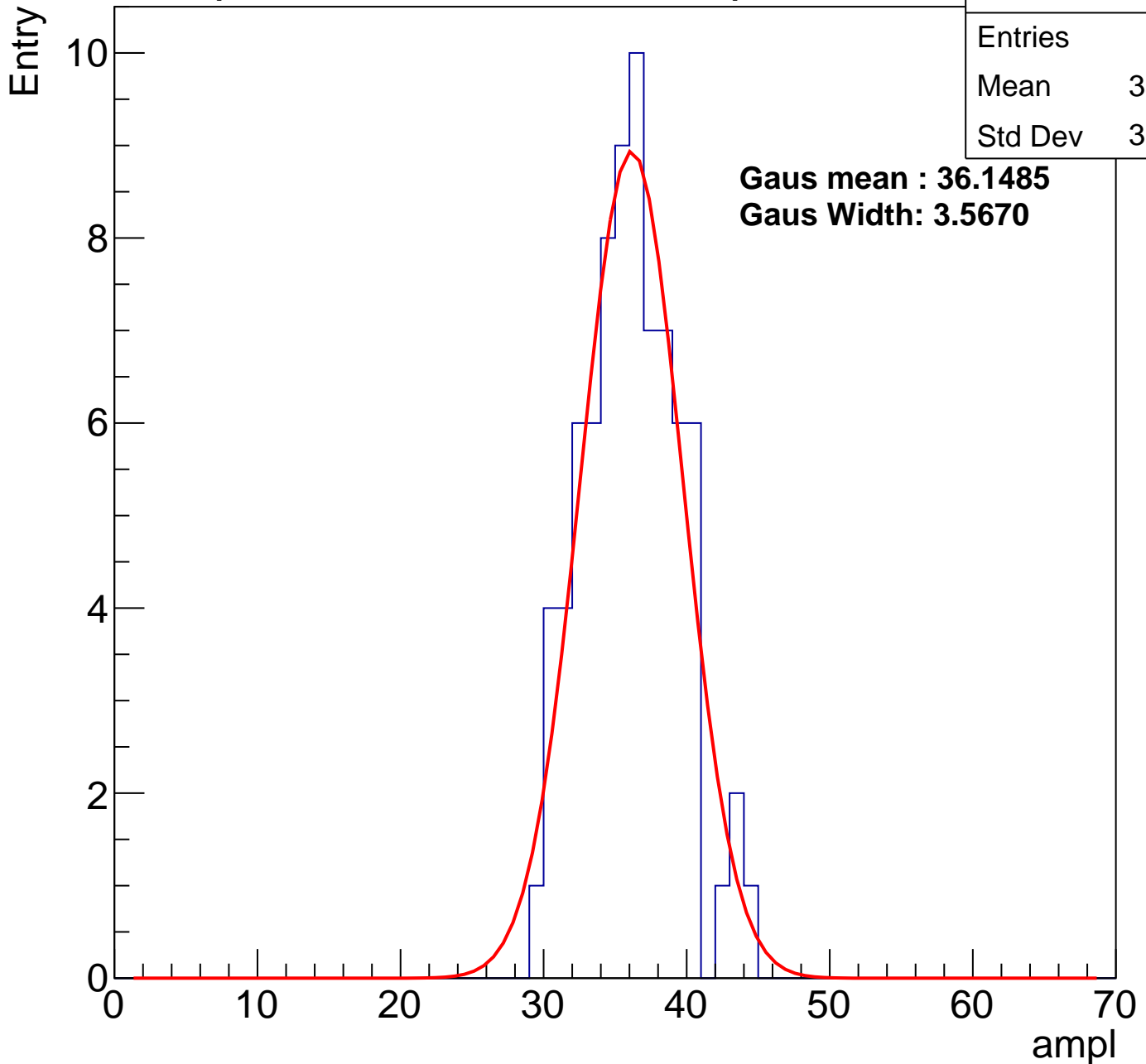
B0L102S, U20-ch86, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 78 |
| Mean | 35.65 |
| Std Dev | 3.316 |

Gaus mean : 36.1485

Gaus Width: 3.5670



B0L102S, U20-ch86, adc2

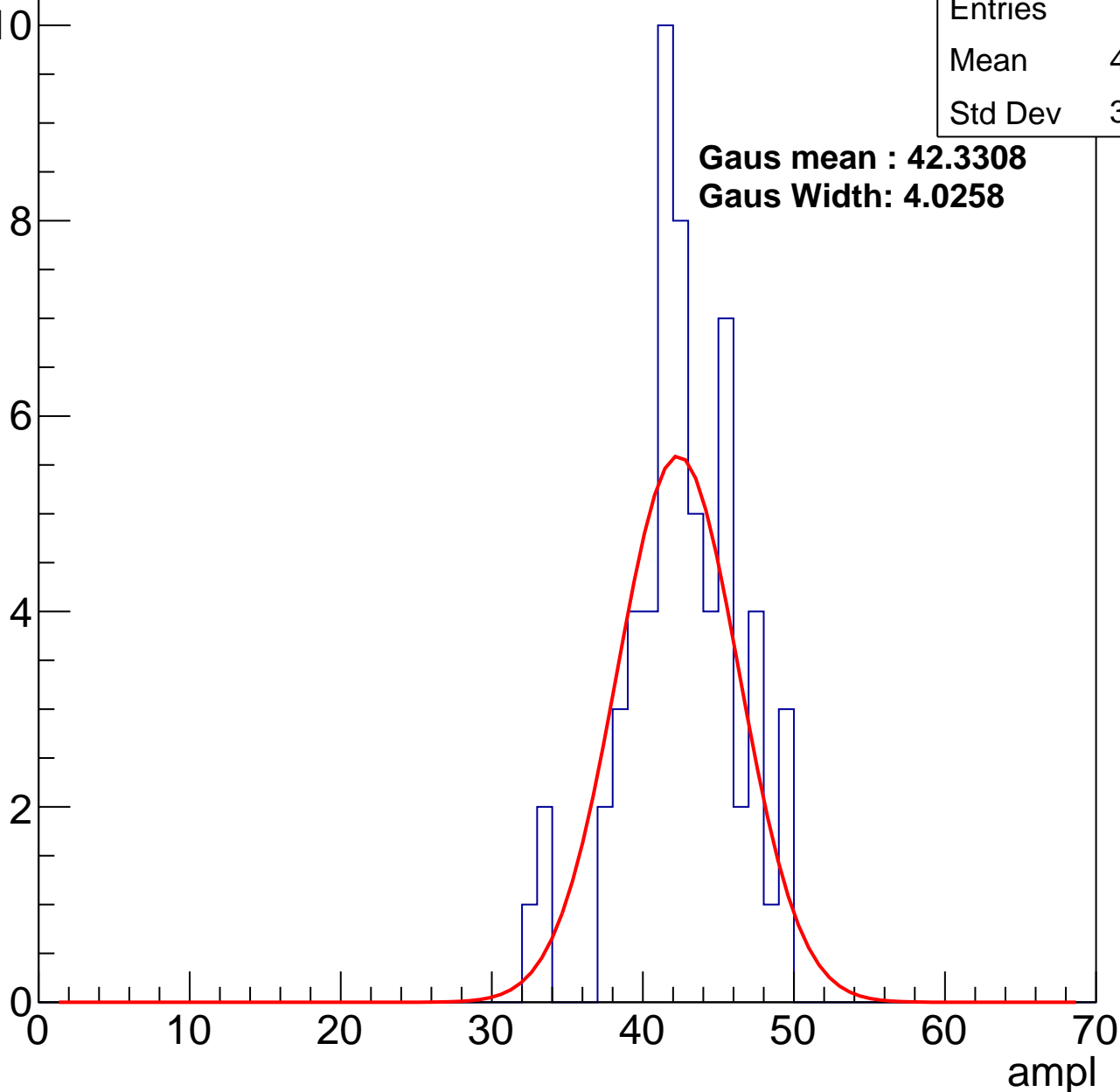
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.15 |
| Std Dev | 3.696 |

Gaus mean : 42.3308

Gaus Width: 4.0258

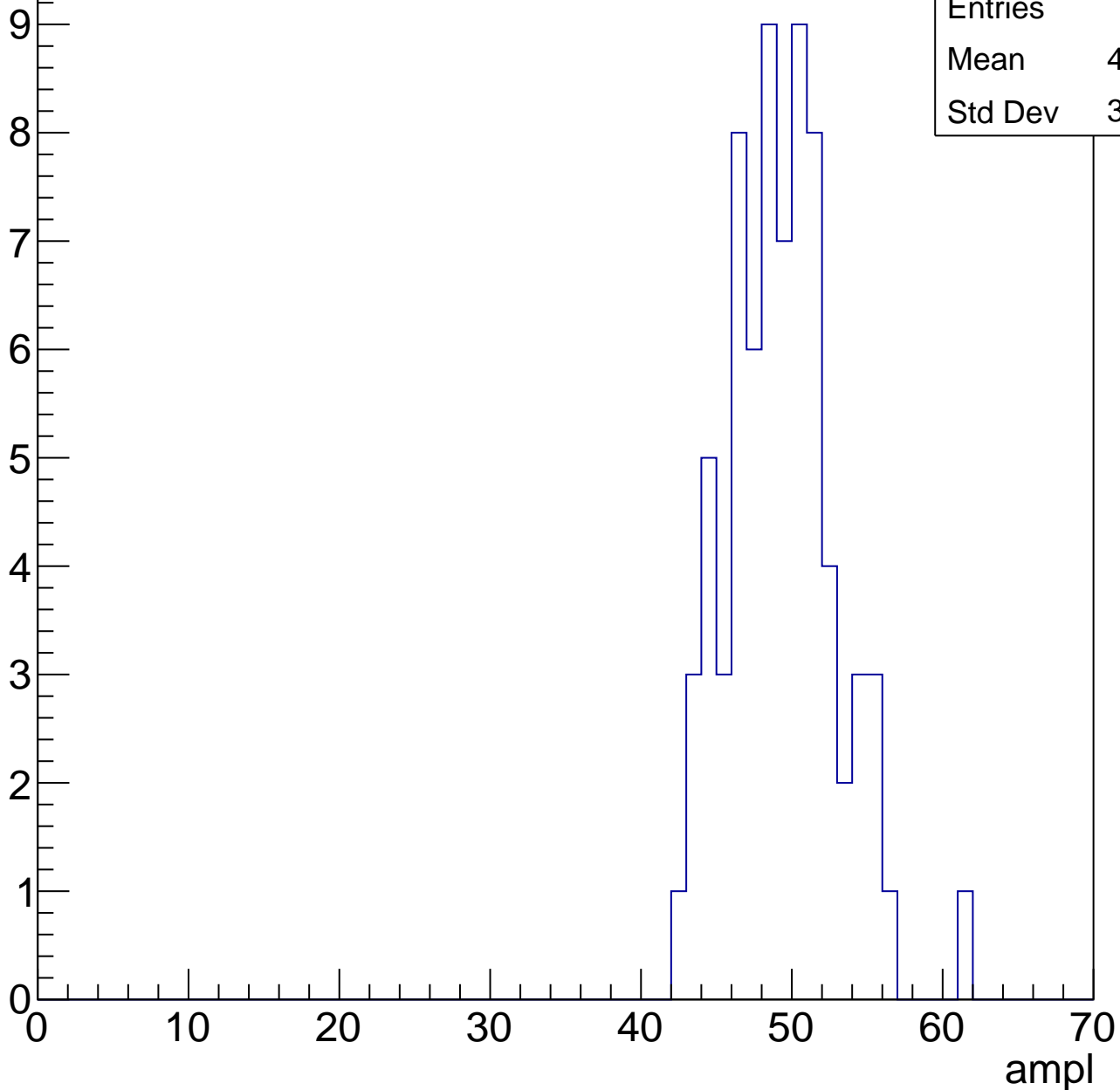


B0L102S, U20-ch86, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 48.86 |
| Std Dev | 3.547 |

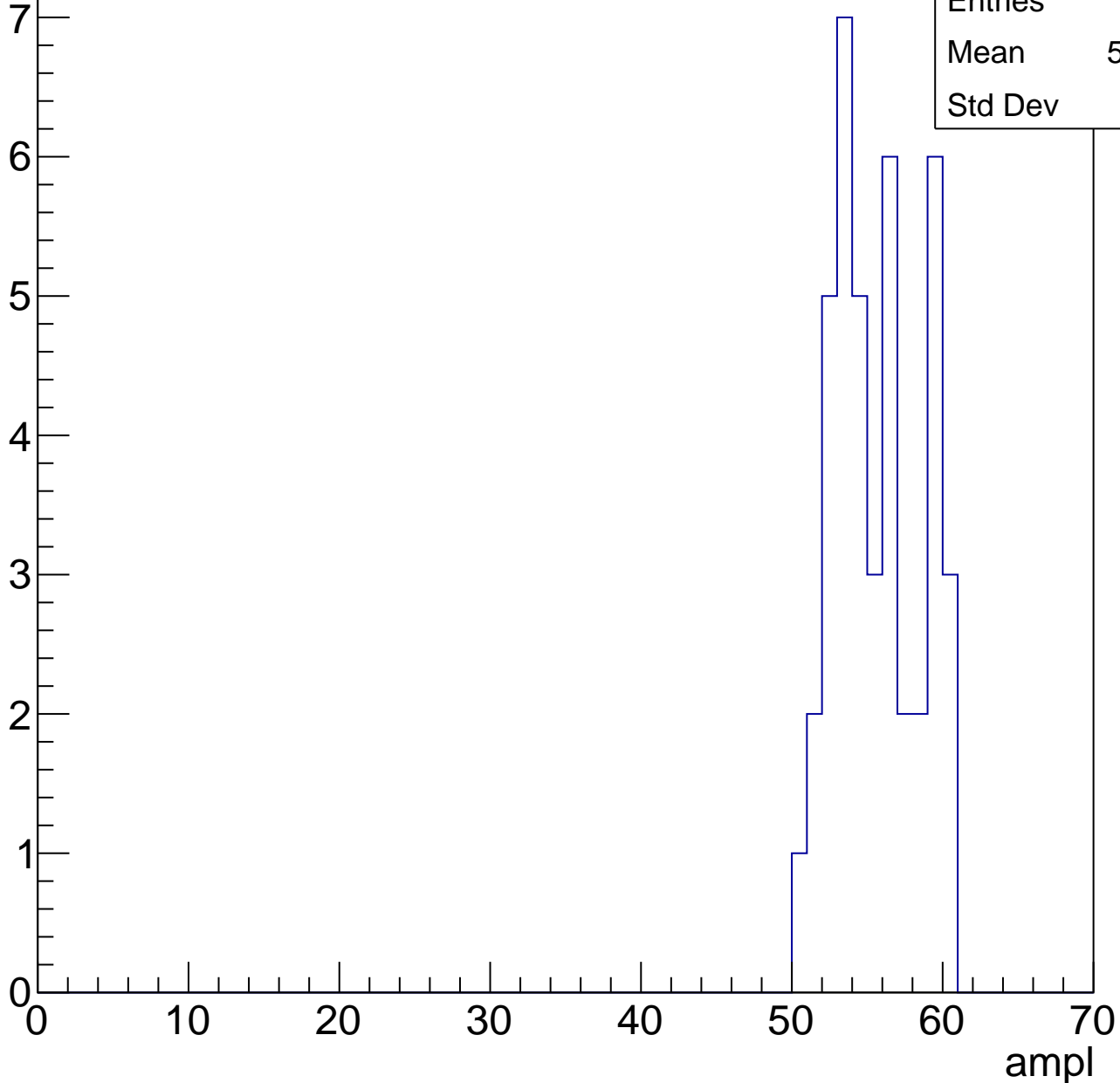


B0L102S, U20-ch86, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 55.19 |
| Std Dev | 2.83 |

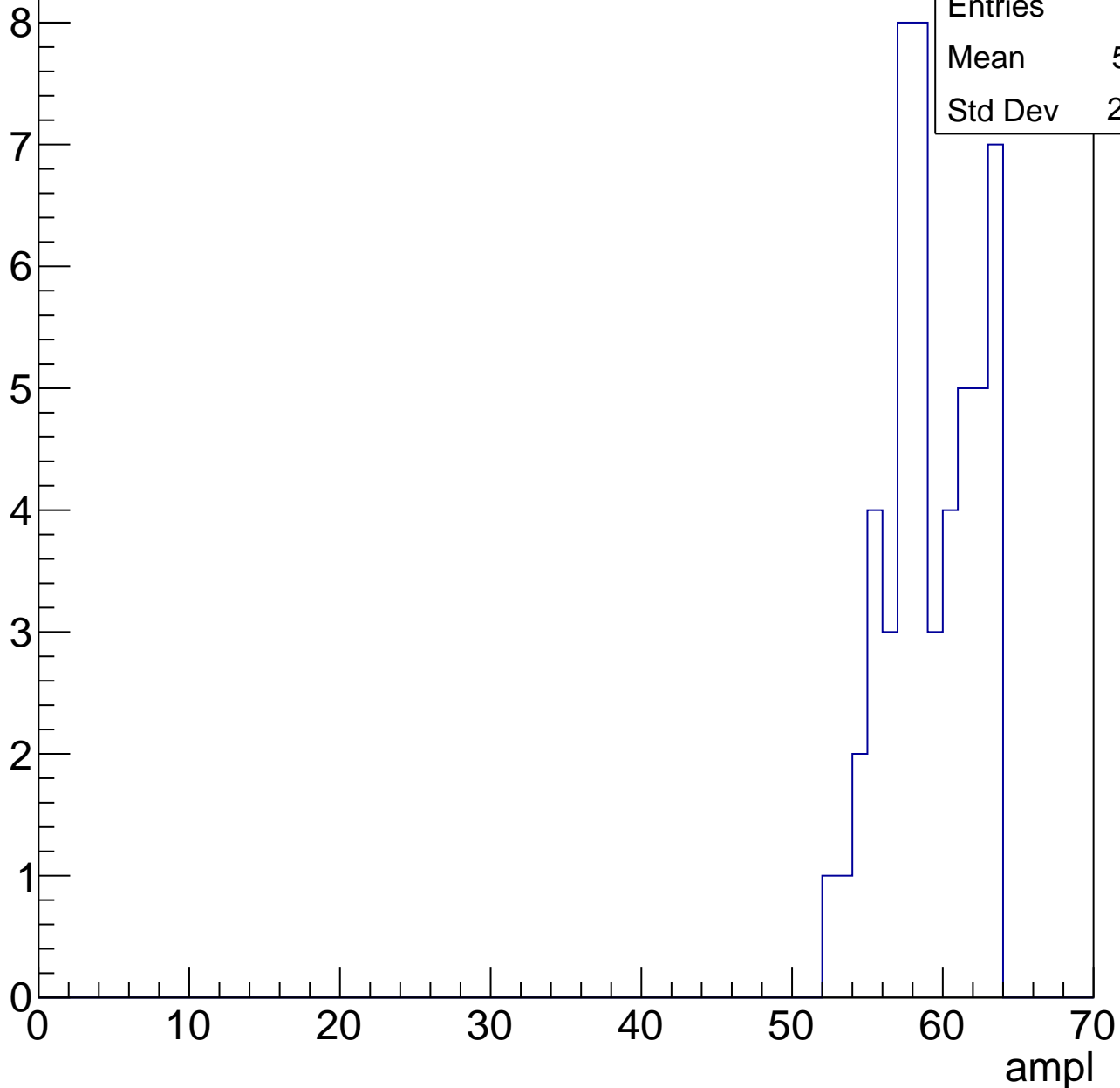


B0L102S, U20-ch86, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.71 |
| Std Dev | 2.946 |

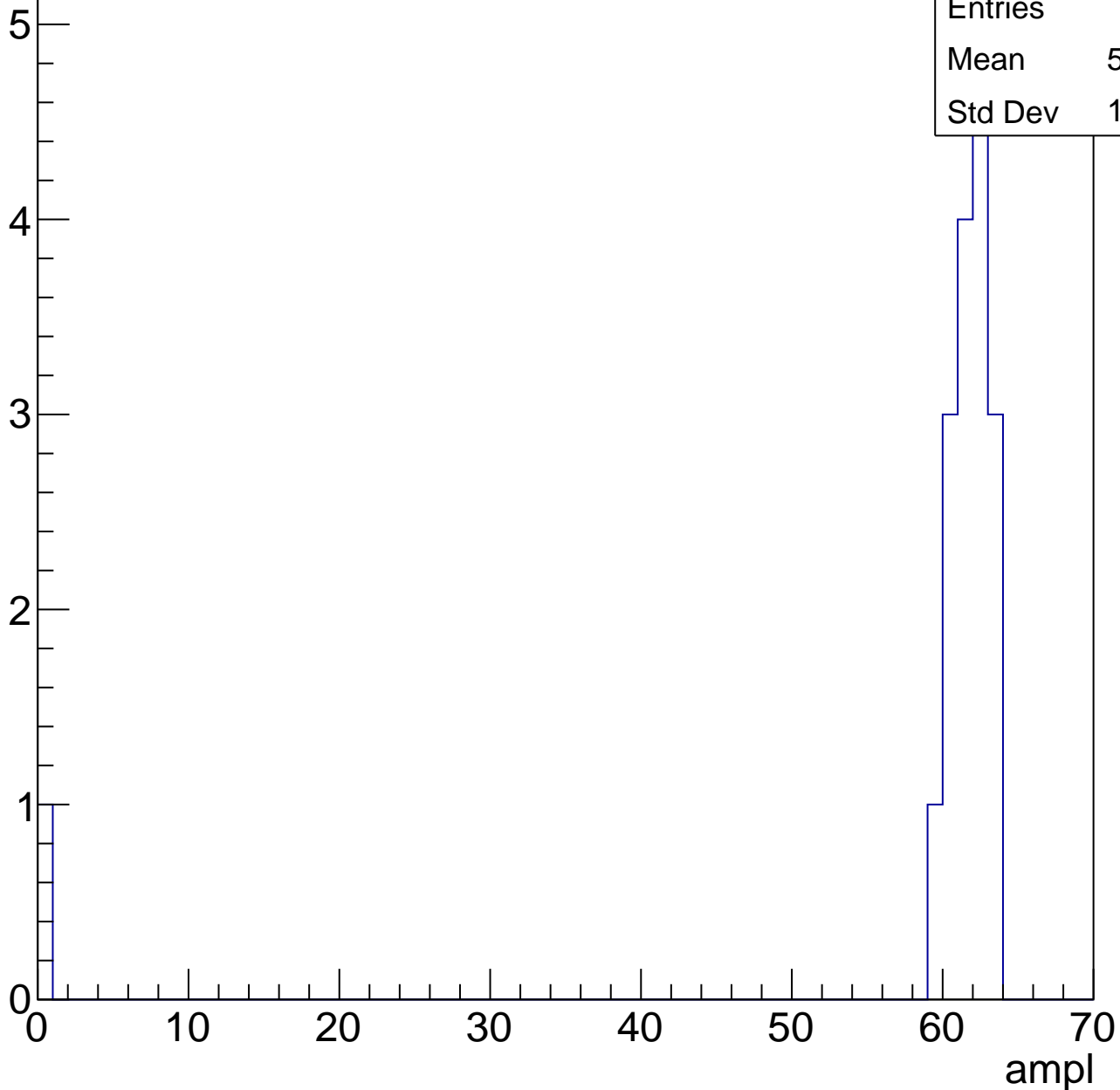


B0L102S, U20-ch86, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 57.76 |
| Std Dev | 14.49 |



B0L102S, U20-ch86, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch87, adc0

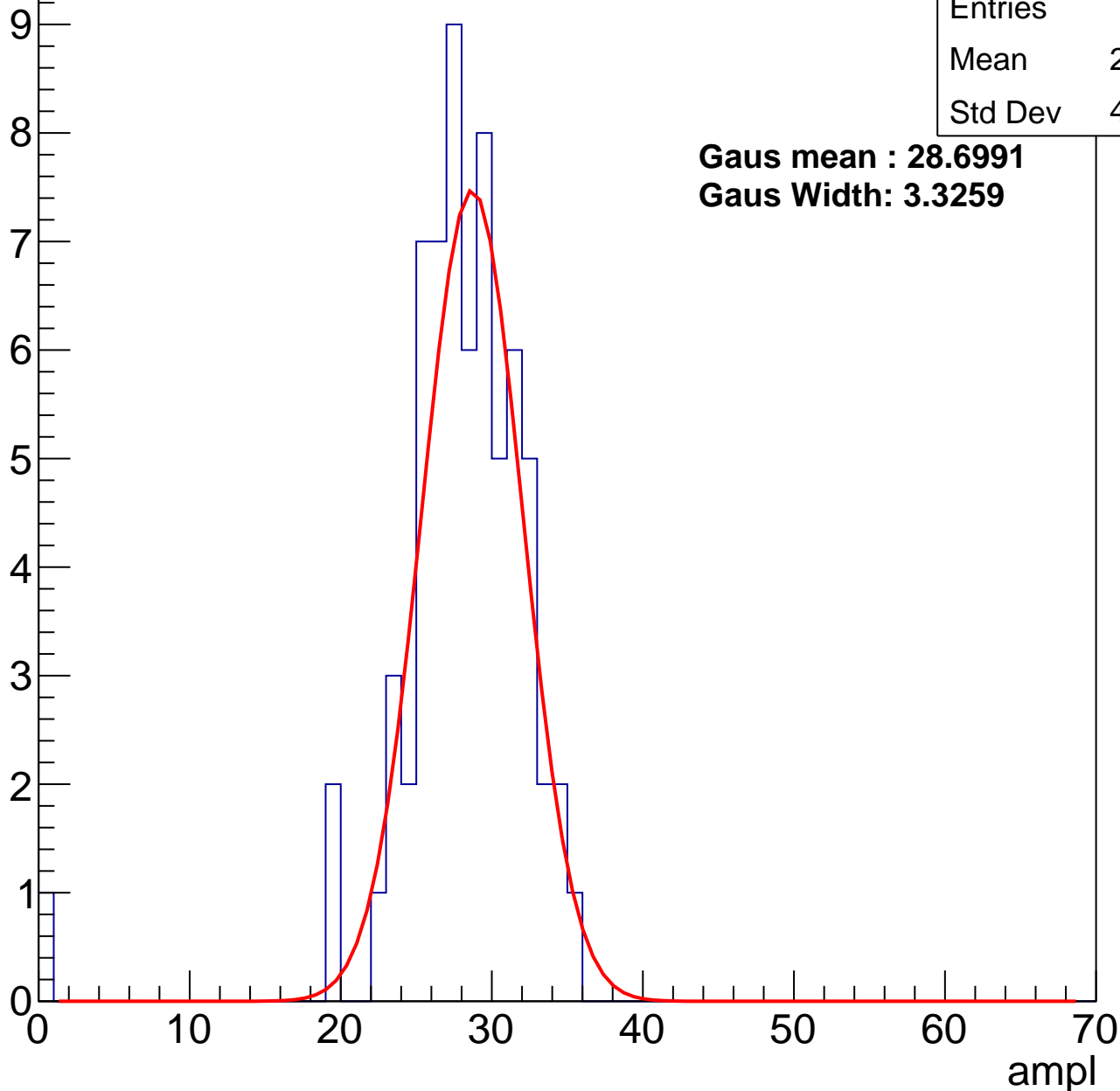
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.49 |
| Std Dev | 4.739 |

Gaus mean : 28.6991

Gaus Width: 3.3259



B0L102S, U20-ch87, adc1

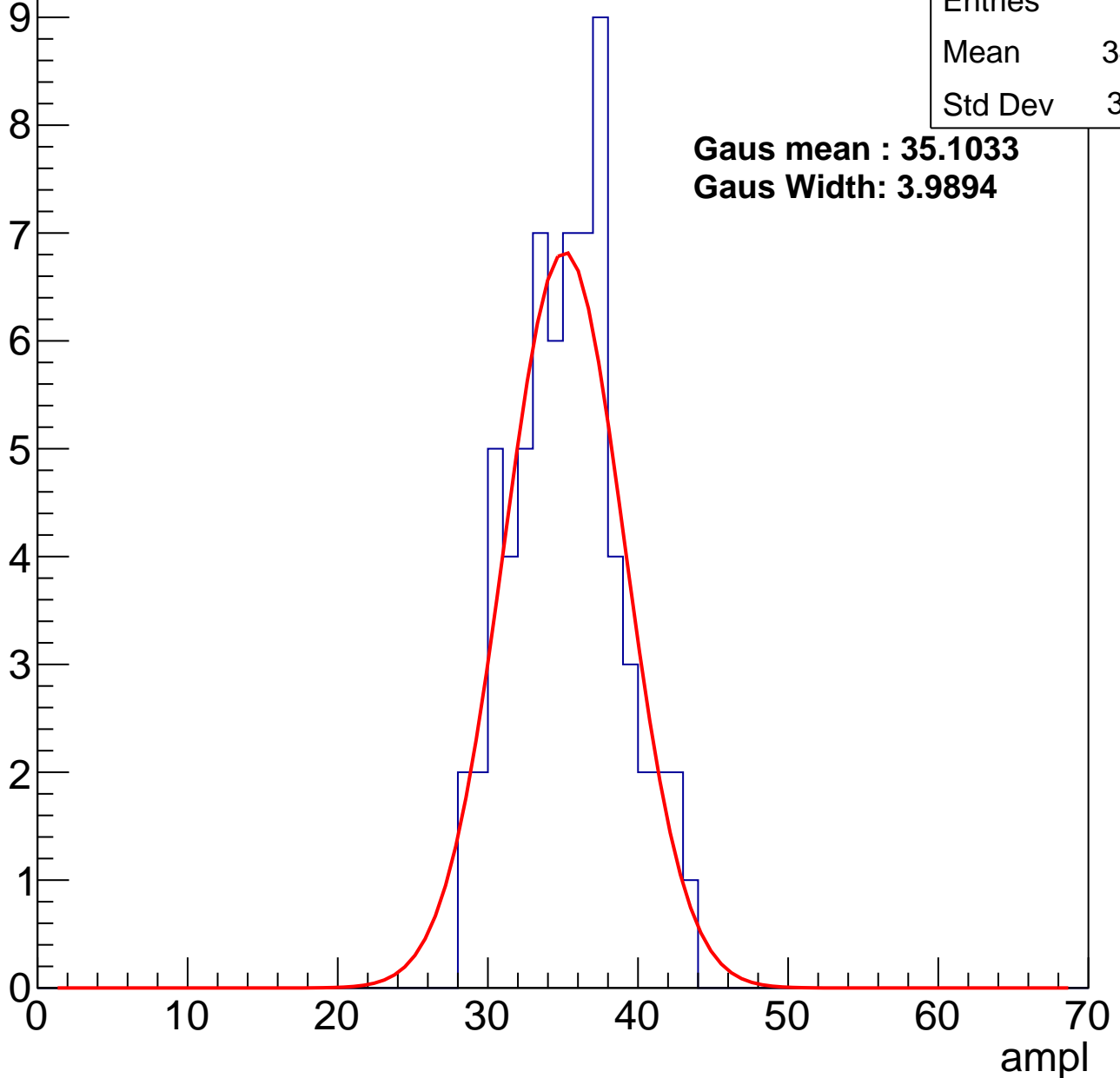
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 34.87 |
| Std Dev | 3.531 |

Gaus mean : 35.1033

Gaus Width: 3.9894



B0L102S, U20-ch87, adc2

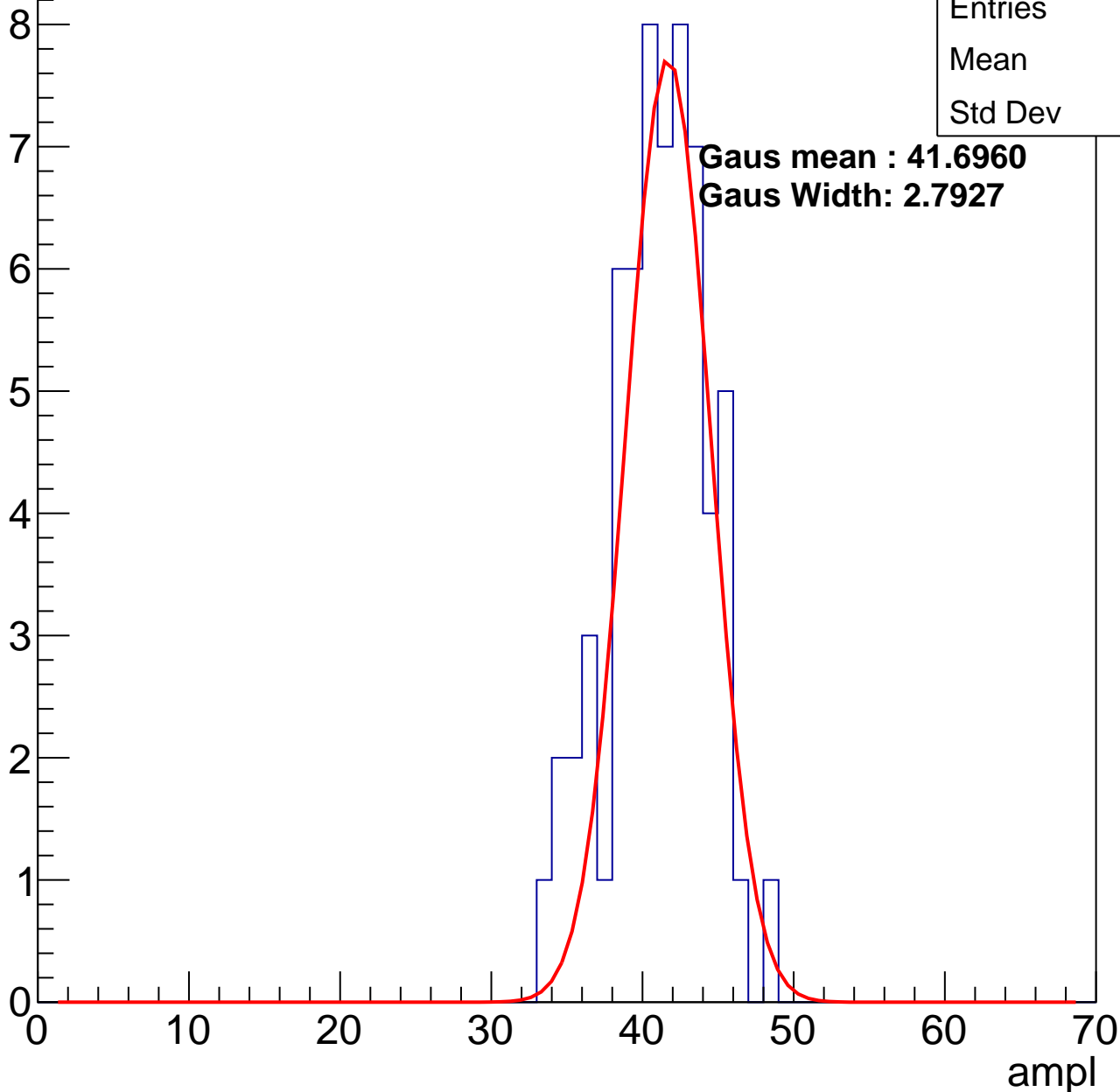
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 62 |
| Mean | 40.6 |
| Std Dev | 3.18 |

Gaus mean : 41.6960

Gaus Width: 2.7927

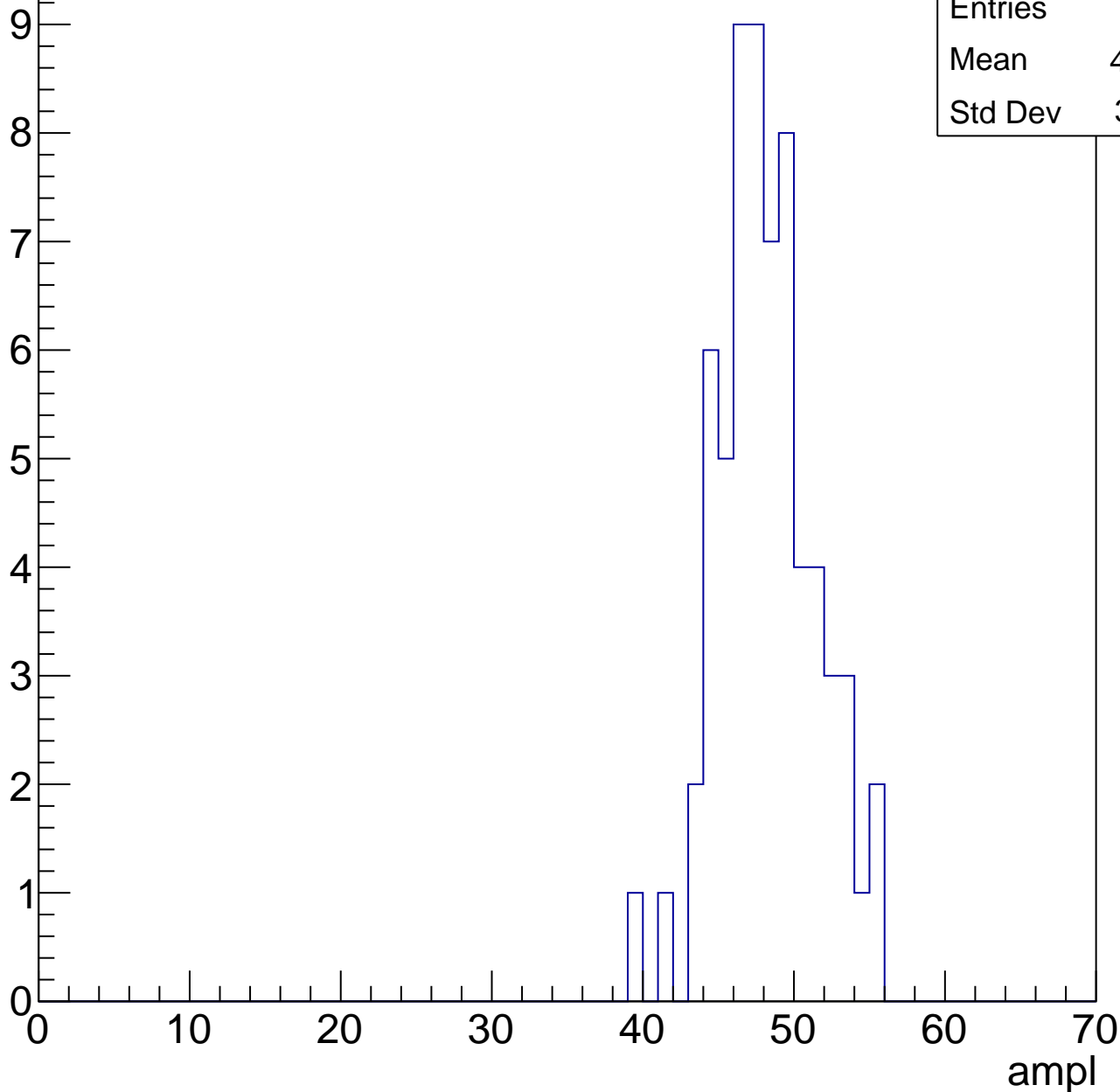


B0L102S, U20-ch87, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 47.74 |
| Std Dev | 3.231 |

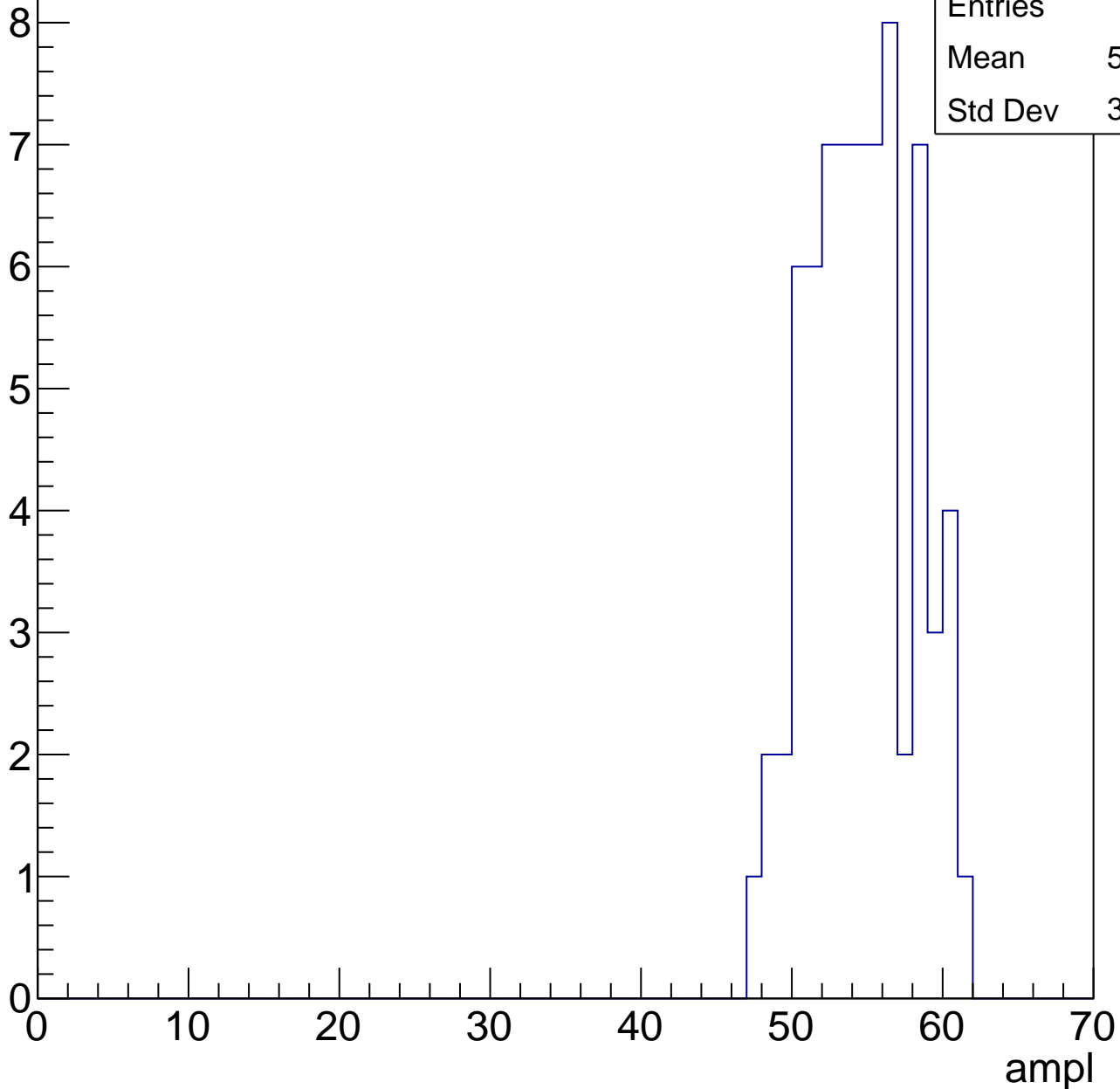


B0L102S, U20-ch87, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

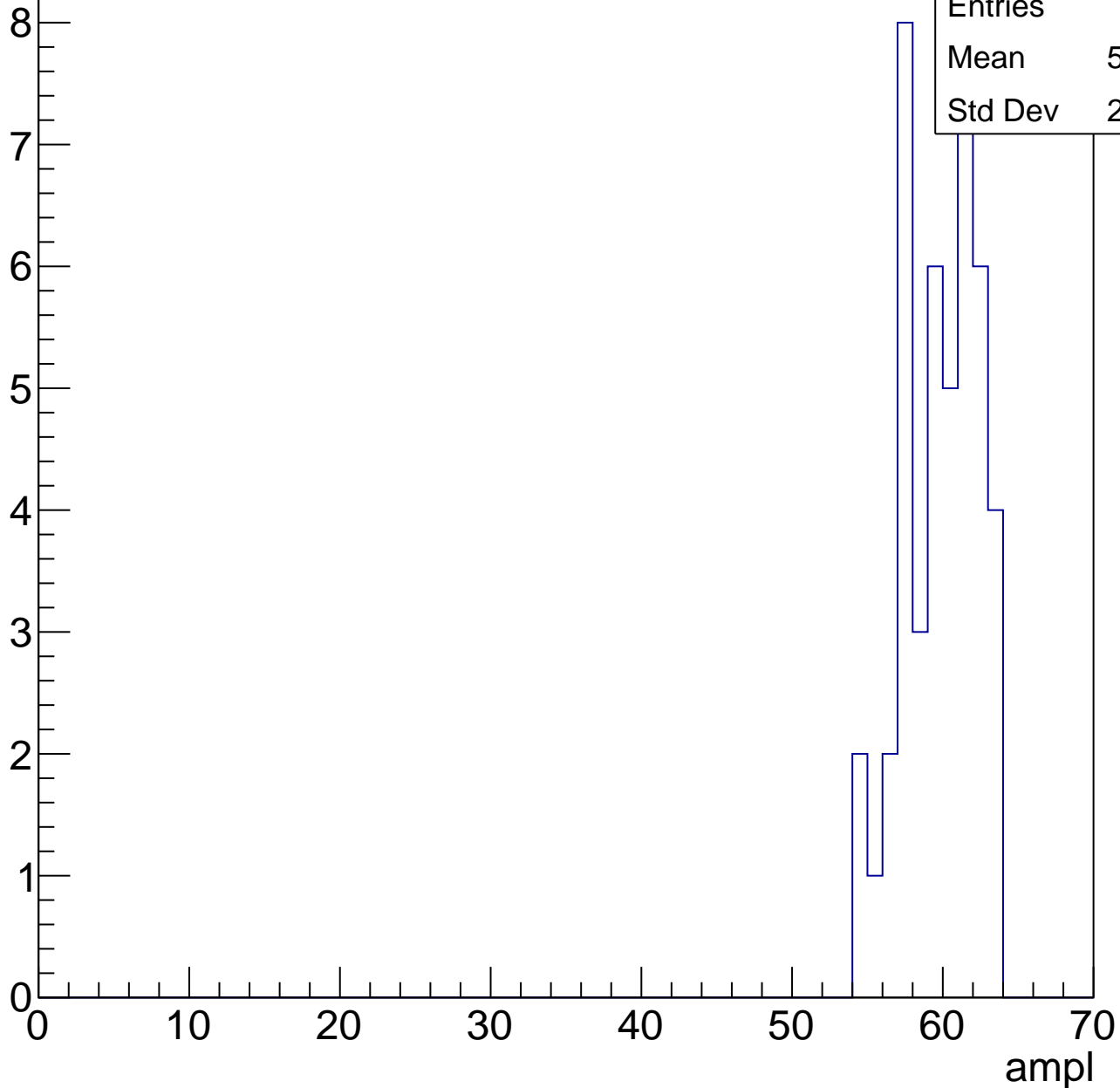
| | |
|---------|-------|
| Entries | 70 |
| Mean | 54.16 |
| Std Dev | 3.362 |



B0L102S, U20-ch87, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

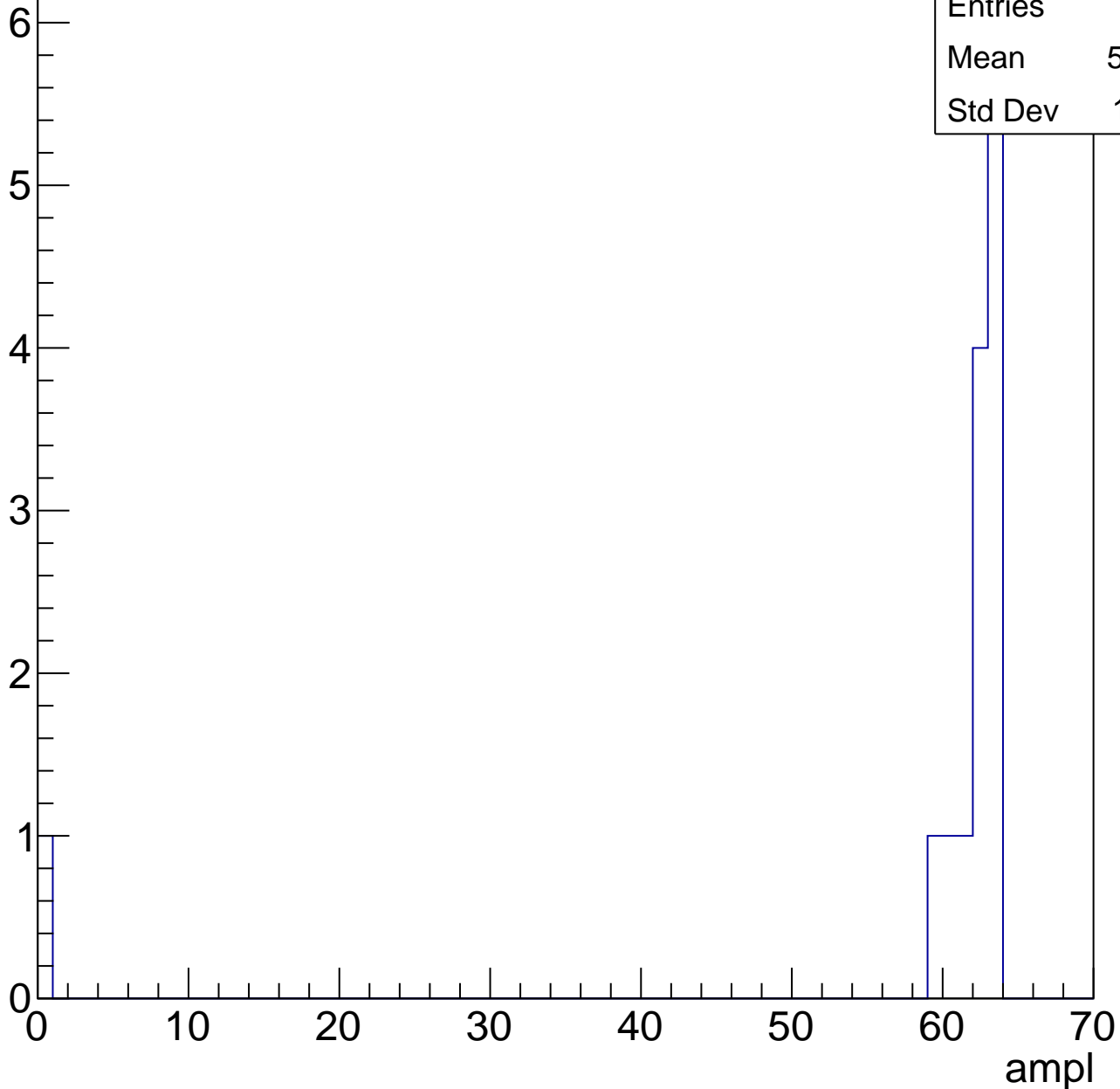


B0L102S, U20-ch87, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

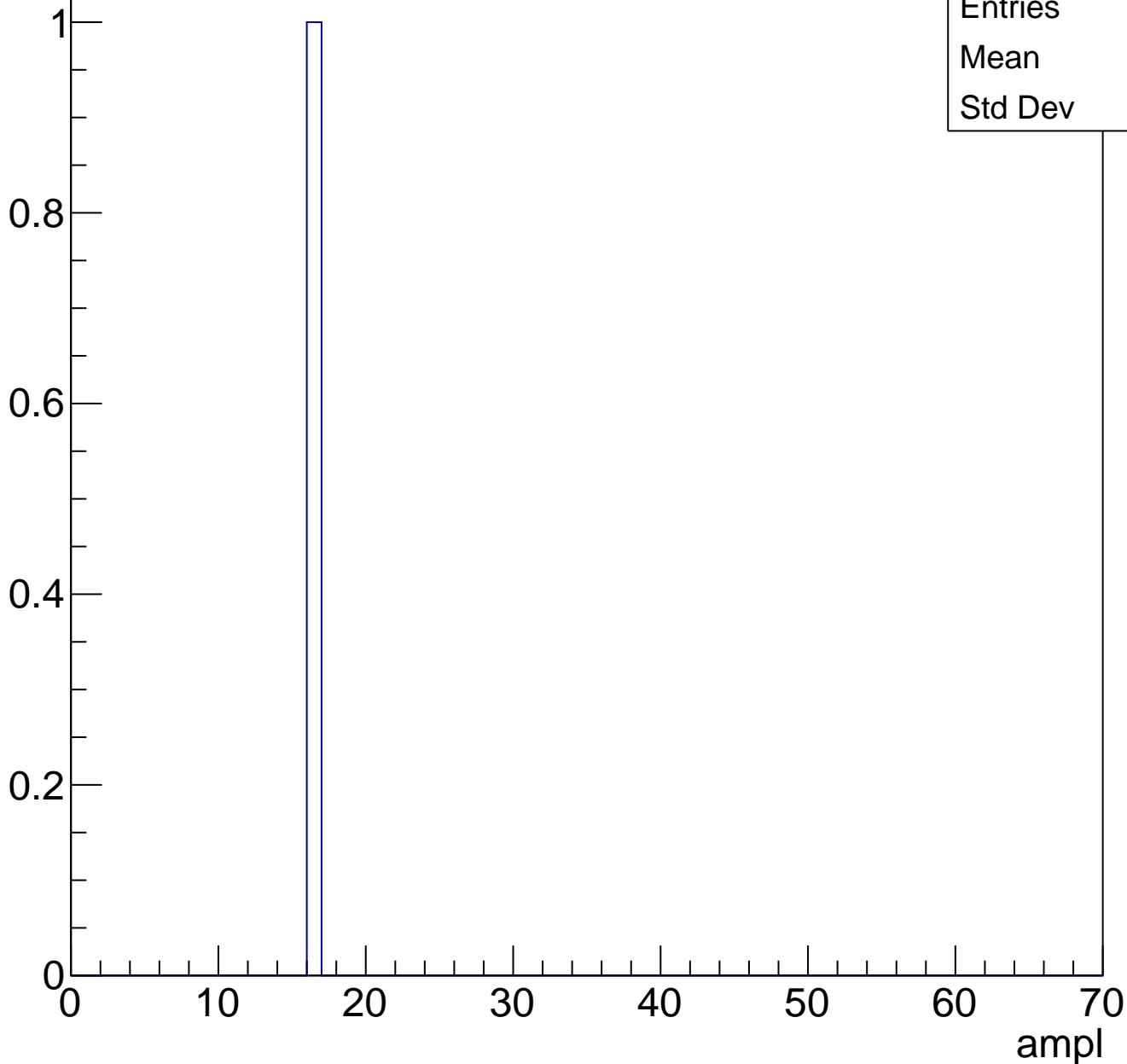
| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.57 |
| Std Dev | 16.01 |



B0L102S, U20-ch87, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch88, adc0

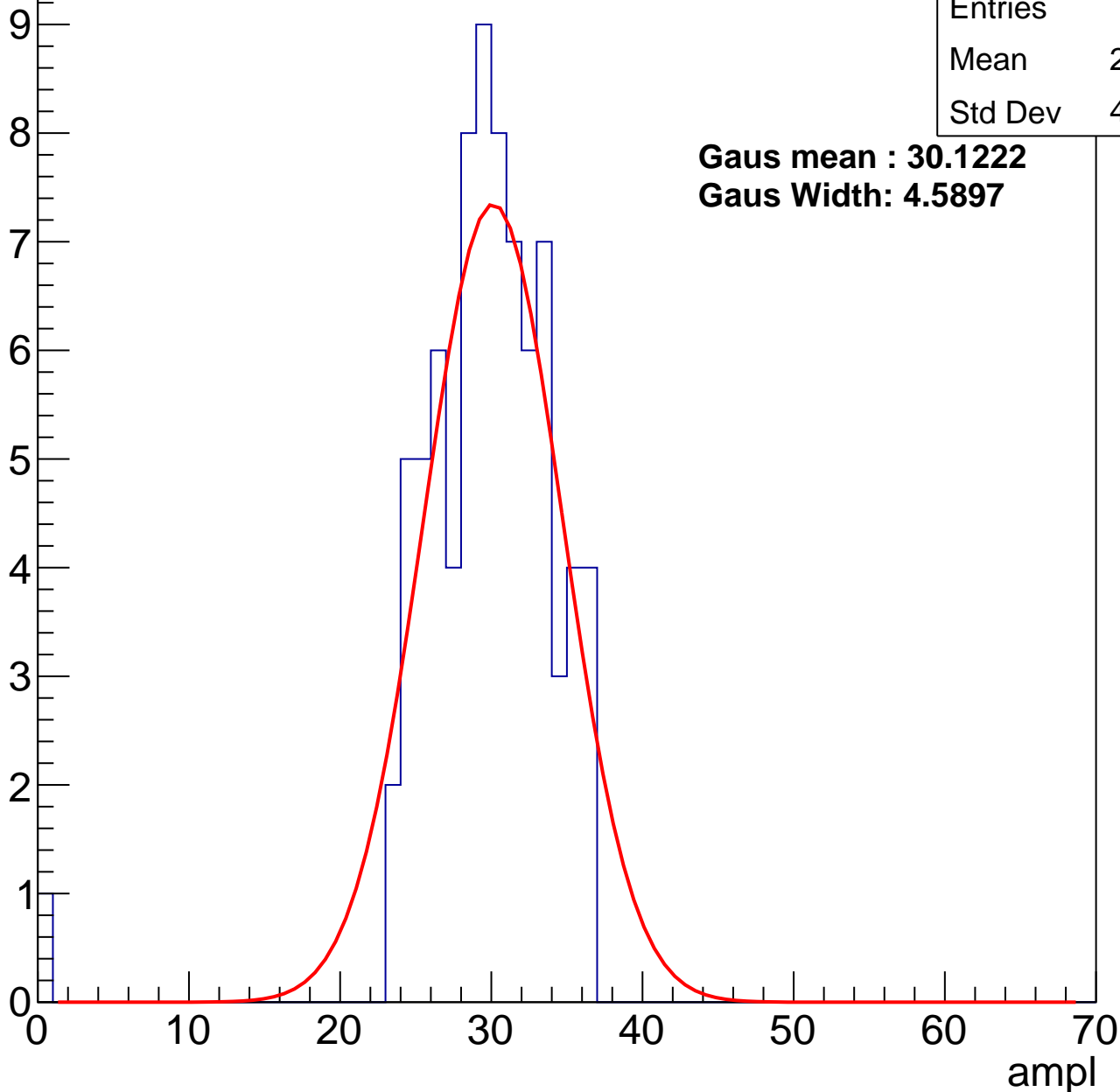
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.19 |
| Std Dev | 4.787 |

Gaus mean : 30.1222

Gaus Width: 4.5897



B0L102S, U20-ch88, adc1

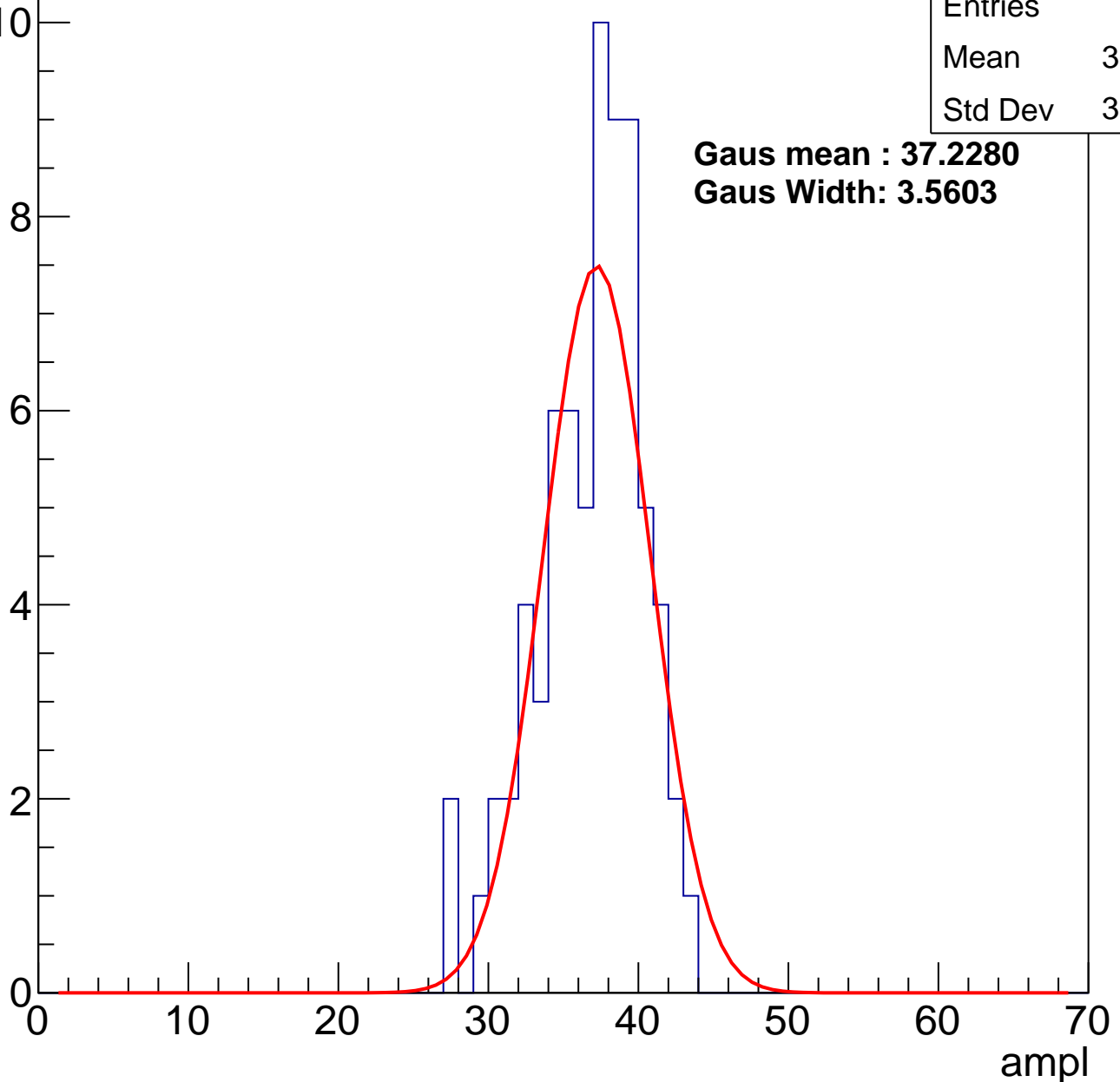
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 36.34 |
| Std Dev | 3.508 |

Gaus mean : 37.2280

Gaus Width: 3.5603



B0L102S, U20-ch88, adc2

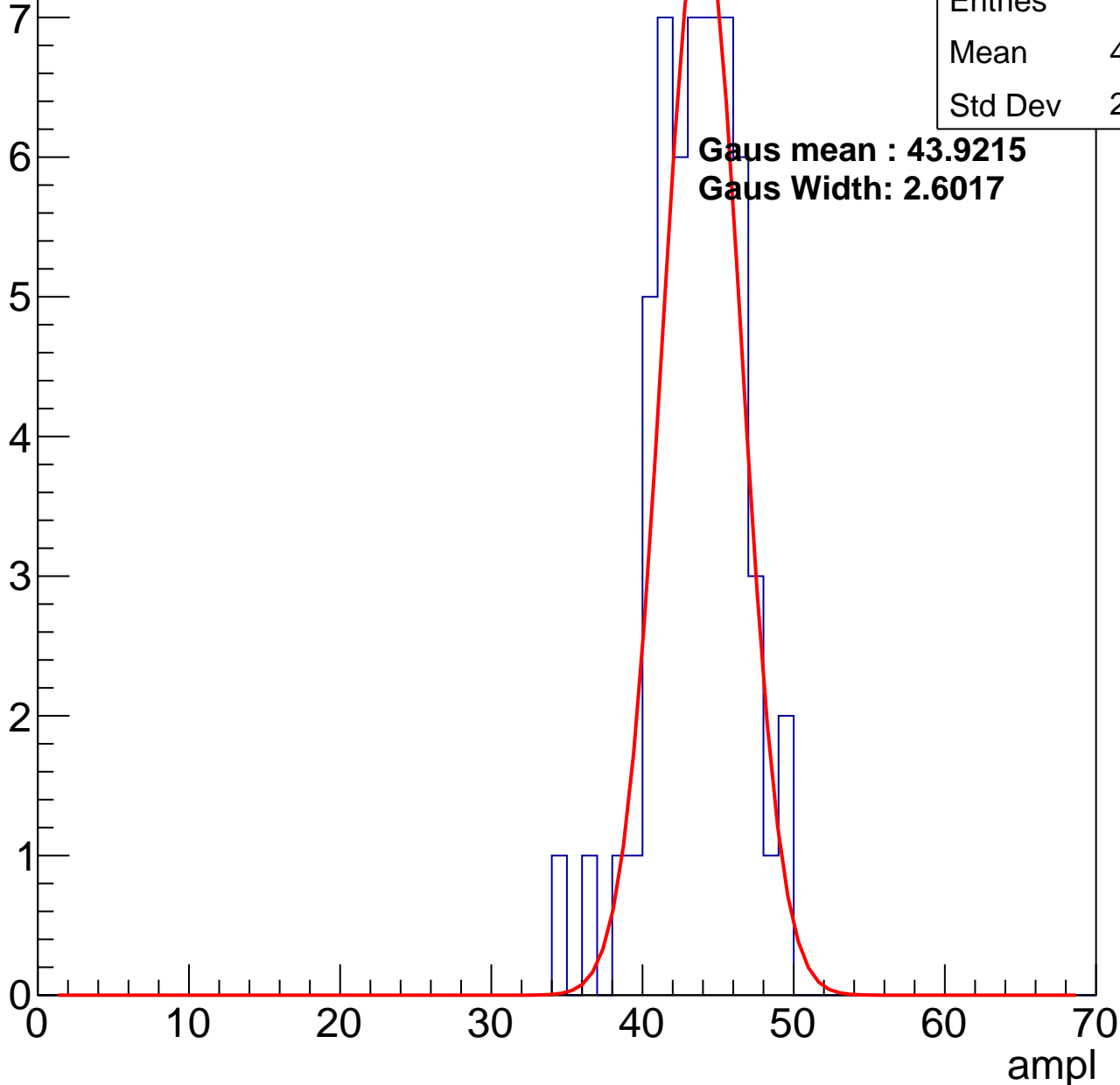
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.15 |
| Std Dev | 2.963 |

Gaus mean : 43.9215

Gaus Width: 2.6017

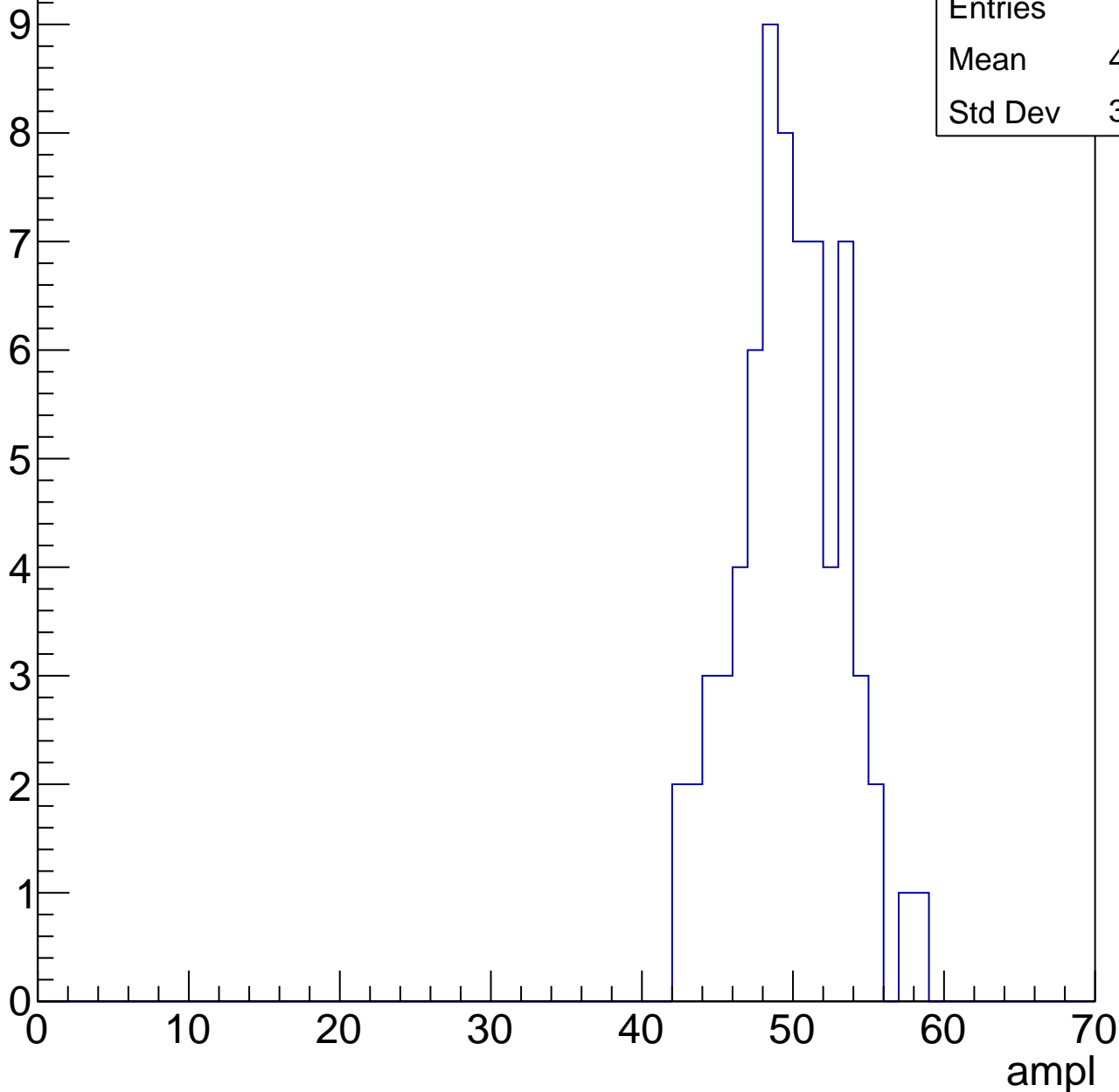


B0L102S, U20-ch88, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 49.28 |
| Std Dev | 3.472 |

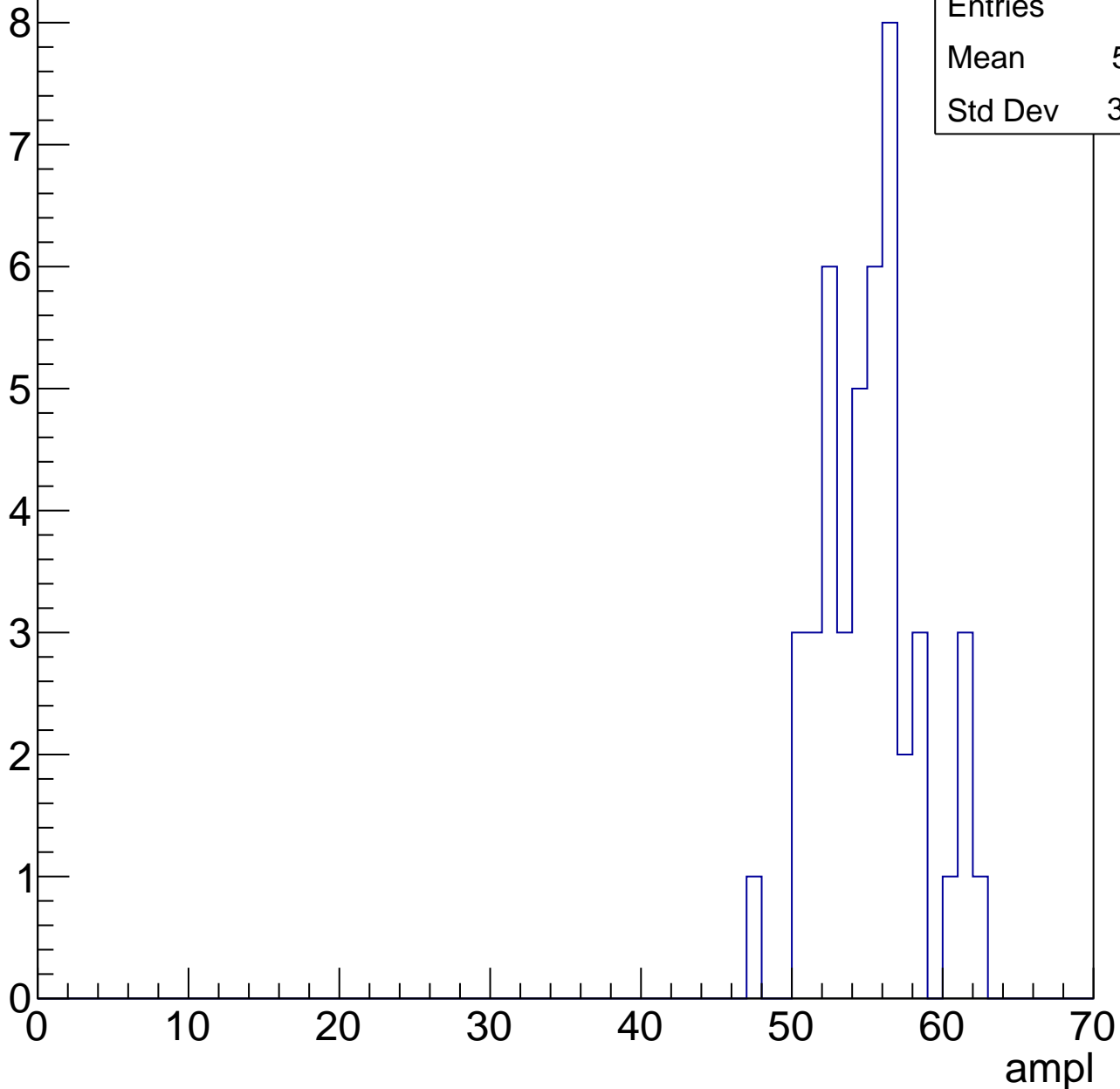


B0L102S, U20-ch88, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 54.71 |
| Std Dev | 3.263 |

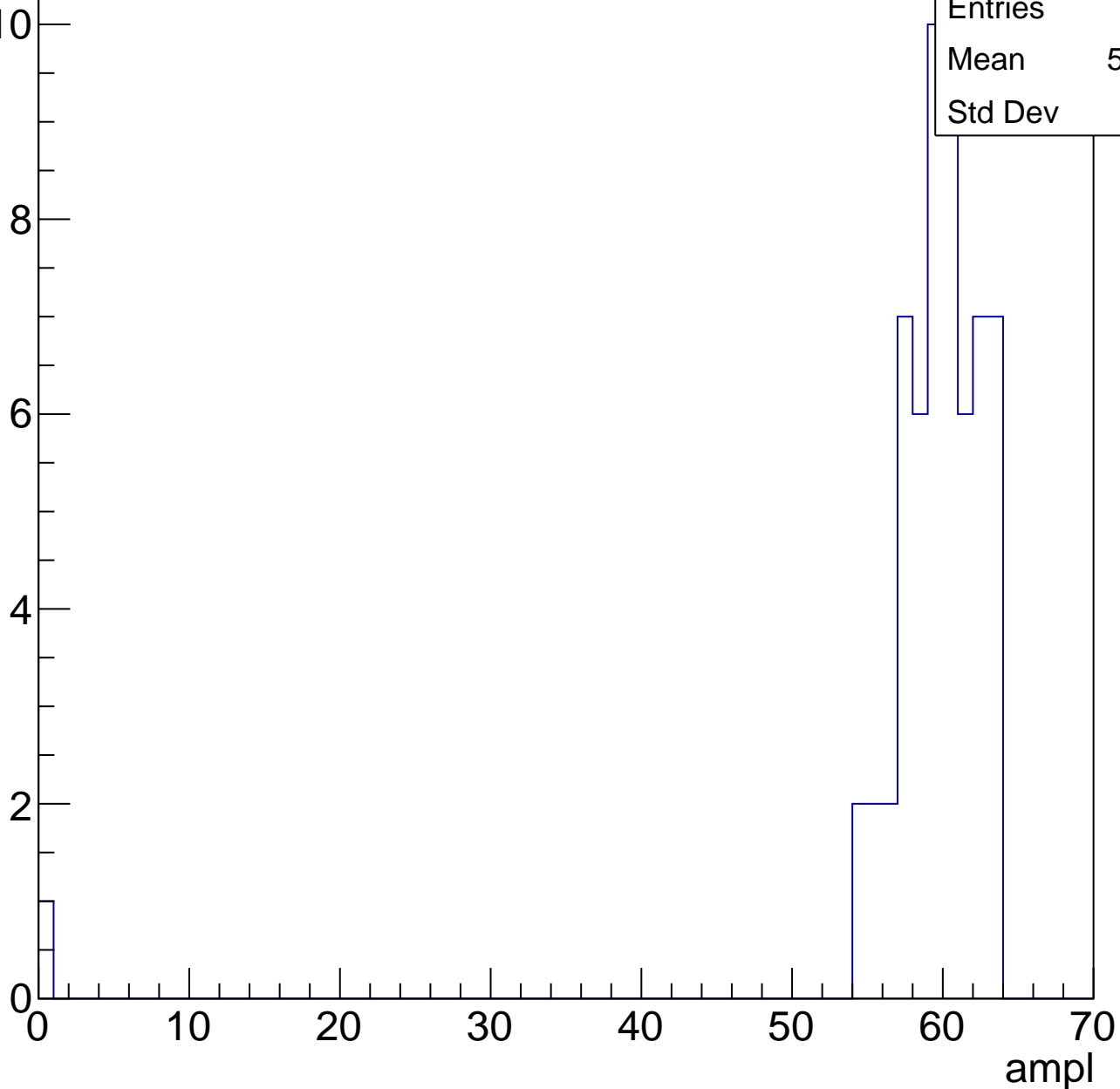


B0L102S, U20-ch88, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.44 |
| Std Dev | 8.03 |



B0L102S, U20-ch88, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch88, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch89, adc0

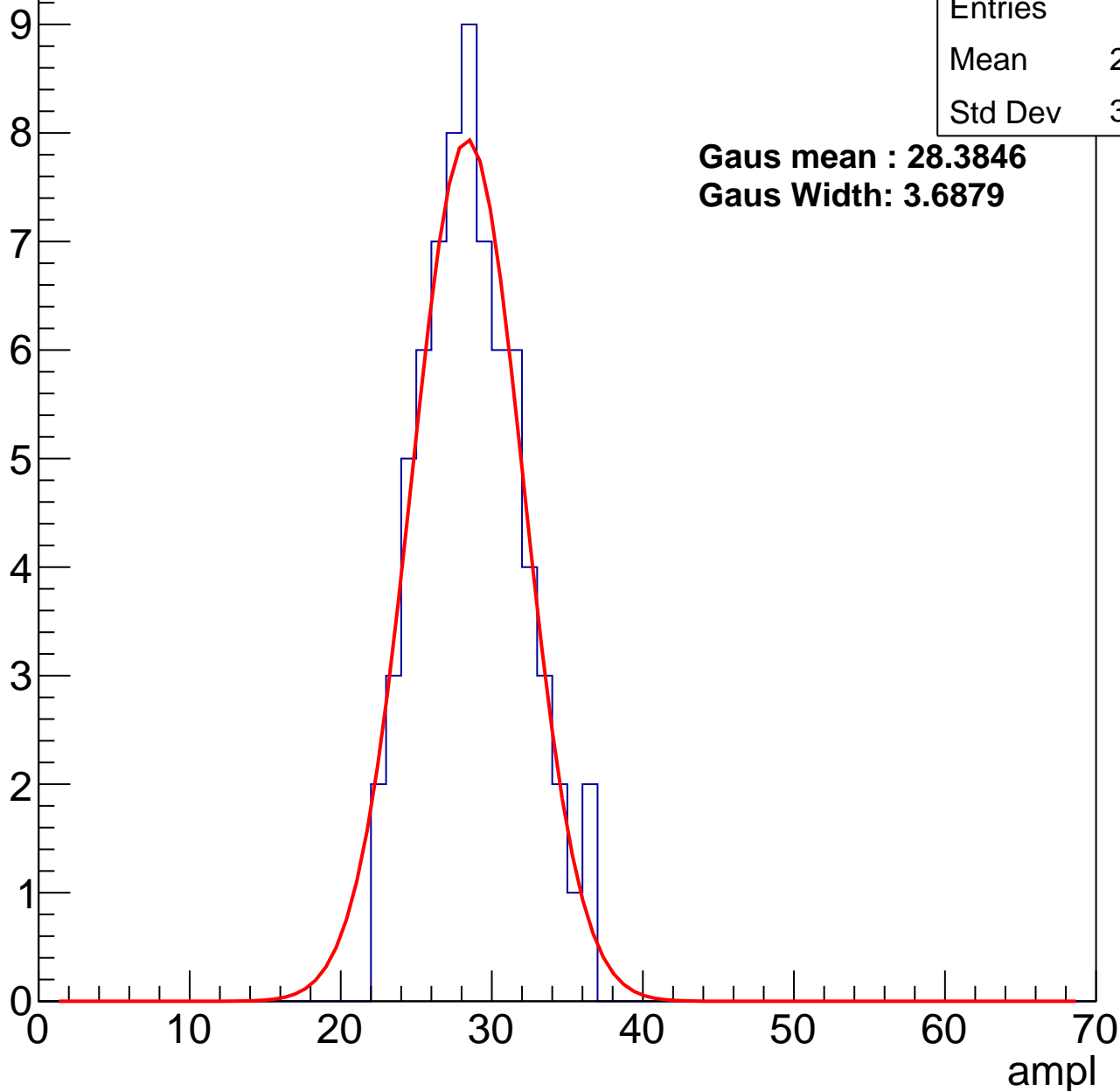
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.23 |
| Std Dev | 3.328 |

Gaus mean : 28.3846

Gaus Width: 3.6879



B0L102S, U20-ch89, adc1

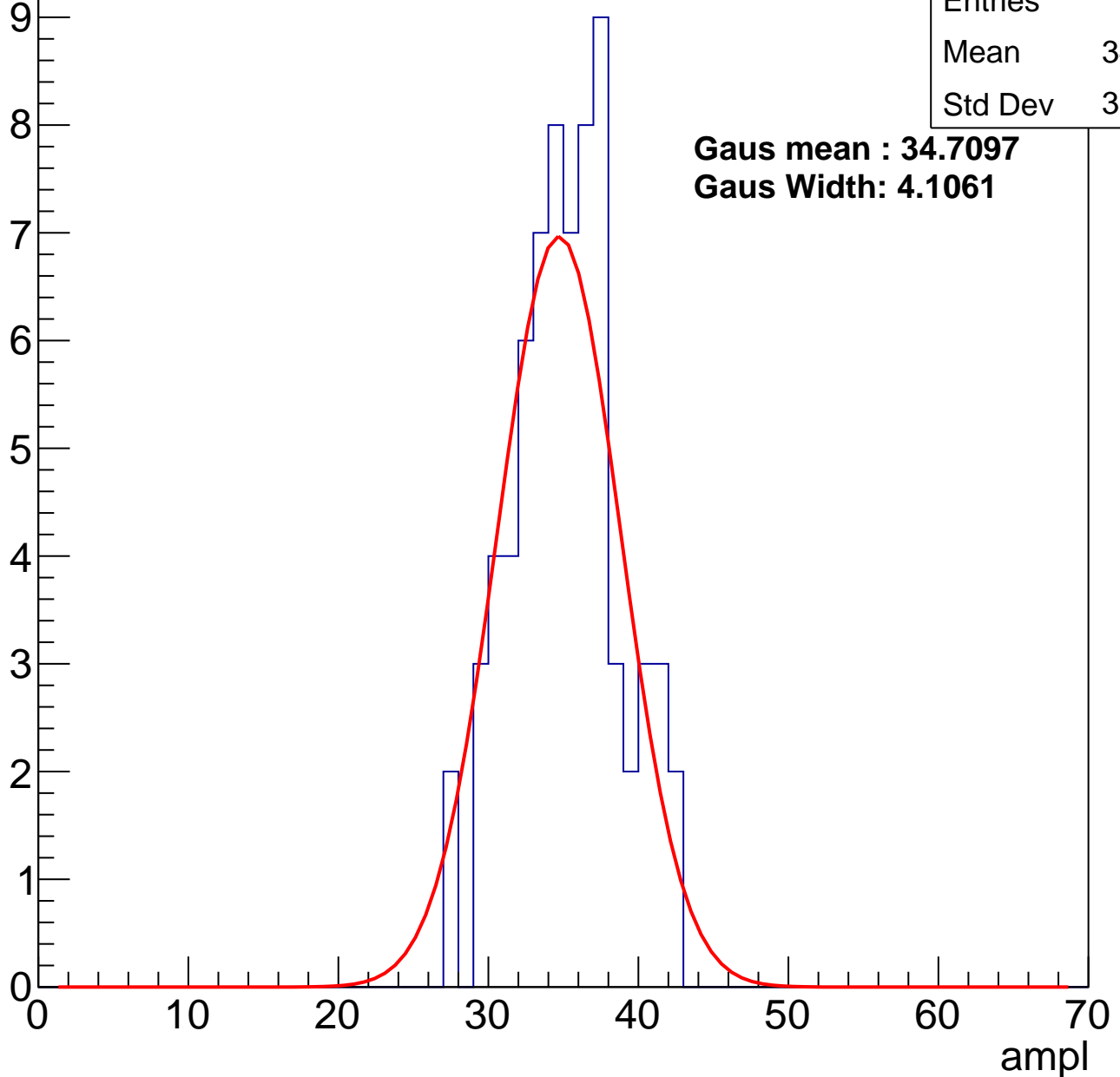
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 34.72 |
| Std Dev | 3.497 |

Gaus mean : 34.7097

Gaus Width: 4.1061



B0L102S, U20-ch89, adc2

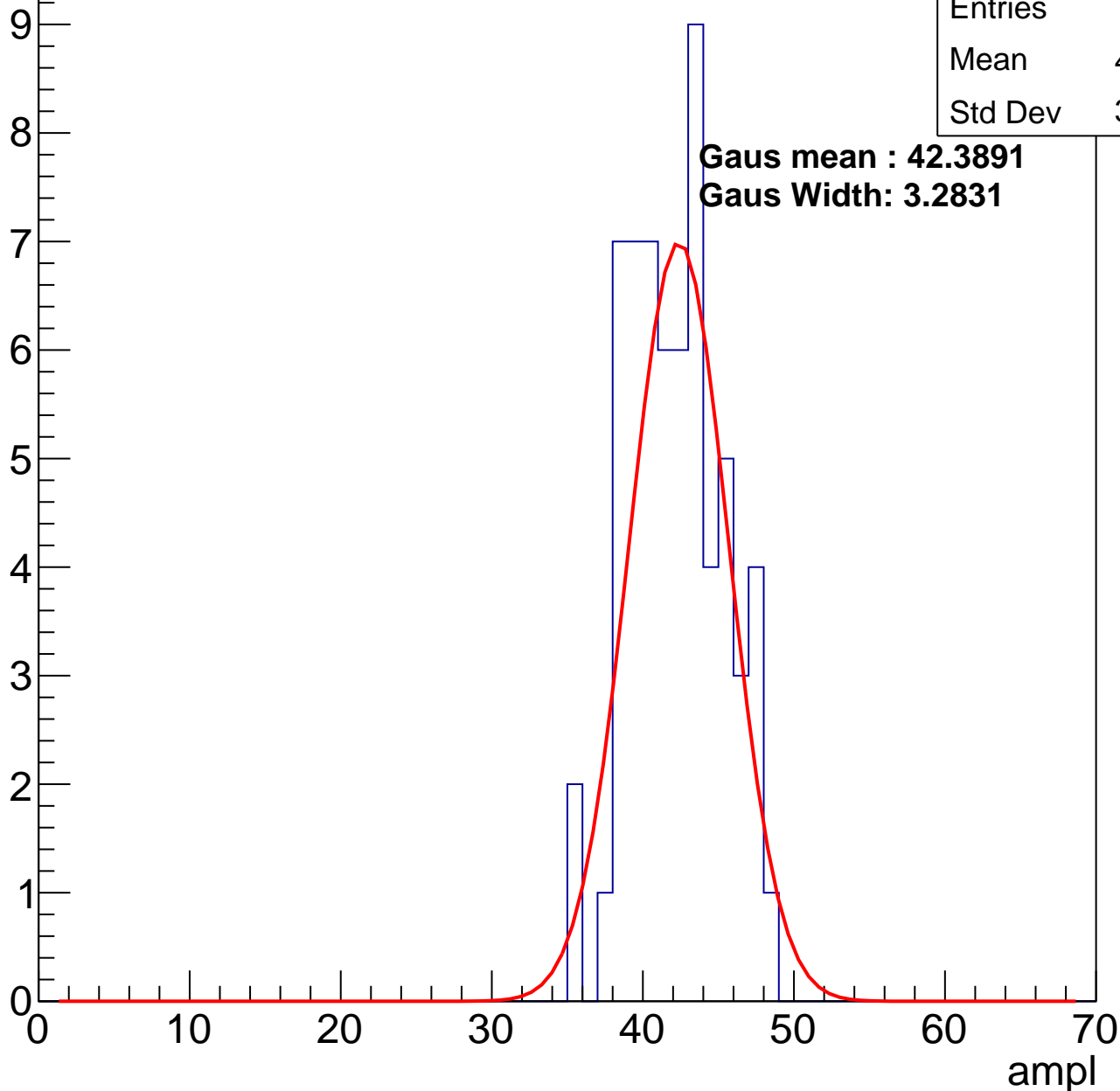
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.71 |
| Std Dev | 3.061 |

Gaus mean : 42.3891

Gaus Width: 3.2831

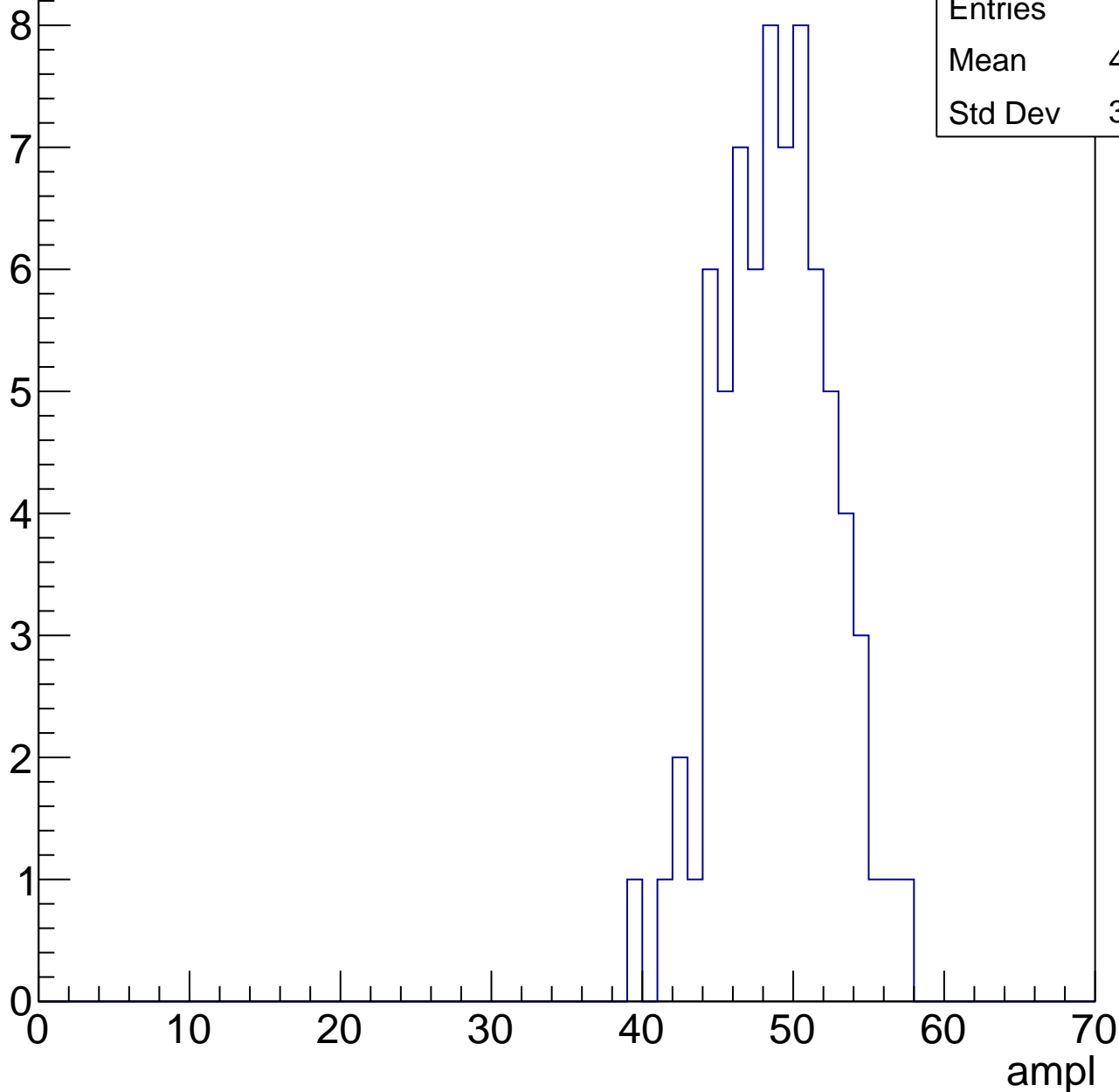


B0L102S, U20-ch89, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 48.42 |
| Std Dev | 3.634 |

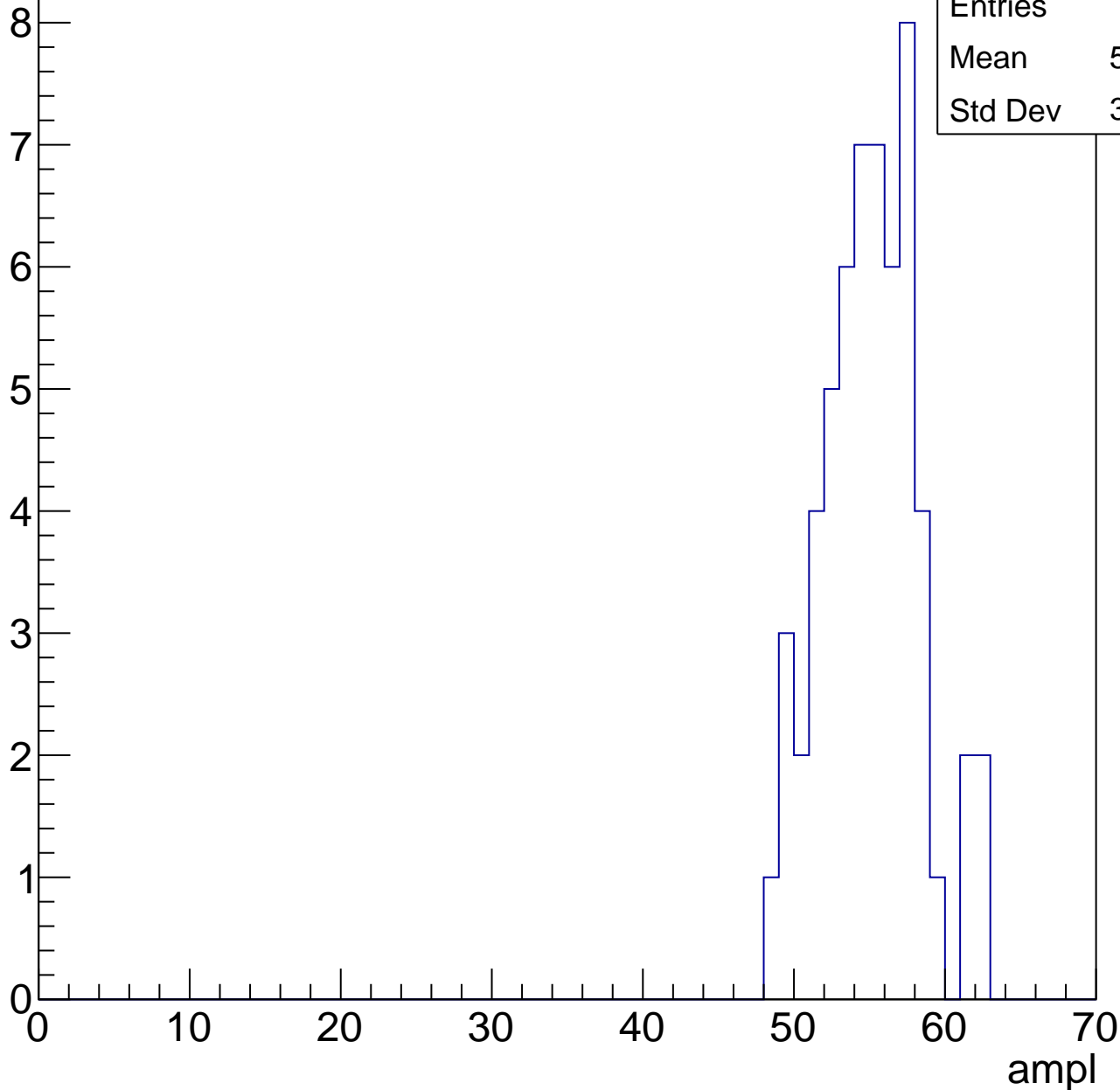


B0L102S, U20-ch89, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 54.64 |
| Std Dev | 3.215 |

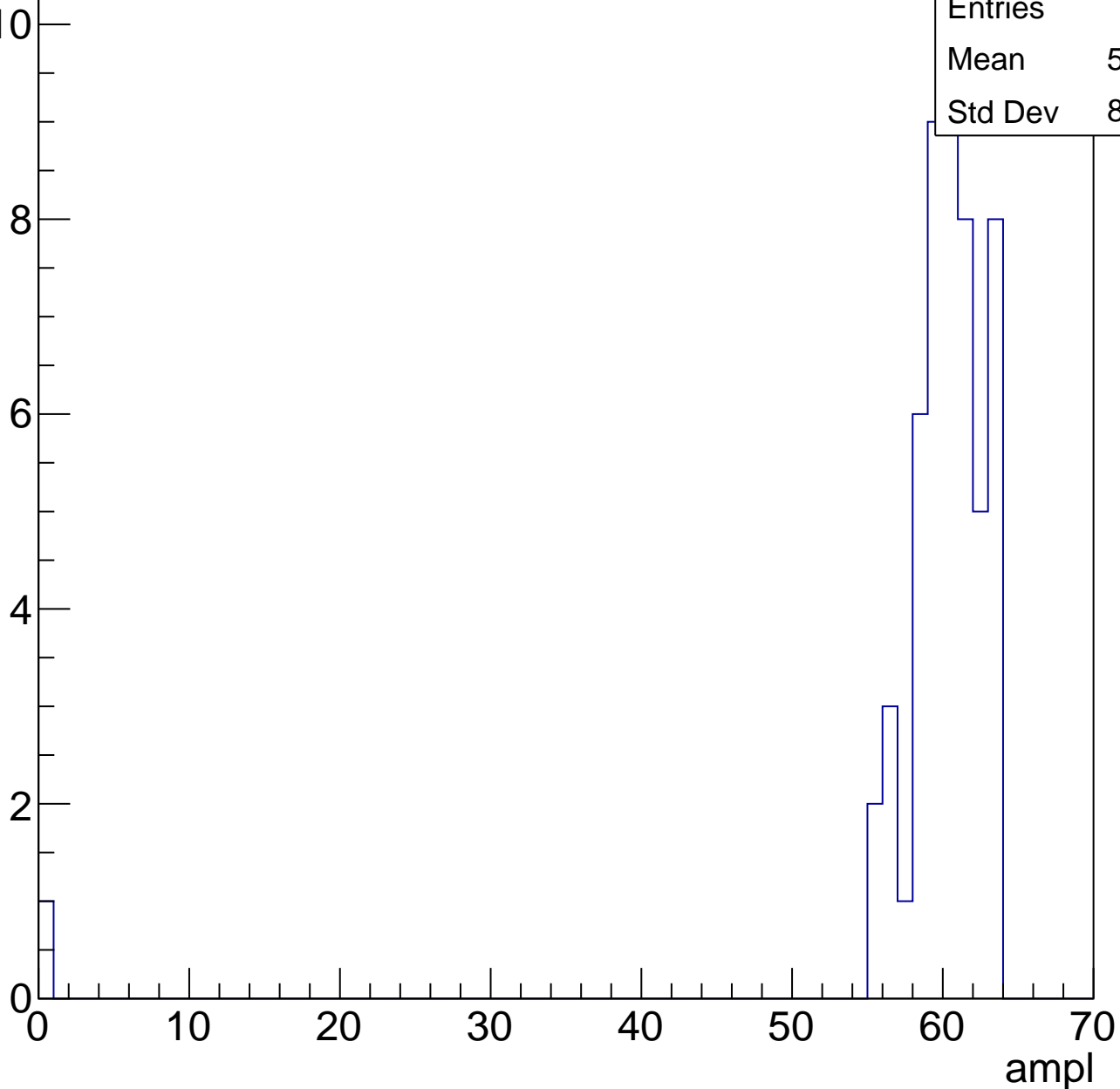


B0L102S, U20-ch89, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.79 |
| Std Dev | 8.426 |



B0L102S, U20-ch89, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch89, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch90, adc0

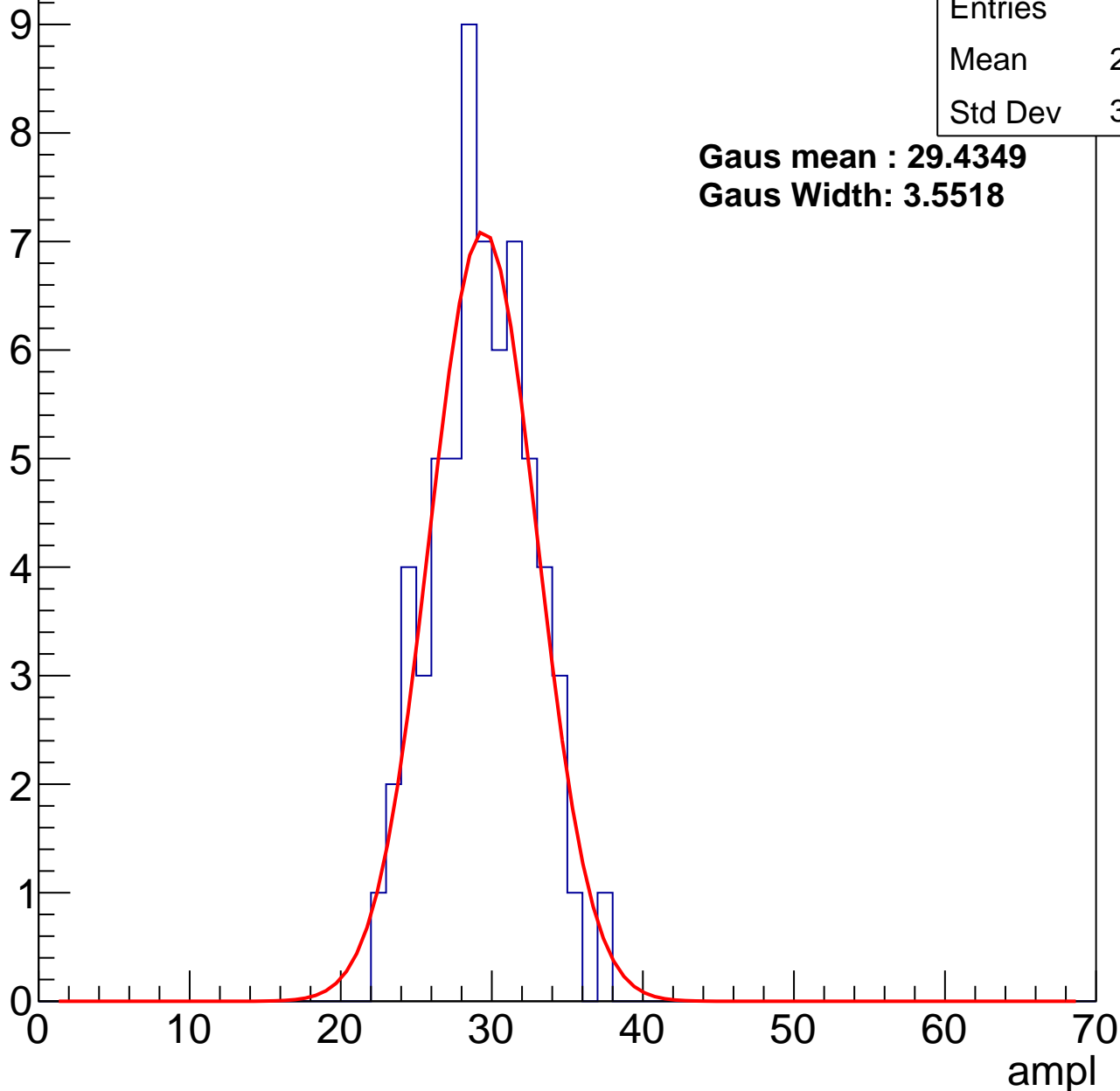
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.92 |
| Std Dev | 3.238 |

Gaus mean : 29.4349

Gaus Width: 3.5518



B0L102S, U20-ch90, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 93 |
| Mean | 36.66 |
| Std Dev | 4.12 |

Gaus mean : 36.9878

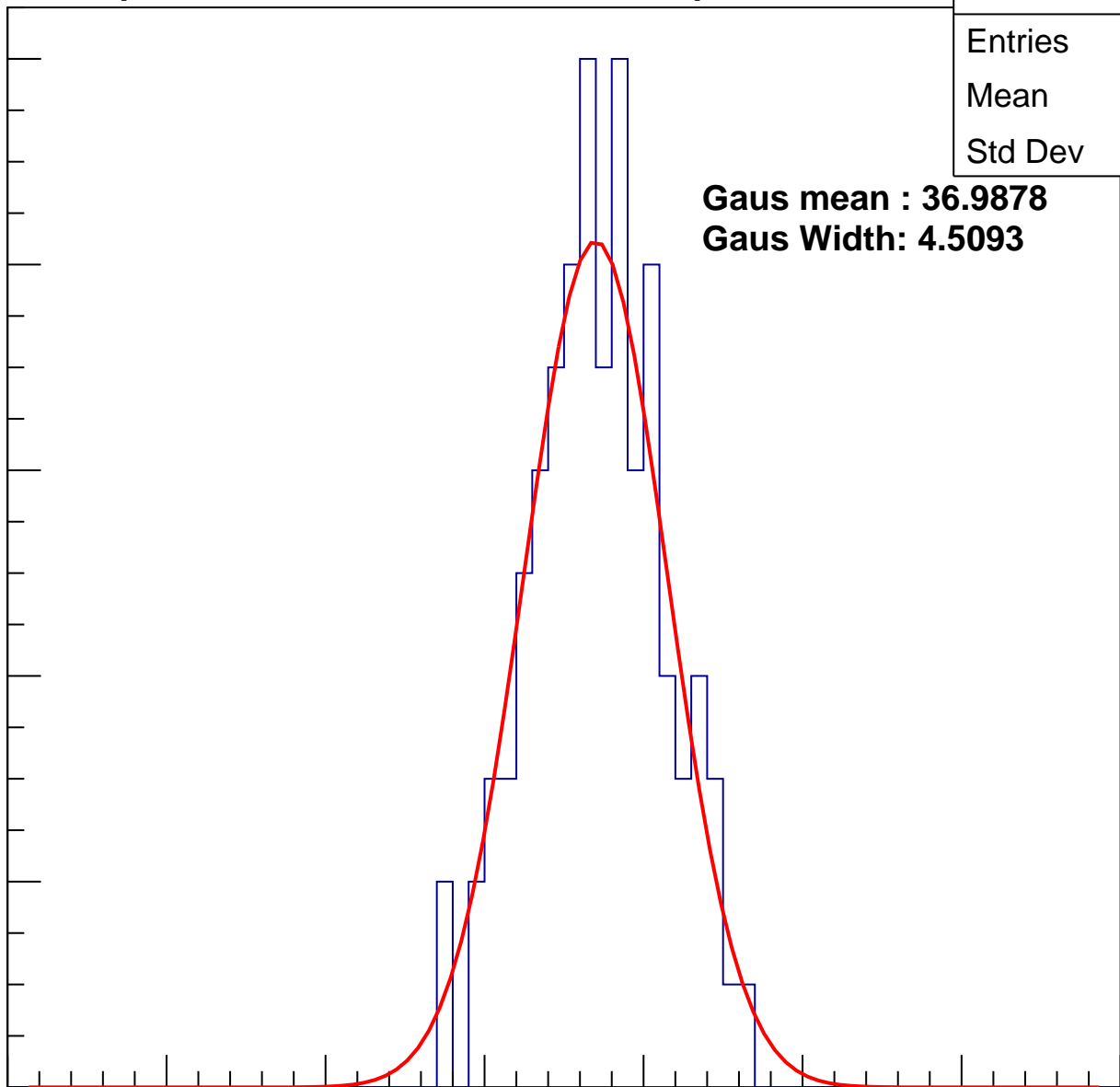
Gaus Width: 4.5093

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch90, adc2

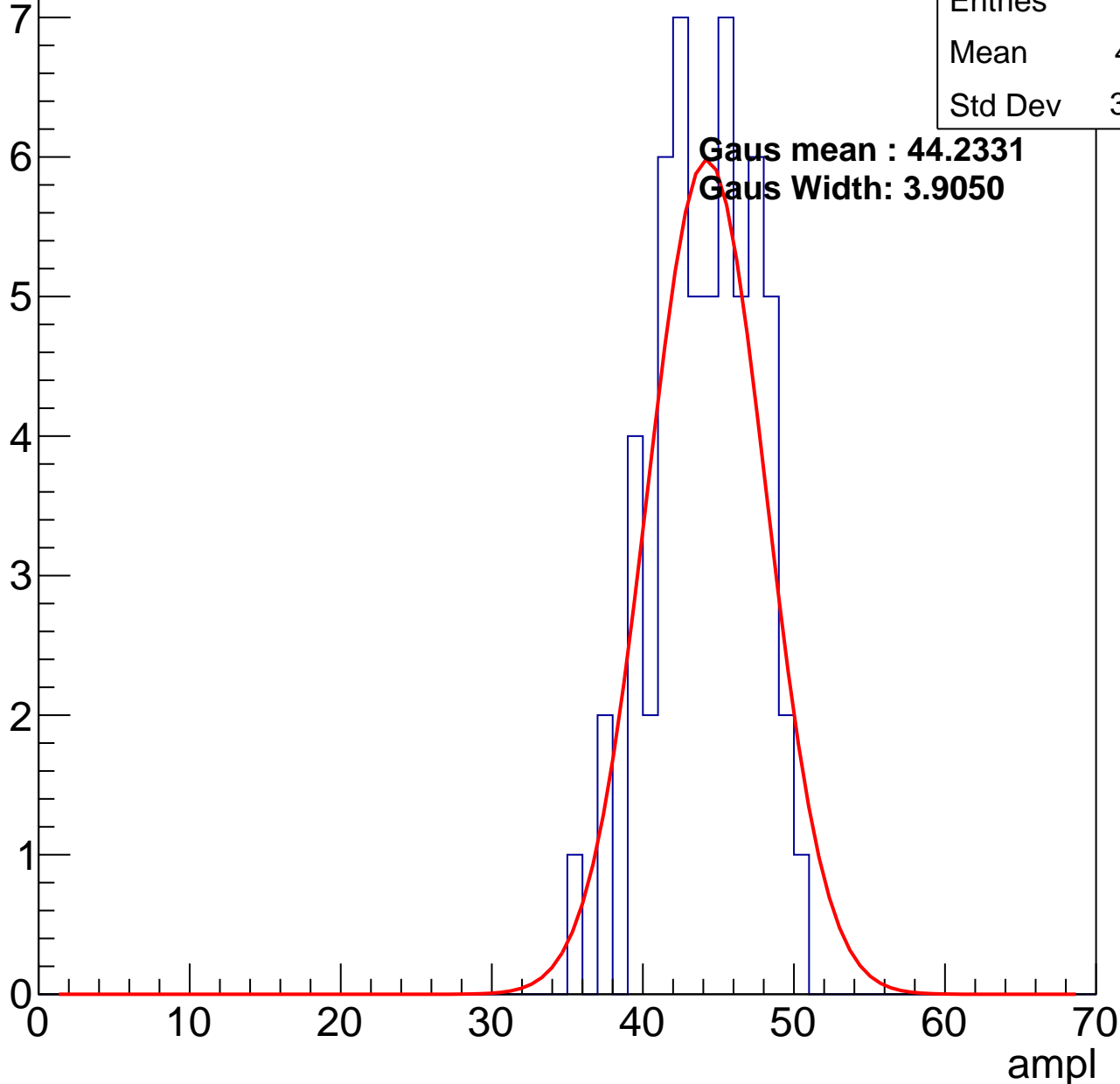
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 43.71 |
| Std Dev | 3.322 |

Gaus mean : 44.2331

Gaus Width: 3.9050

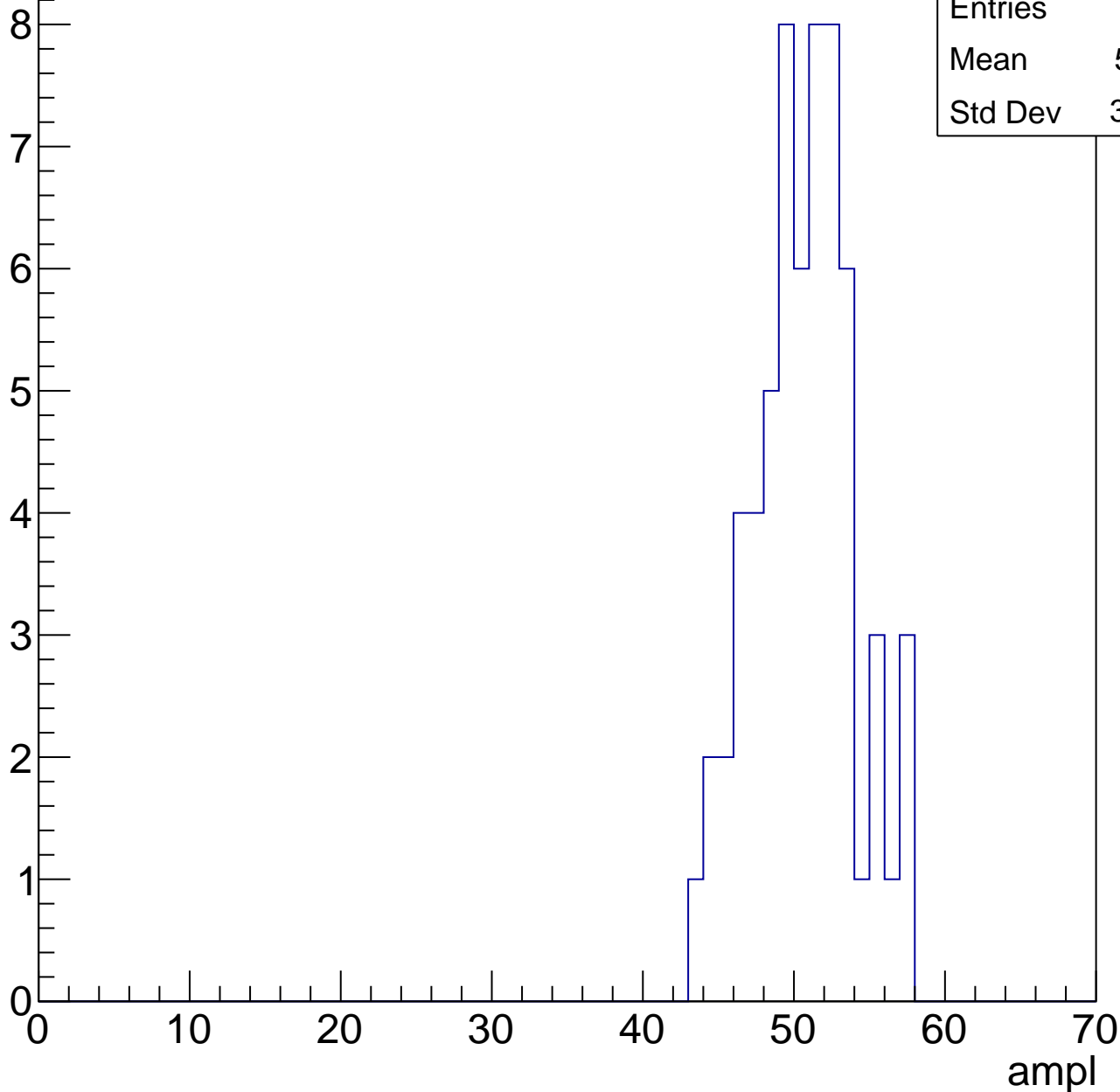


B0L102S, U20-ch90, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 50.21 |
| Std Dev | 3.273 |

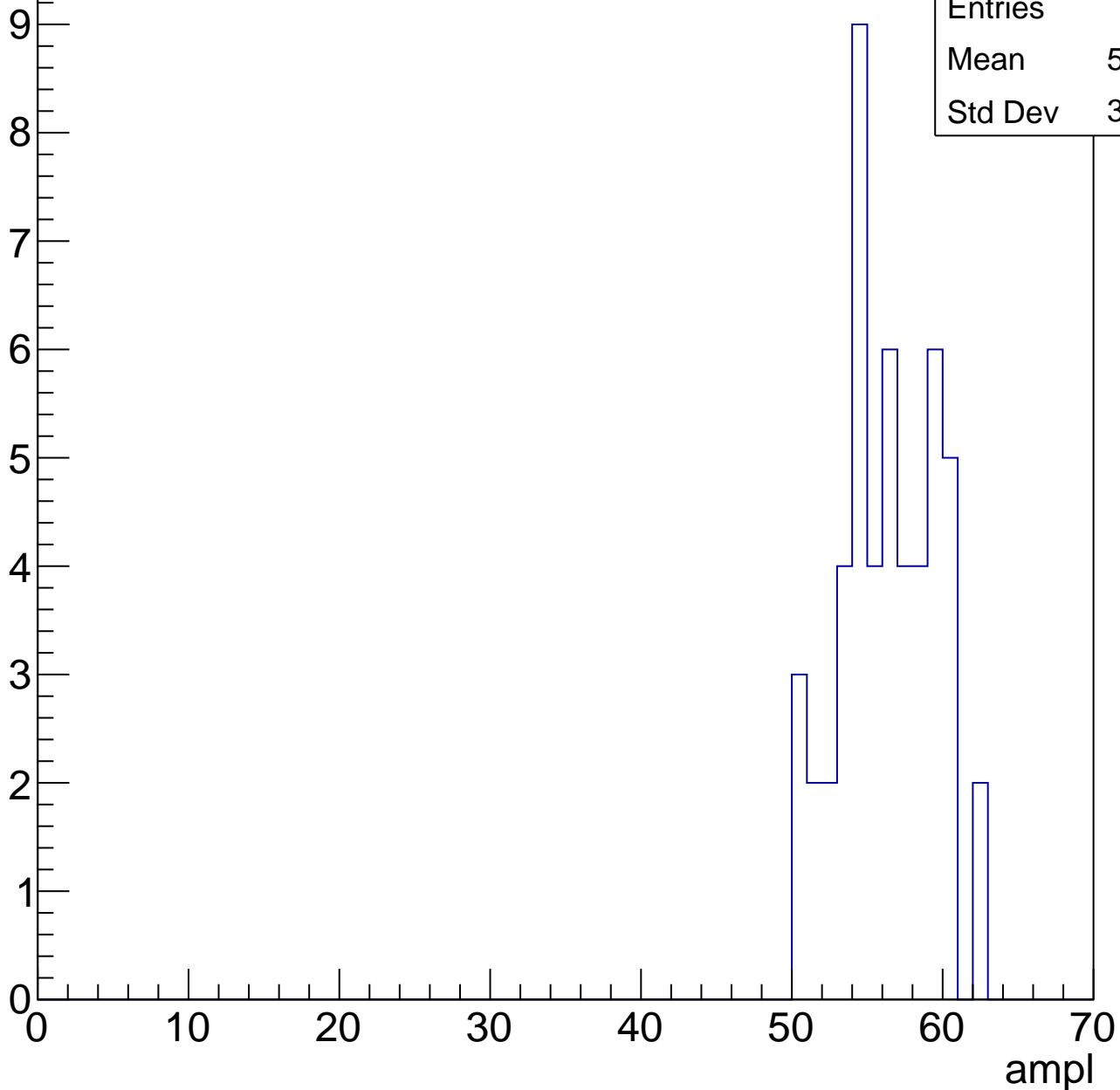


B0L102S, U20-ch90, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 55.84 |
| Std Dev | 3.102 |

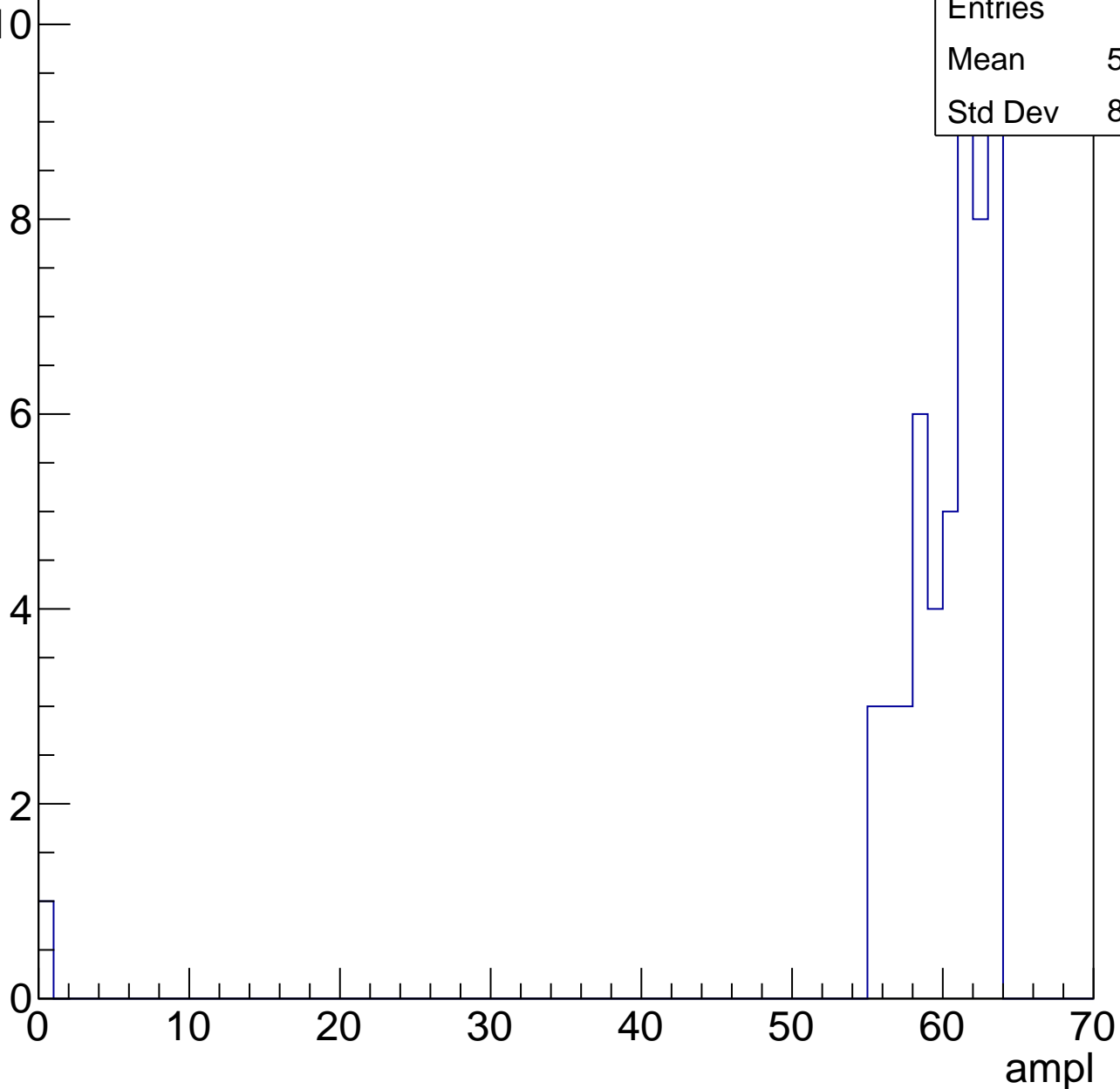


B0L102S, U20-ch90, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.94 |
| Std Dev | 8.524 |



B0L102S, U20-ch90, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch90, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.333 |
| Std Dev | 10.37 |

B0L102S, U20-ch91, adc0

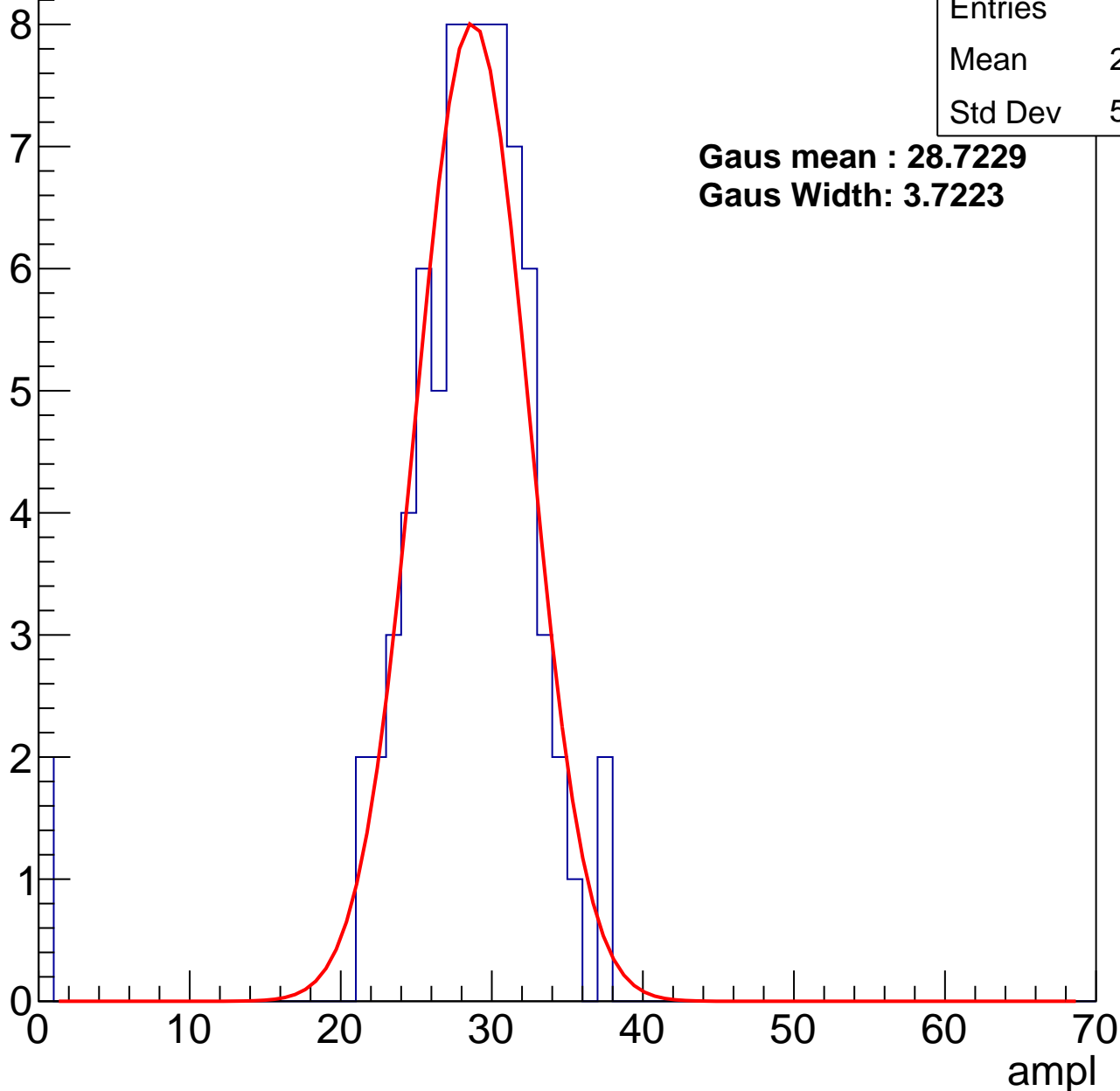
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 27.64 |
| Std Dev | 5.705 |

Gaus mean : 28.7229

Gaus Width: 3.7223



B0L102S, U20-ch91, adc1

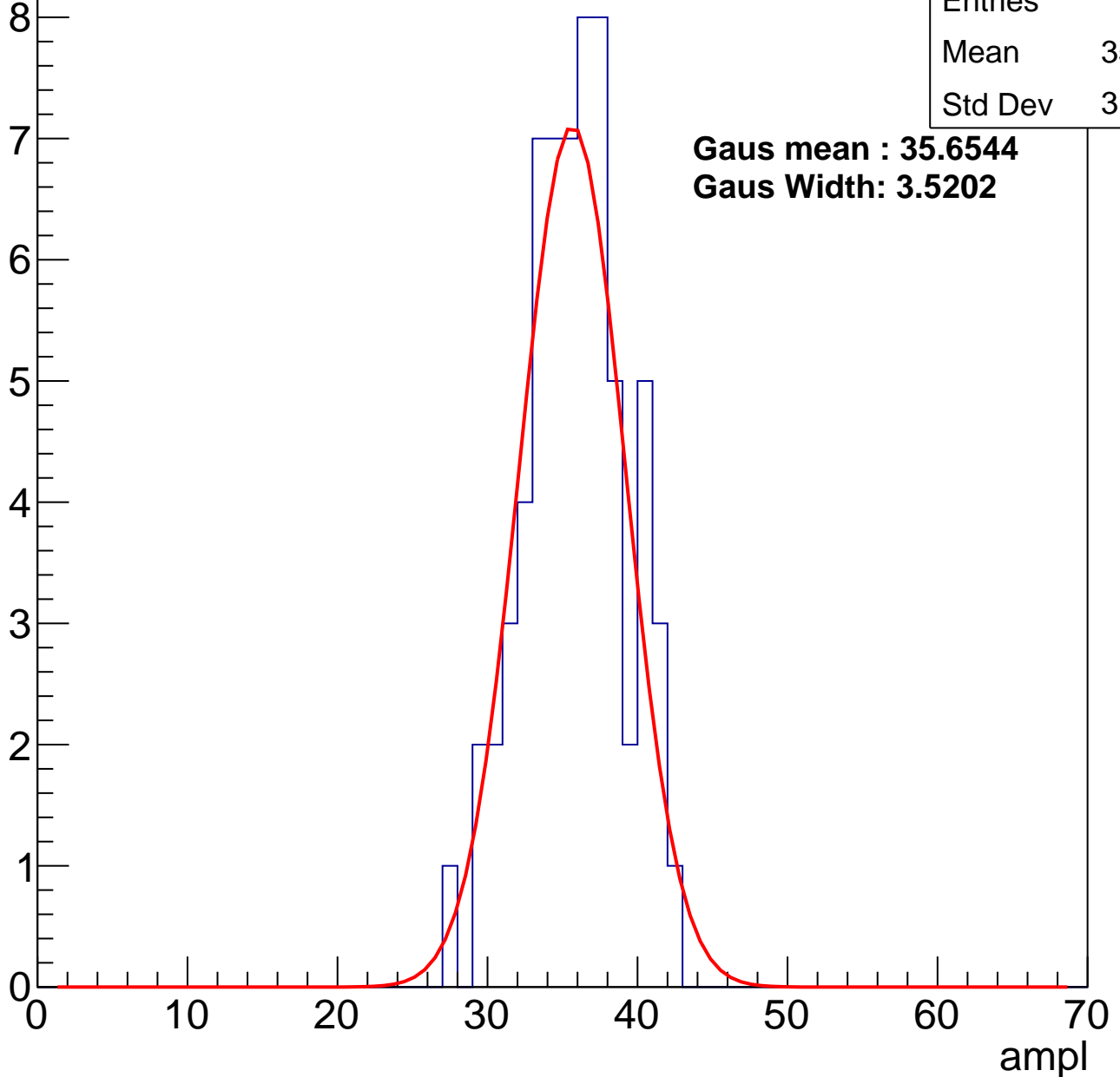
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.34 |
| Std Dev | 3.274 |

Gaus mean : 35.6544

Gaus Width: 3.5202



B0L102S, U20-ch91, adc2

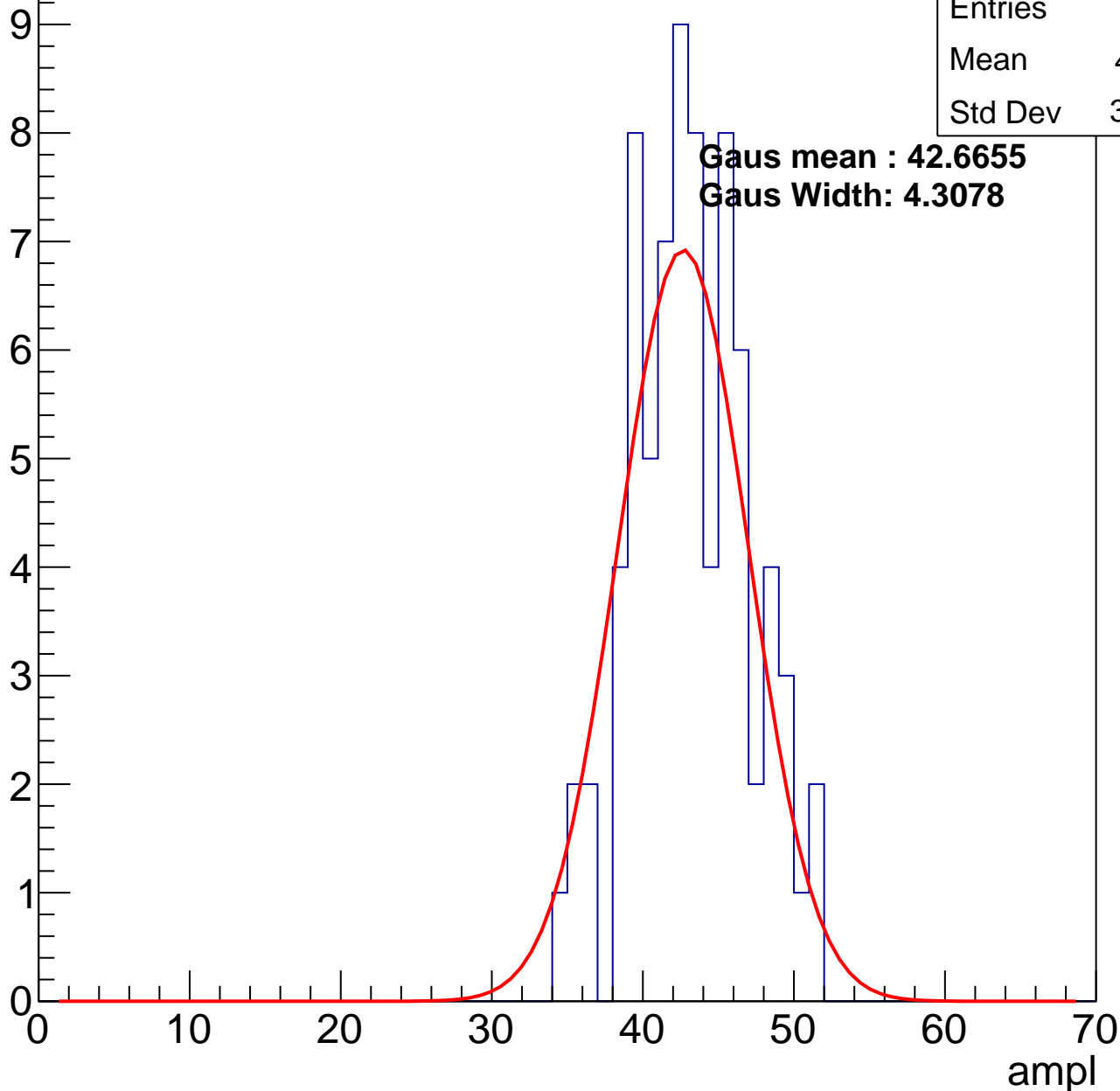
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.71 |
| Std Dev | 3.835 |

Gaus mean : 42.6655

Gaus Width: 4.3078

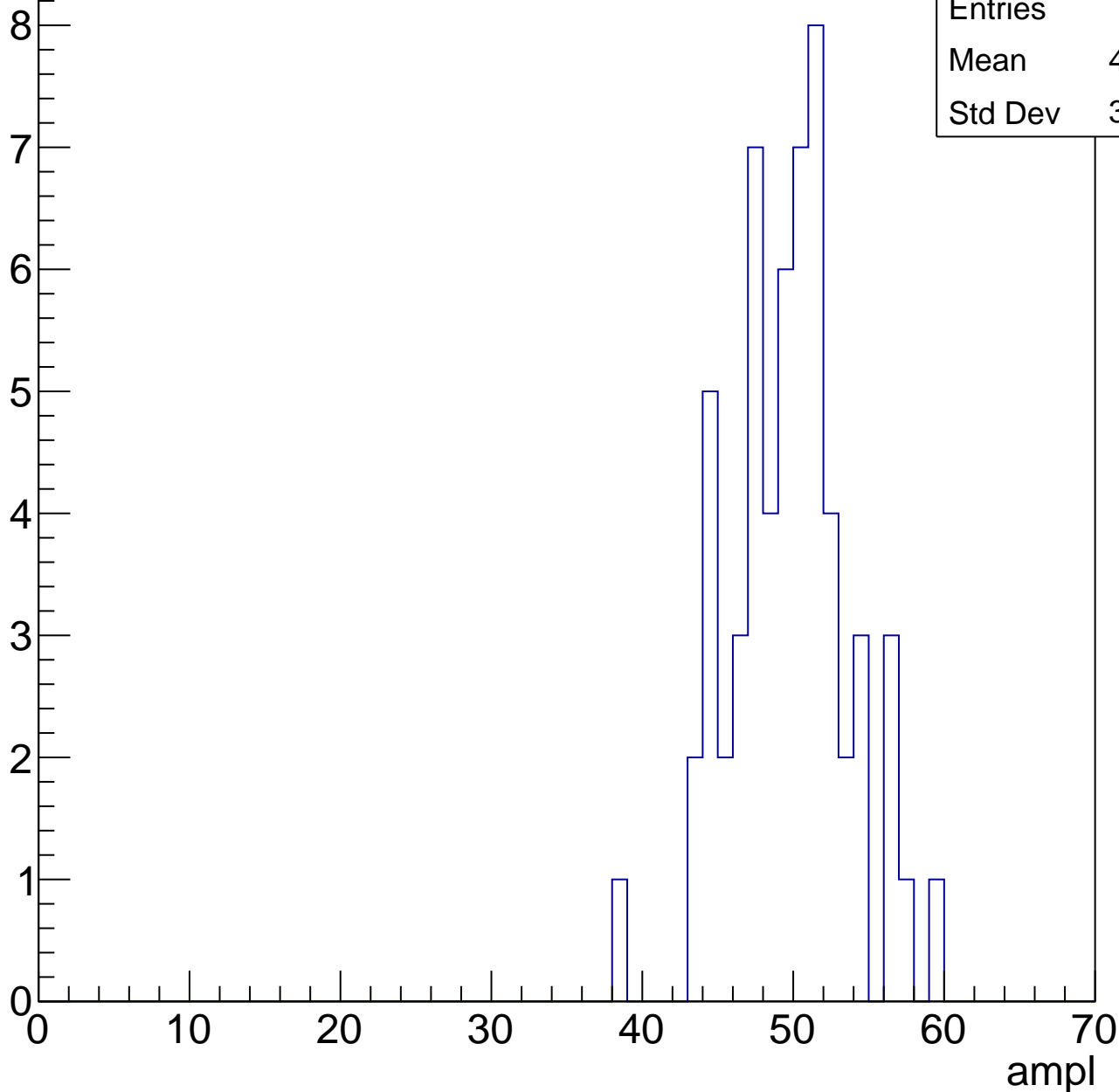


B0L102S, U20-ch91, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.24 |
| Std Dev | 3.903 |



B0L102S, U20-ch91, adc4

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.33 |
| Std Dev | 3.511 |

Entry

10

8

6

4

2

0

0

10

20

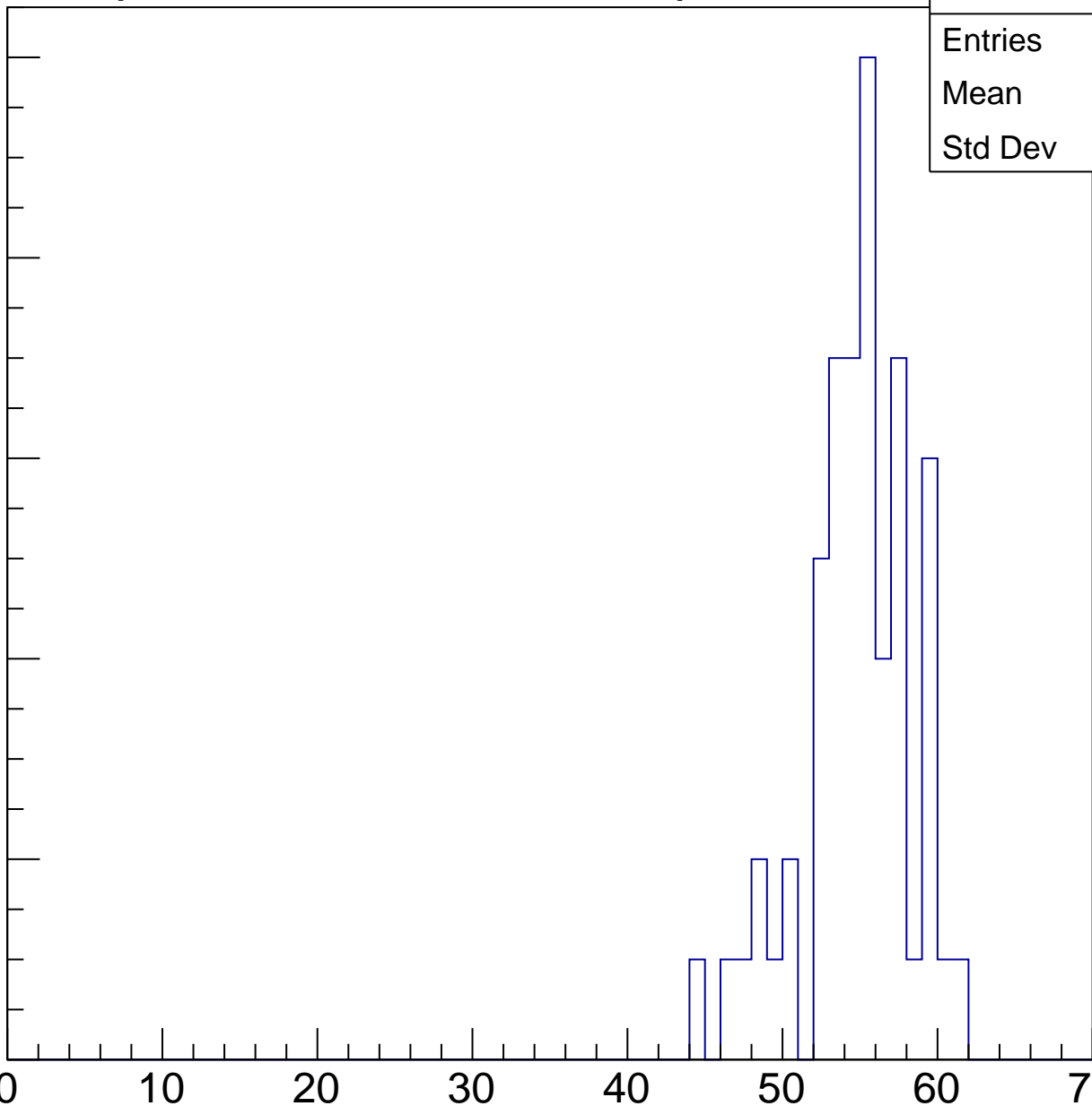
30

40

50

60

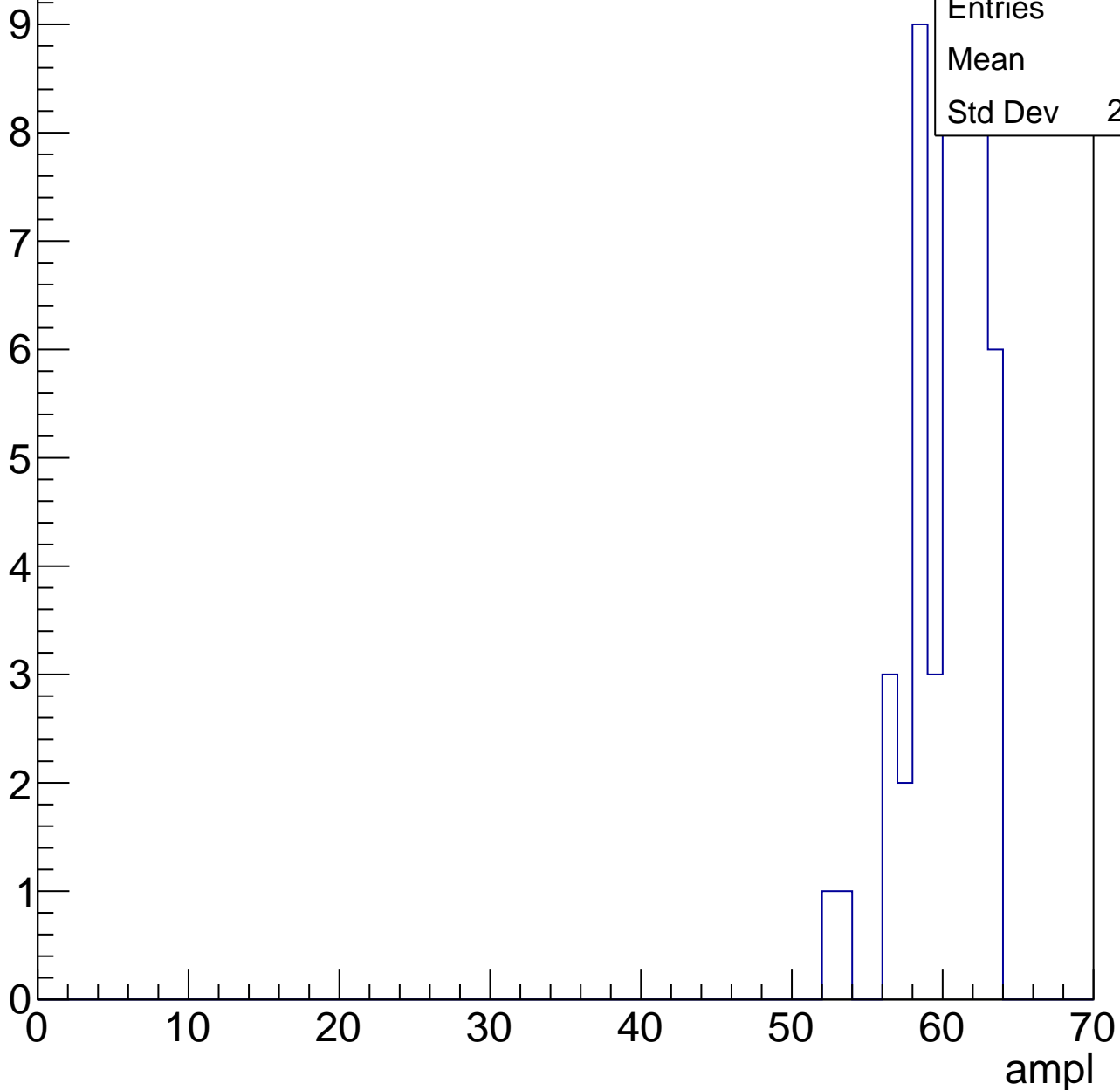
ampl



B0L102S, U20-ch91, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

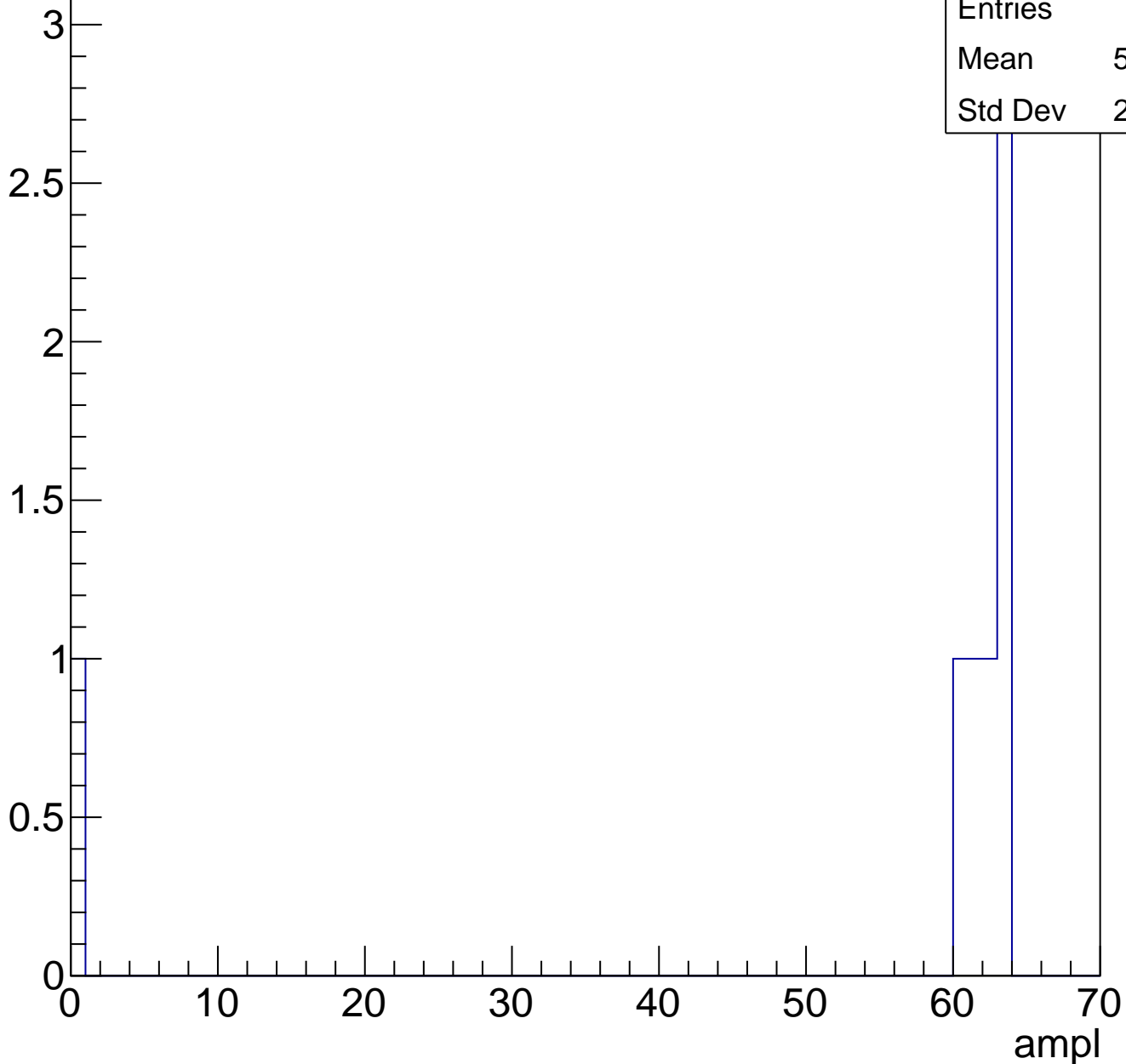


| | |
|---------|-------|
| Entries | 50 |
| Mean | 59.8 |
| Std Dev | 2.506 |

B0L102S, U20-ch91, adc6

calib_packv5_042523_0143.root, FC#12, port B1

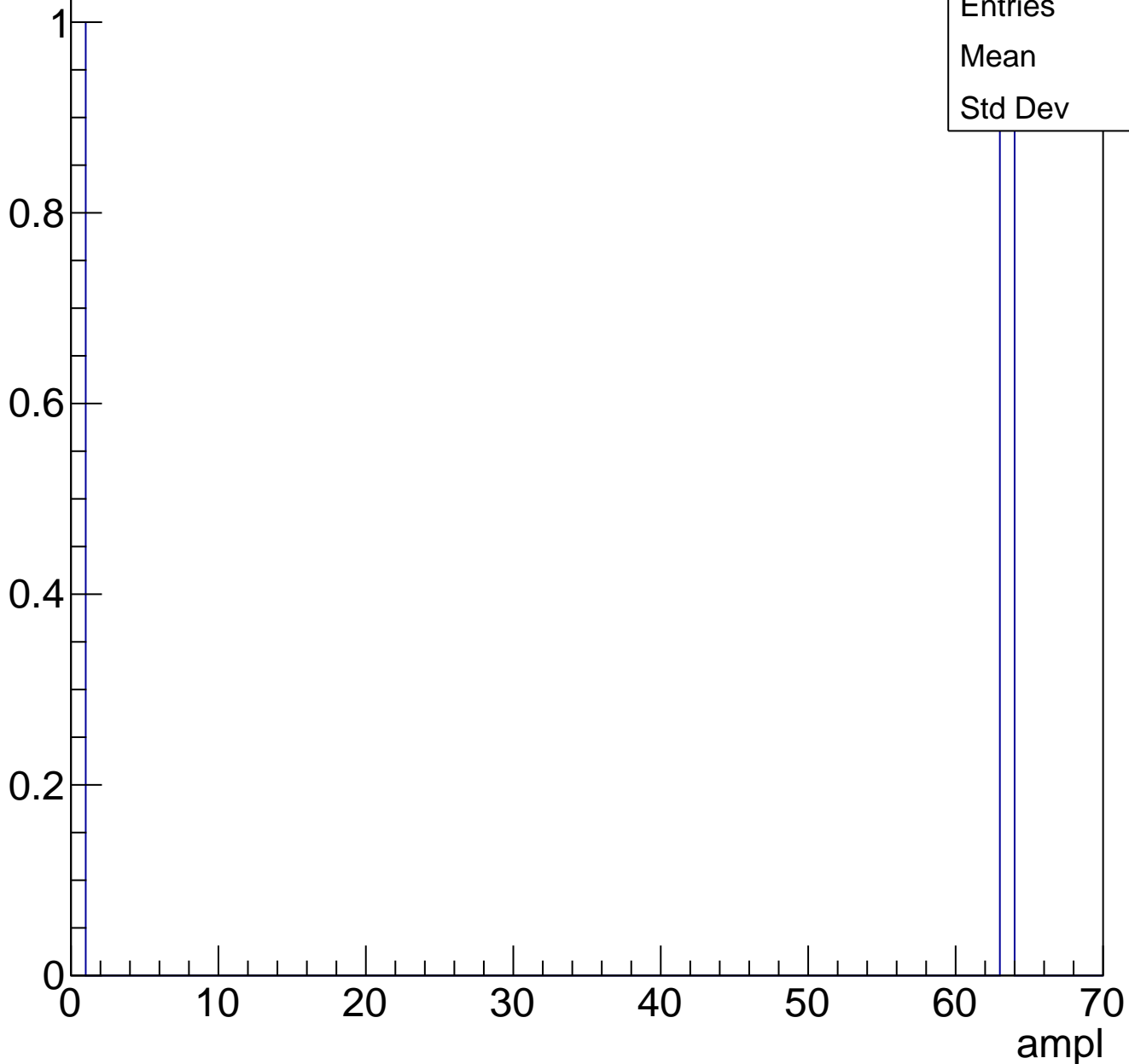
Entry



B0L102S, U20-ch91, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch92, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 58 |
| Mean | 29.88 |
| Std Dev | 3.135 |

Gaus mean : 29.6828

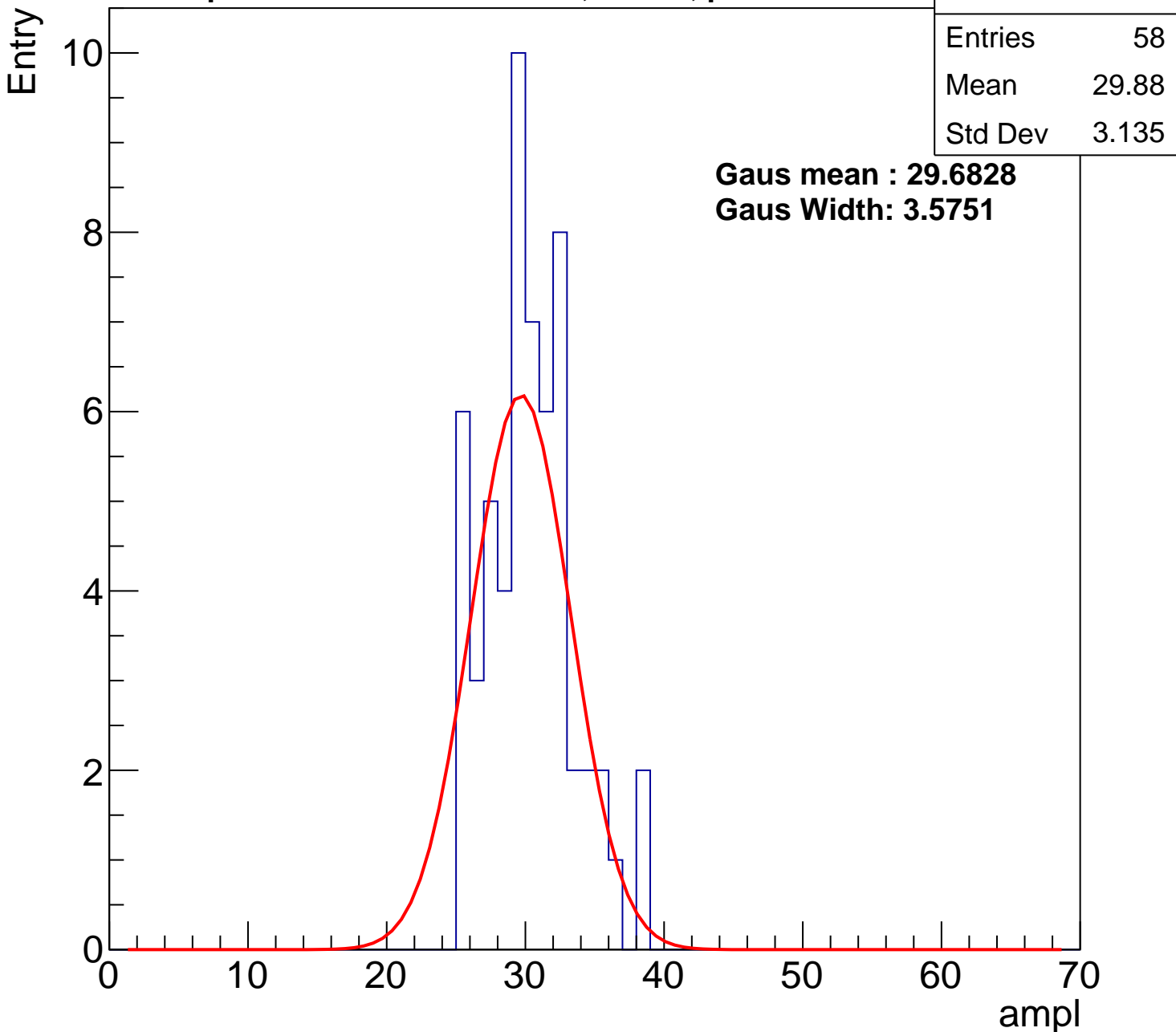
Gaus Width: 3.5751

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch92, adc1

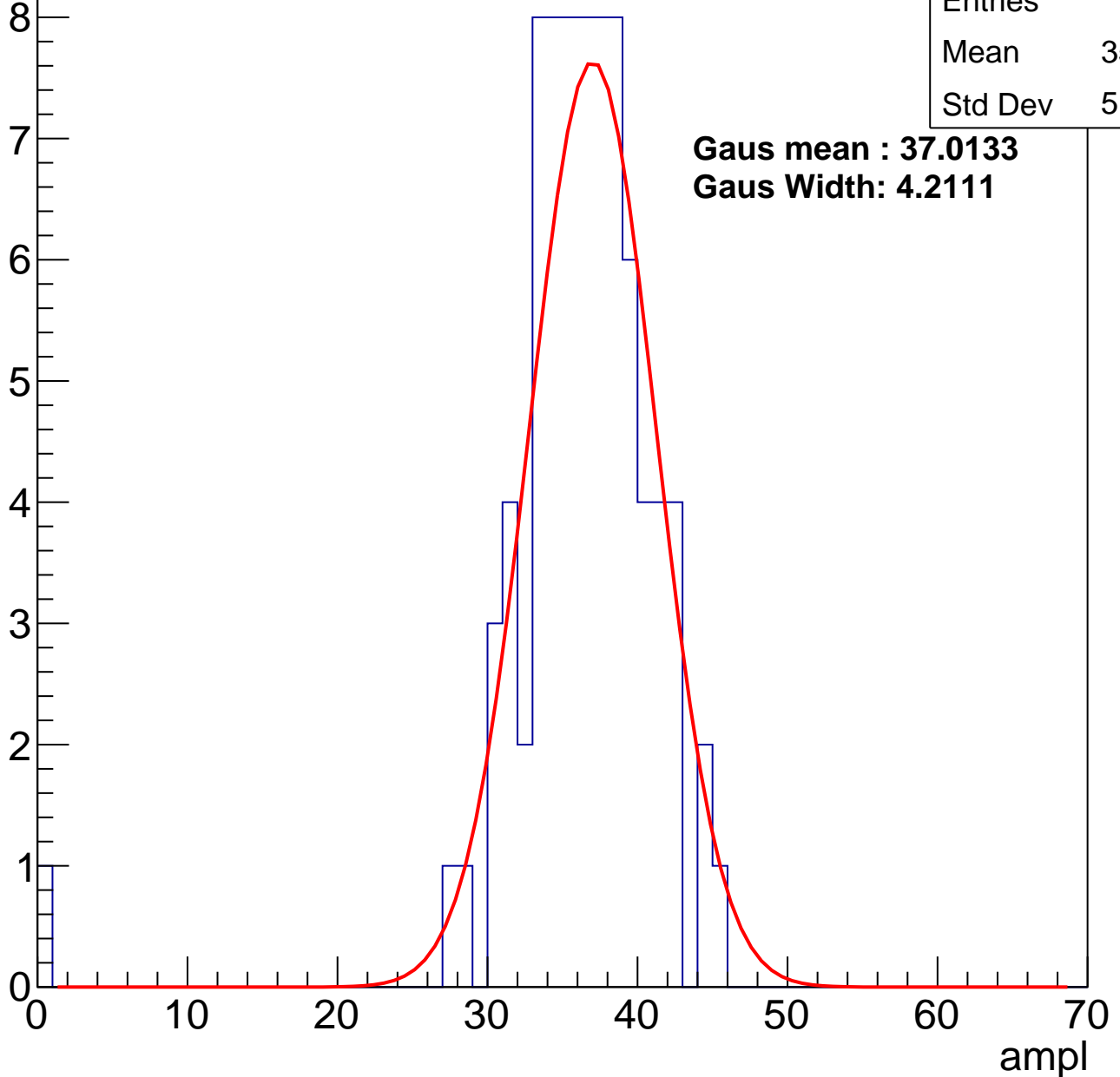
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 35.75 |
| Std Dev | 5.442 |

Gaus mean : 37.0133

Gaus Width: 4.2111



B0L102S, U20-ch92, adc2

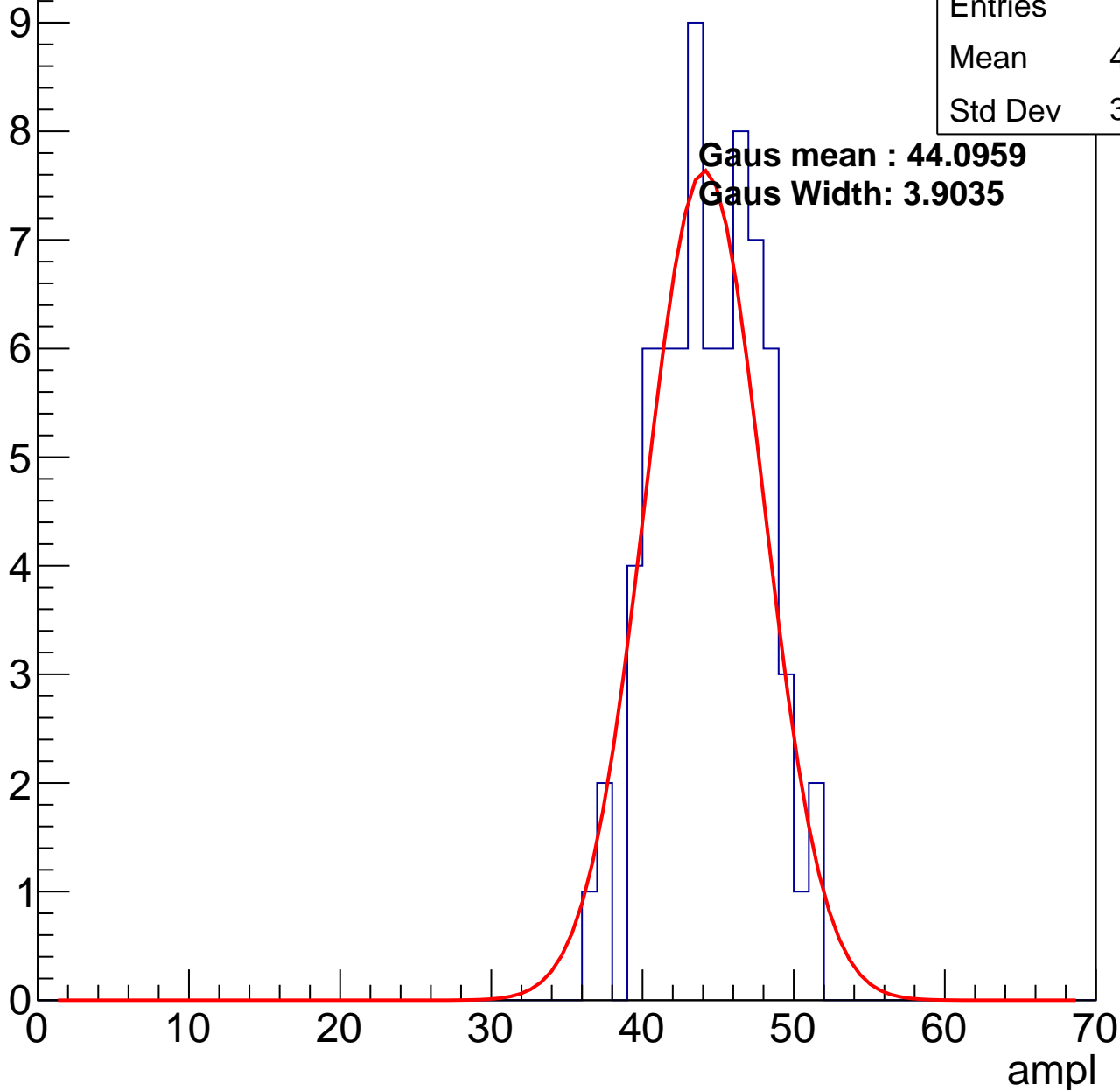
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.96 |
| Std Dev | 3.414 |

Gaus mean : 44.0959

Gaus Width: 3.9035

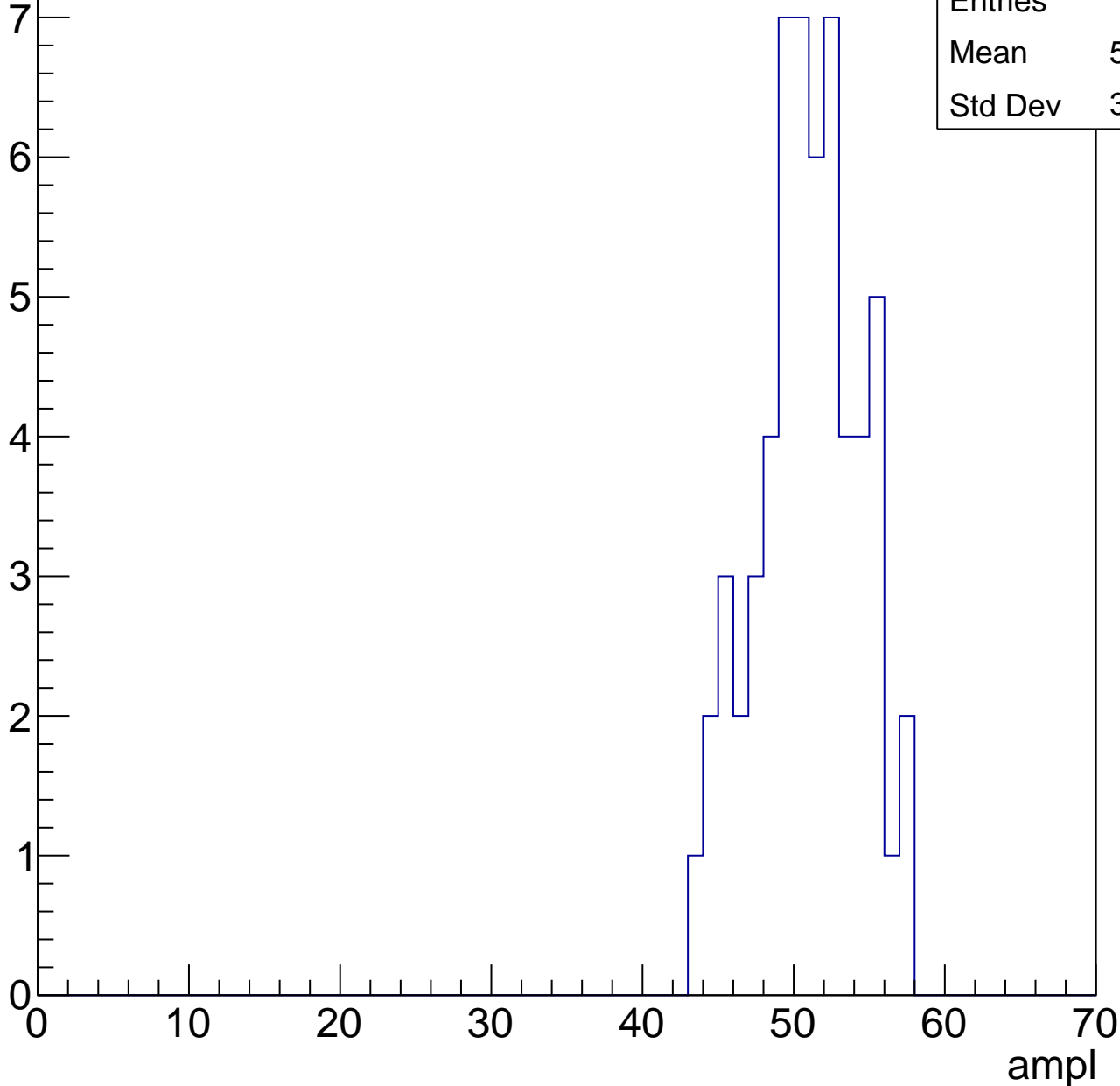


B0L102S, U20-ch92, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 50.47 |
| Std Dev | 3.369 |



B0L102S, U20-ch92, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 55.47

Std Dev 2.731

0

10

20

30

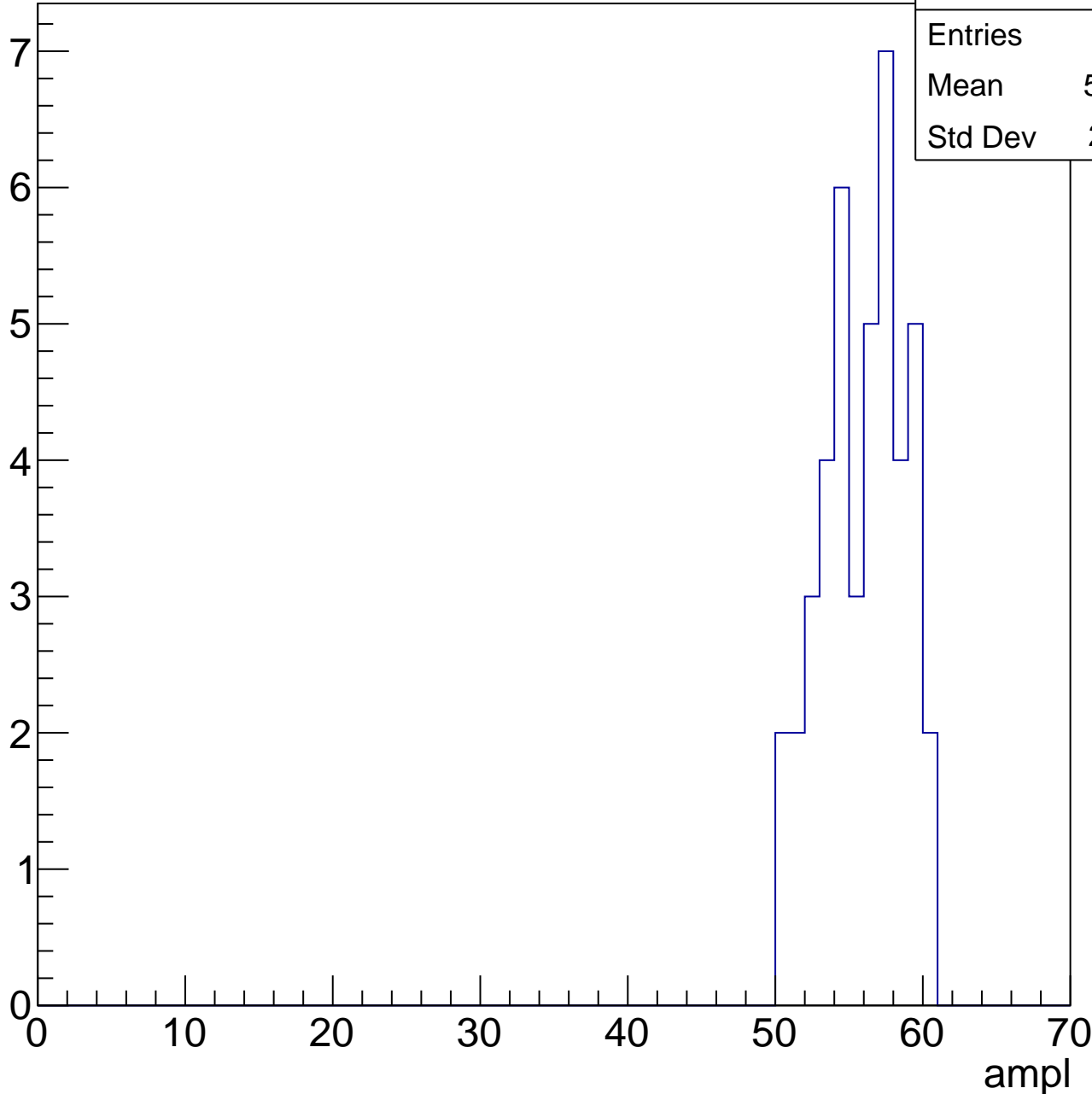
40

50

60

70

ampl

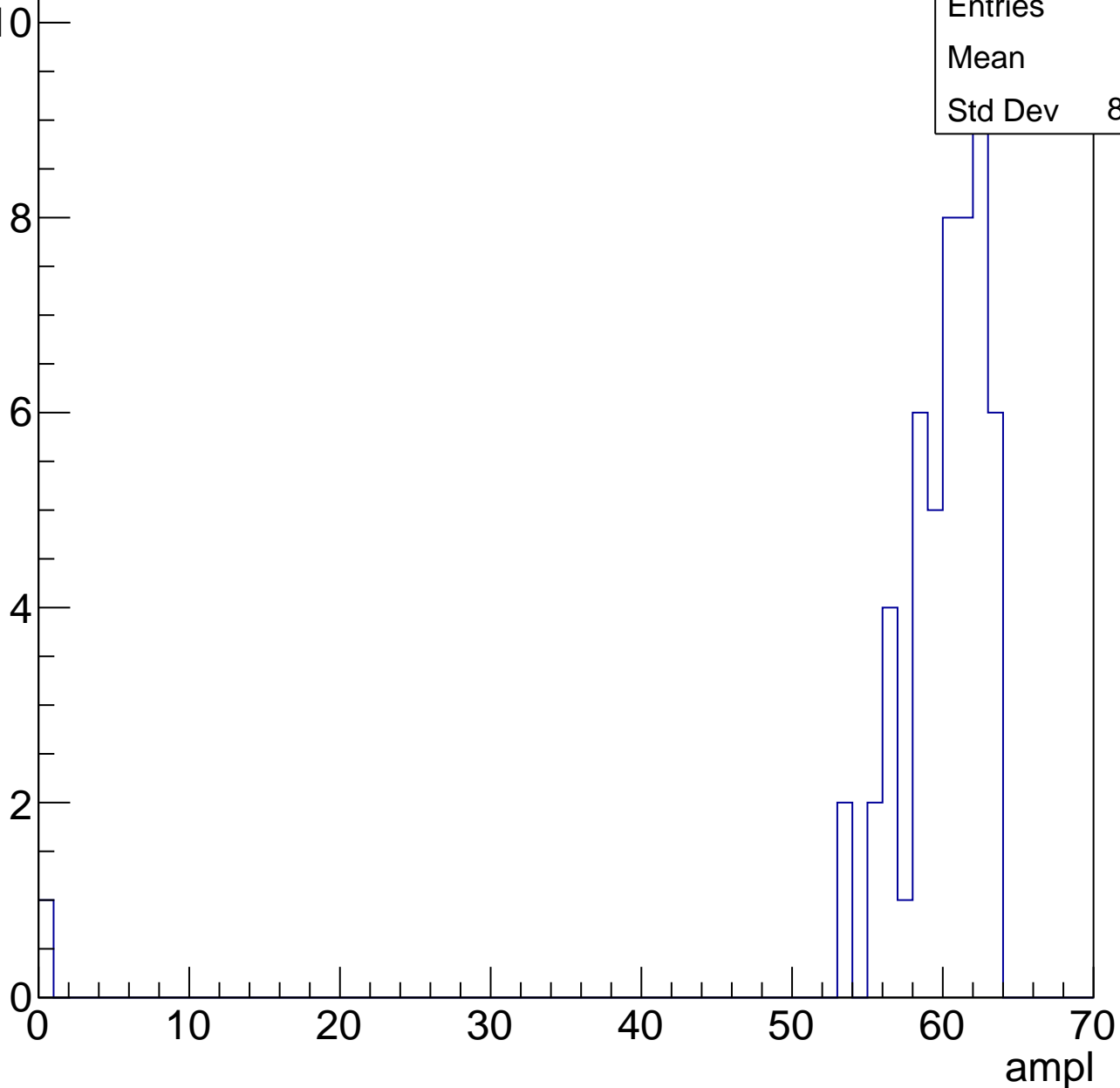


B0L102S, U20-ch92, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

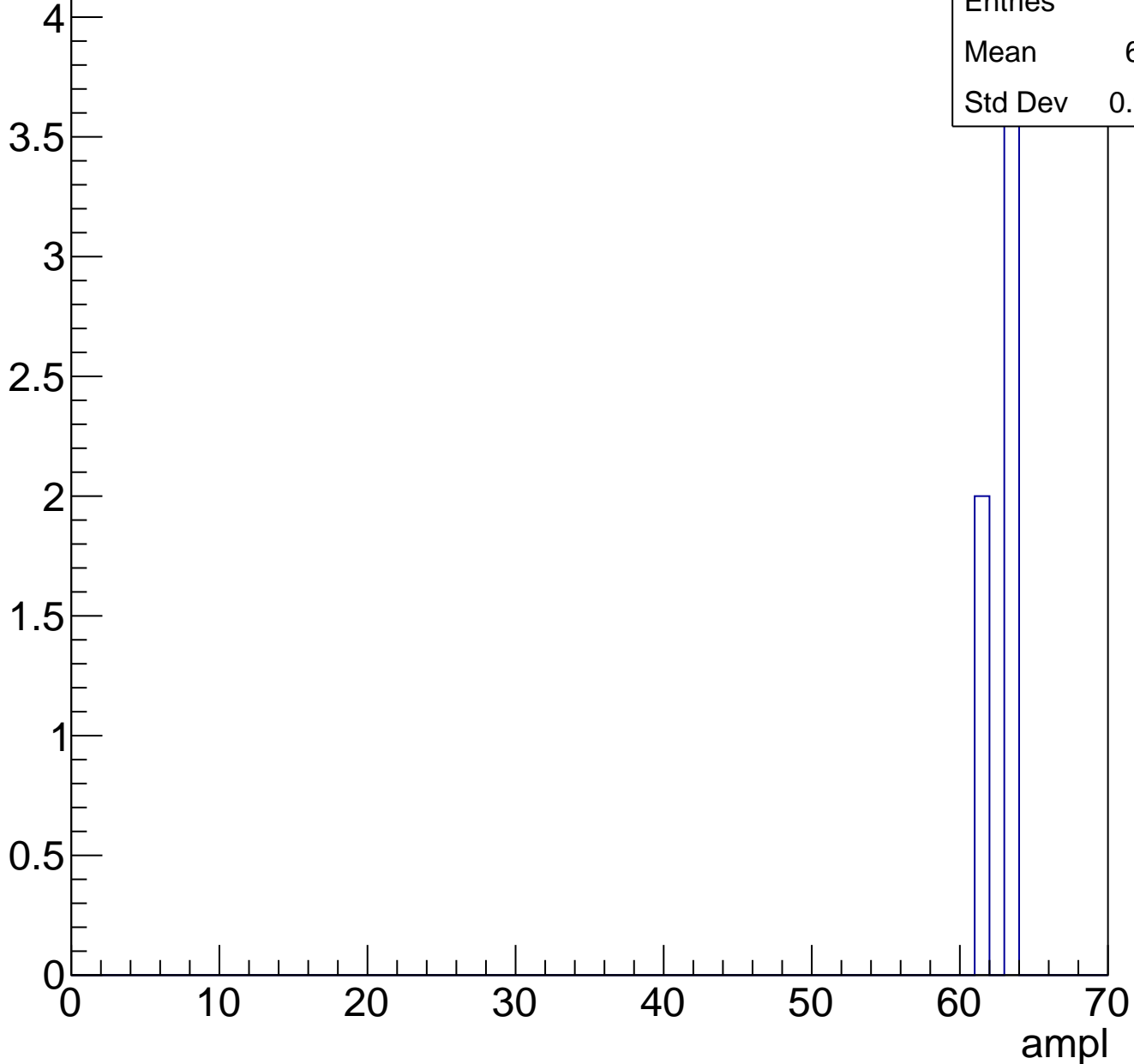
| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.6 |
| Std Dev | 8.522 |



B0L102S, U20-ch92, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch92, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U20-ch93, adc0

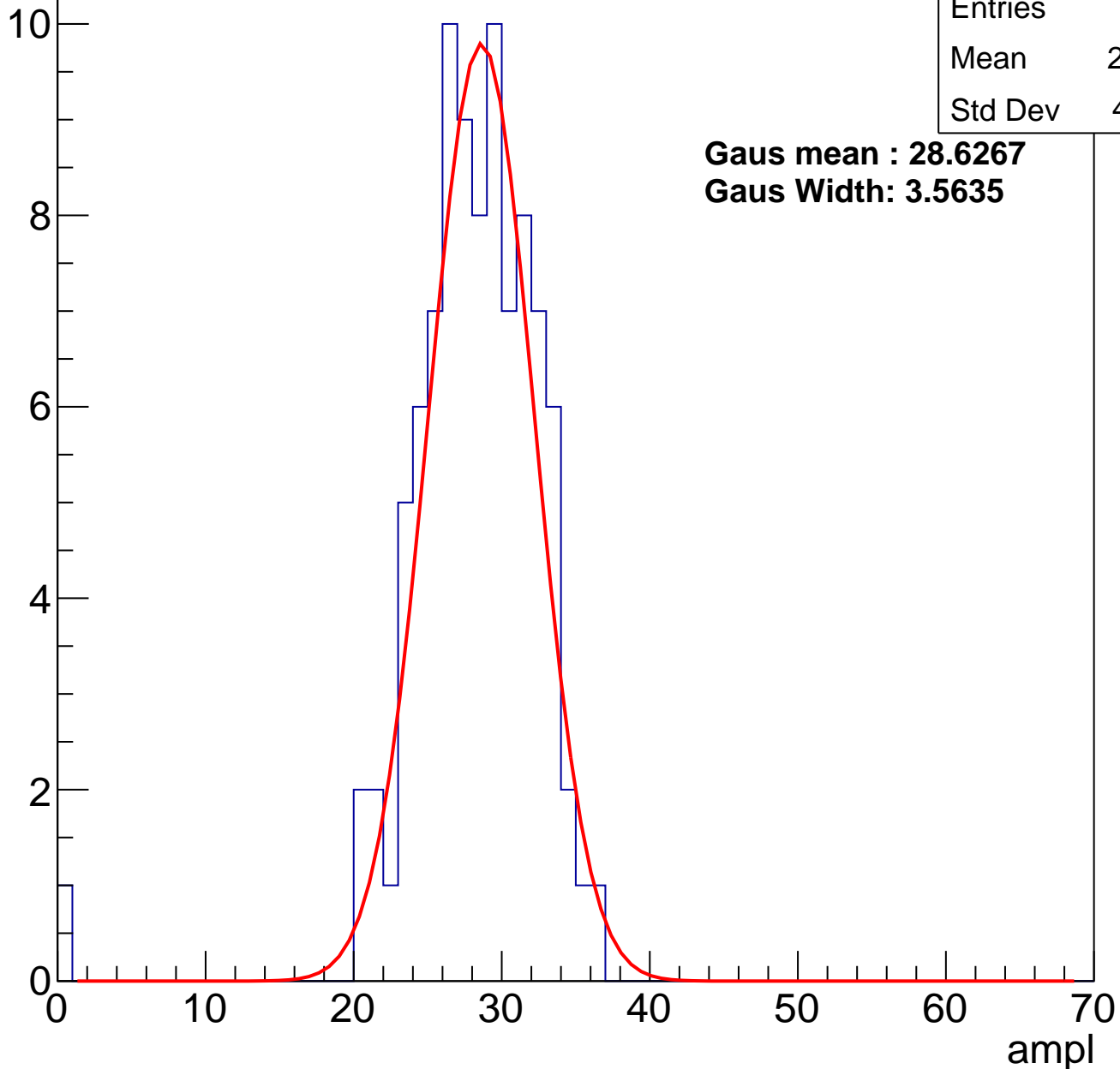
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 93 |
| Mean | 27.68 |
| Std Dev | 4.551 |

Gaus mean : 28.6267

Gaus Width: 3.5635

Entry



B0L102S, U20-ch93, adc1

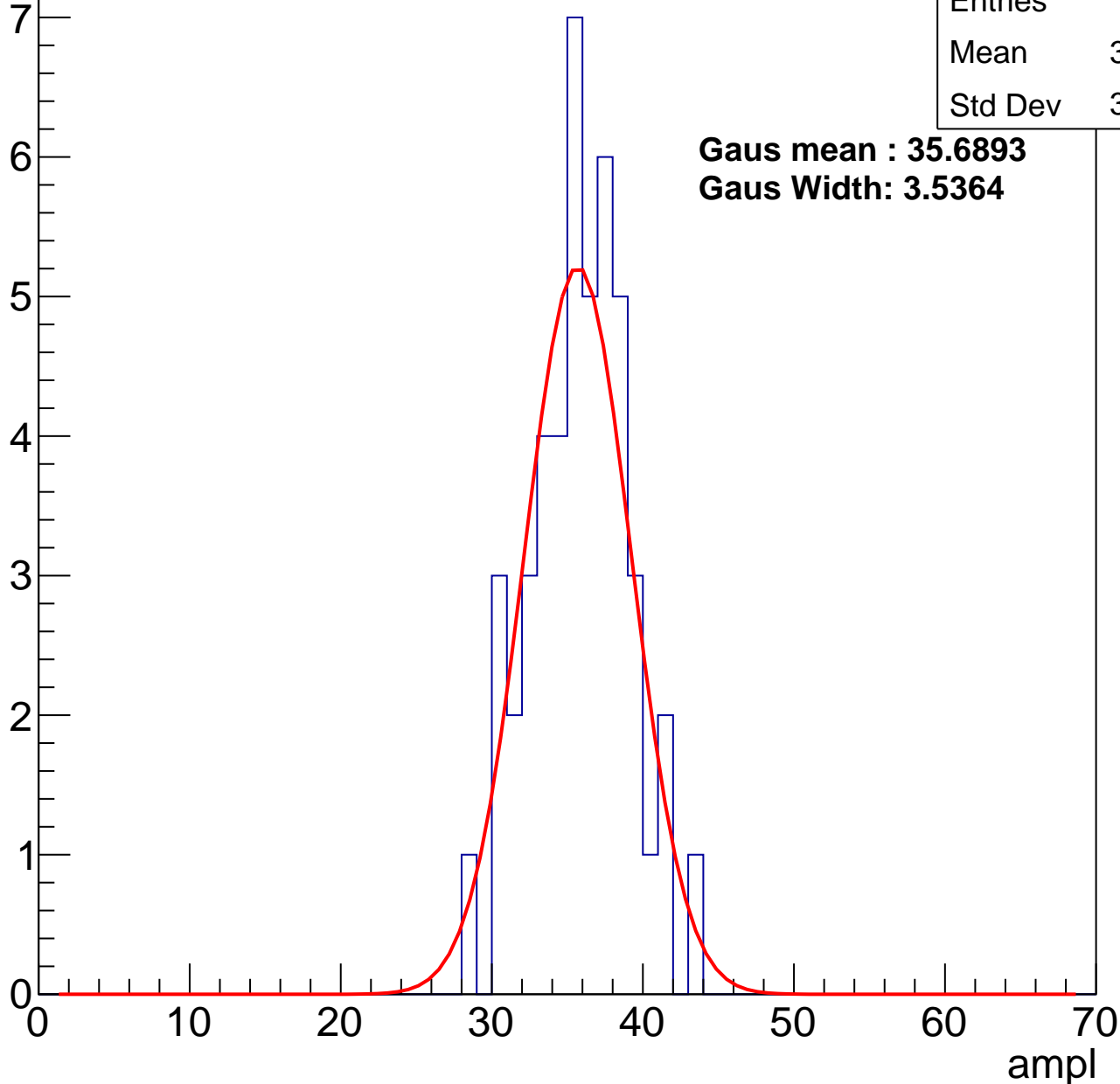
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 35.38 |
| Std Dev | 3.193 |

Gaus mean : 35.6893

Gaus Width: 3.5364



B0L102S, U20-ch93, adc2

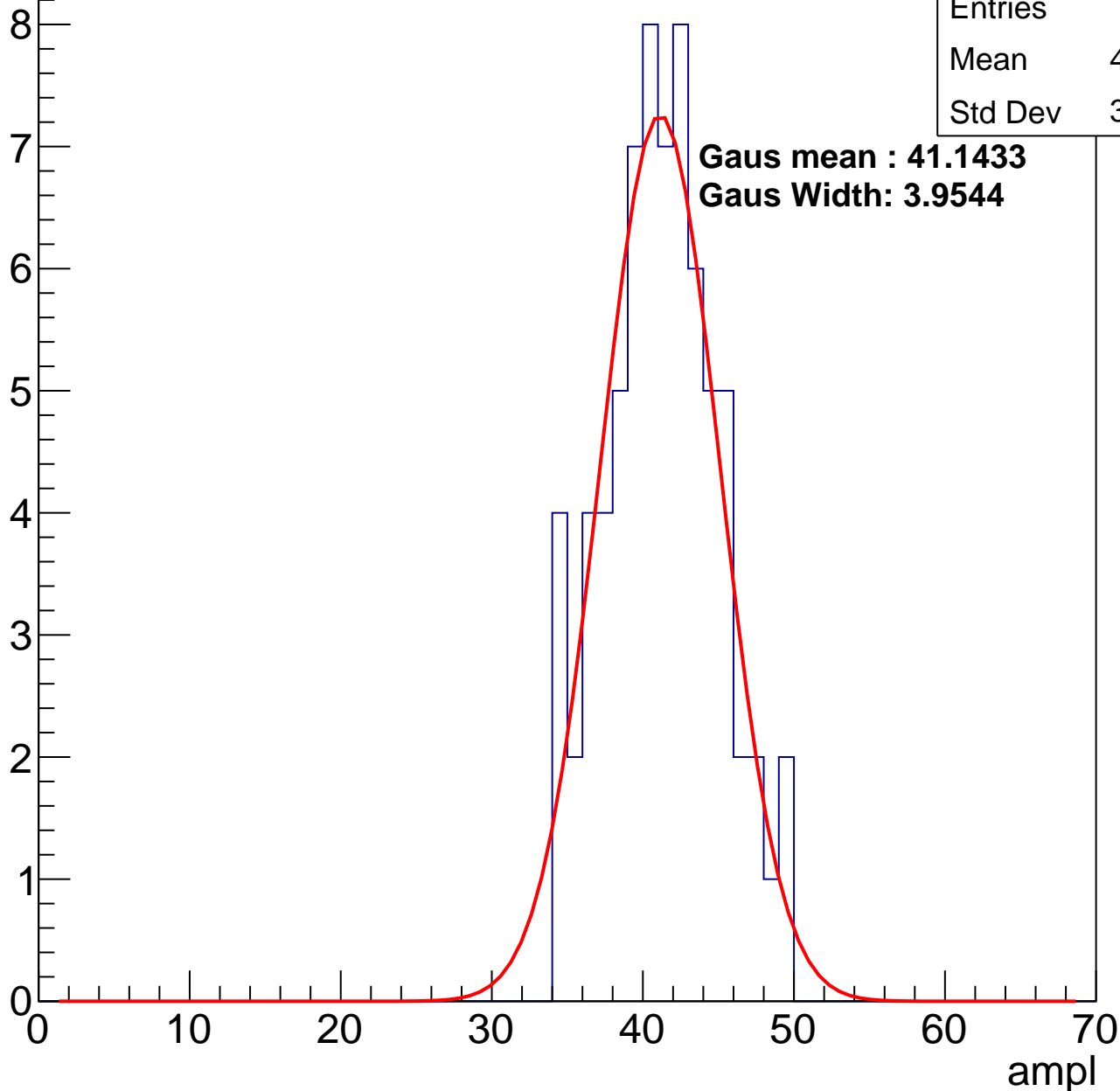
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 40.82 |
| Std Dev | 3.664 |

Gaus mean : 41.1433

Gaus Width: 3.9544

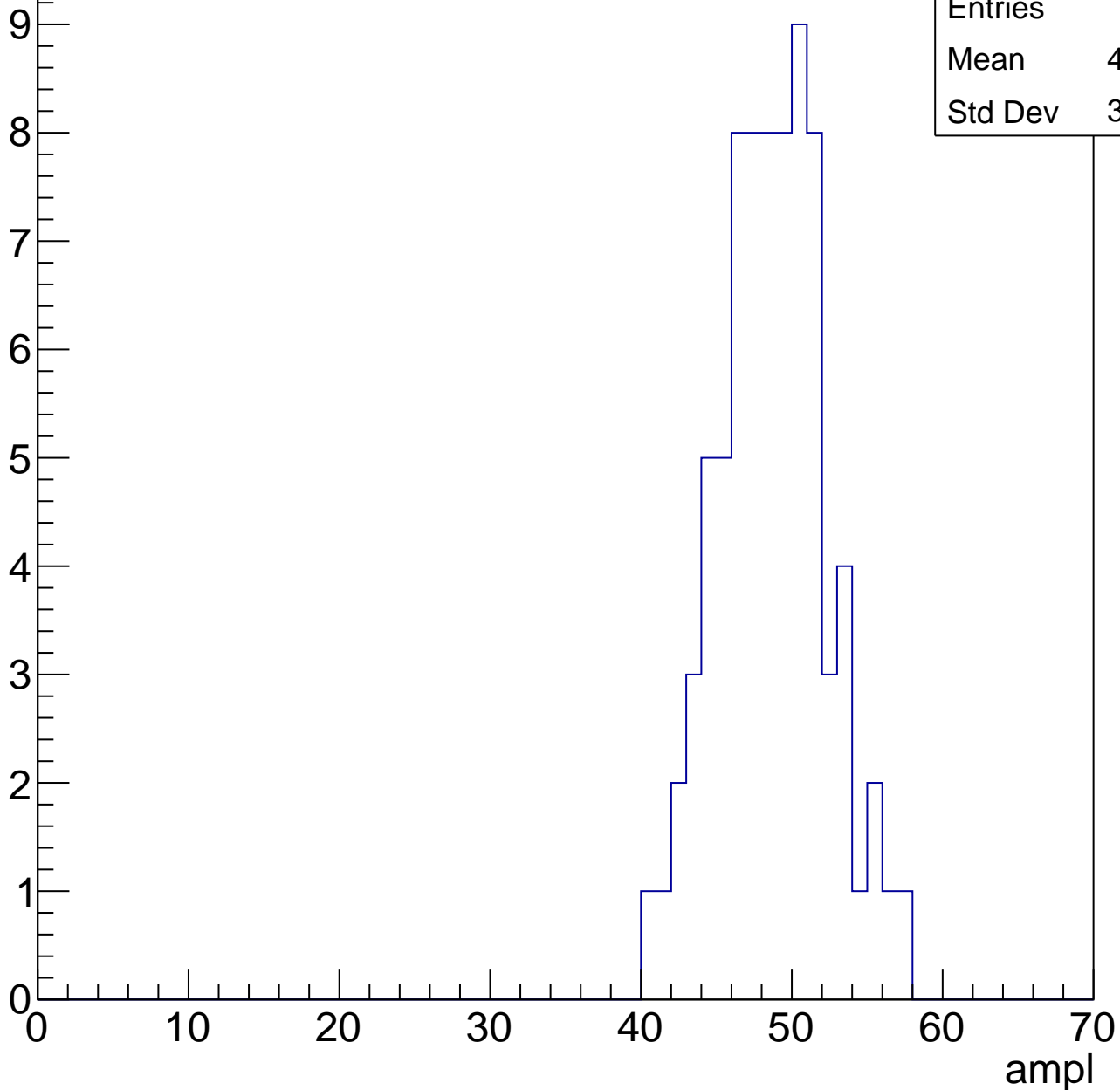


B0L102S, U20-ch93, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 48.23 |
| Std Dev | 3.515 |

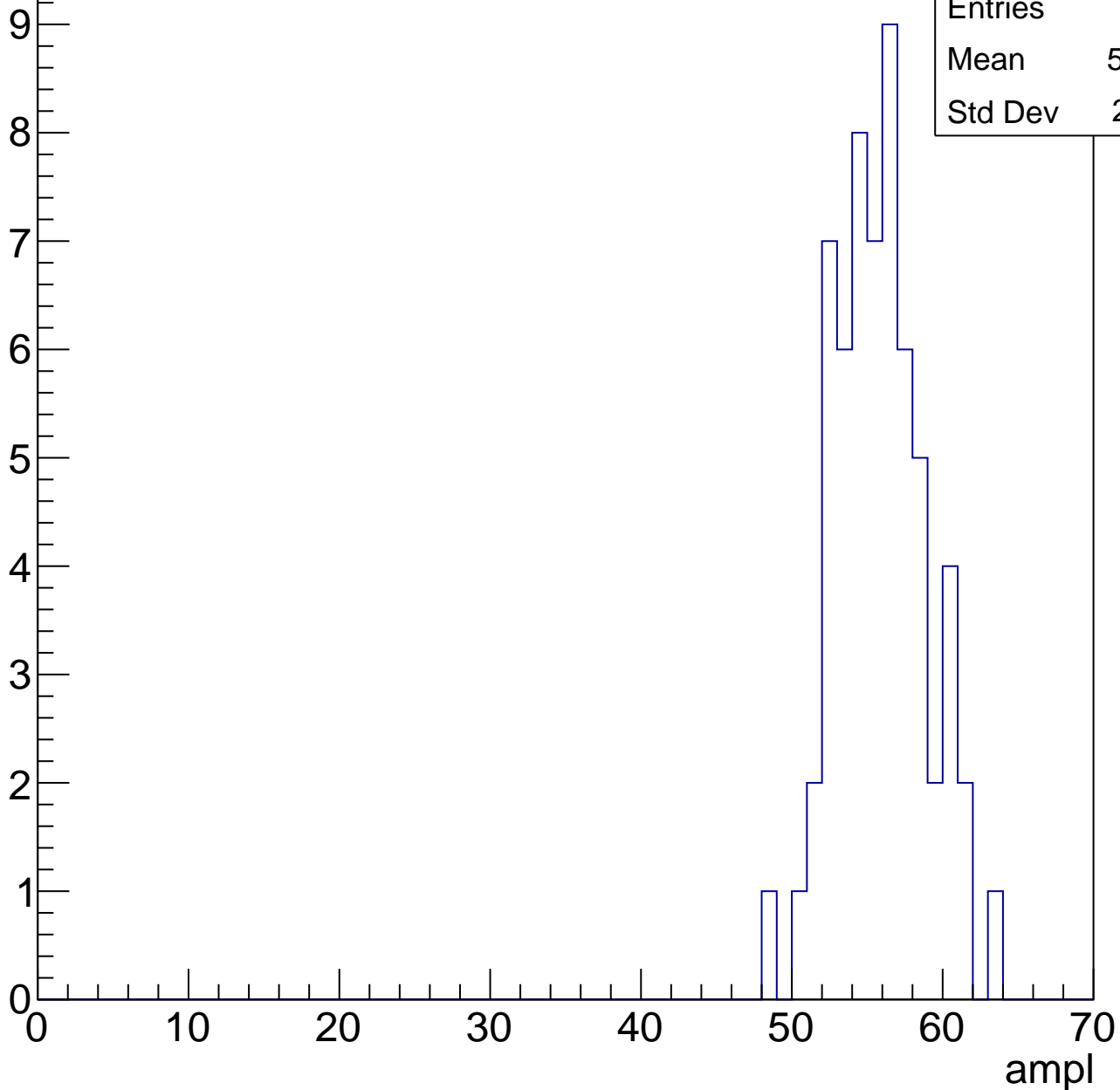


B0L102S, U20-ch93, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.38 |
| Std Dev | 2.971 |

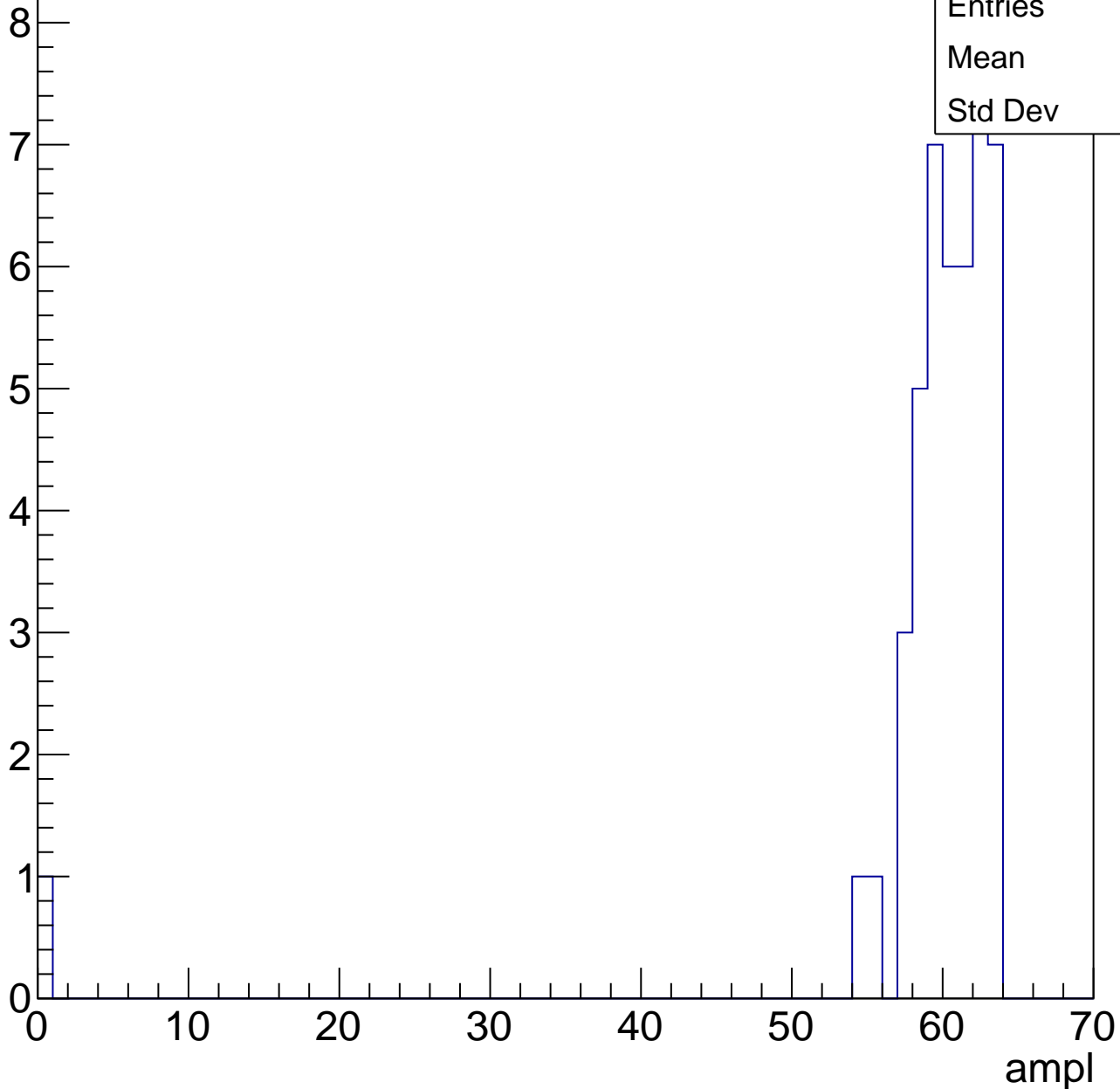


B0L102S, U20-ch93, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

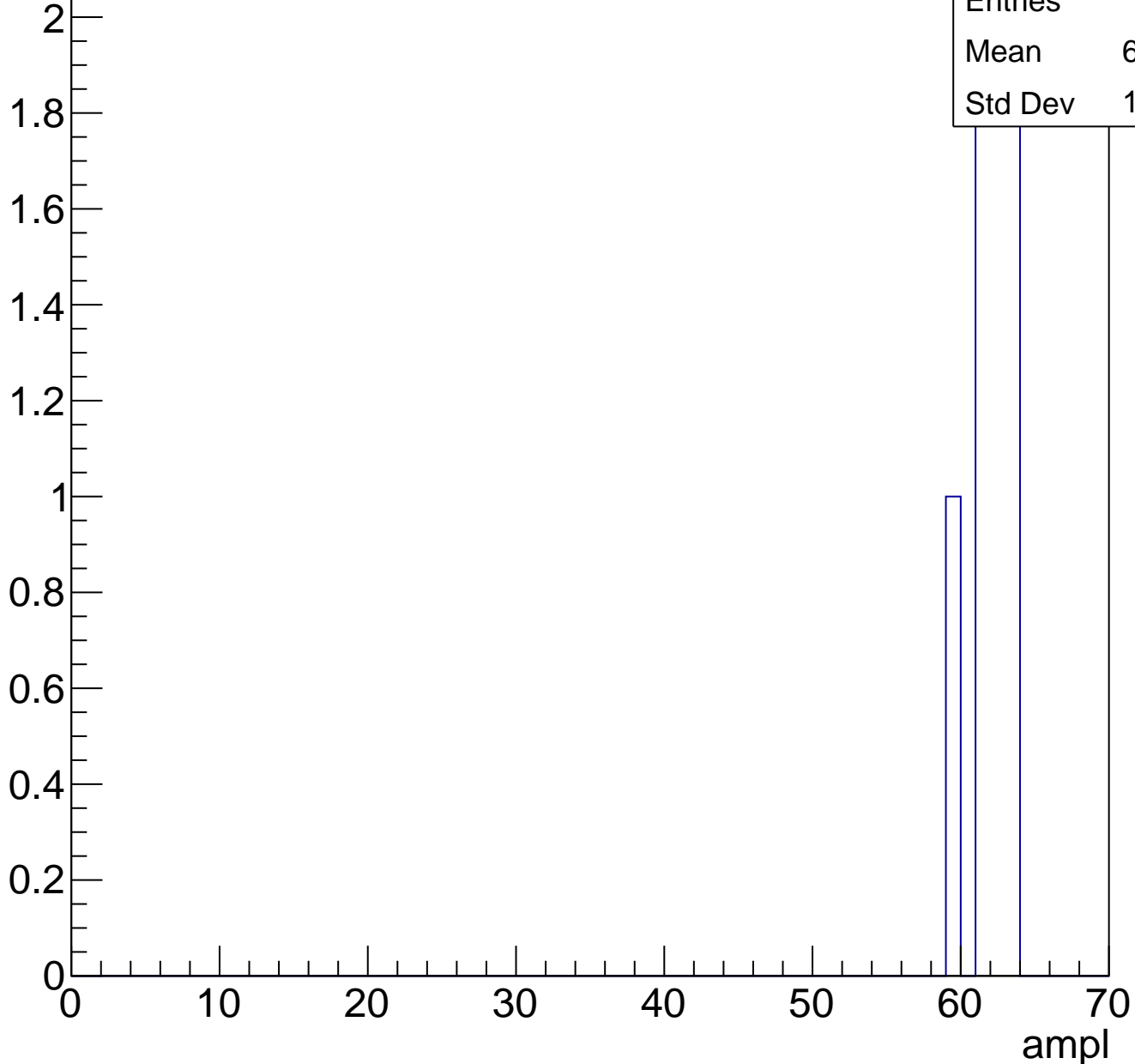
| | |
|---------|------|
| Entries | 45 |
| Mean | 58.8 |
| Std Dev | 9.13 |



B0L102S, U20-ch93, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch93, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch94, adc0

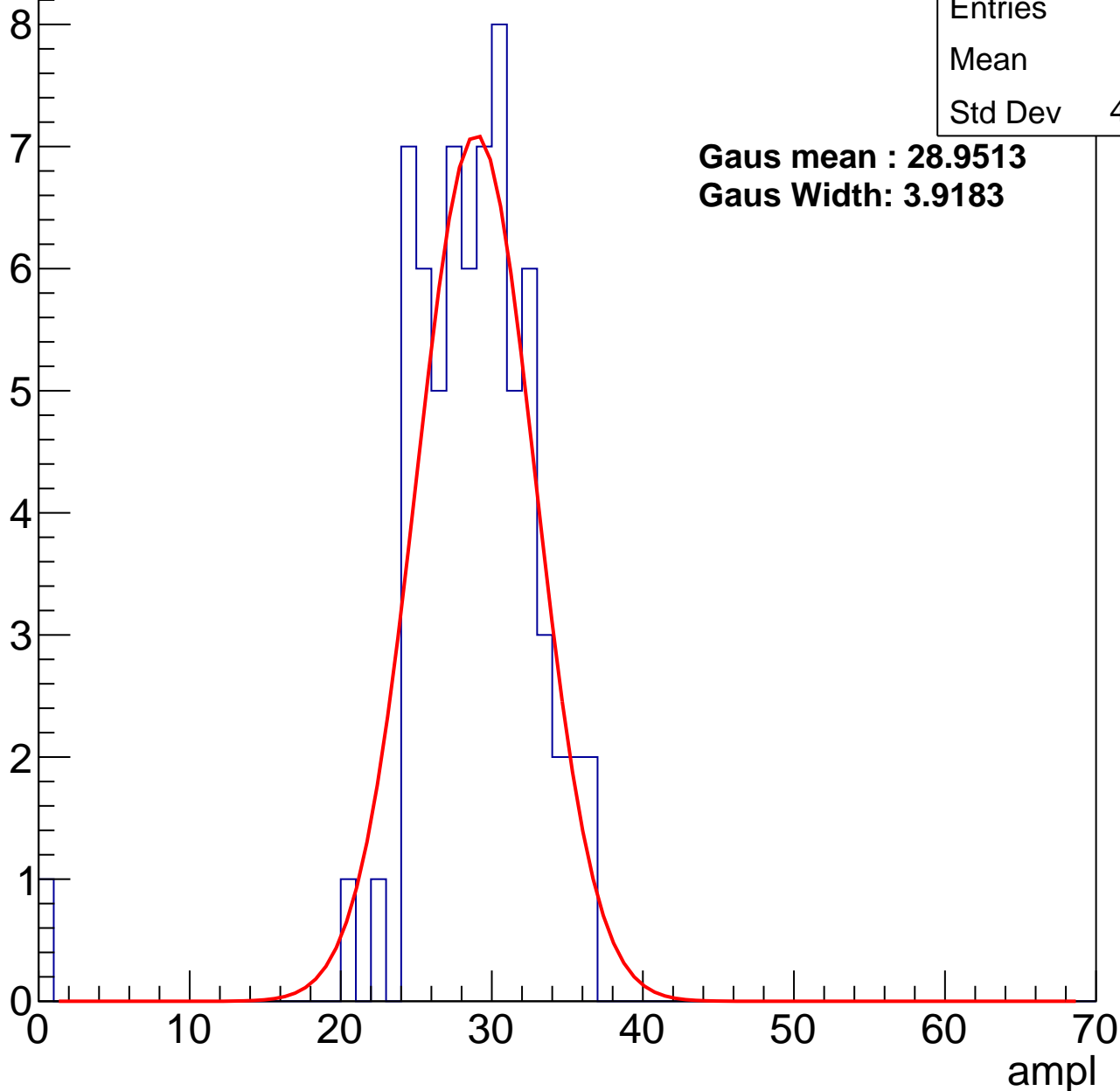
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.2 |
| Std Dev | 4.856 |

Gaus mean : 28.9513

Gaus Width: 3.9183



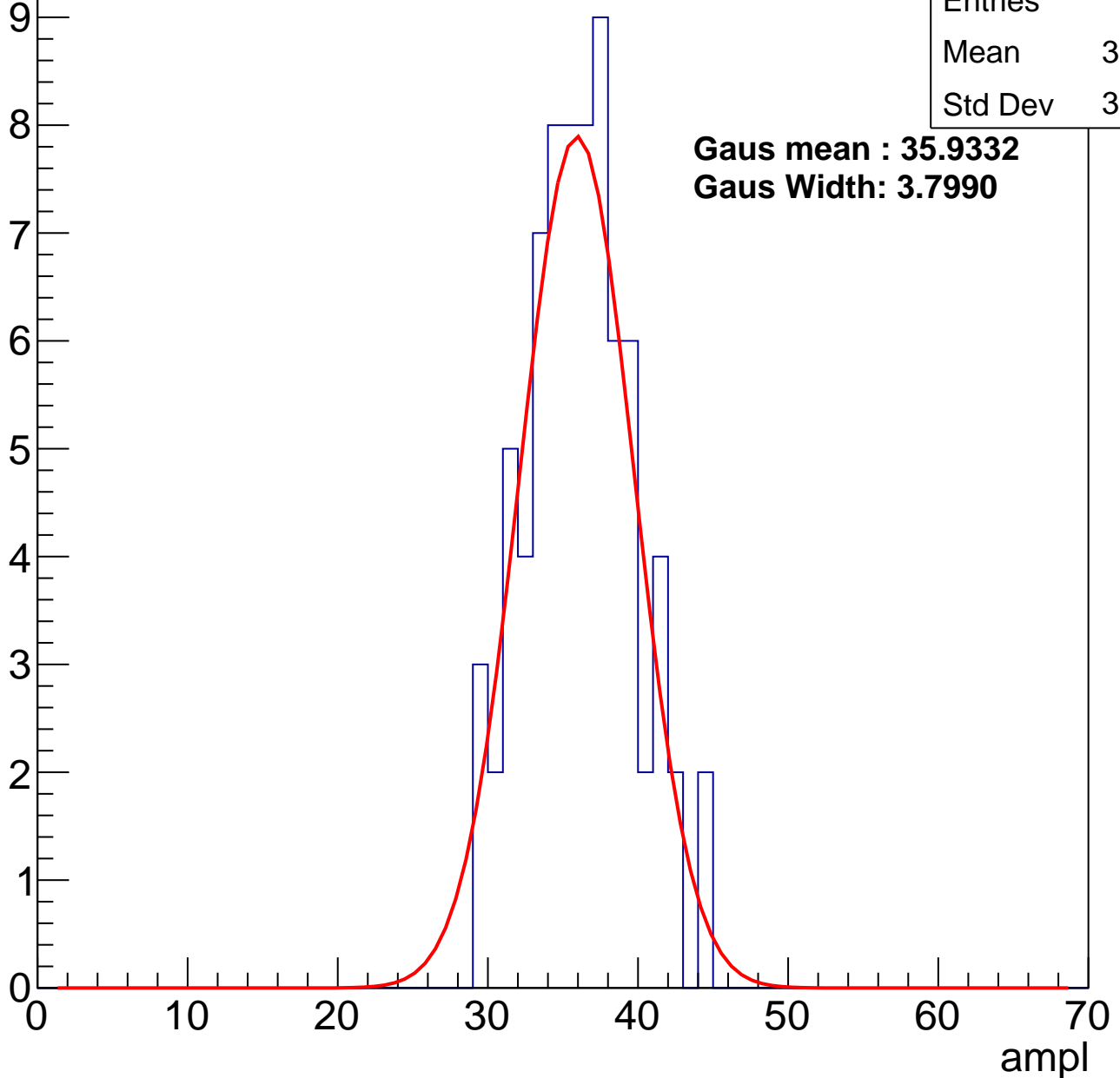
B0L102S, U20-ch94, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.68 |
| Std Dev | 3.484 |

Gaus mean : 35.9332
Gaus Width: 3.7990



B0L102S, U20-ch94, adc2

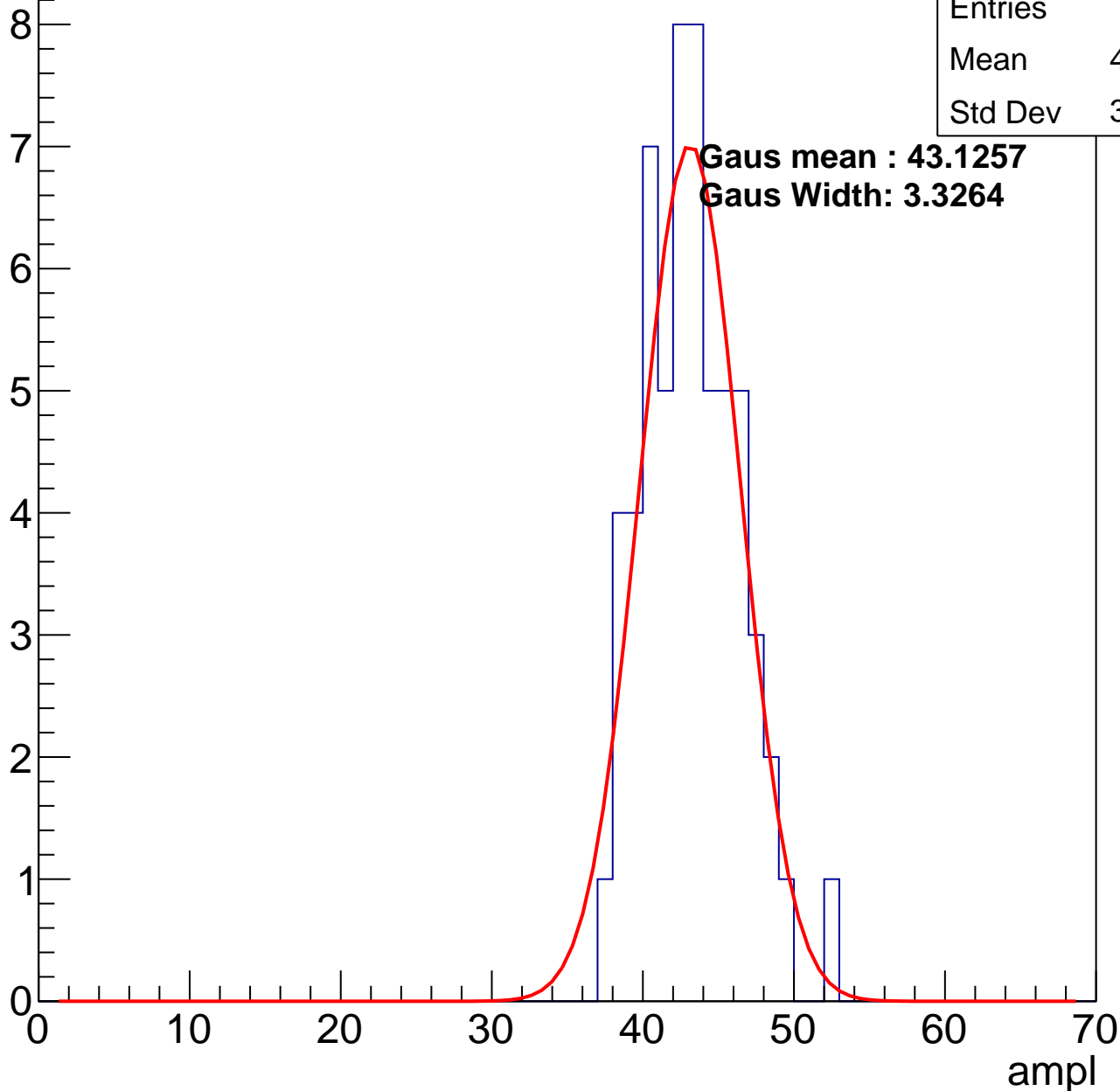
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.76 |
| Std Dev | 3.116 |

Gaus mean : 43.1257

Gaus Width: 3.3264

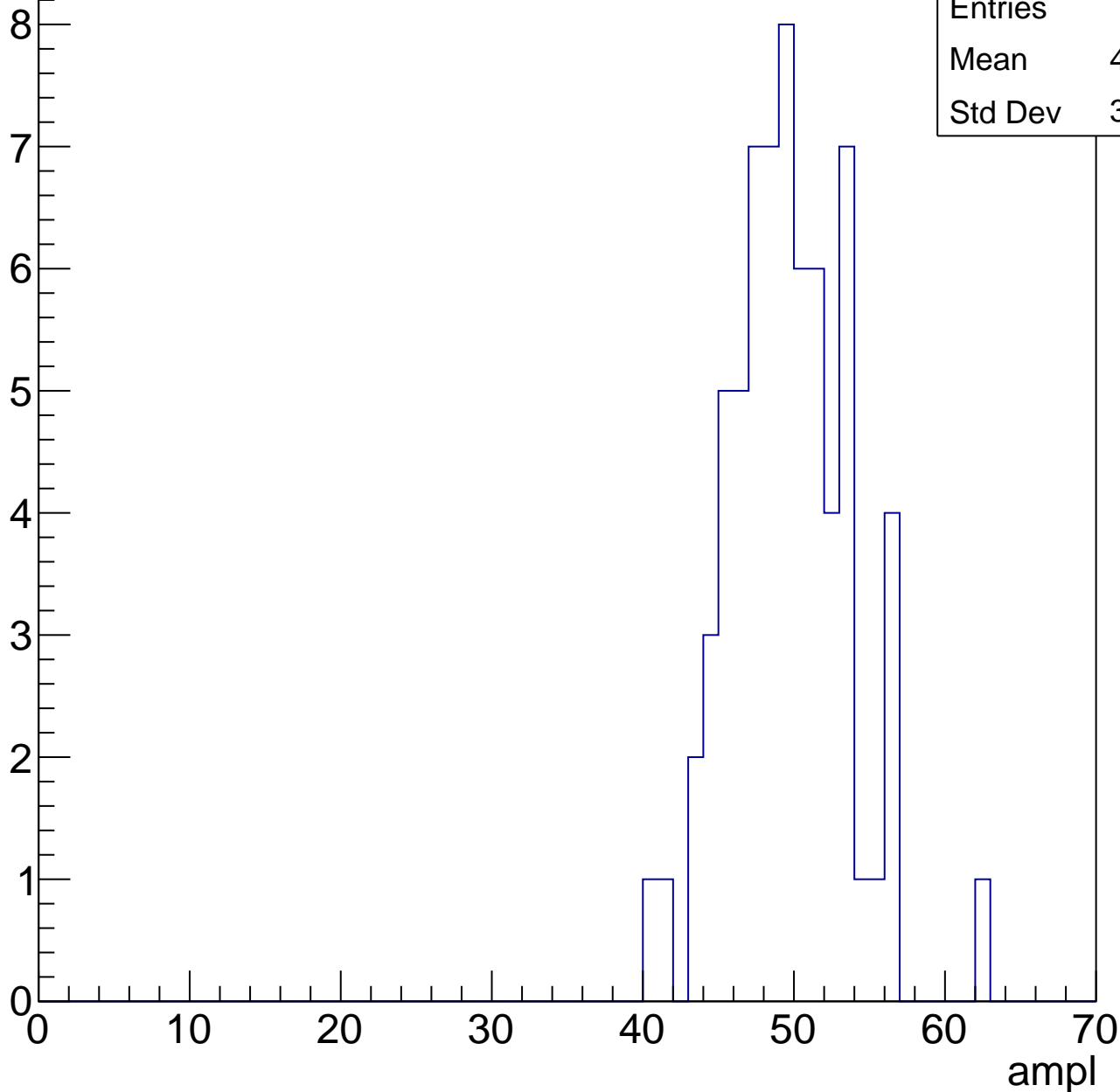


B0L102S, U20-ch94, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 49.14 |
| Std Dev | 3.909 |

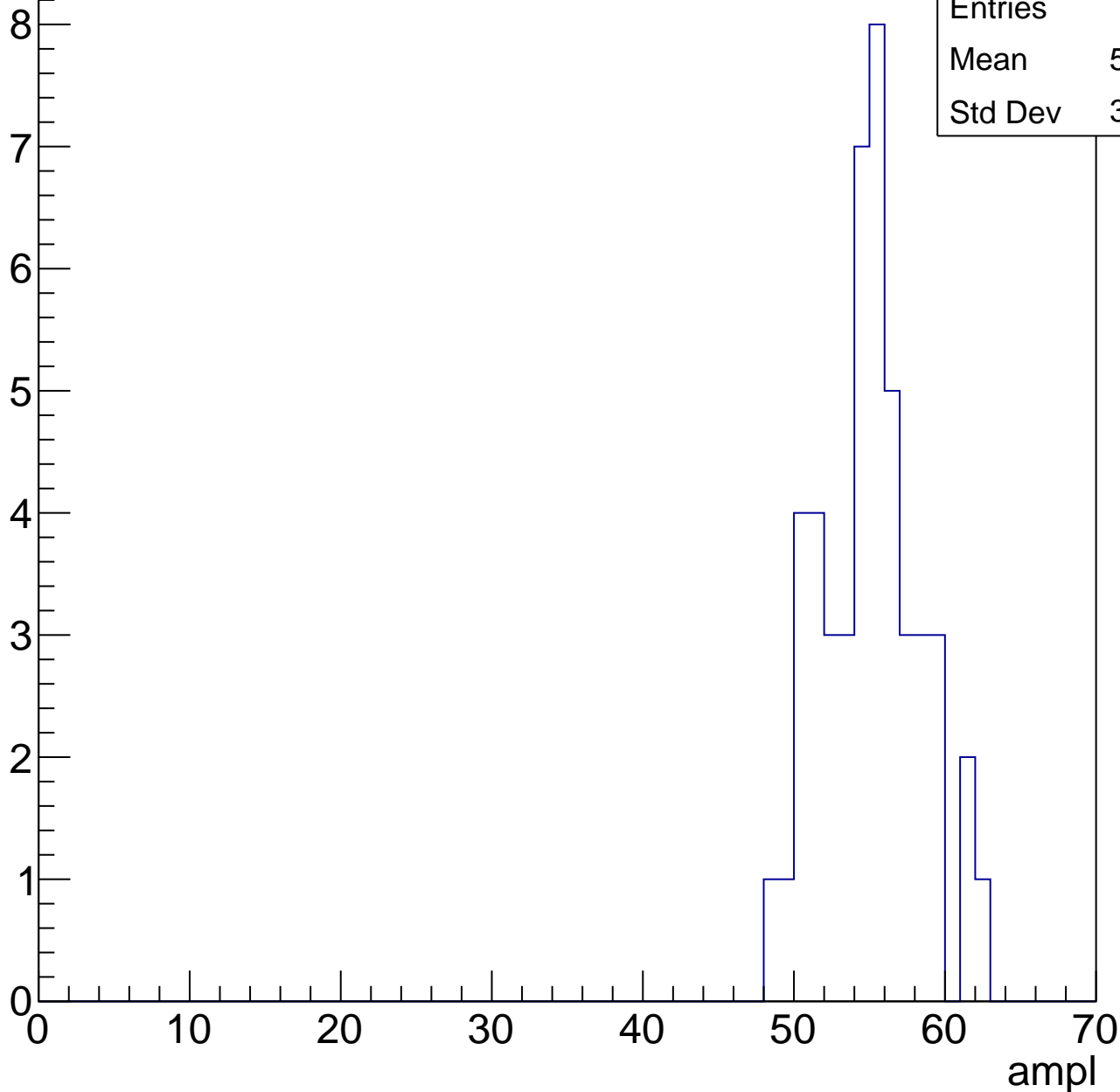


B0L102S, U20-ch94, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 54.58 |
| Std Dev | 3.226 |

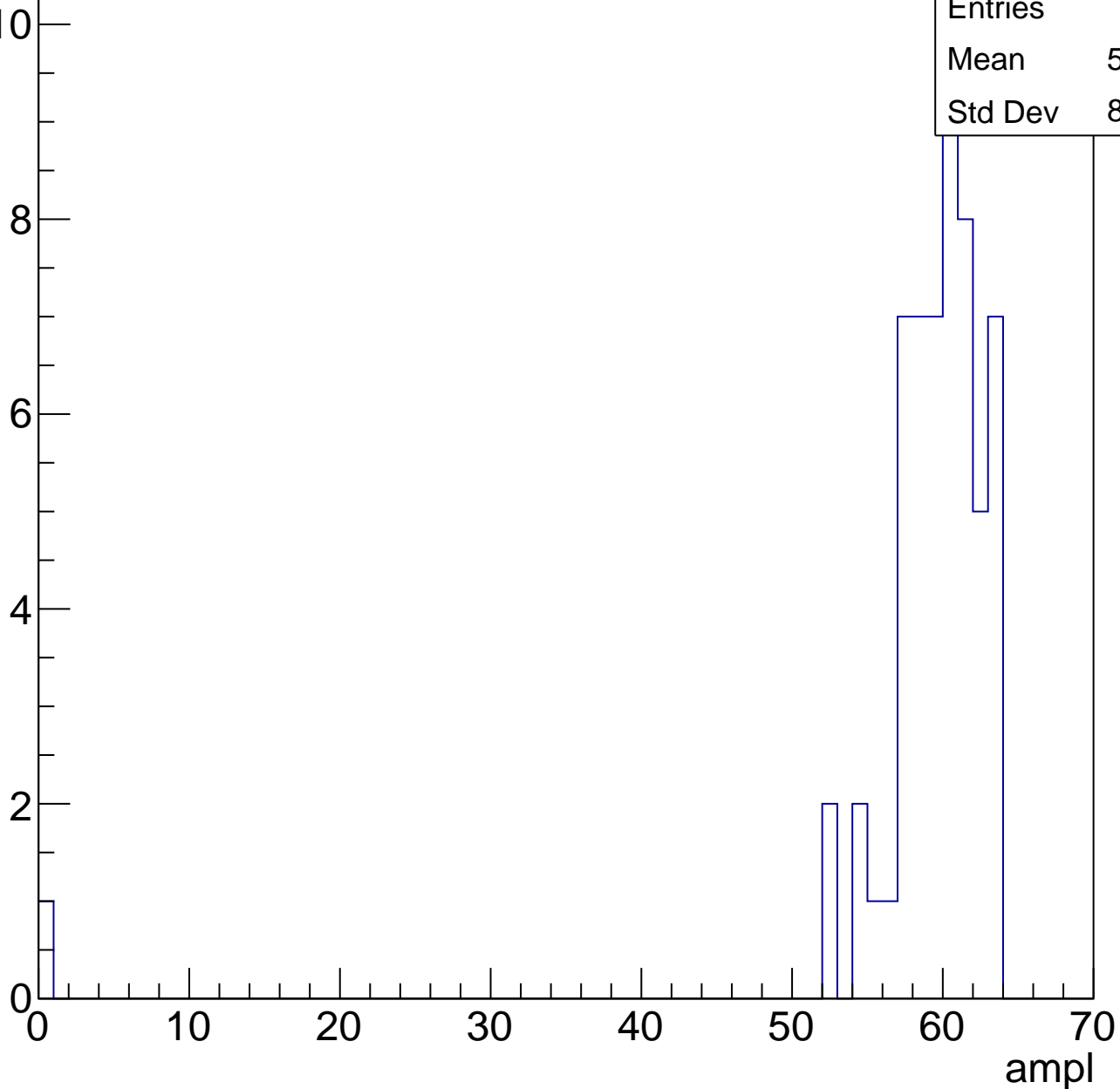


B0L102S, U20-ch94, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.28 |
| Std Dev | 8.155 |



B0L102S, U20-ch94, adc6

calib_packv5_042523_0143.root, FC#12, port B1

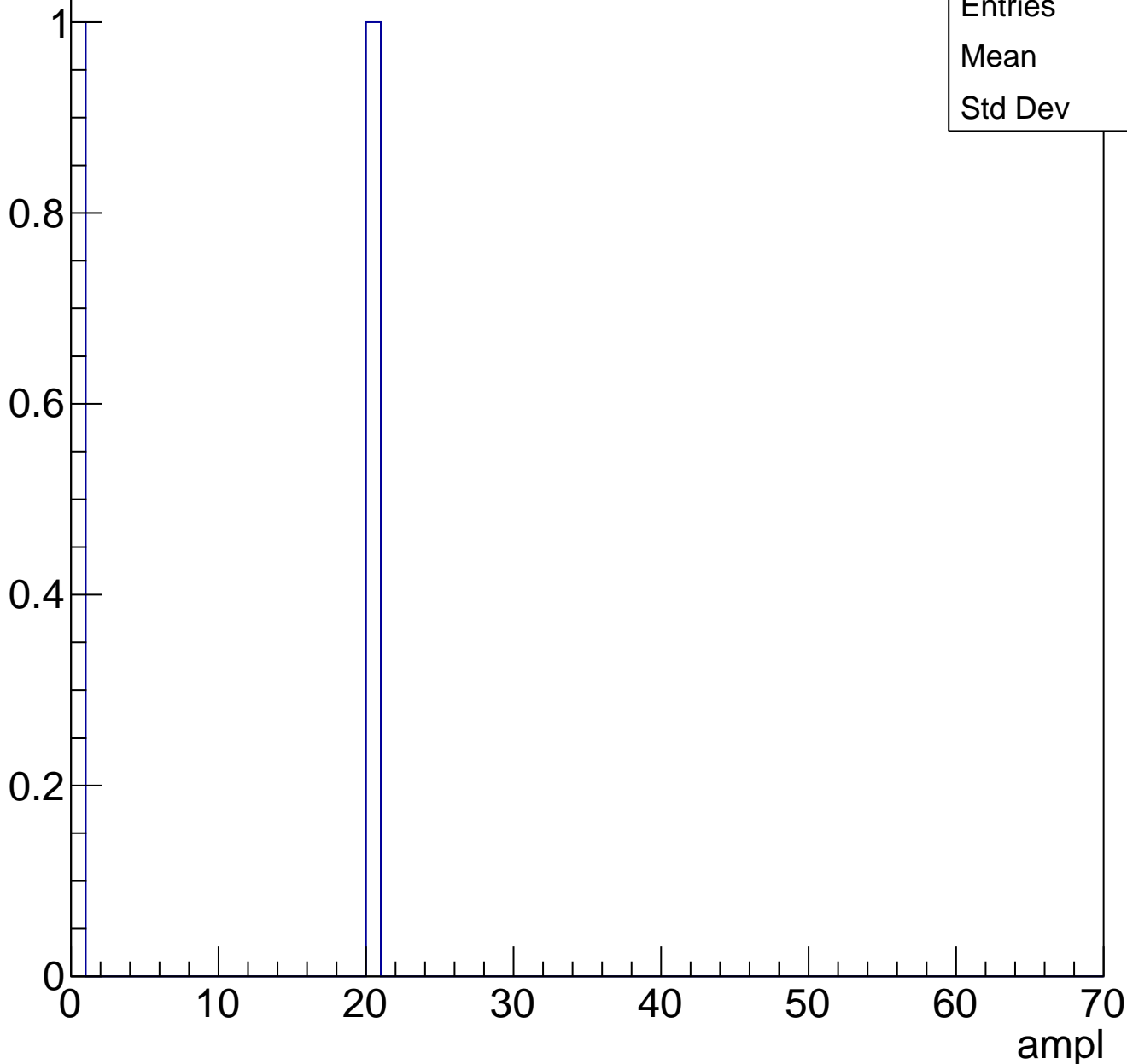
Entry



B0L102S, U20-ch94, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 10 |
| Std Dev | 10 |

B0L102S, U20-ch95, adc0

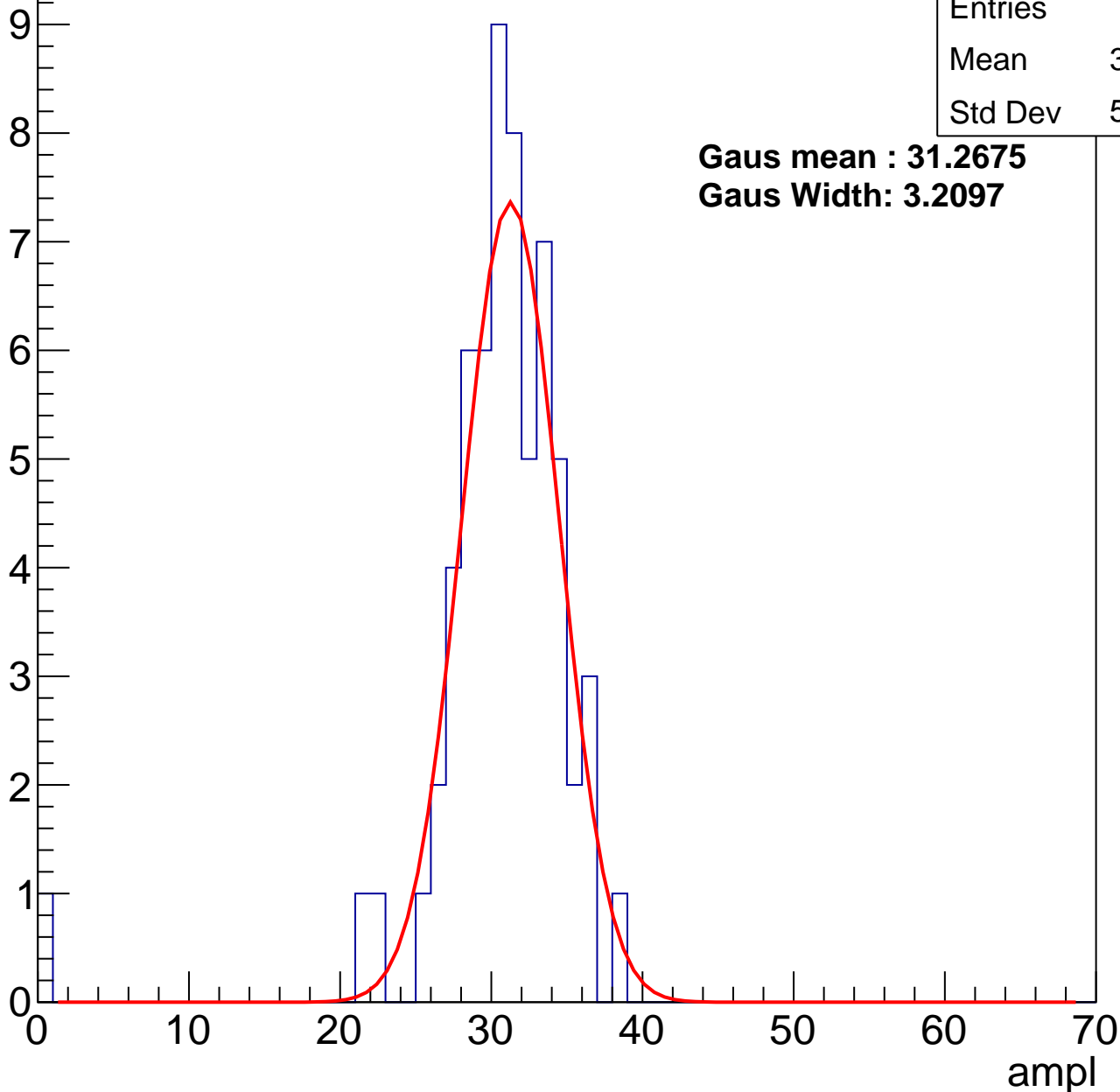
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.08 |
| Std Dev | 5.017 |

Gaus mean : 31.2675

Gaus Width: 3.2097

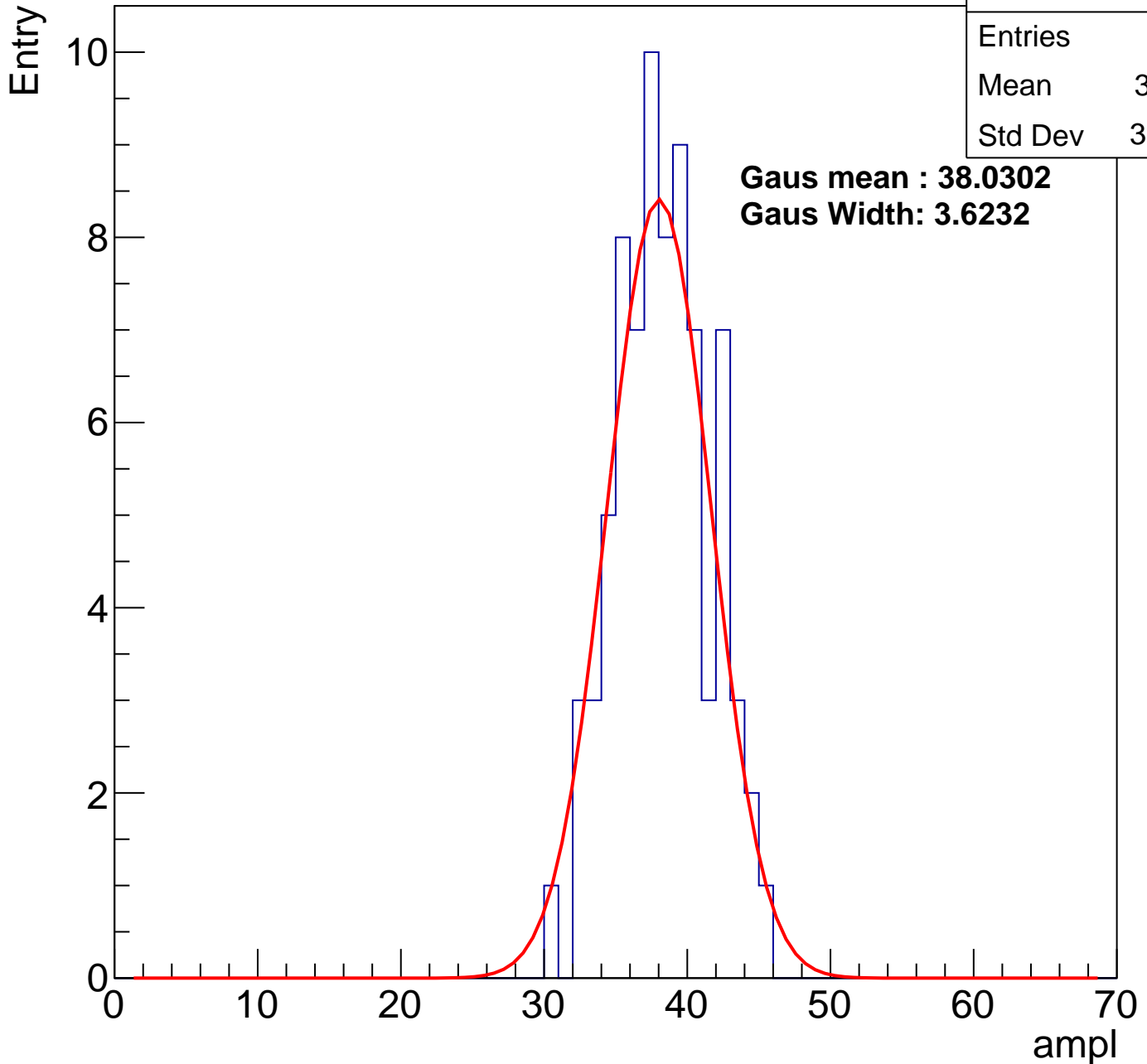


B0L102S, U20-ch95, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.81 |
| Std Dev | 3.236 |

Gaus mean : 38.0302
Gaus Width: 3.6232



B0L102S, U20-ch95, adc2

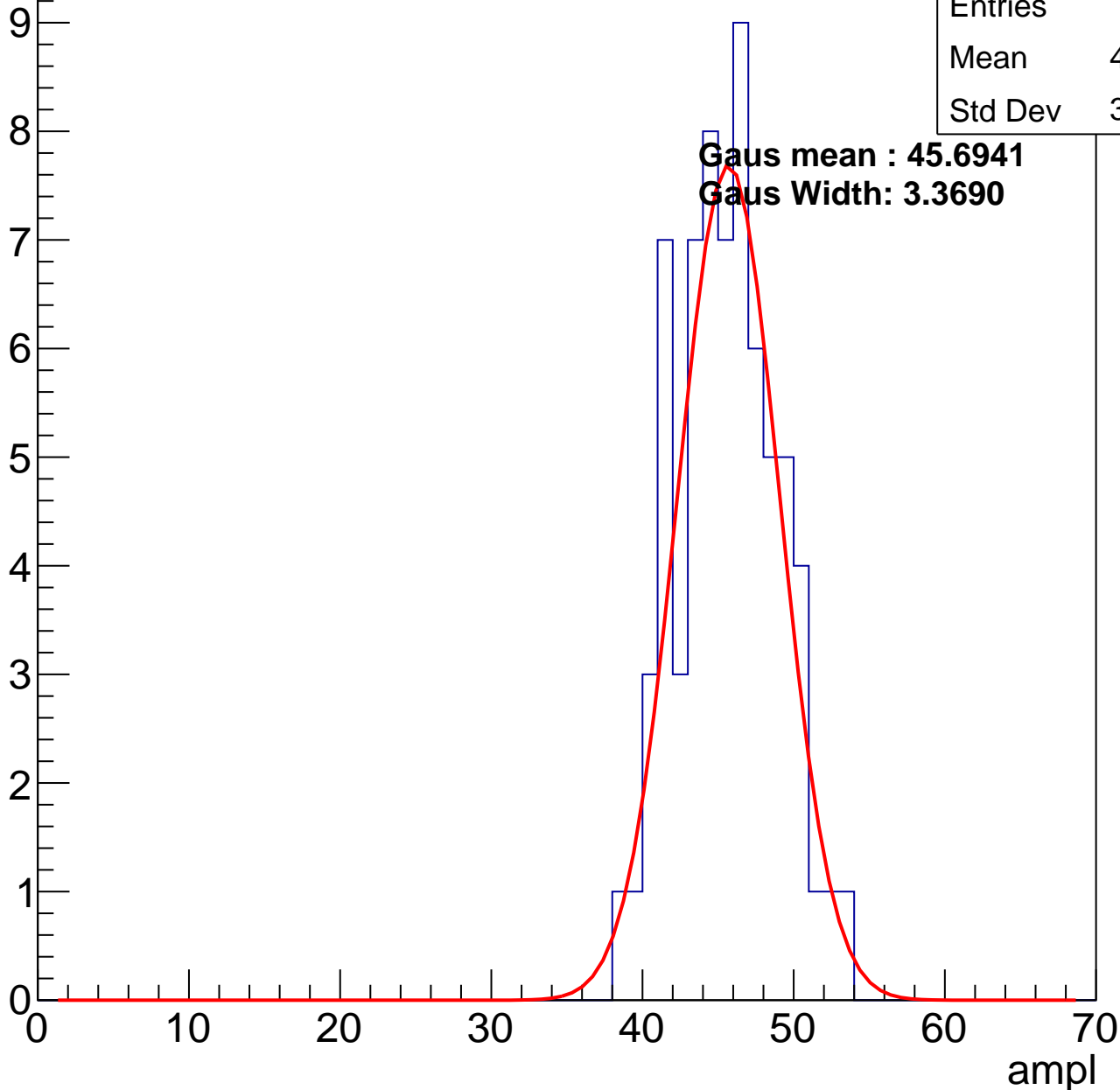
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 45.14 |
| Std Dev | 3.276 |

Gaus mean : 45.6941

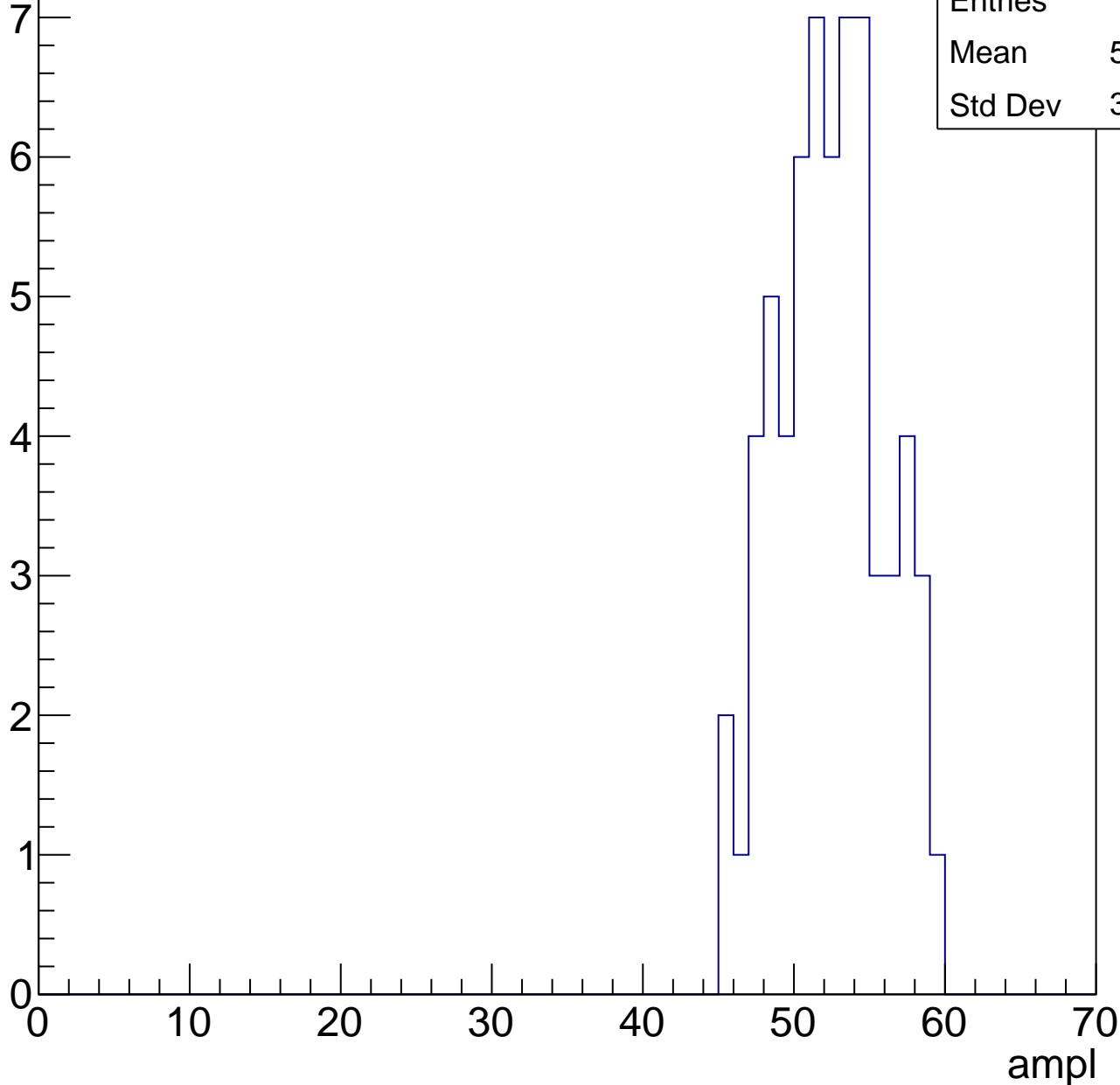
Gaus Width: 3.3690



B0L102S, U20-ch95, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

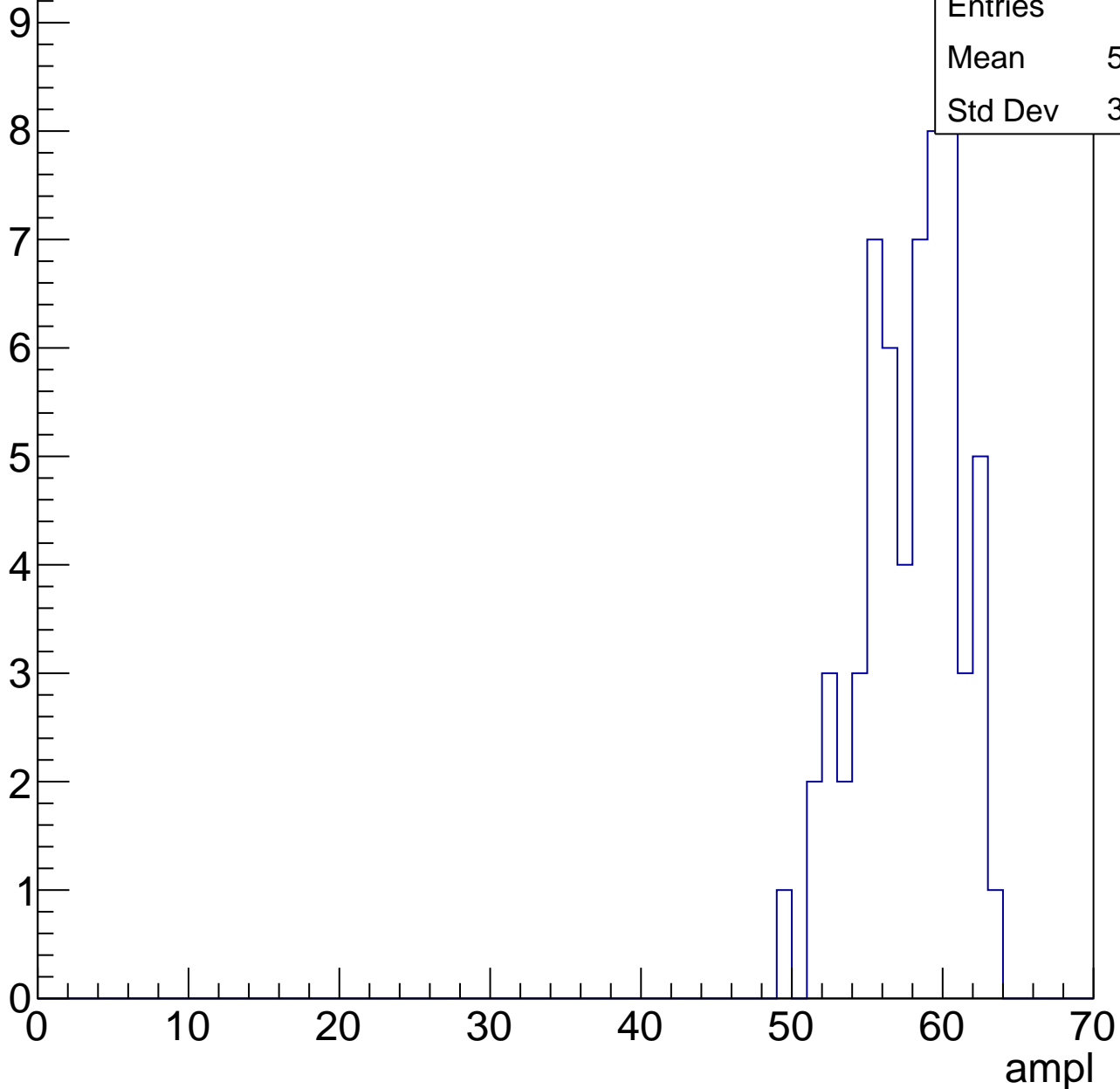


B0L102S, U20-ch95, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 57.34 |
| Std Dev | 3.203 |

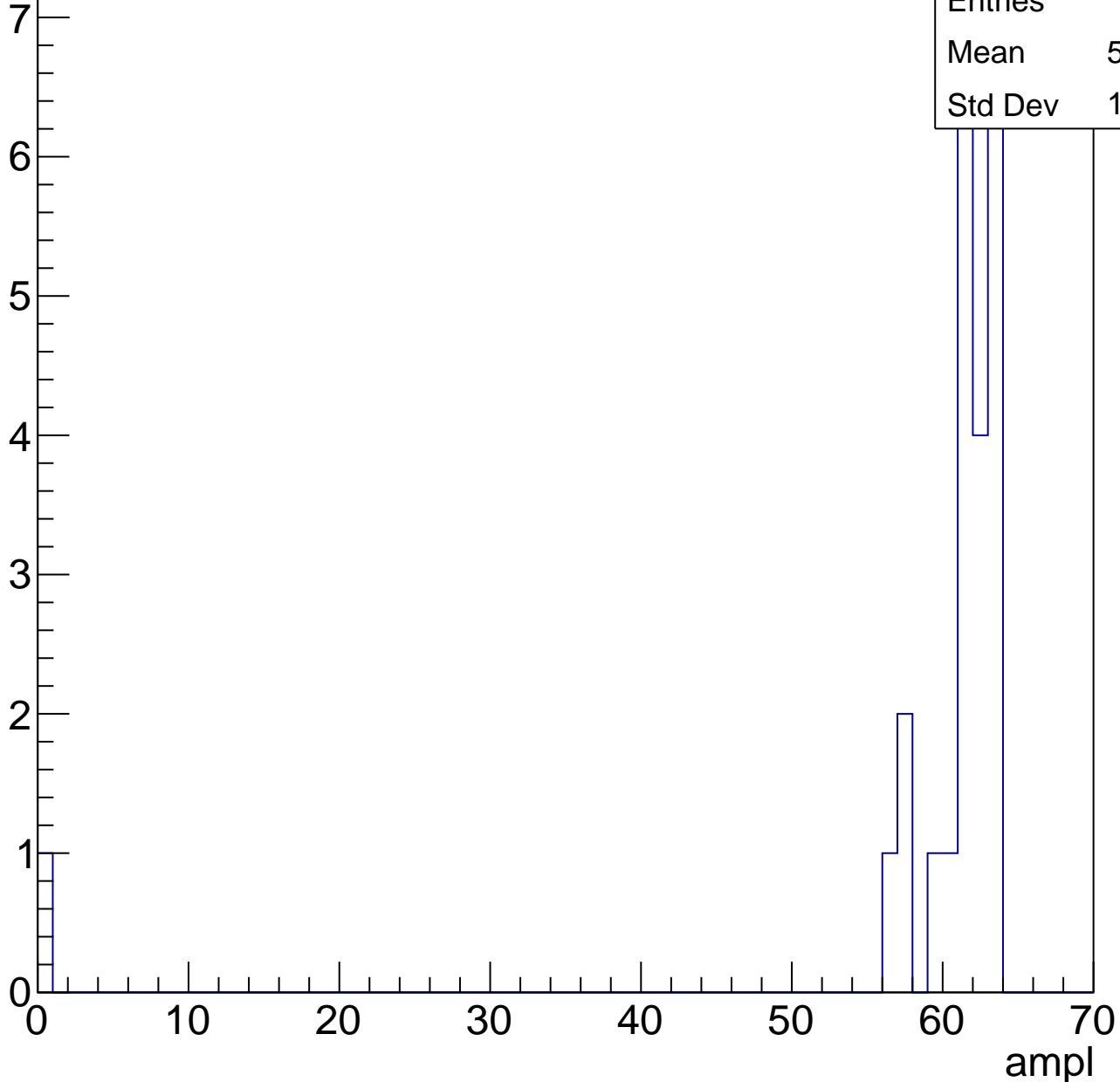


B0L102S, U20-ch95, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.54 |
| Std Dev | 12.37 |



B0L102S, U20-ch95, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch95, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch96, adc0

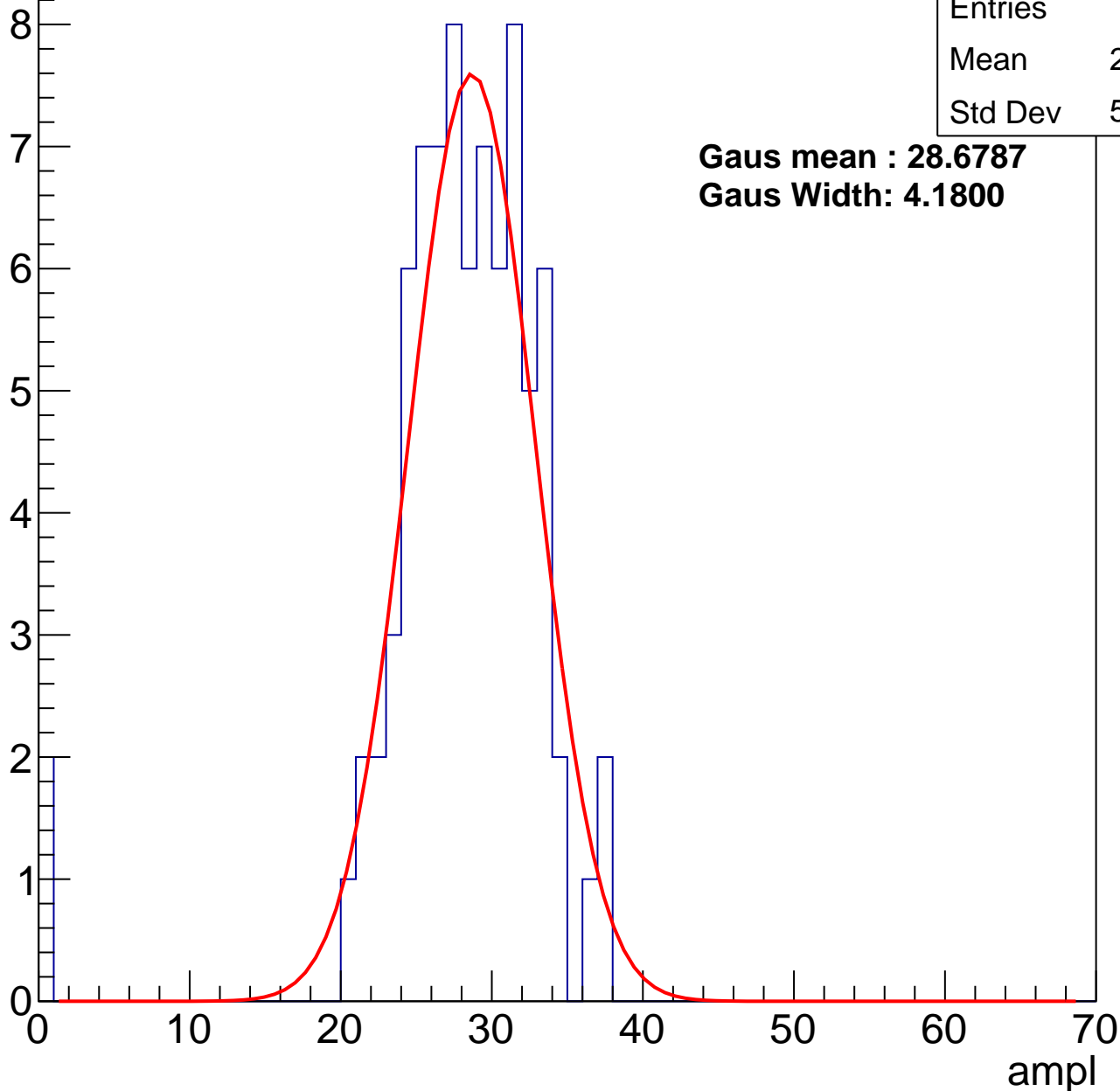
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 27.49 |
| Std Dev | 5.754 |

Gaus mean : 28.6787

Gaus Width: 4.1800



B0L102S, U20-ch96, adc1

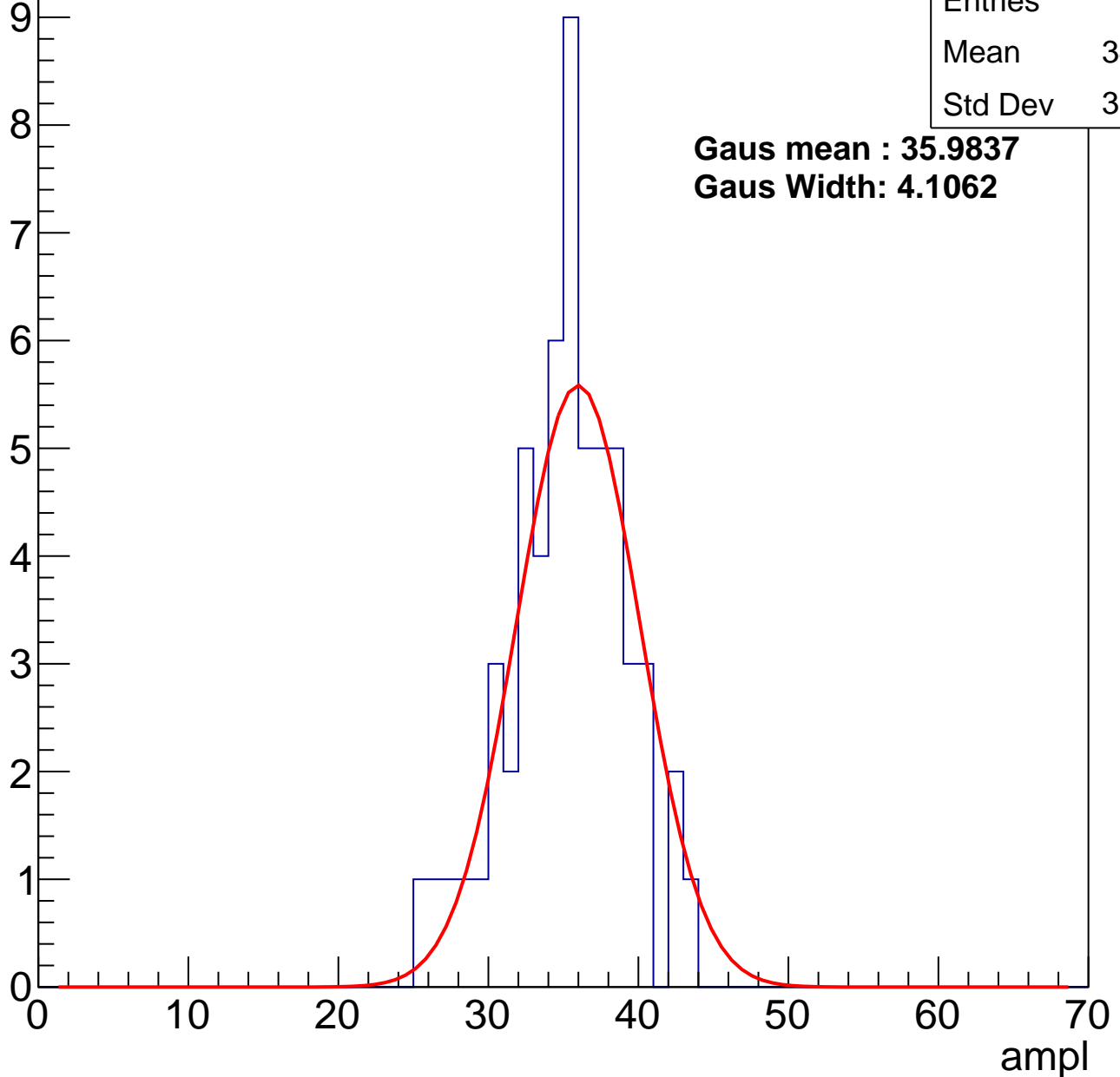
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 34.78 |
| Std Dev | 3.842 |

Gaus mean : 35.9837

Gaus Width: 4.1062



B0L102S, U20-ch96, adc2

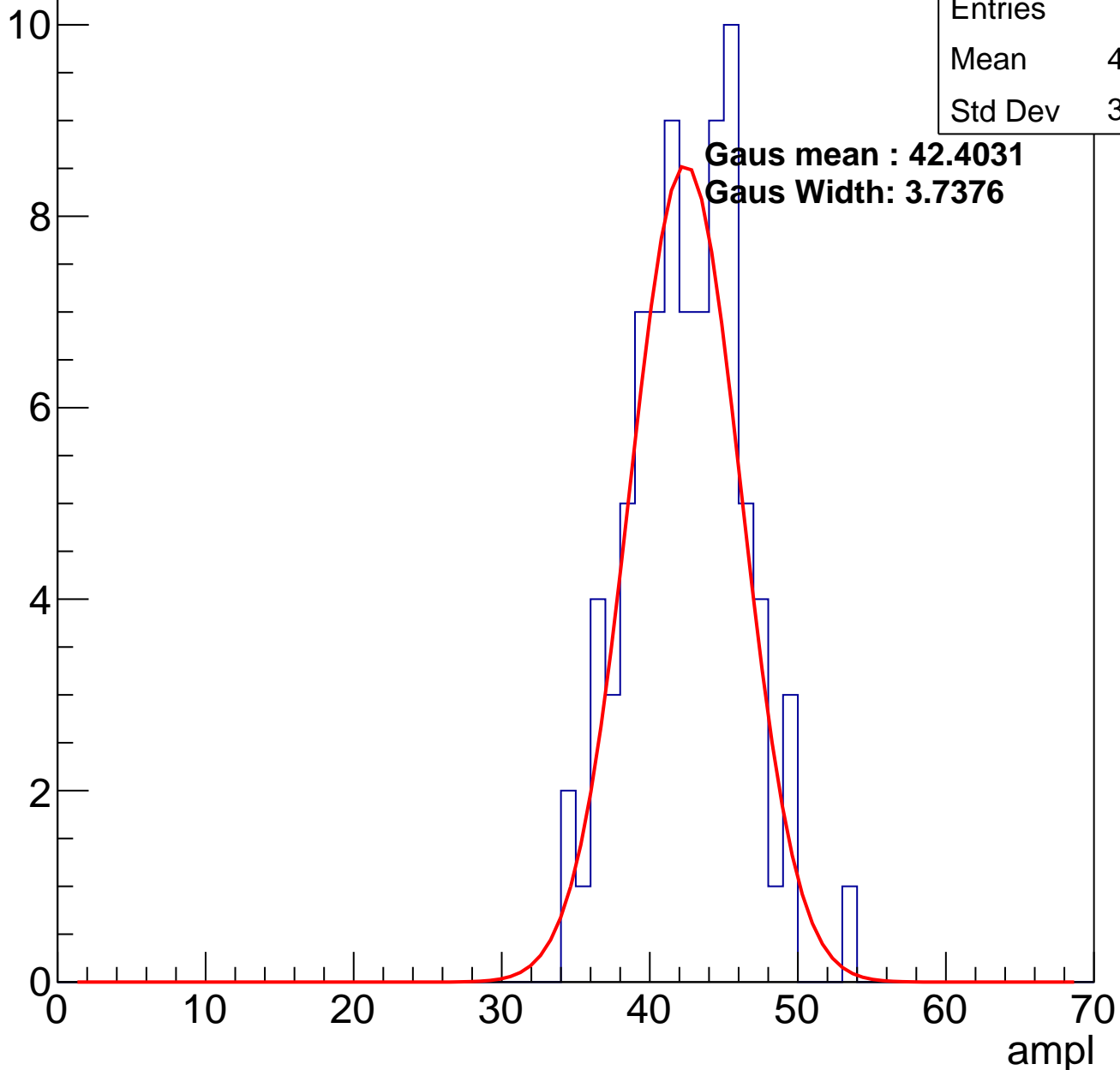
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 85 |
| Mean | 42.08 |
| Std Dev | 3.745 |

Gaus mean : 42.4031

Gaus Width: 3.7376

Entry

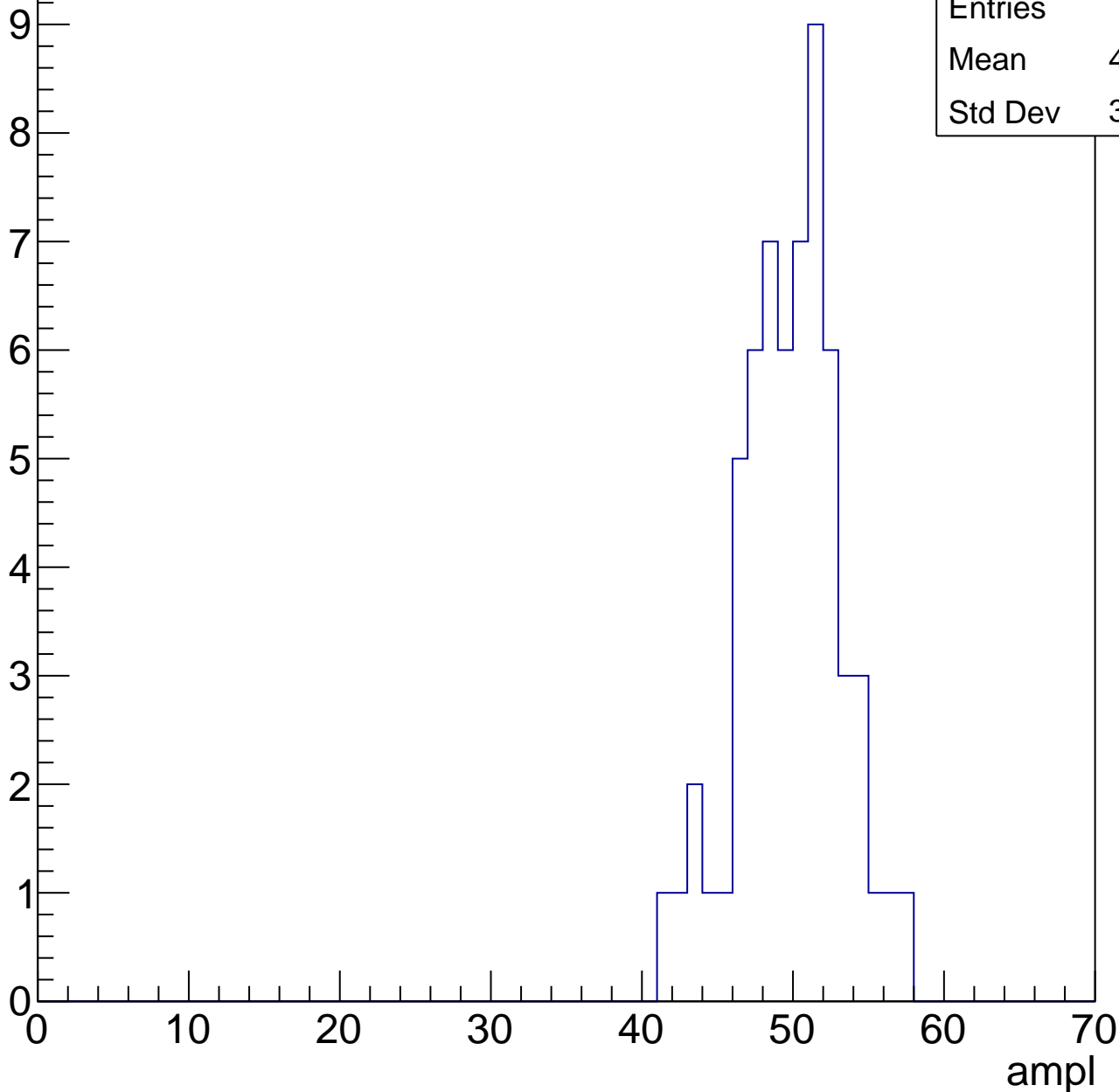


B0L102S, U20-ch96, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

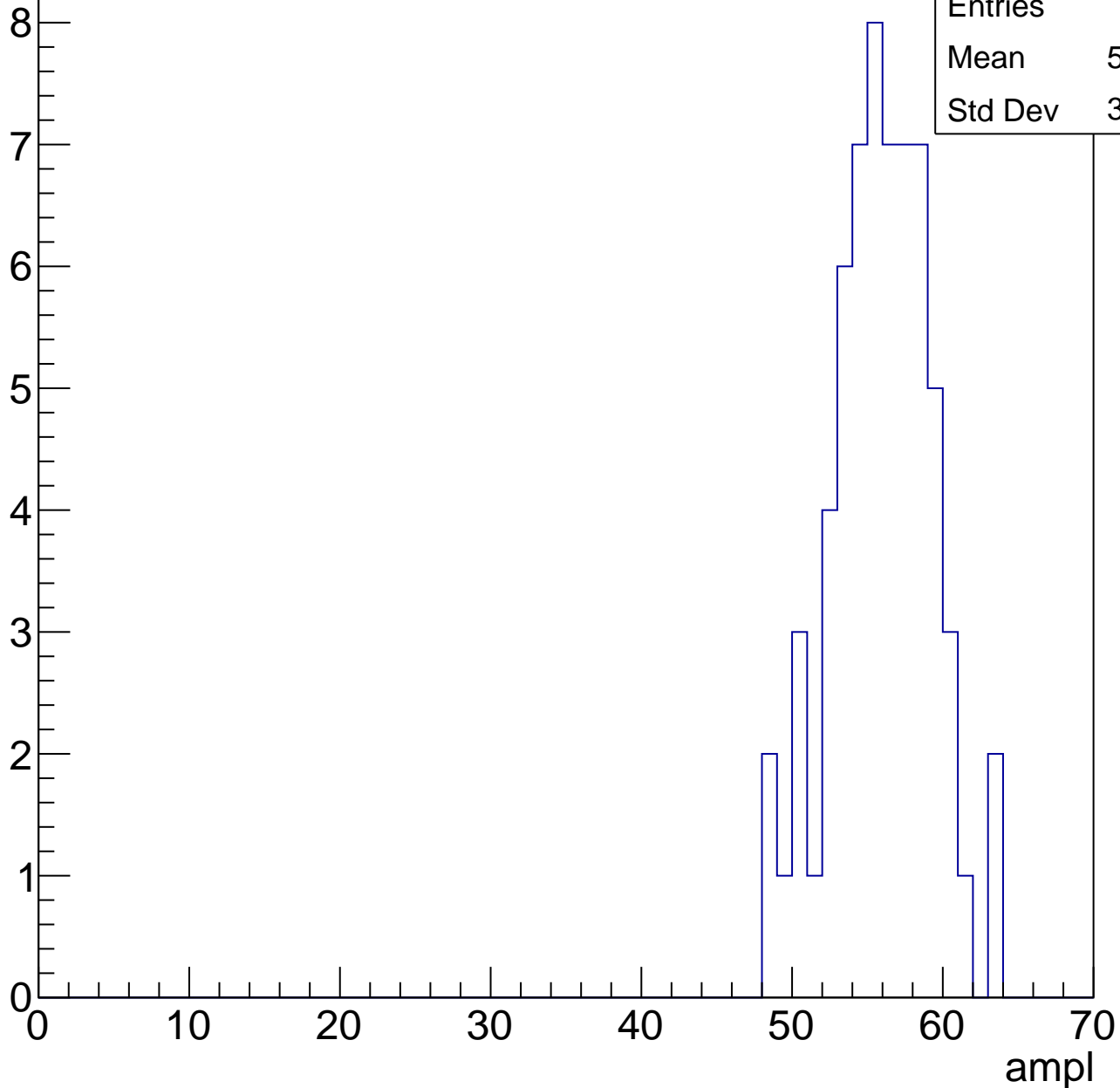
| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.34 |
| Std Dev | 3.299 |



B0L102S, U20-ch96, adc4

calib_packv5_042523_0143.root, FC#12, port B1

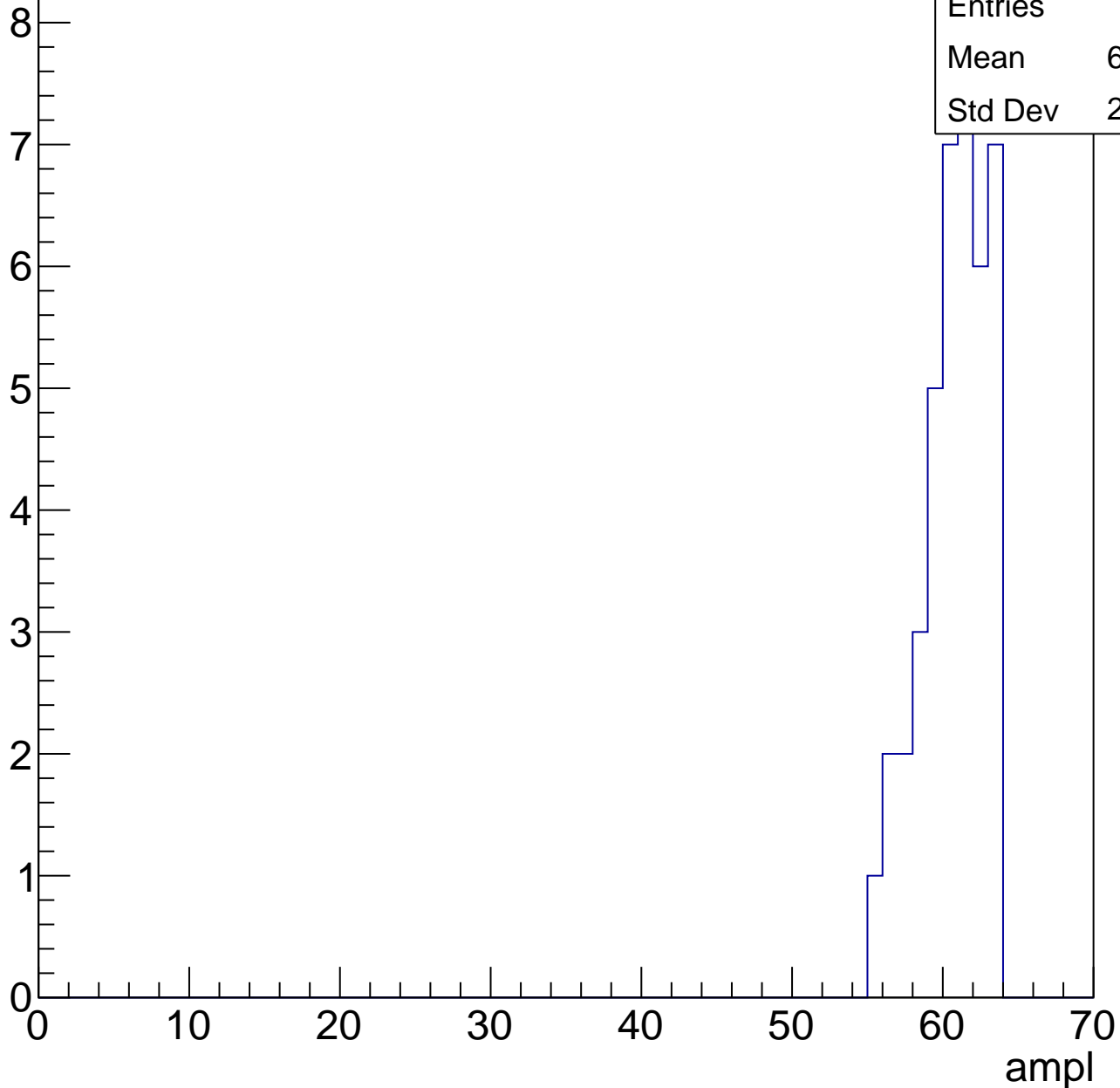
Entry



B0L102S, U20-ch96, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

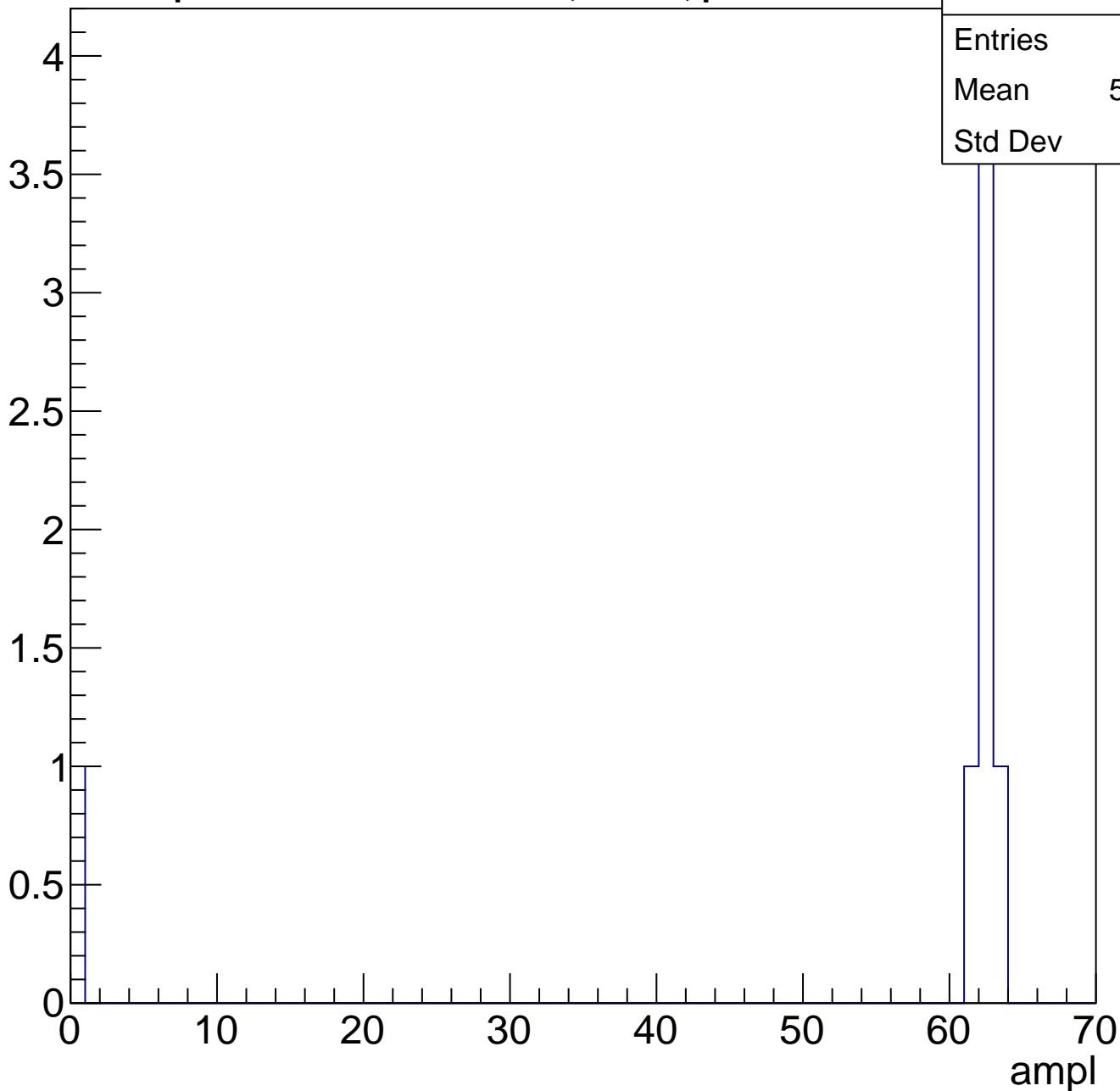


| | |
|---------|-------|
| Entries | 41 |
| Mean | 60.27 |
| Std Dev | 2.119 |

B0L102S, U20-ch96, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch96, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch97, adc0

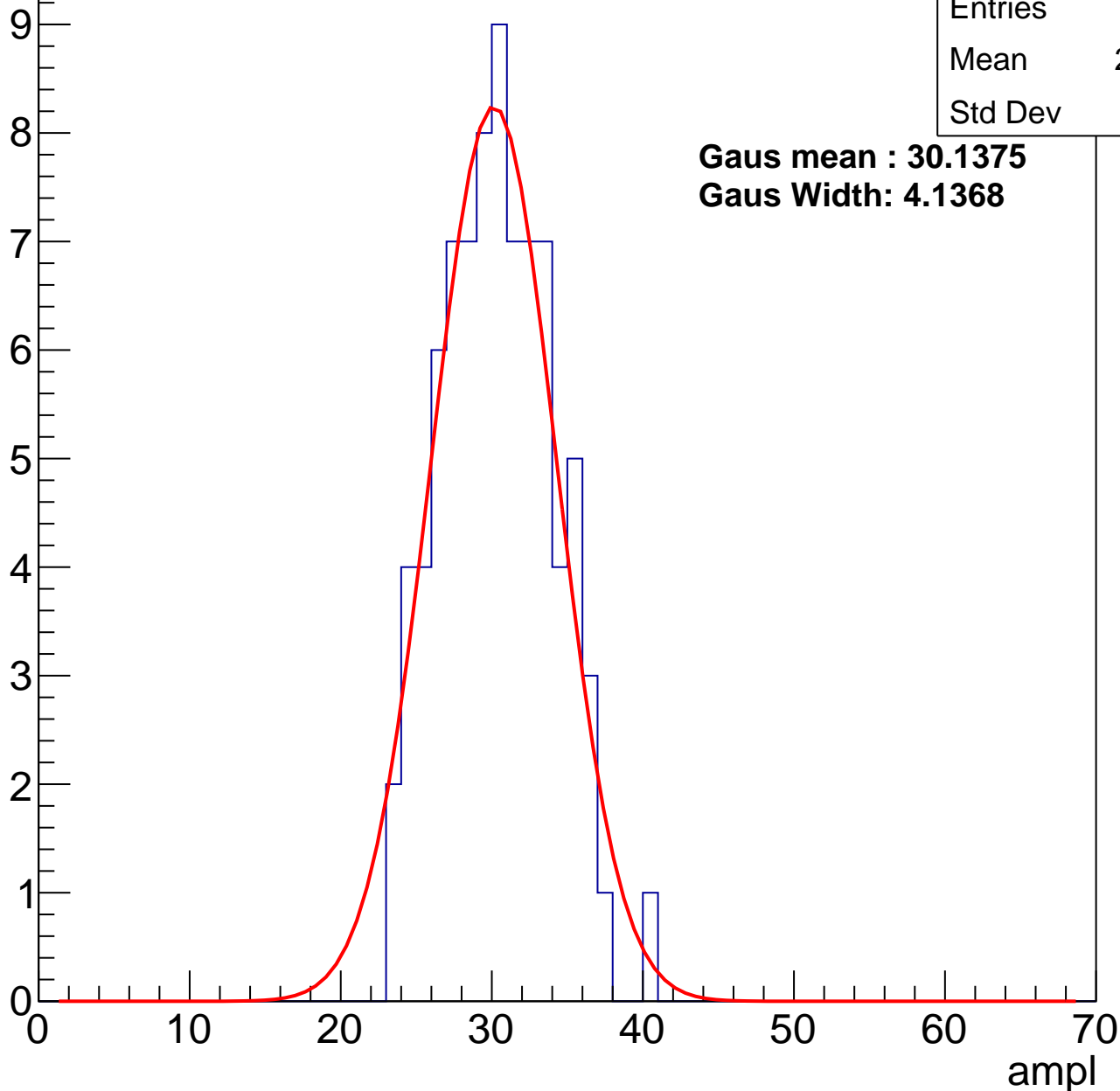
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 29.91 |
| Std Dev | 3.63 |

Gaus mean : 30.1375

Gaus Width: 4.1368



B0L102S, U20-ch97, adc1

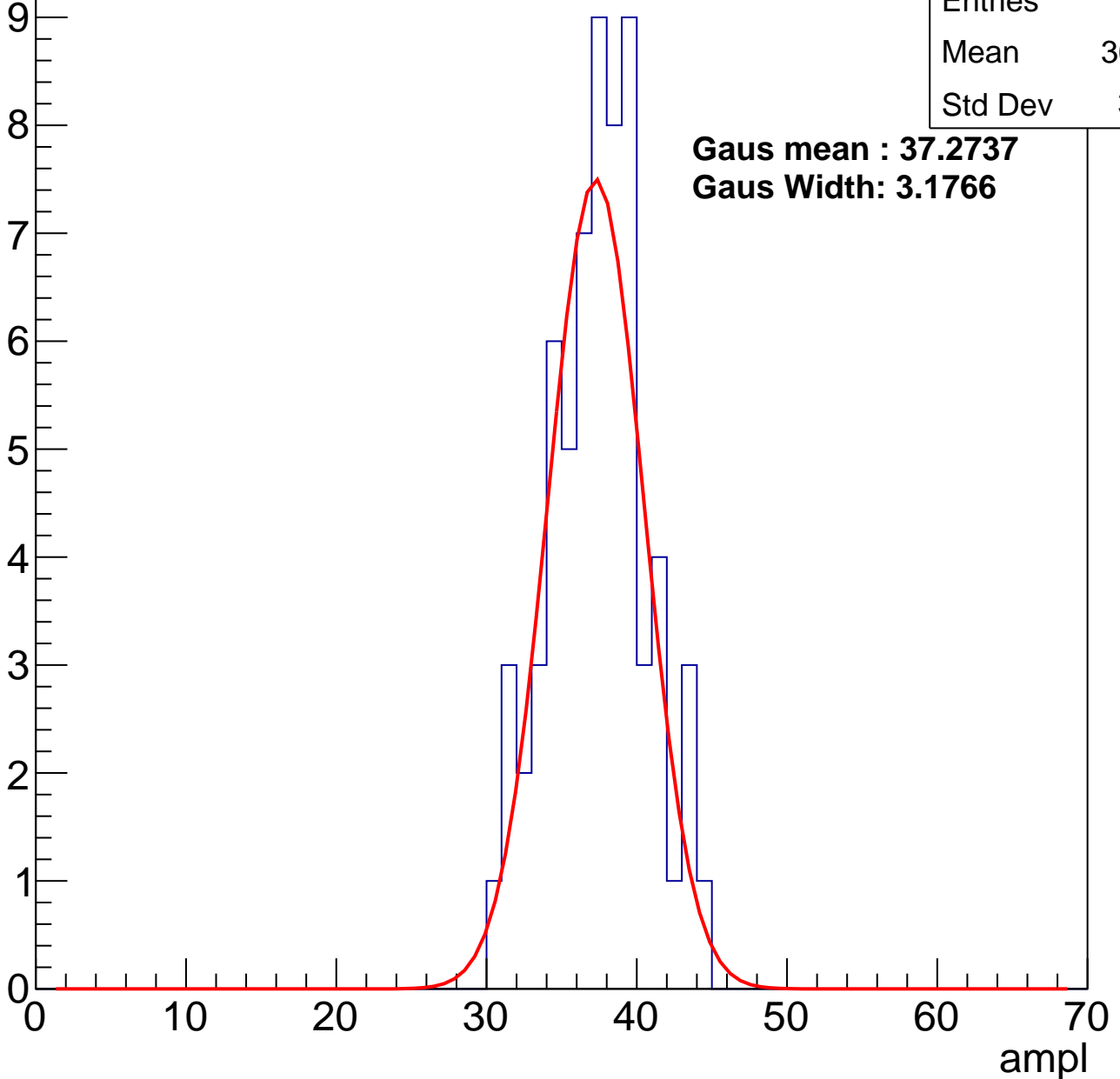
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.98 |
| Std Dev | 3.17 |

Gaus mean : 37.2737

Gaus Width: 3.1766



B0L102S, U20-ch97, adc2

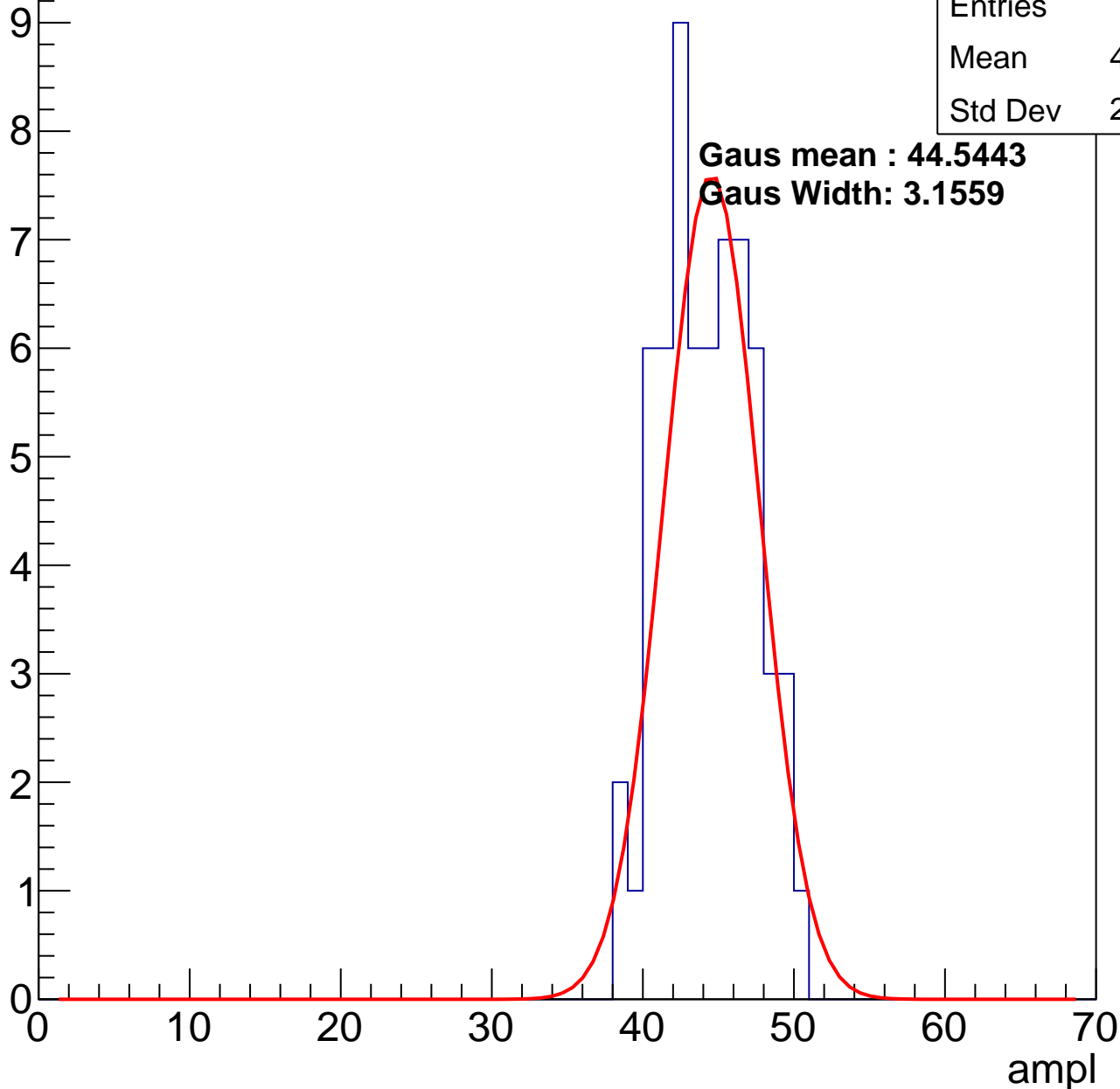
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.83 |
| Std Dev | 2.914 |

Gaus mean : 44.5443

Gaus Width: 3.1559

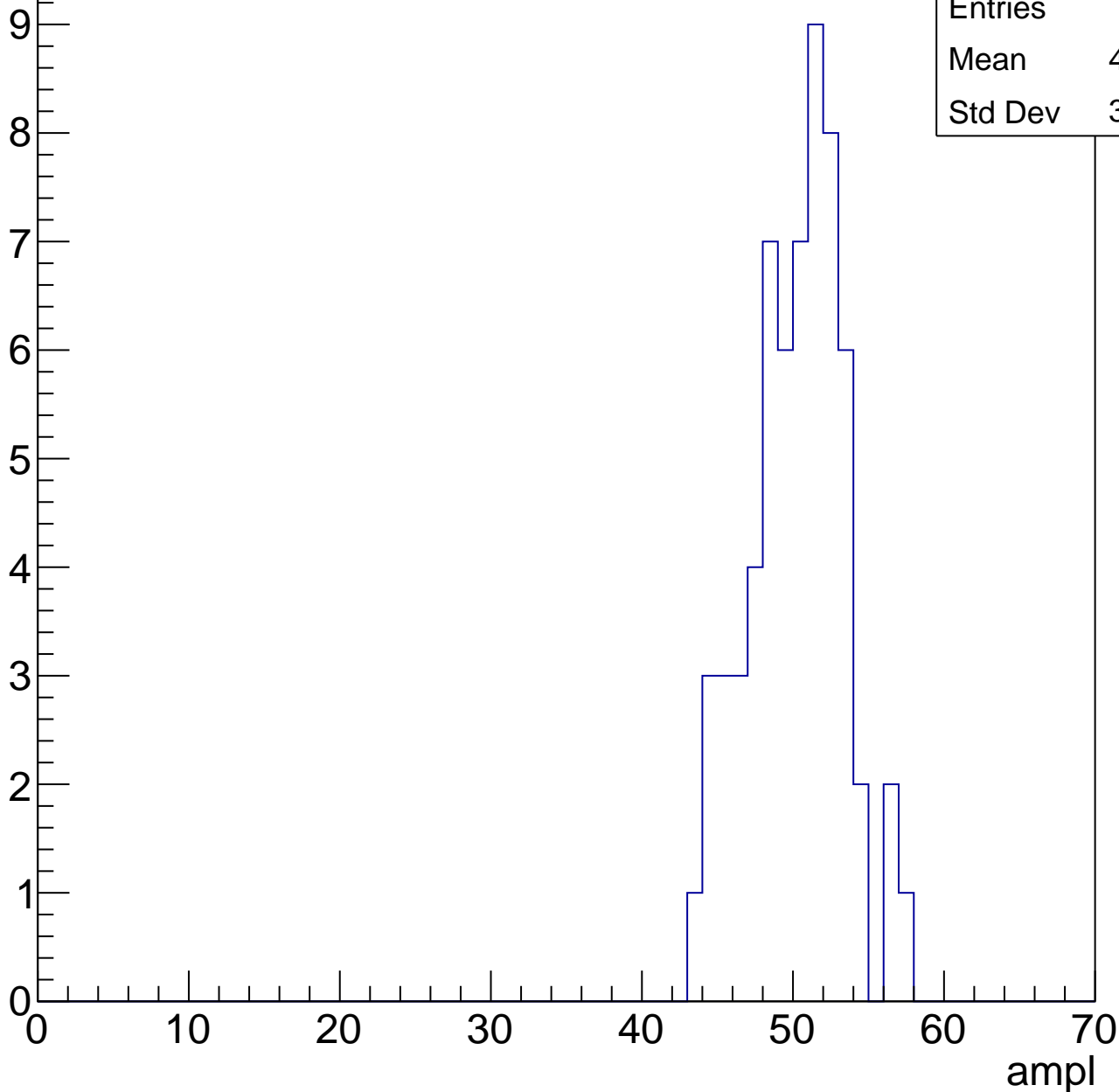


B0L102S, U20-ch97, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.77 |
| Std Dev | 3.097 |

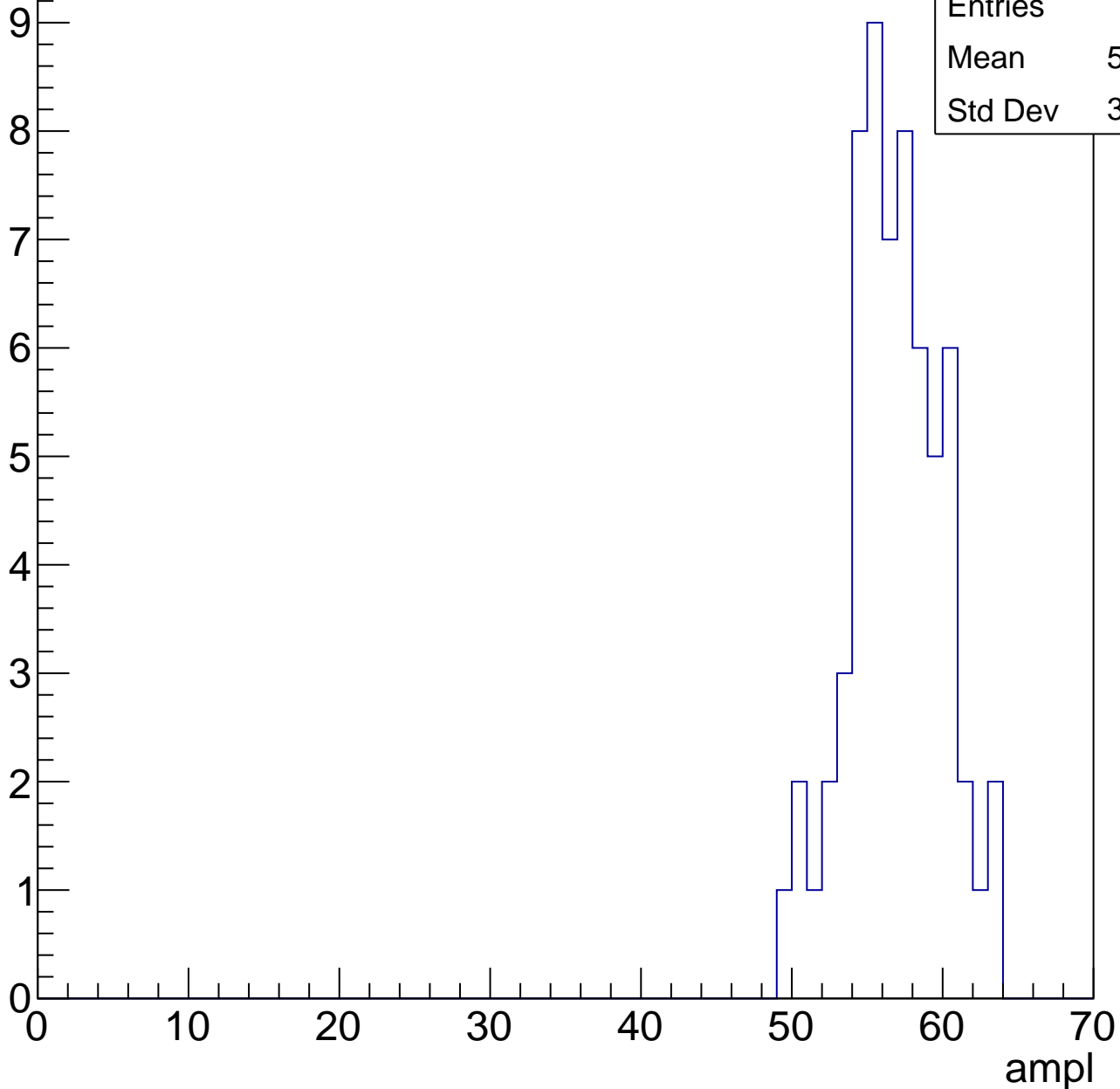


B0L102S, U20-ch97, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 56.37 |
| Std Dev | 3.072 |

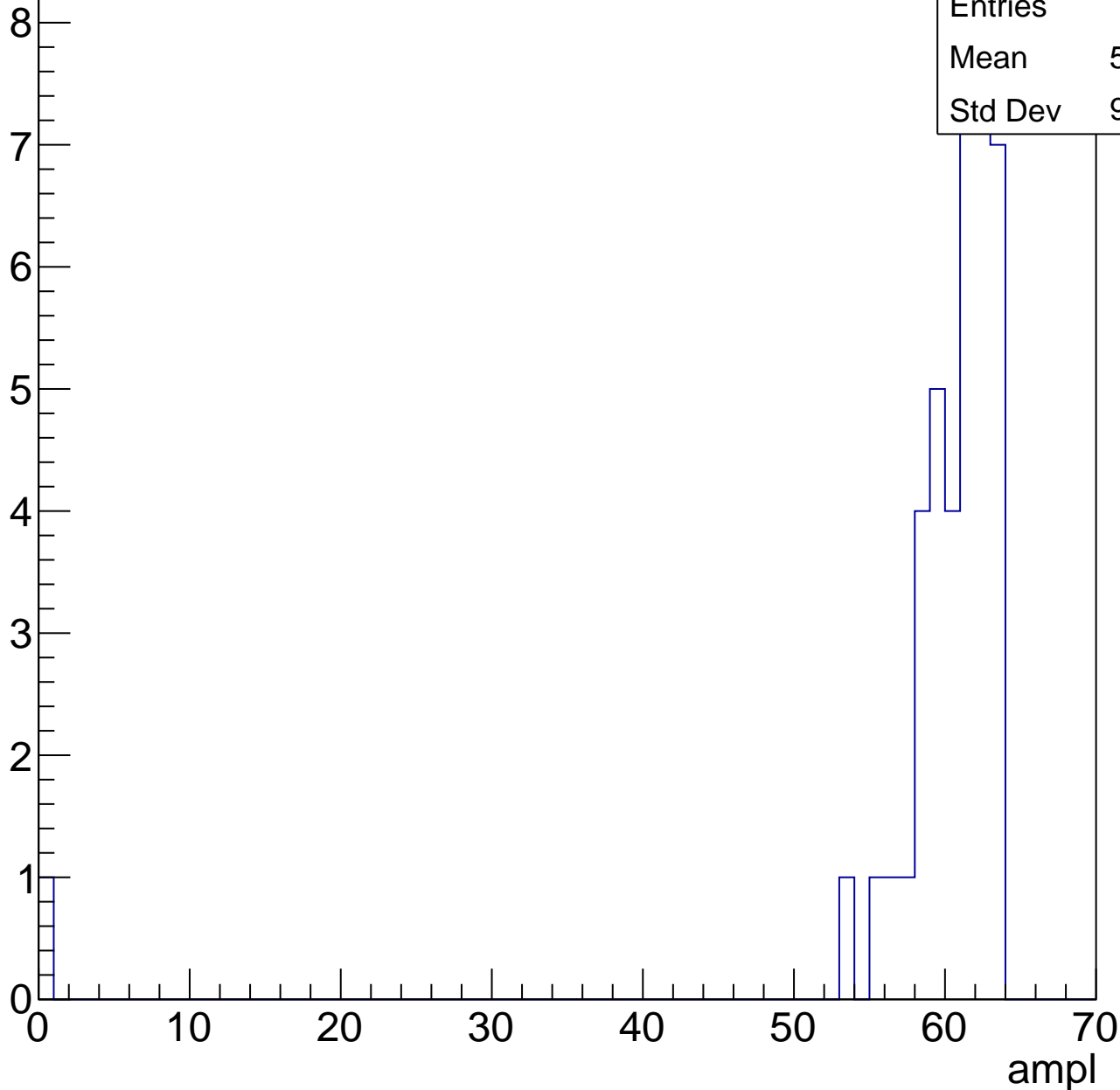


B0L102S, U20-ch97, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 58.85 |
| Std Dev | 9.588 |



B0L102S, U20-ch97, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch97, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch98, adc0

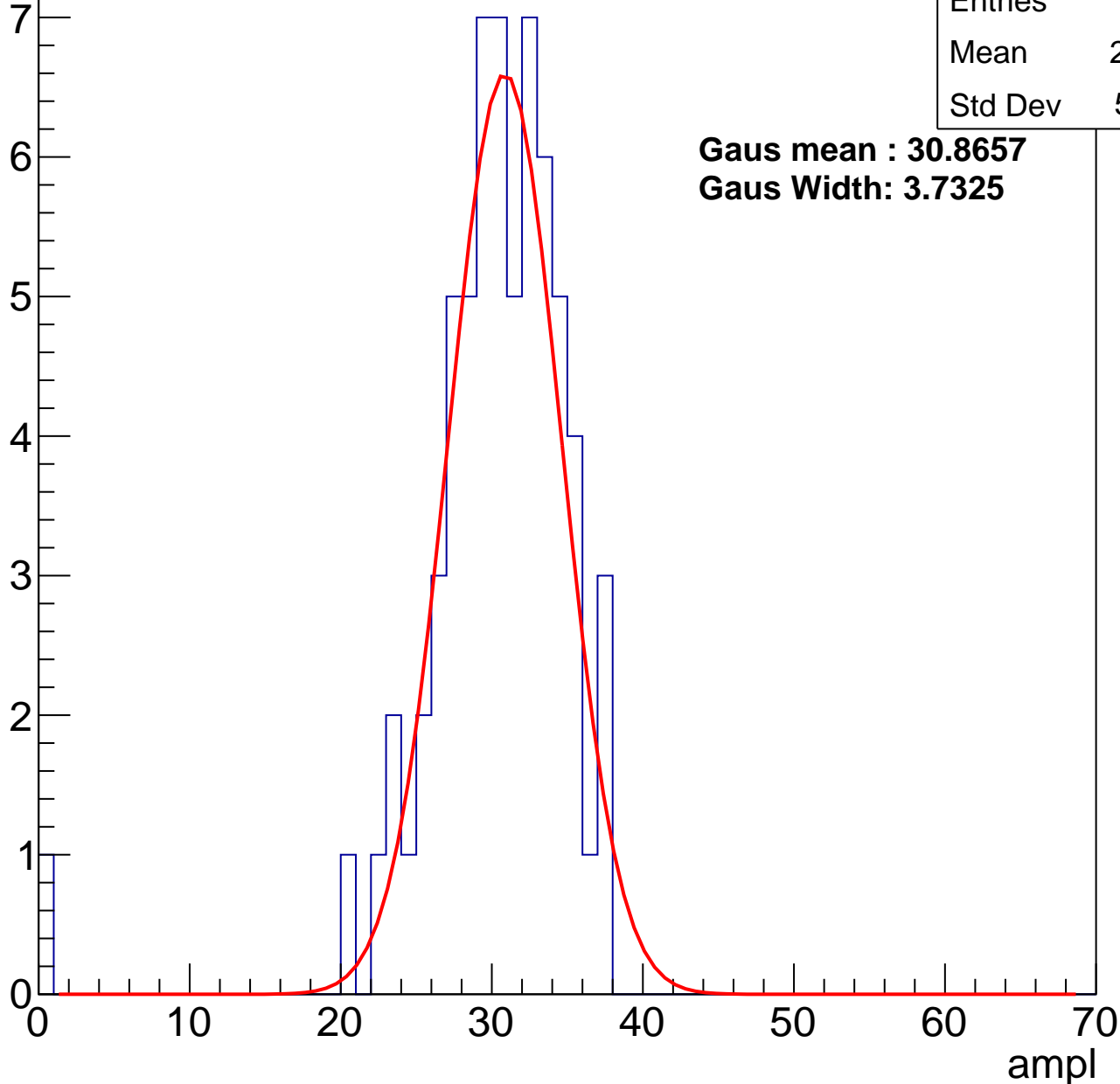
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 29.73 |
| Std Dev | 5.241 |

Gaus mean : 30.8657

Gaus Width: 3.7325



B0L102S, U20-ch98, adc1

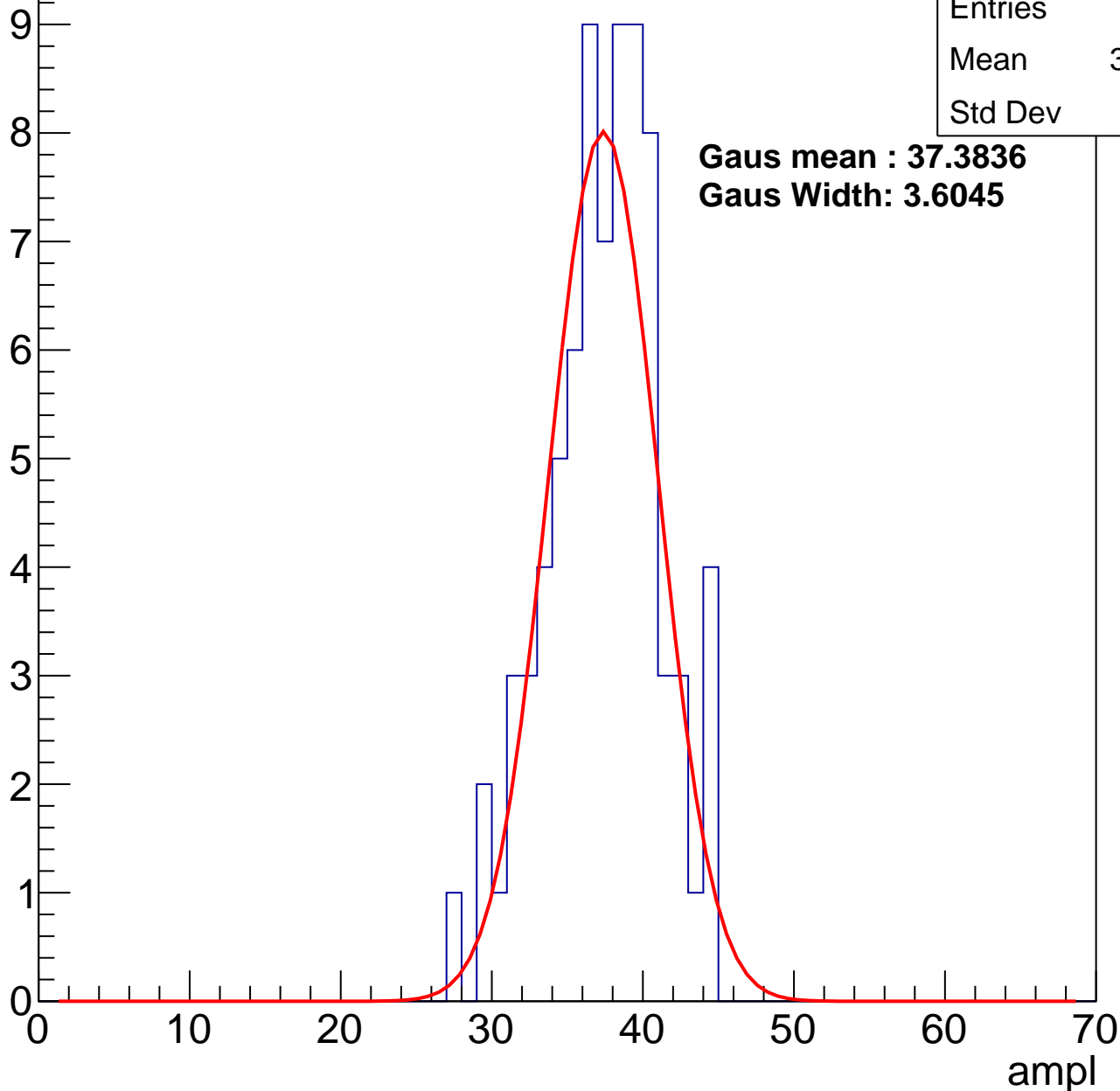
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 36.92 |
| Std Dev | 3.71 |

Gaus mean : 37.3836

Gaus Width: 3.6045



B0L102S, U20-ch98, adc2

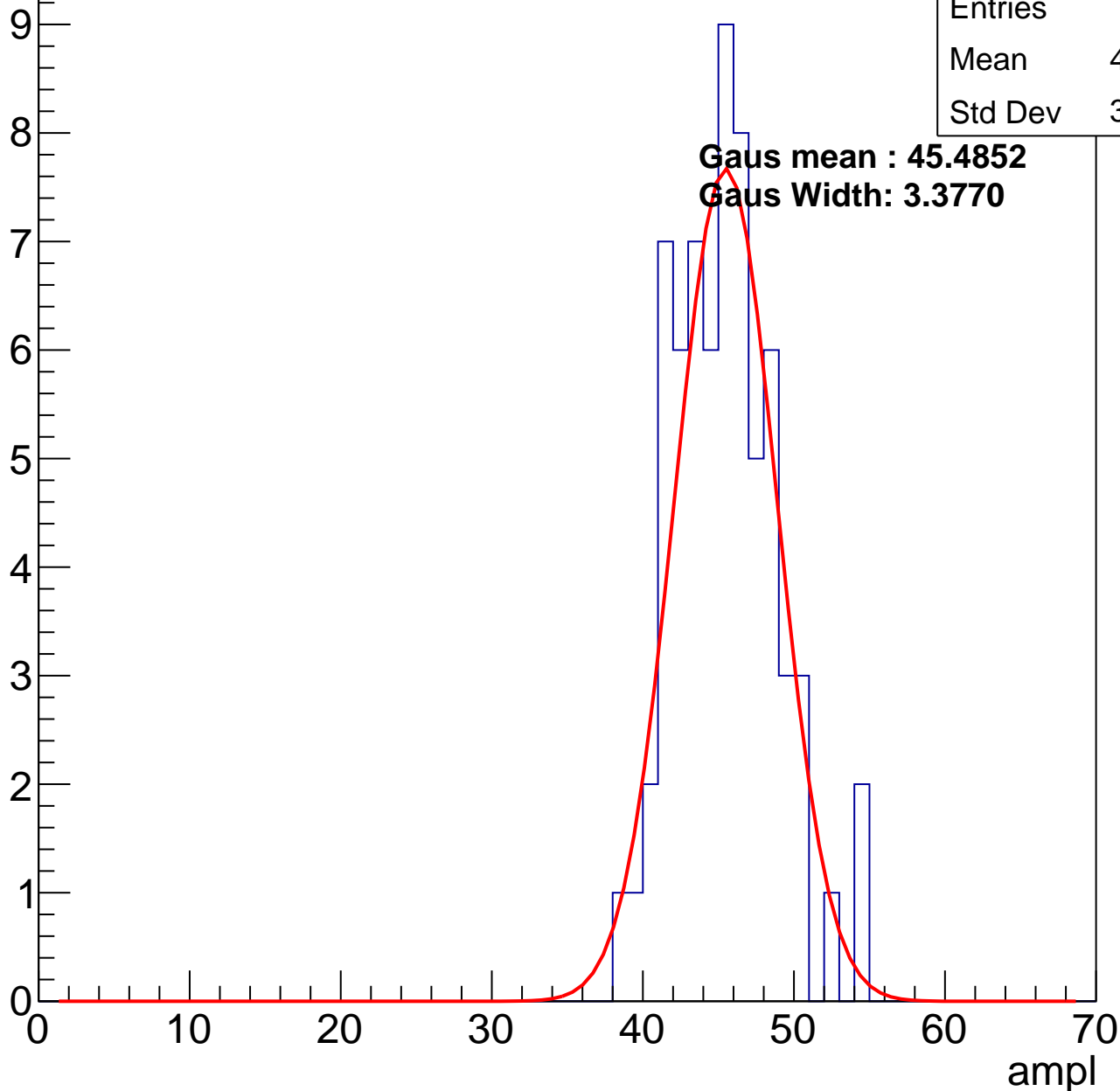
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.99 |
| Std Dev | 3.348 |

Gaus mean : 45.4852

Gaus Width: 3.3770

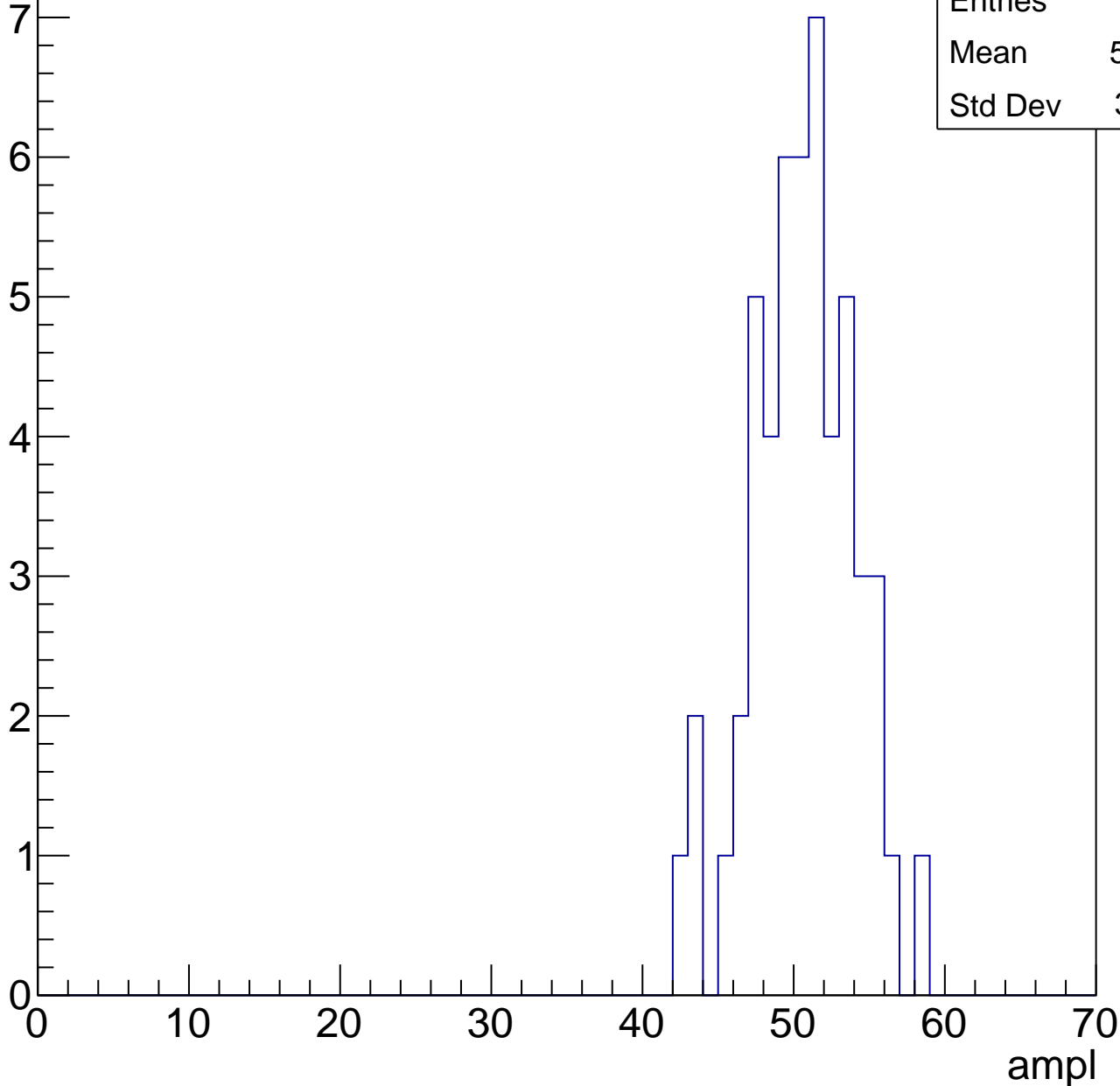


B0L102S, U20-ch98, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 50.14 |
| Std Dev | 3.361 |

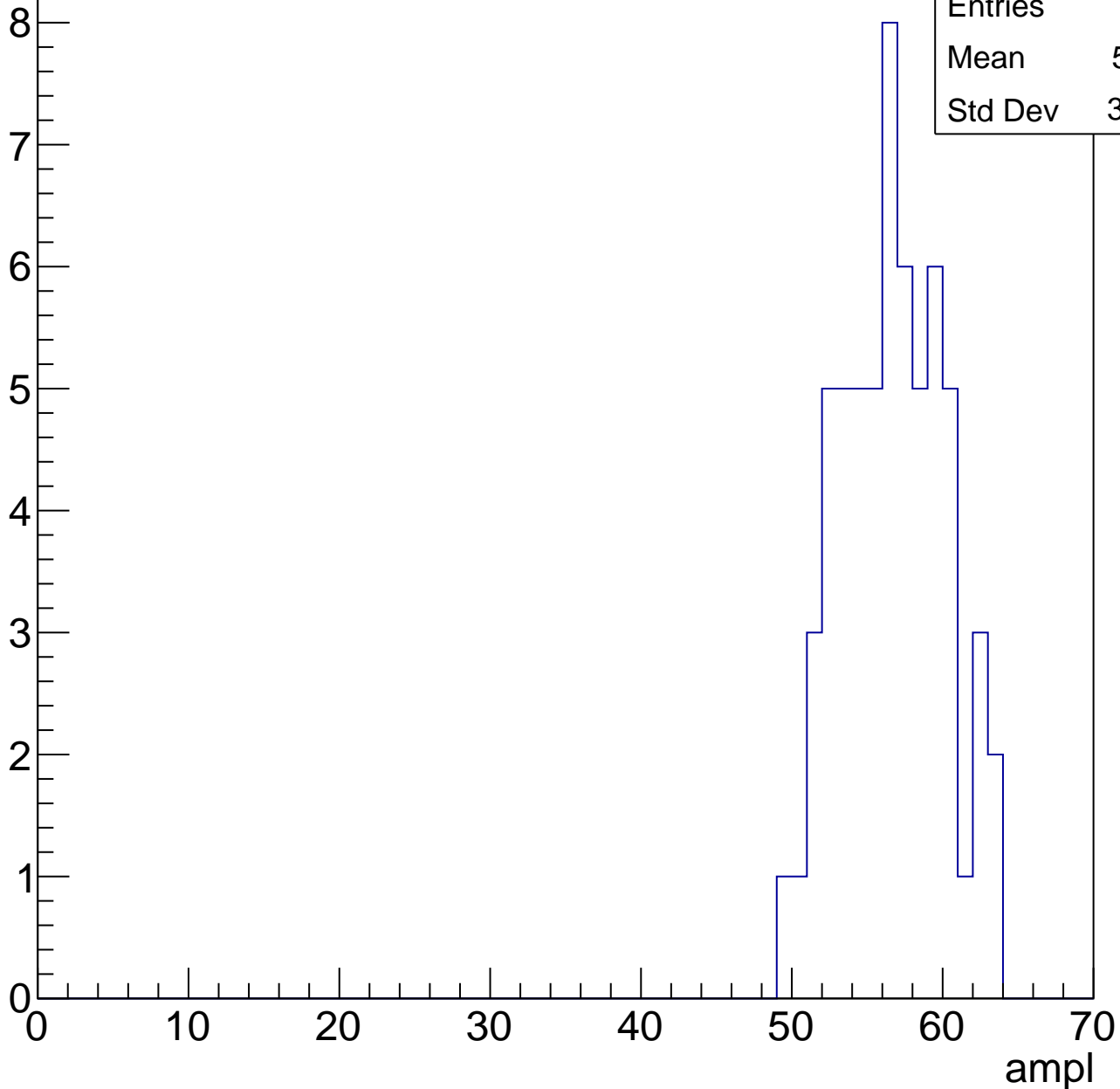


B0L102S, U20-ch98, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 56.21 |
| Std Dev | 3.383 |

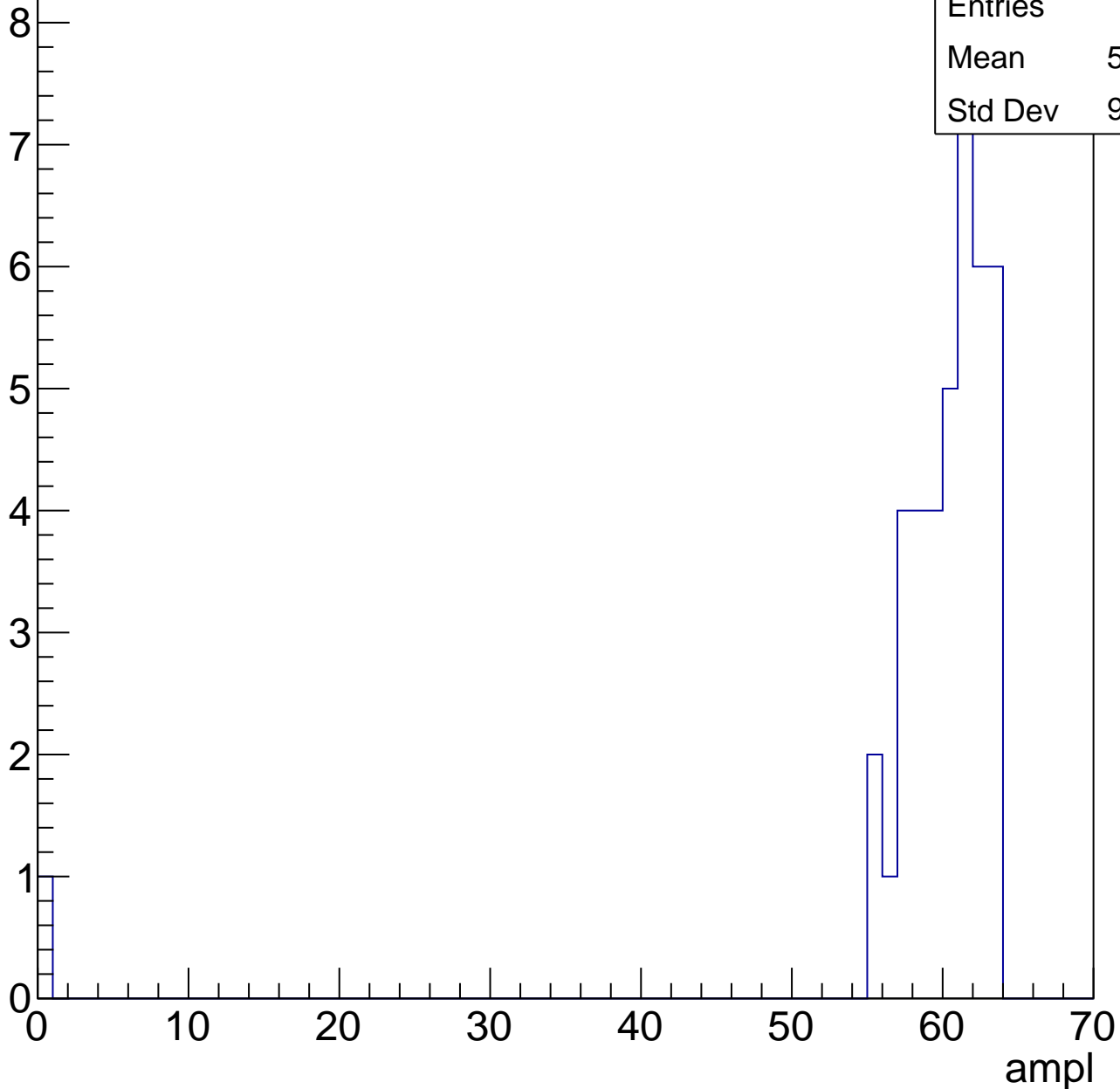


B0L102S, U20-ch98, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 58.54 |
| Std Dev | 9.526 |



B0L102S, U20-ch98, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 4 |
| Mean | 62.25 |
| Std Dev | 0.8292 |

ampl

0 10 20 30 40 50 60 70

B0L102S, U20-ch98, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch99, adc0

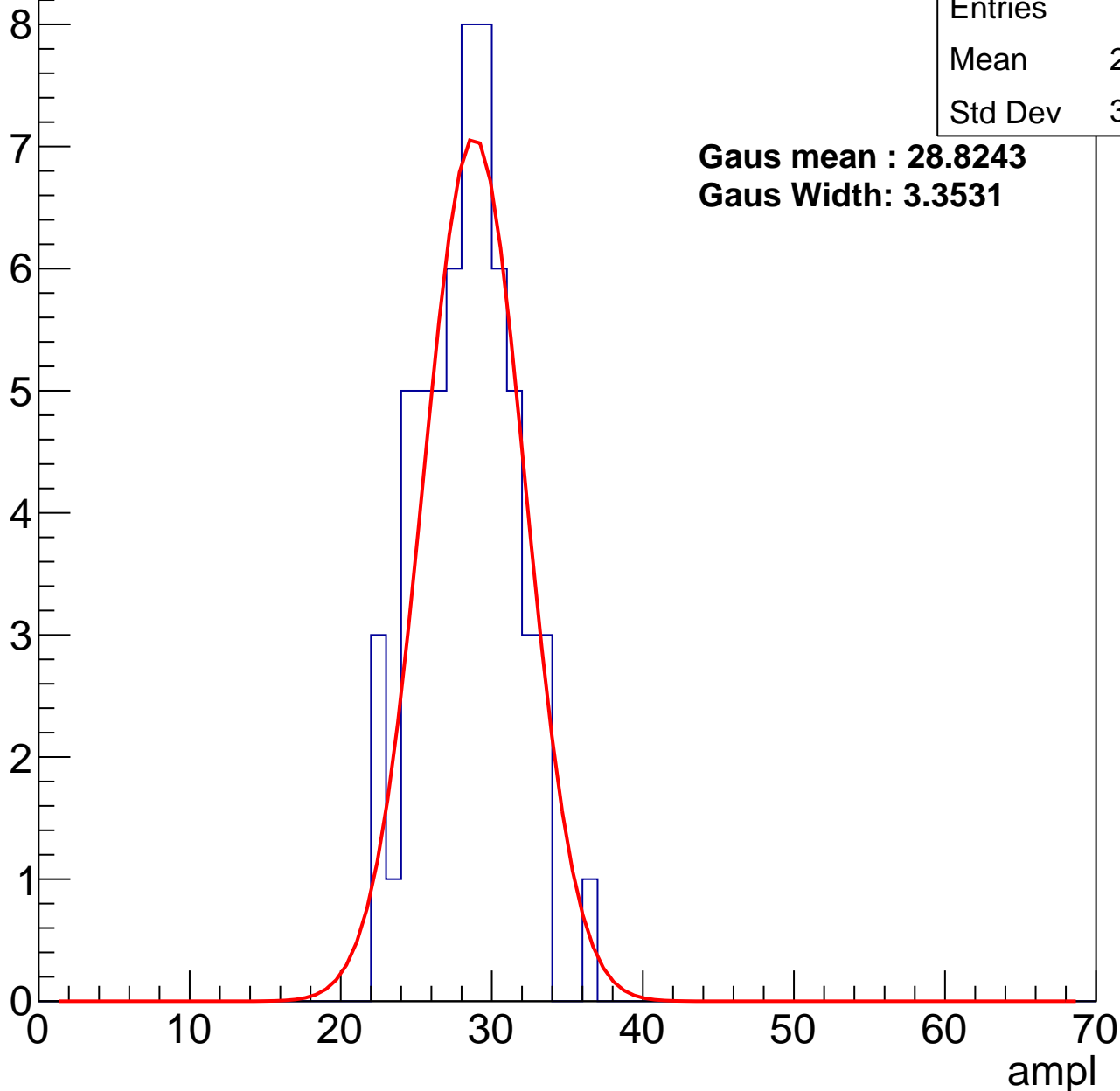
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 27.93 |
| Std Dev | 3.047 |

Gaus mean : 28.8243

Gaus Width: 3.3531



B0L102S, U20-ch99, adc1

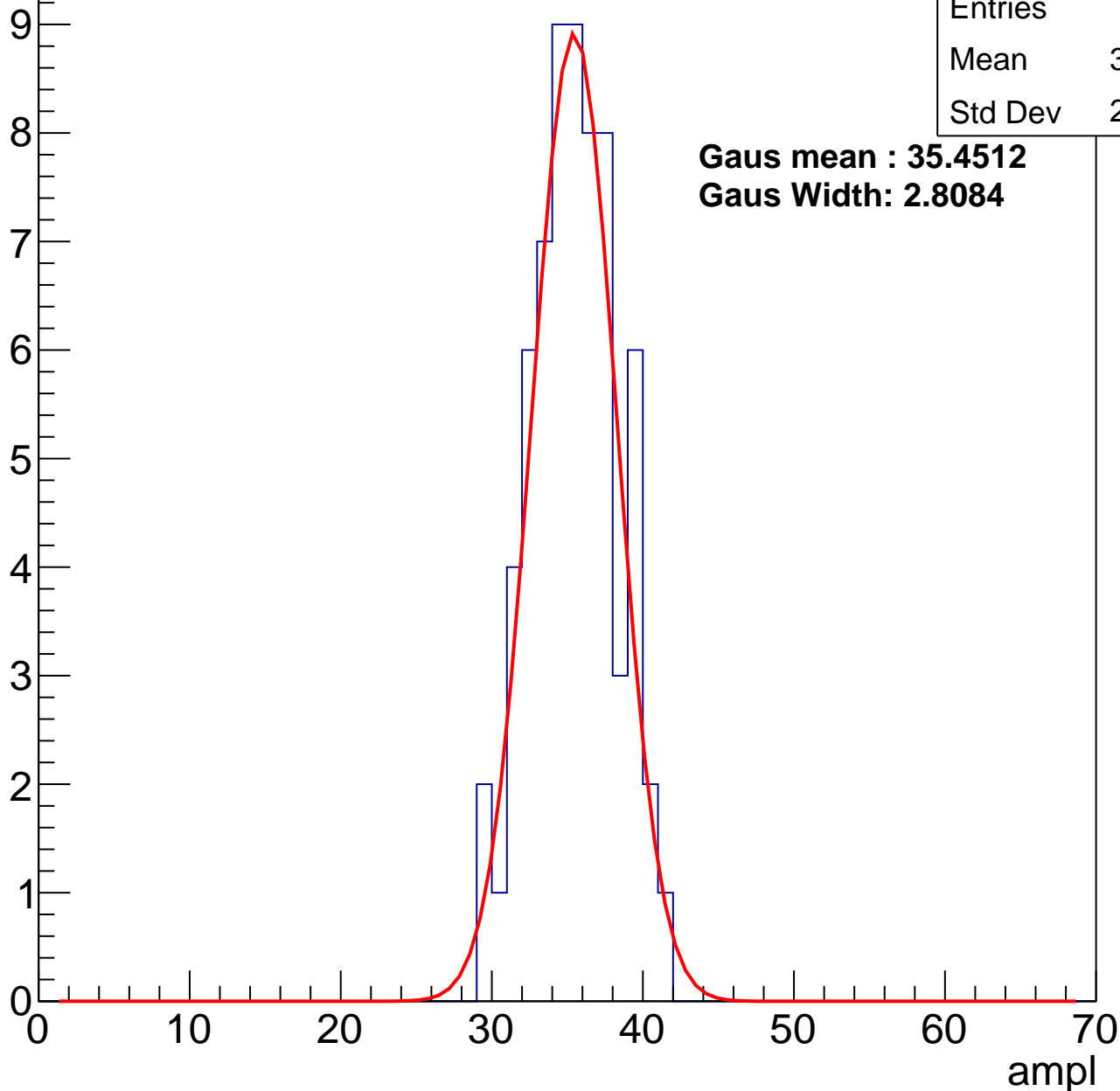
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 34.98 |
| Std Dev | 2.755 |

Gaus mean : 35.4512

Gaus Width: 2.8084



B0L102S, U20-ch99, adc2

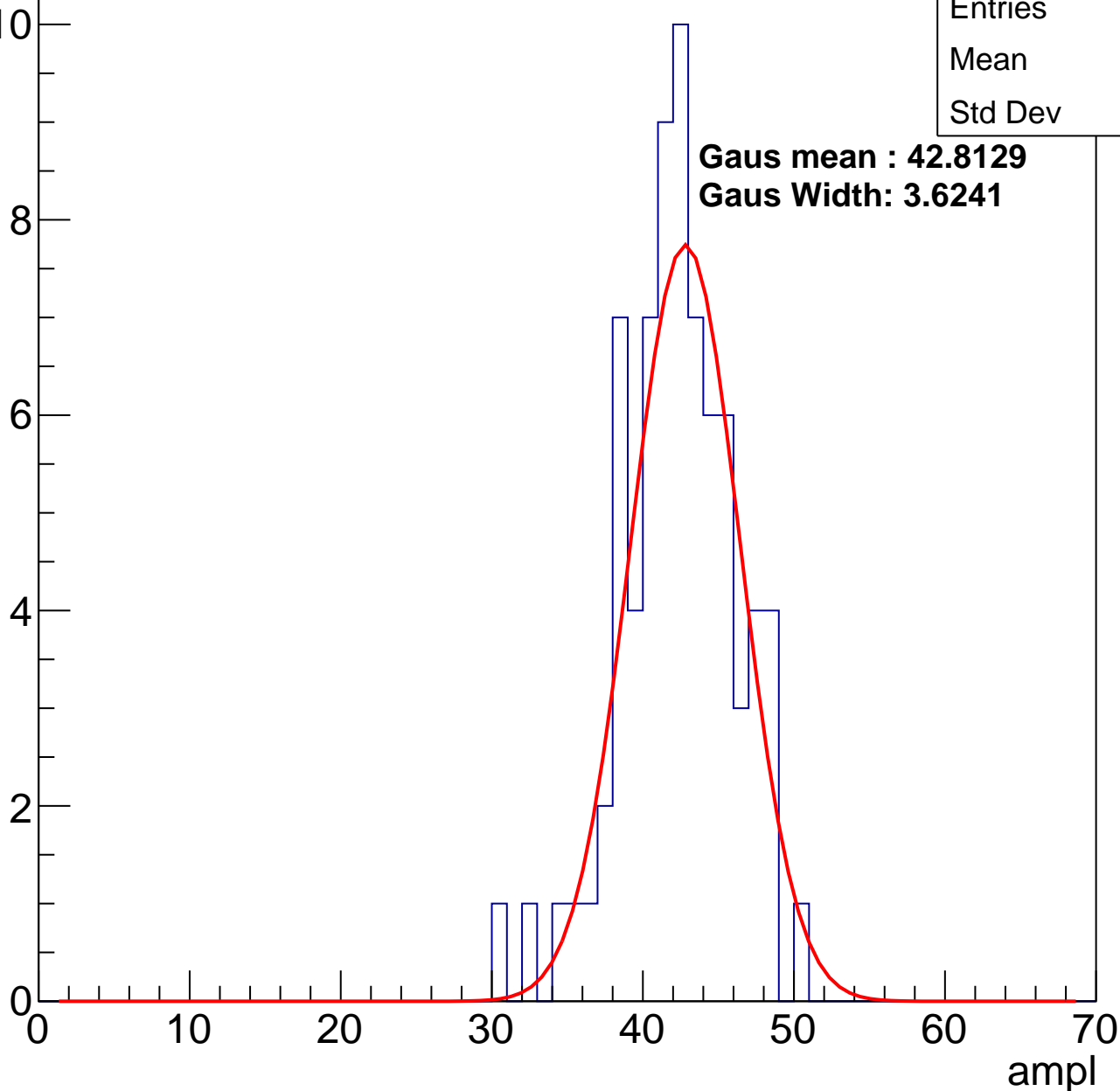
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 75 |
| Mean | 41.8 |
| Std Dev | 3.77 |

Gaus mean : 42.8129

Gaus Width: 3.6241

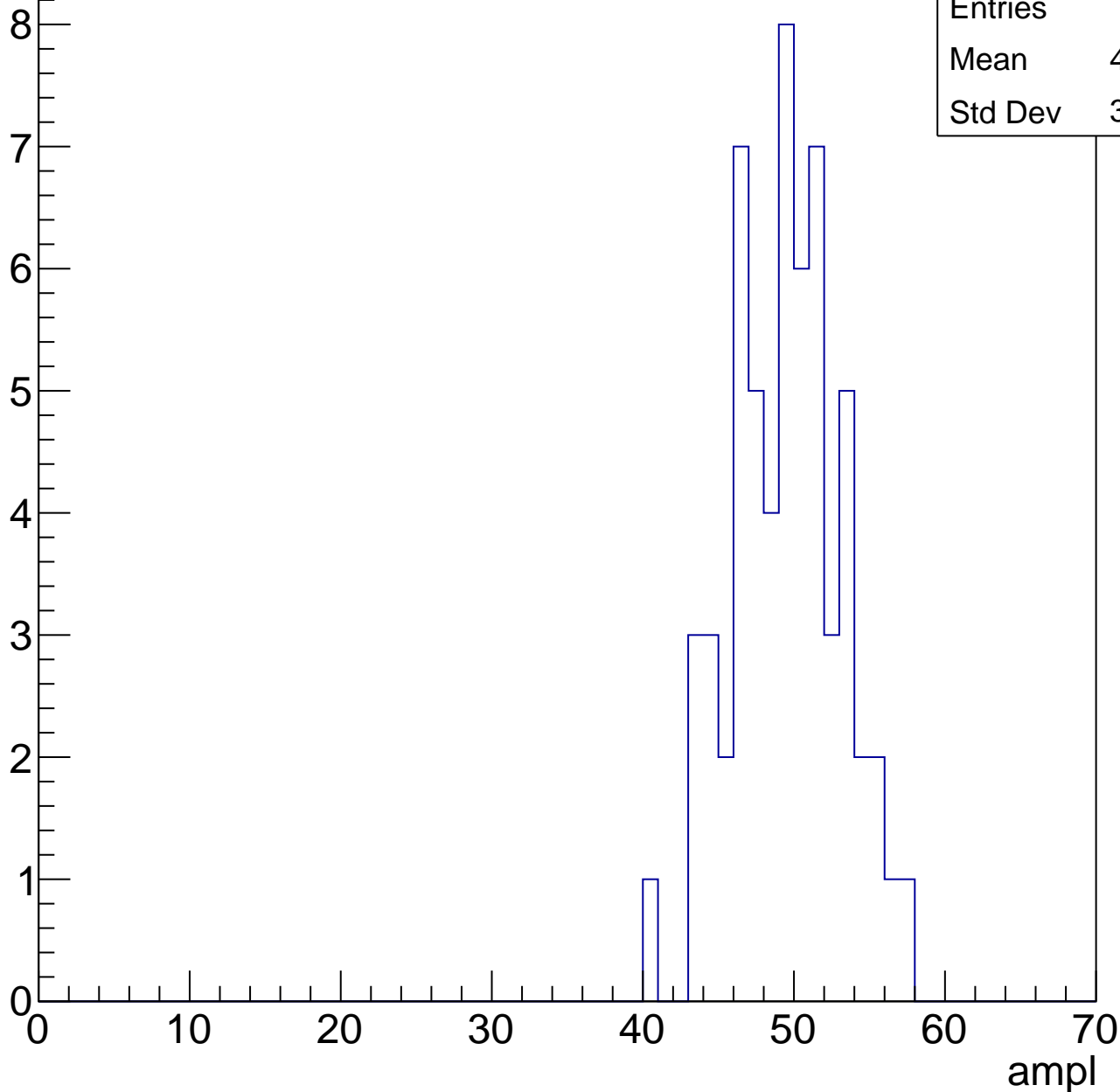


B0L102S, U20-ch99, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.02 |
| Std Dev | 3.557 |

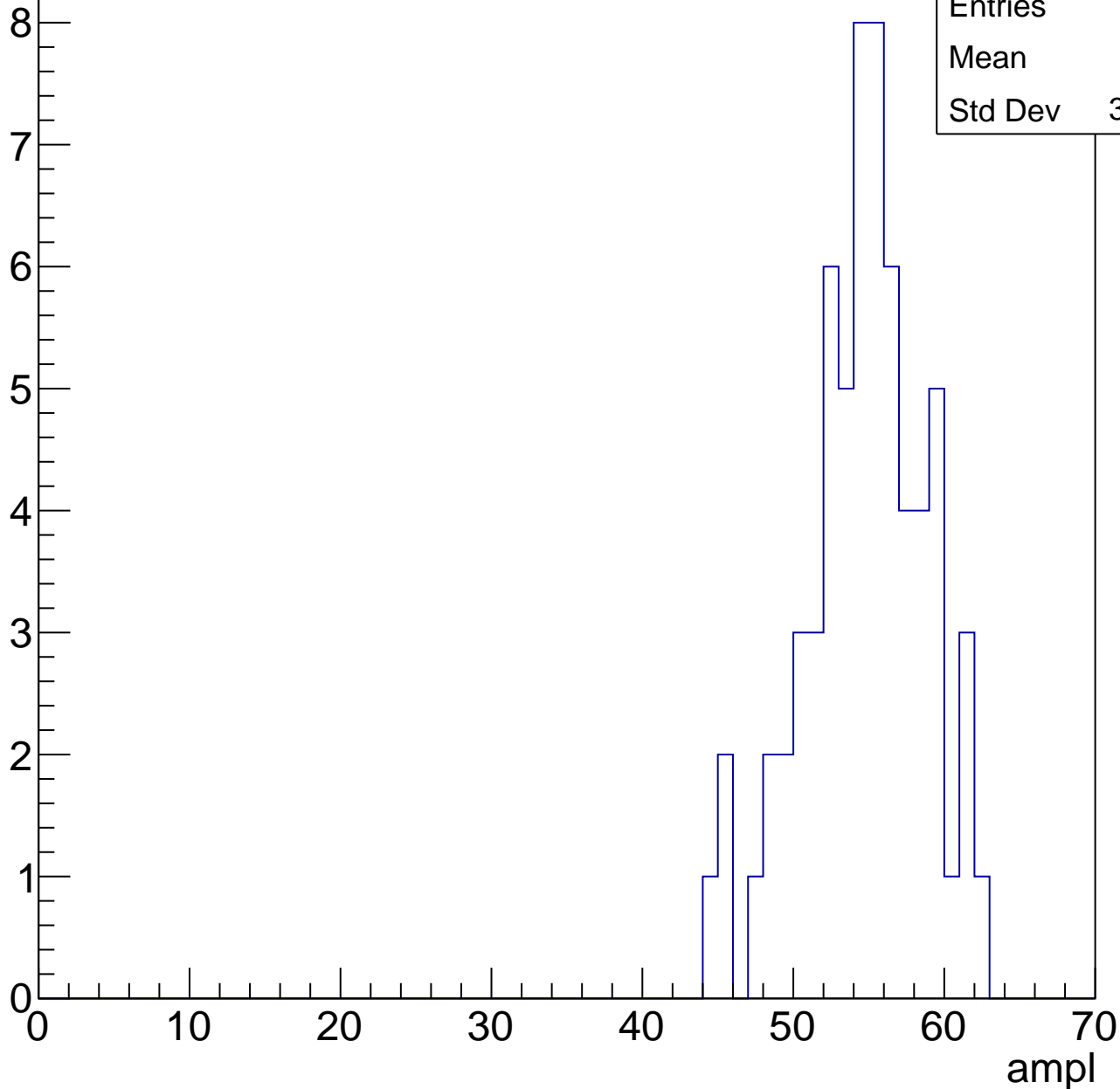


B0L102S, U20-ch99, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.2 |
| Std Dev | 3.997 |

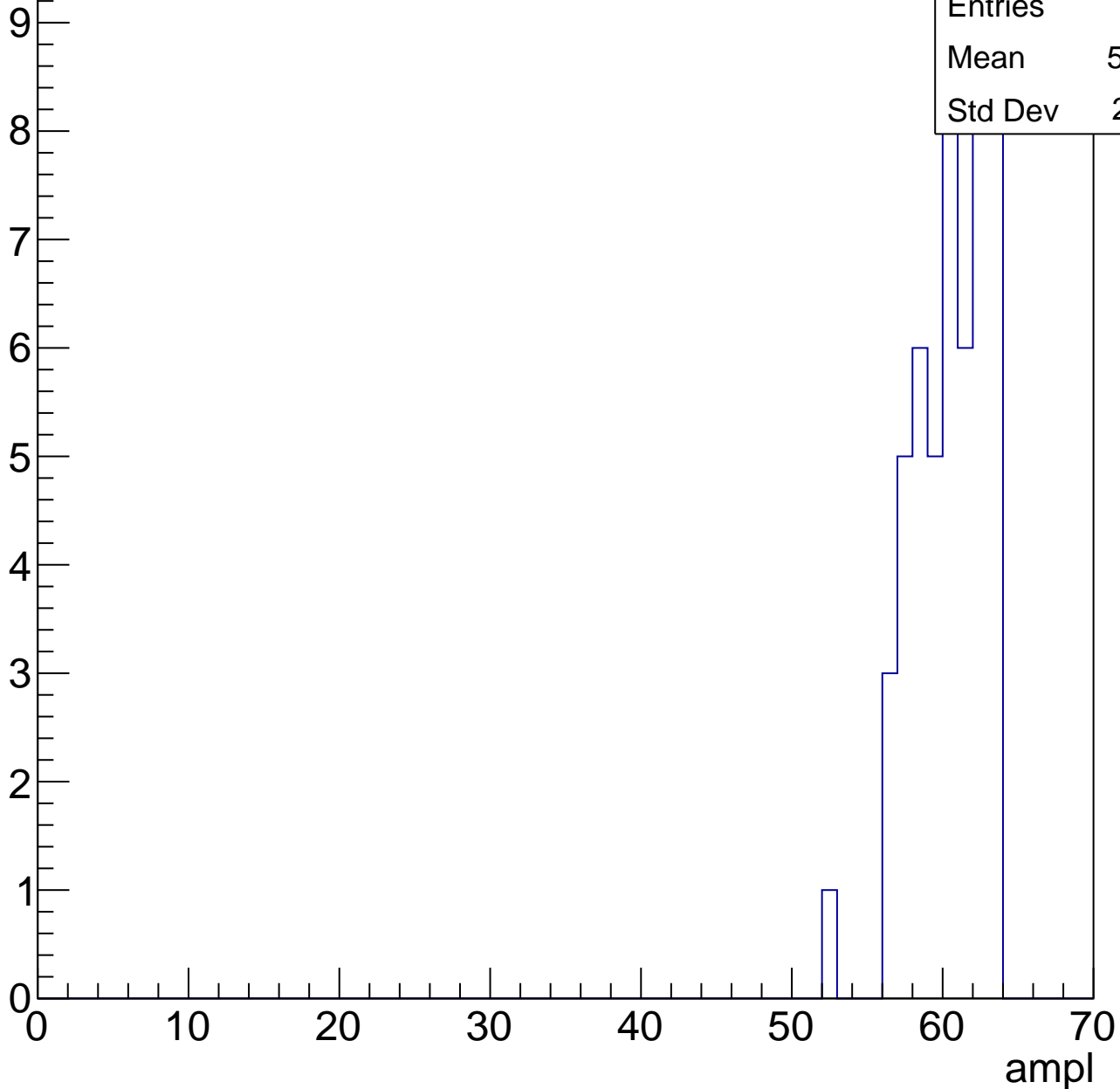


B0L102S, U20-ch99, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

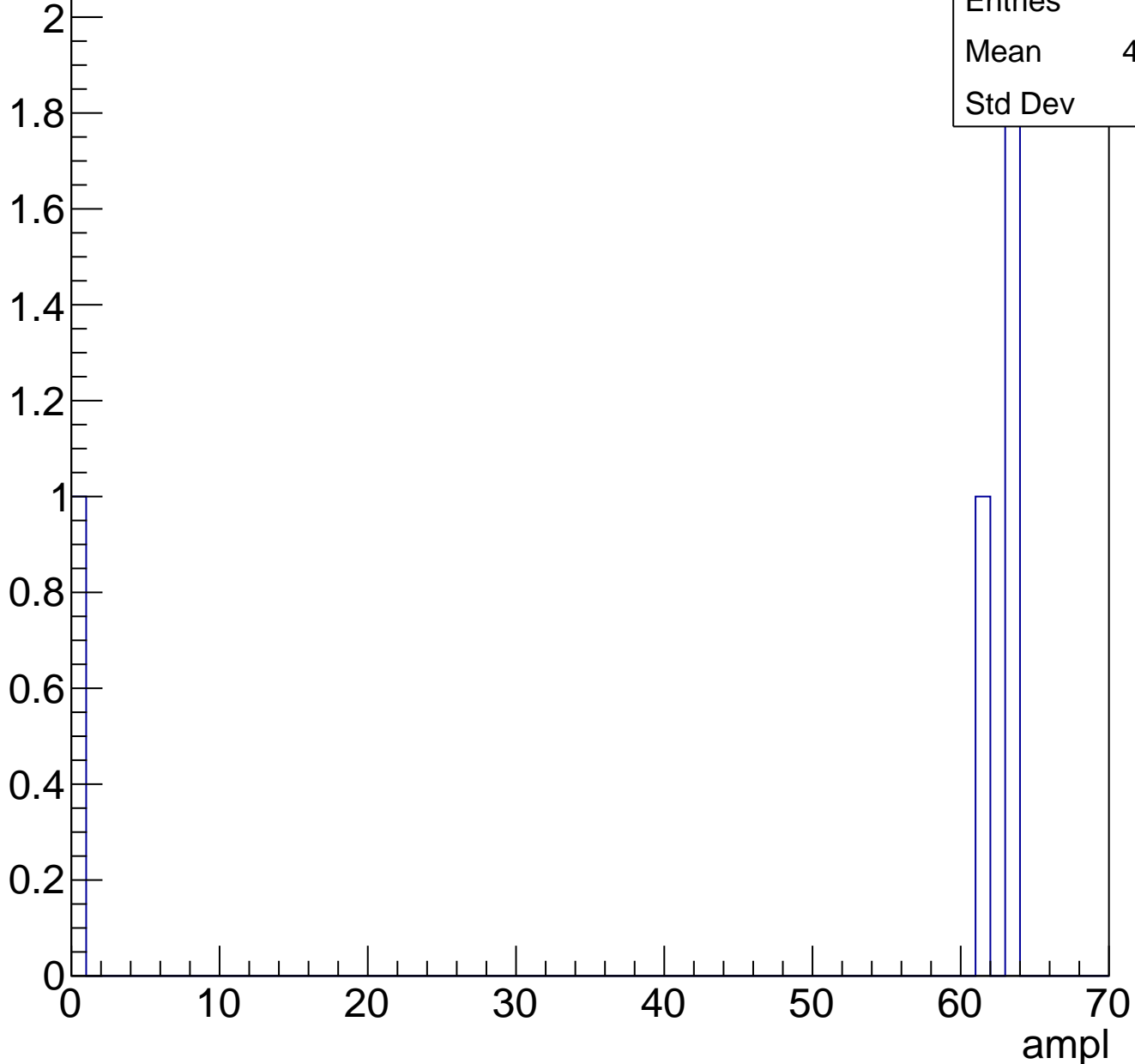
| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.92 |
| Std Dev | 2.401 |



B0L102S, U20-ch99, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 46.75 |
| Std Dev | 27 |

B0L102S, U20-ch99, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch100, adc0

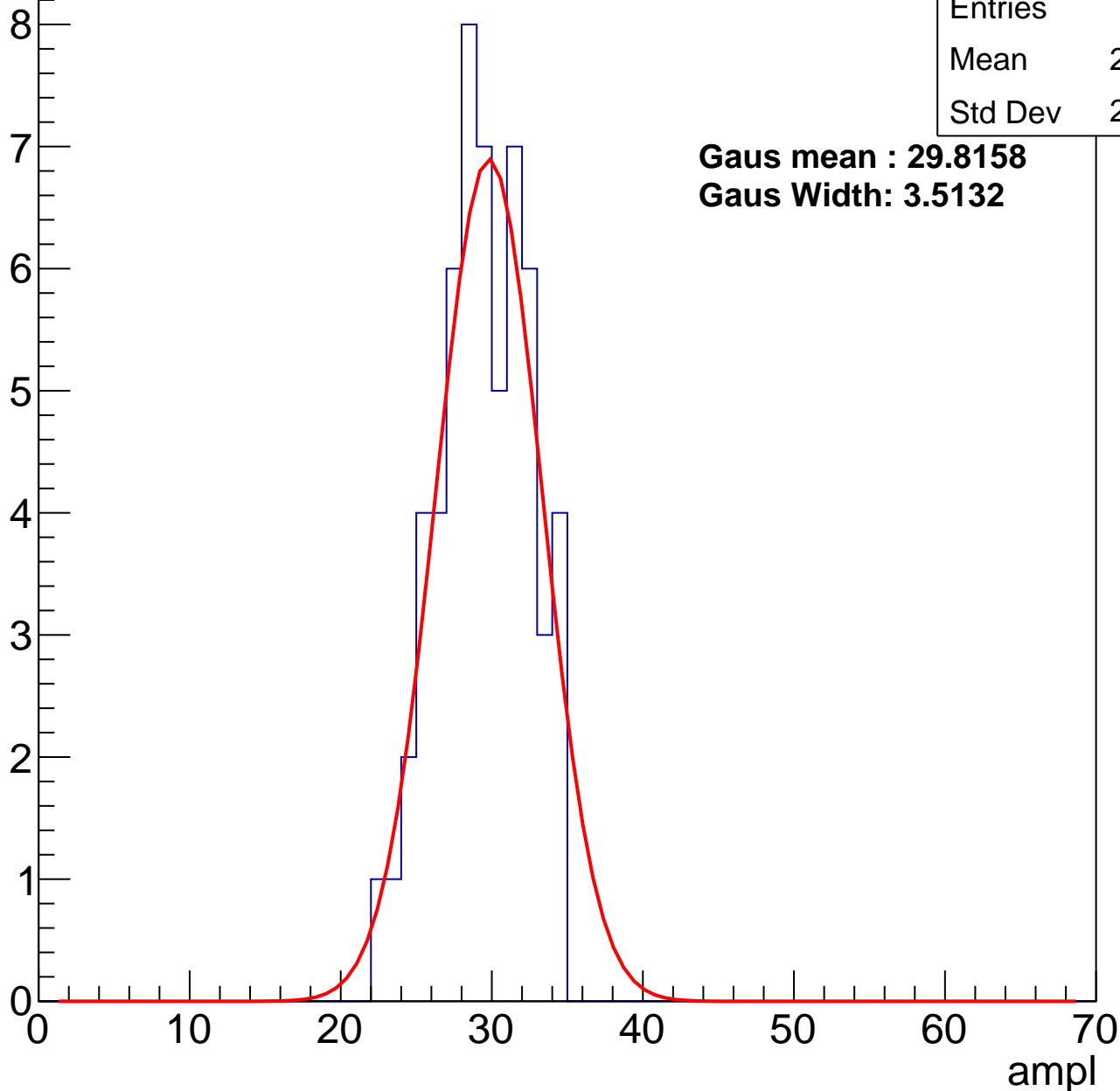
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 28.97 |
| Std Dev | 2.942 |

Gaus mean : 29.8158

Gaus Width: 3.5132



B0L102S, U20-ch100, adc1

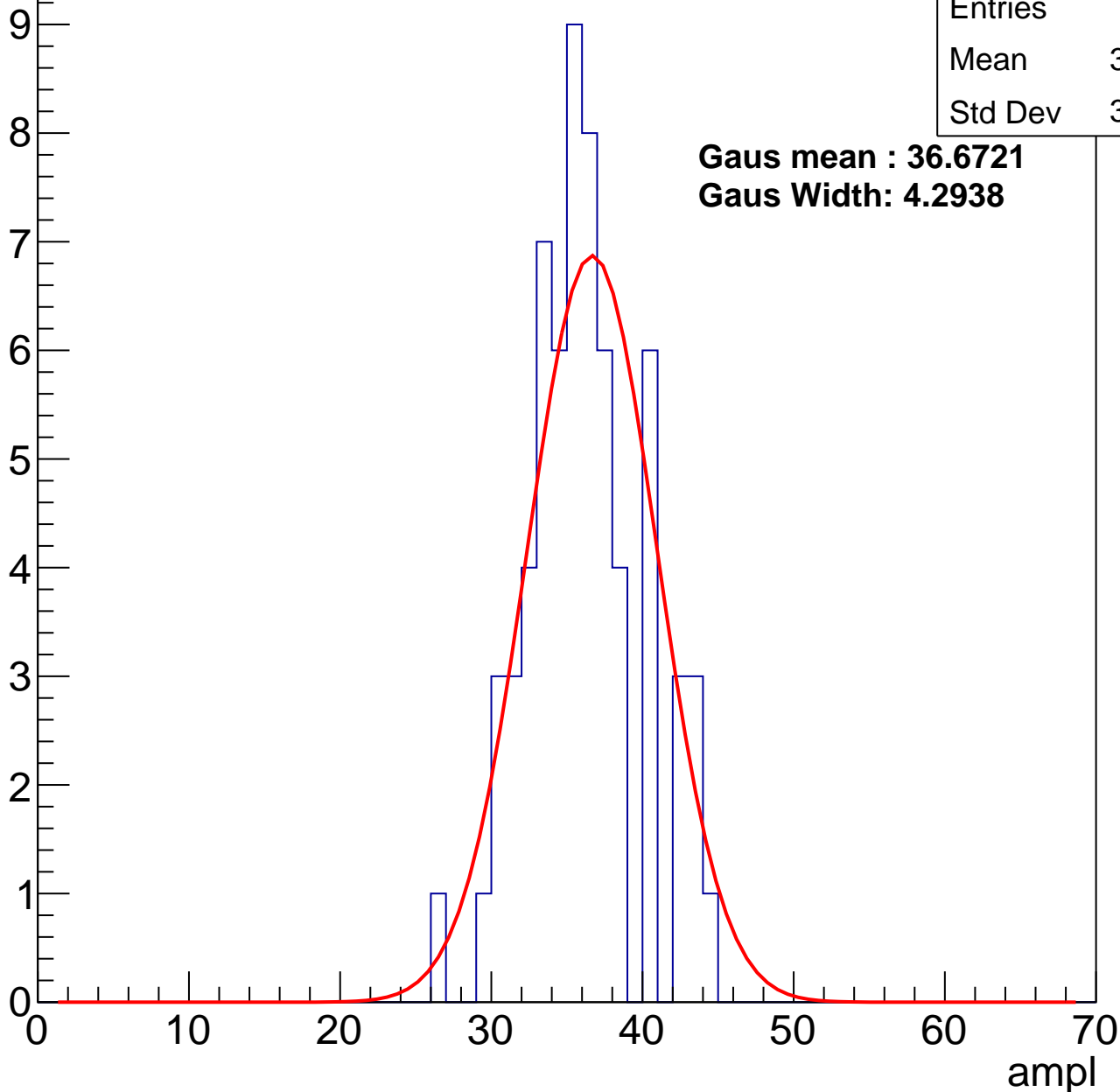
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.65 |
| Std Dev | 3.764 |

Gaus mean : 36.6721

Gaus Width: 4.2938



B0L102S, U20-ch100, adc2

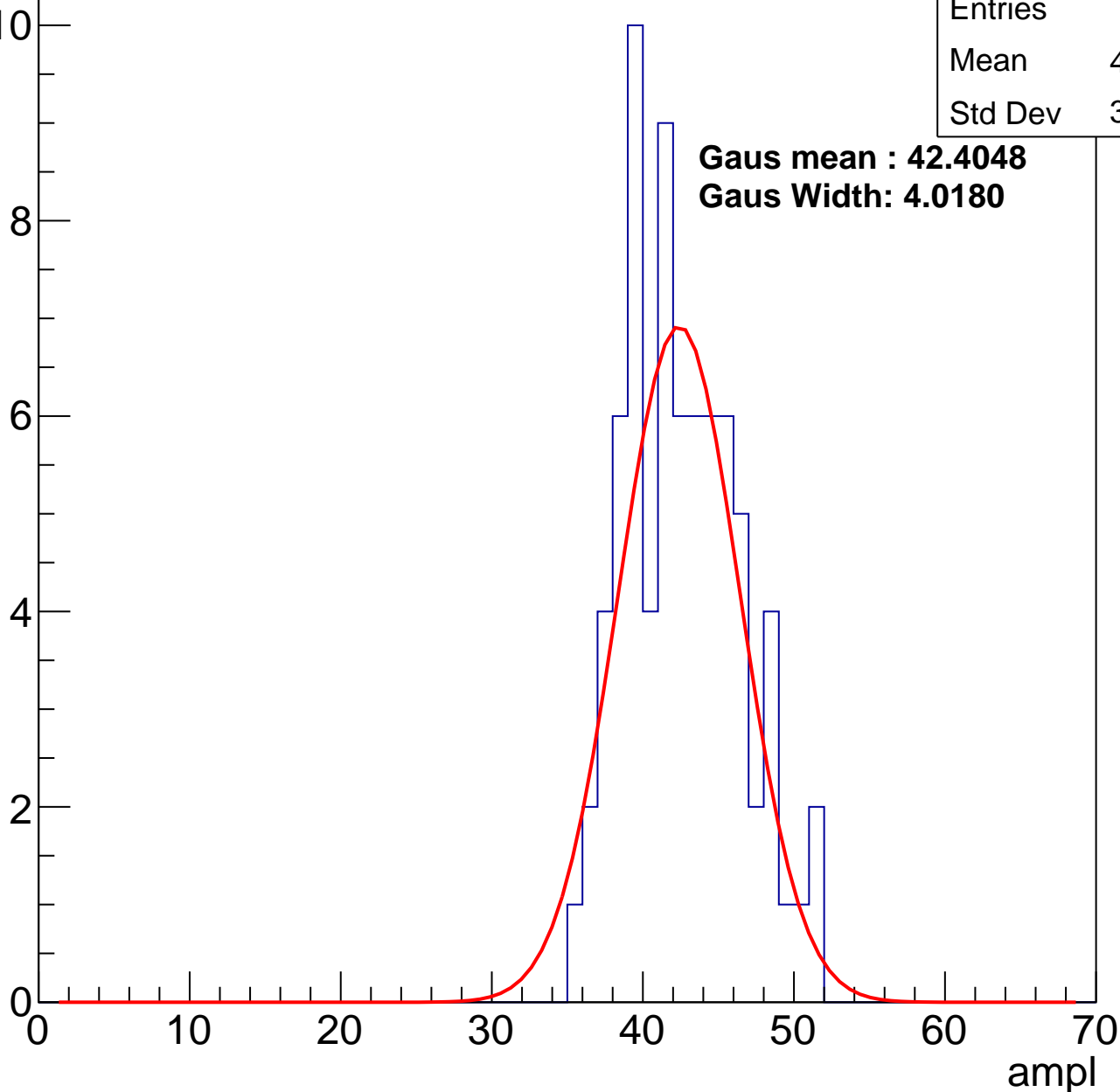
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 42.17 |
| Std Dev | 3.764 |

Gaus mean : 42.4048

Gaus Width: 4.0180

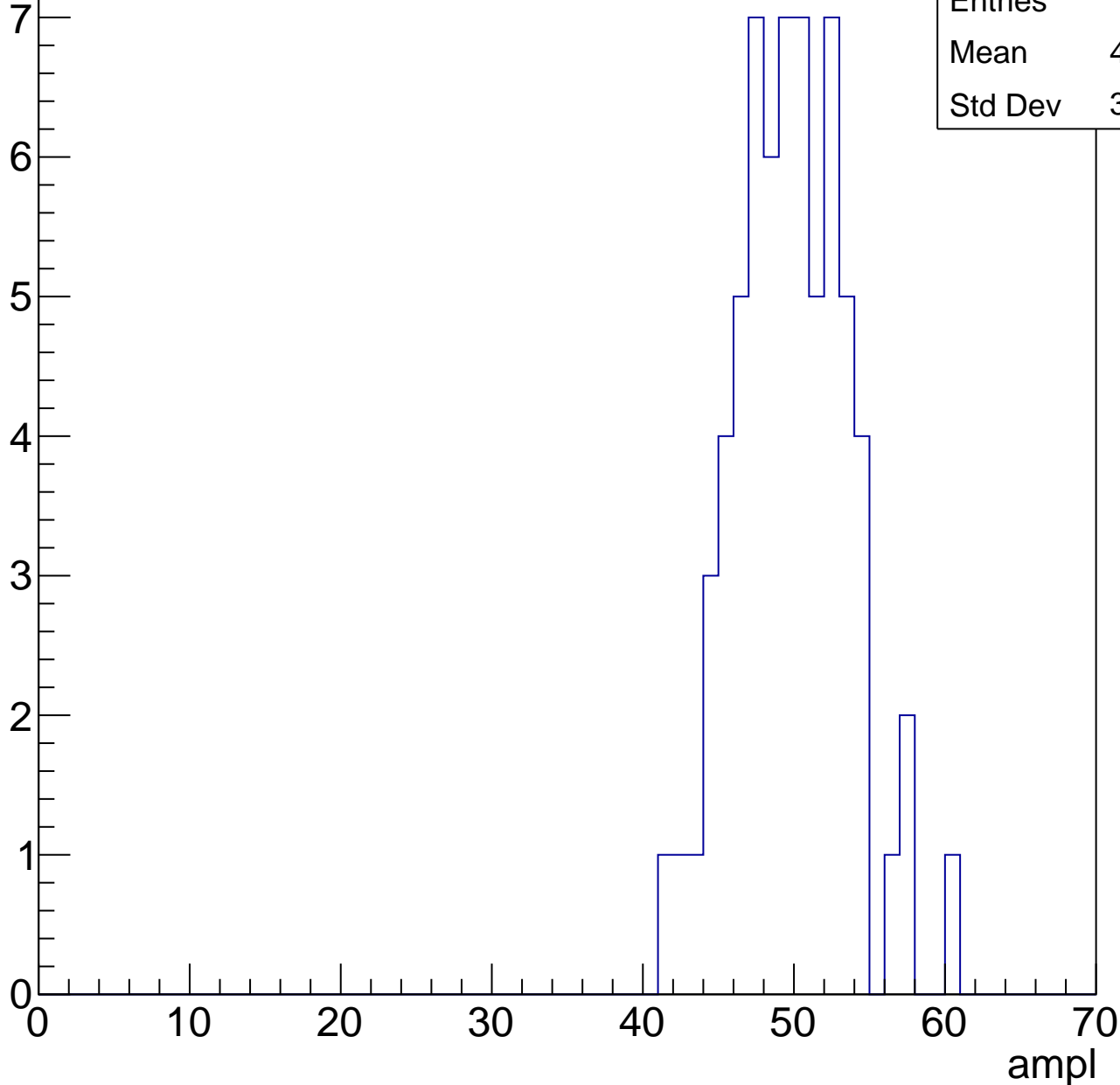


B0L102S, U20-ch100, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 49.37 |
| Std Dev | 3.717 |

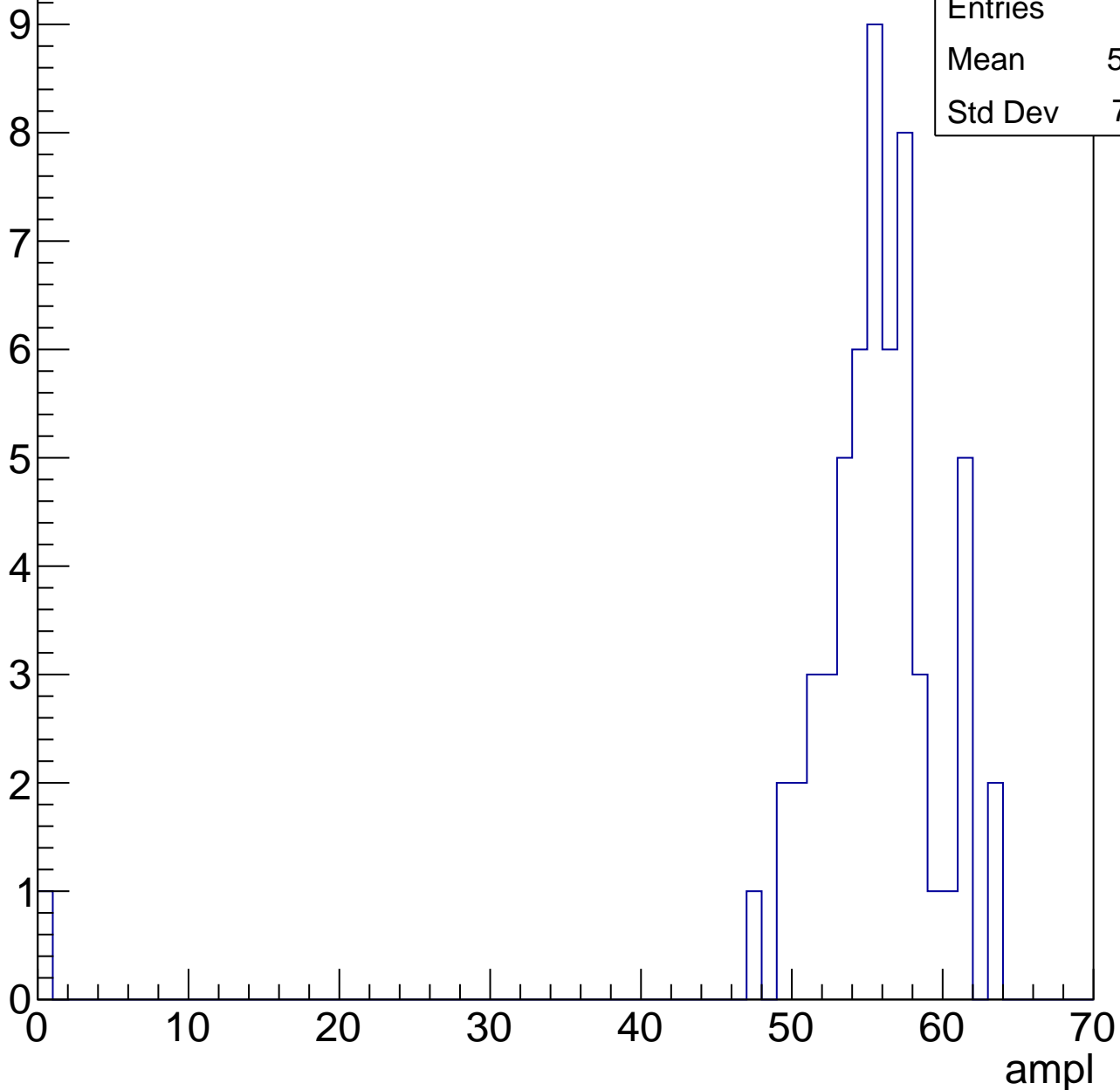


B0L102S, U20-ch100, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 54.38 |
| Std Dev | 7.991 |

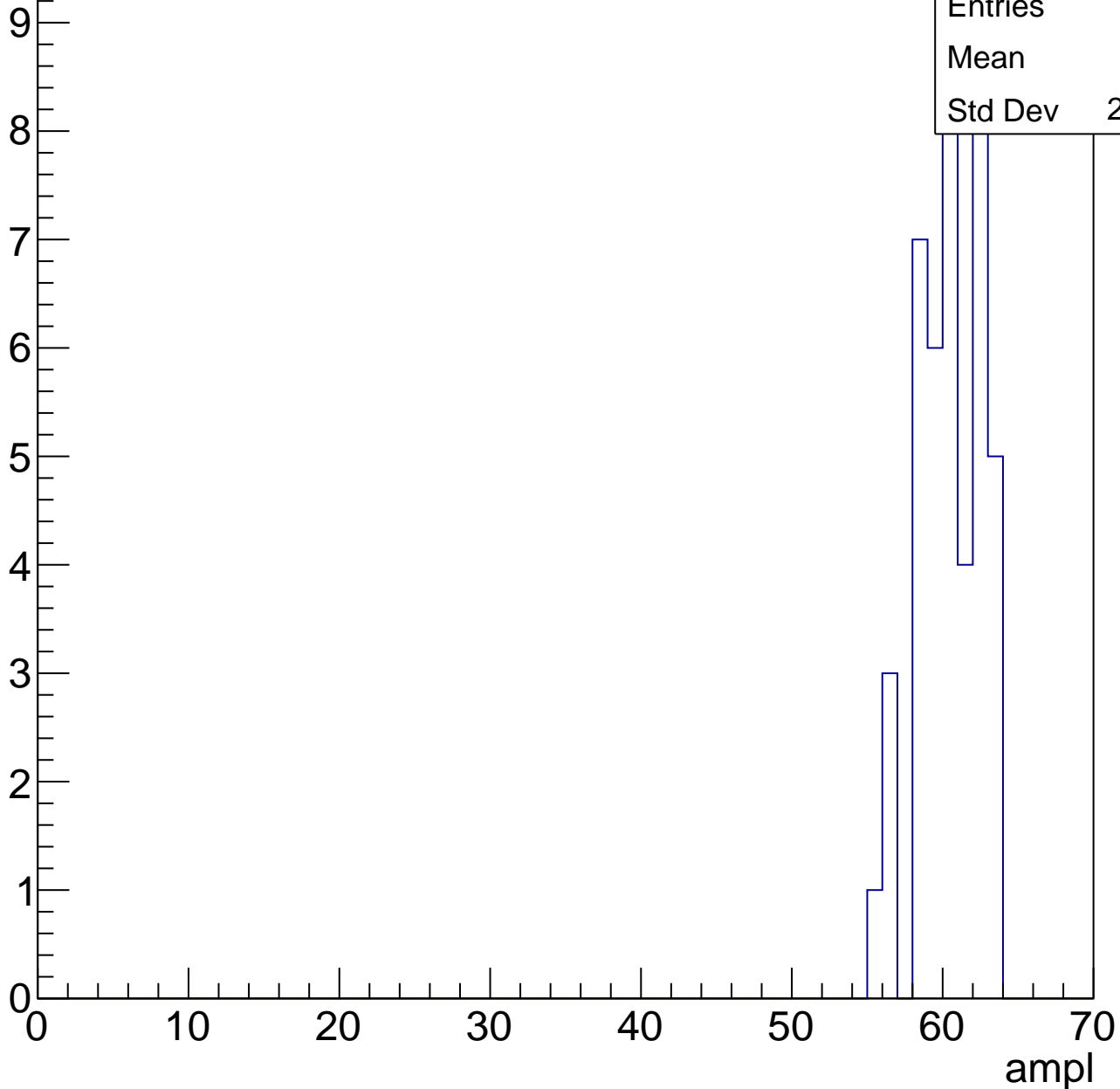


B0L102S, U20-ch100, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

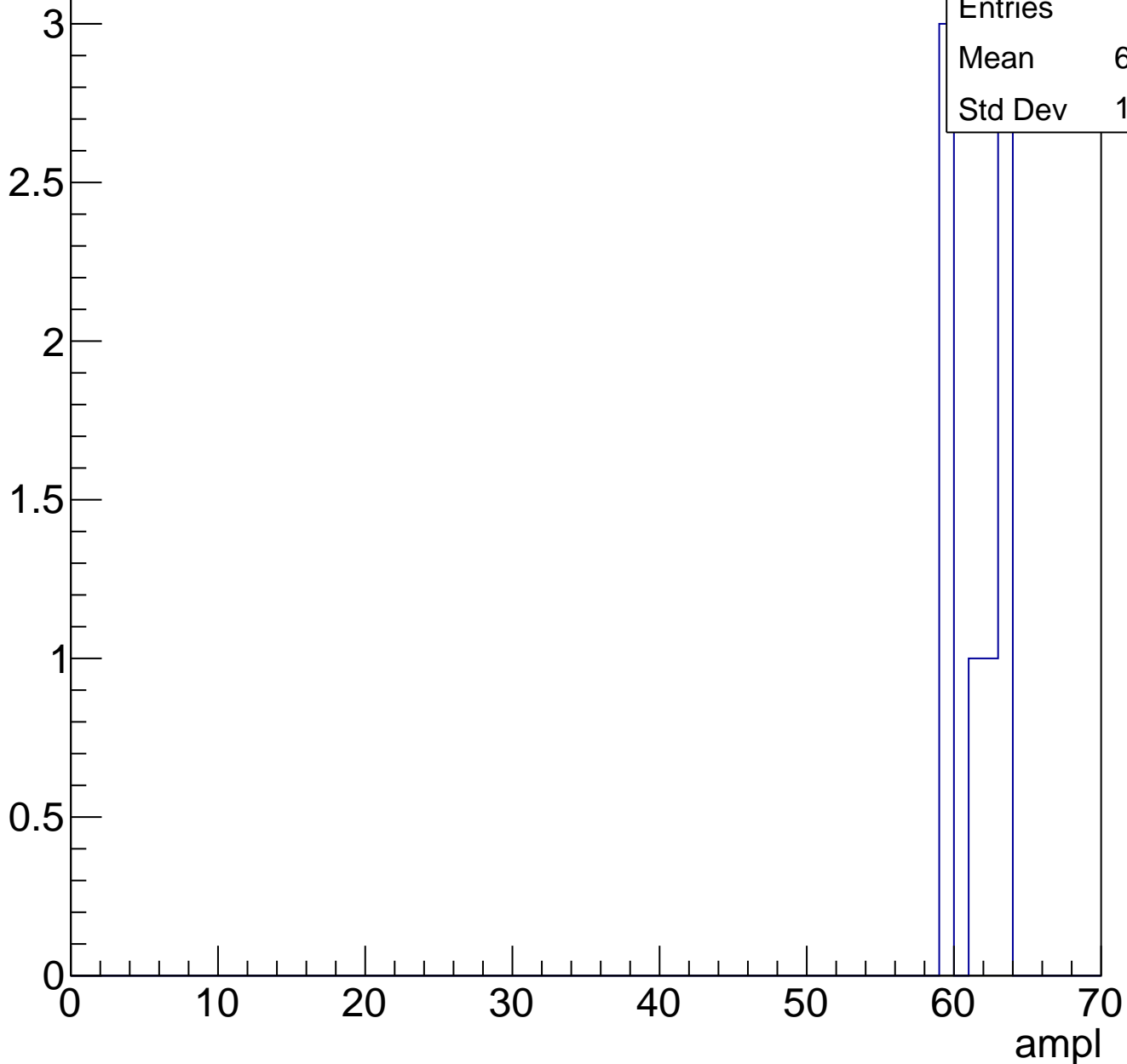
| | |
|---------|-------|
| Entries | 43 |
| Mean | 60 |
| Std Dev | 2.113 |



B0L102S, U20-ch100, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch100, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch101, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 27.99 |
| Std Dev | 4.659 |

Gaus mean : 28.5380

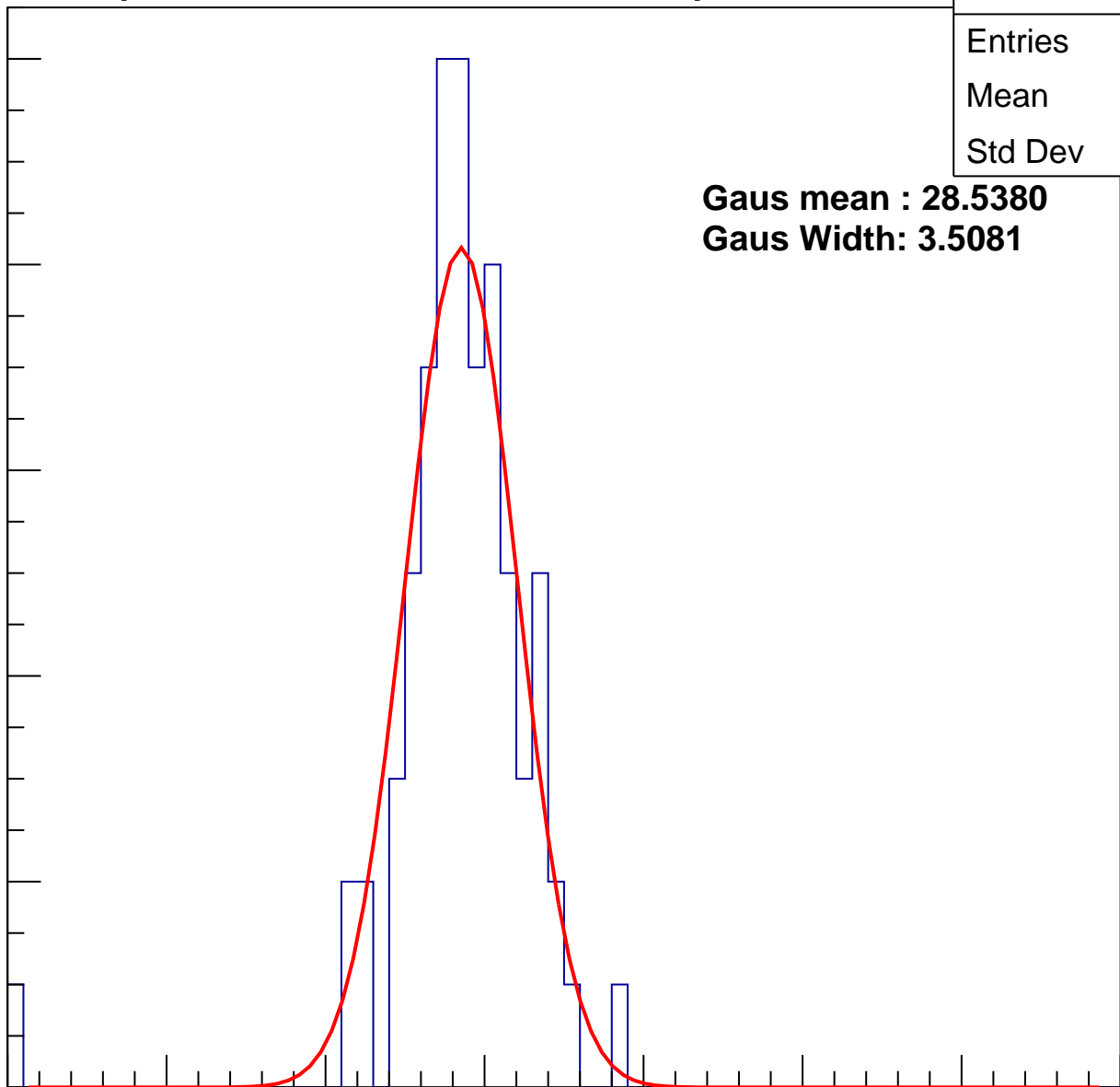
Gaus Width: 3.5081

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch101, adc1

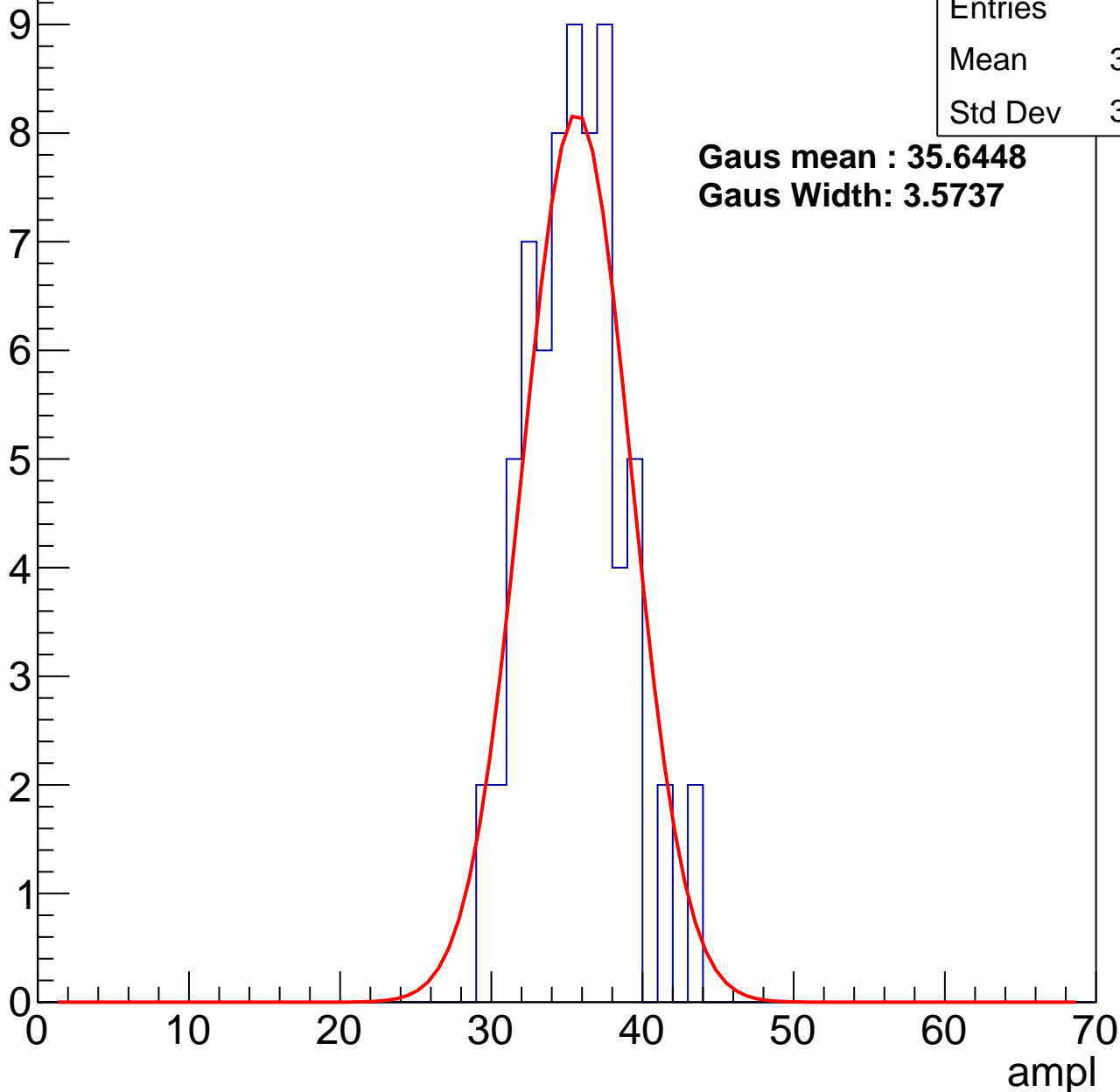
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.04 |
| Std Dev | 3.085 |

Gaus mean : 35.6448

Gaus Width: 3.5737



B0L102S, U20-ch101, adc2

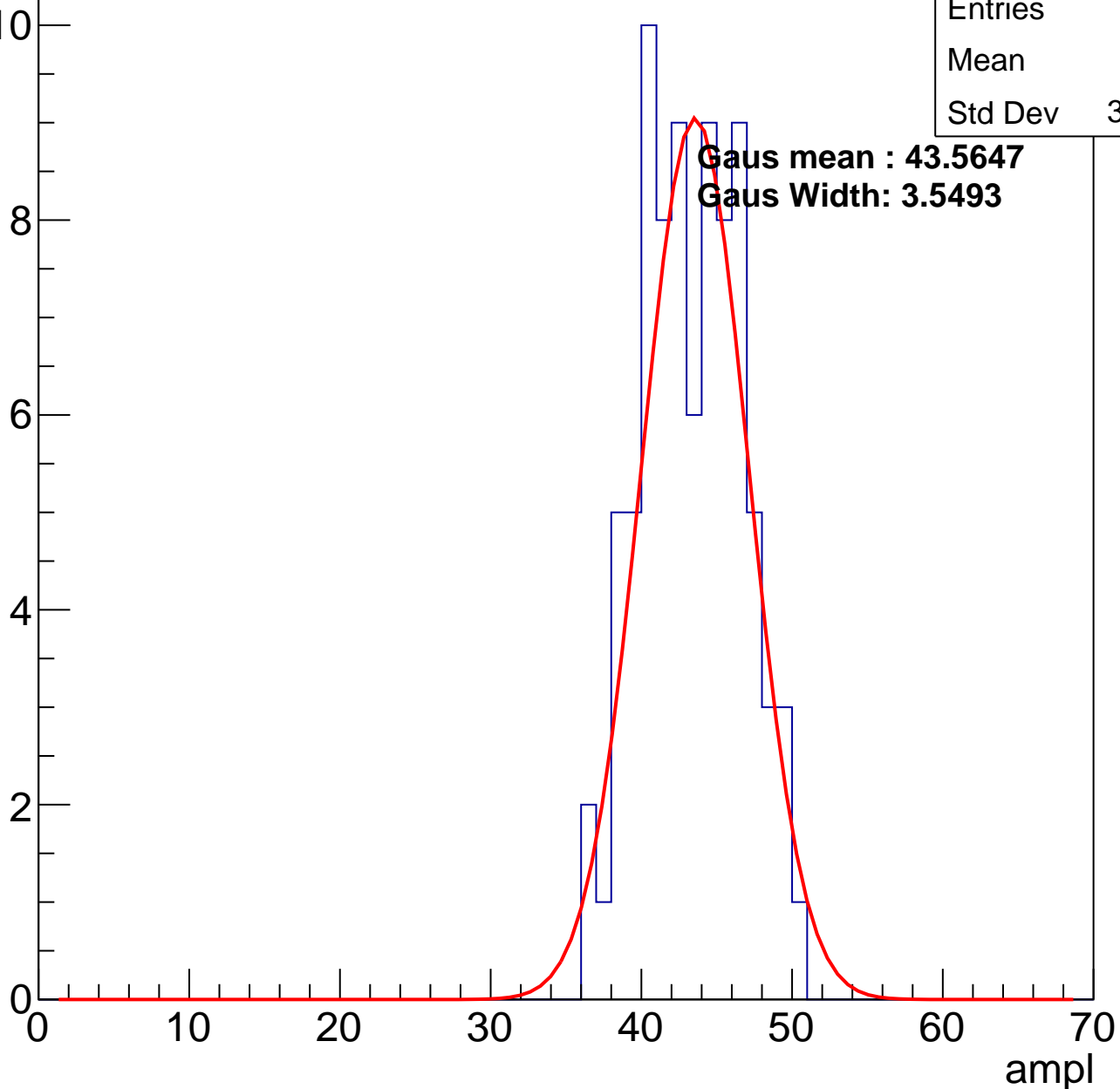
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 42.9 |
| Std Dev | 3.279 |

Gaus mean : 43.5647

Gaus Width: 3.5493

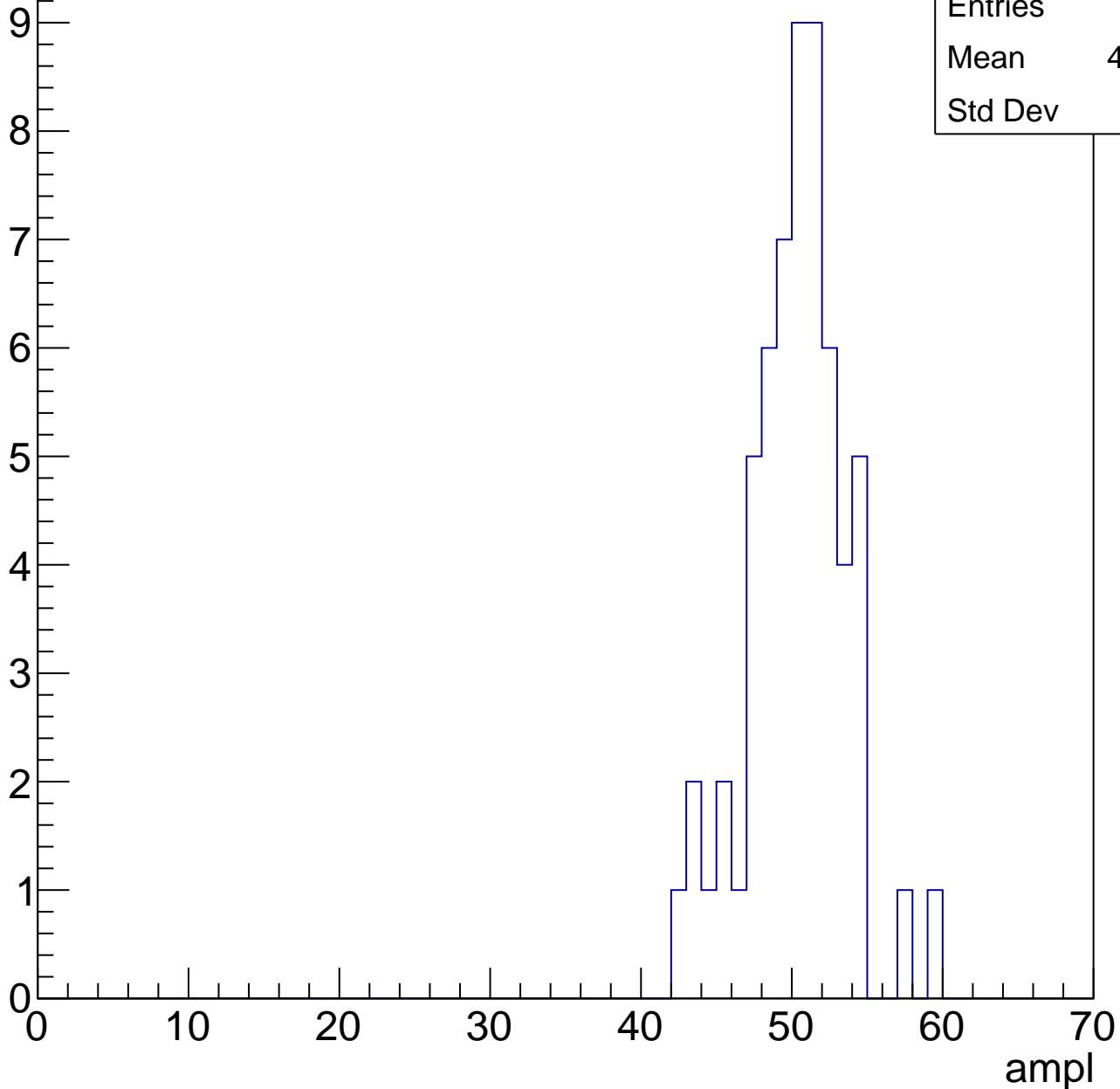


B0L102S, U20-ch101, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.88 |
| Std Dev | 3.21 |

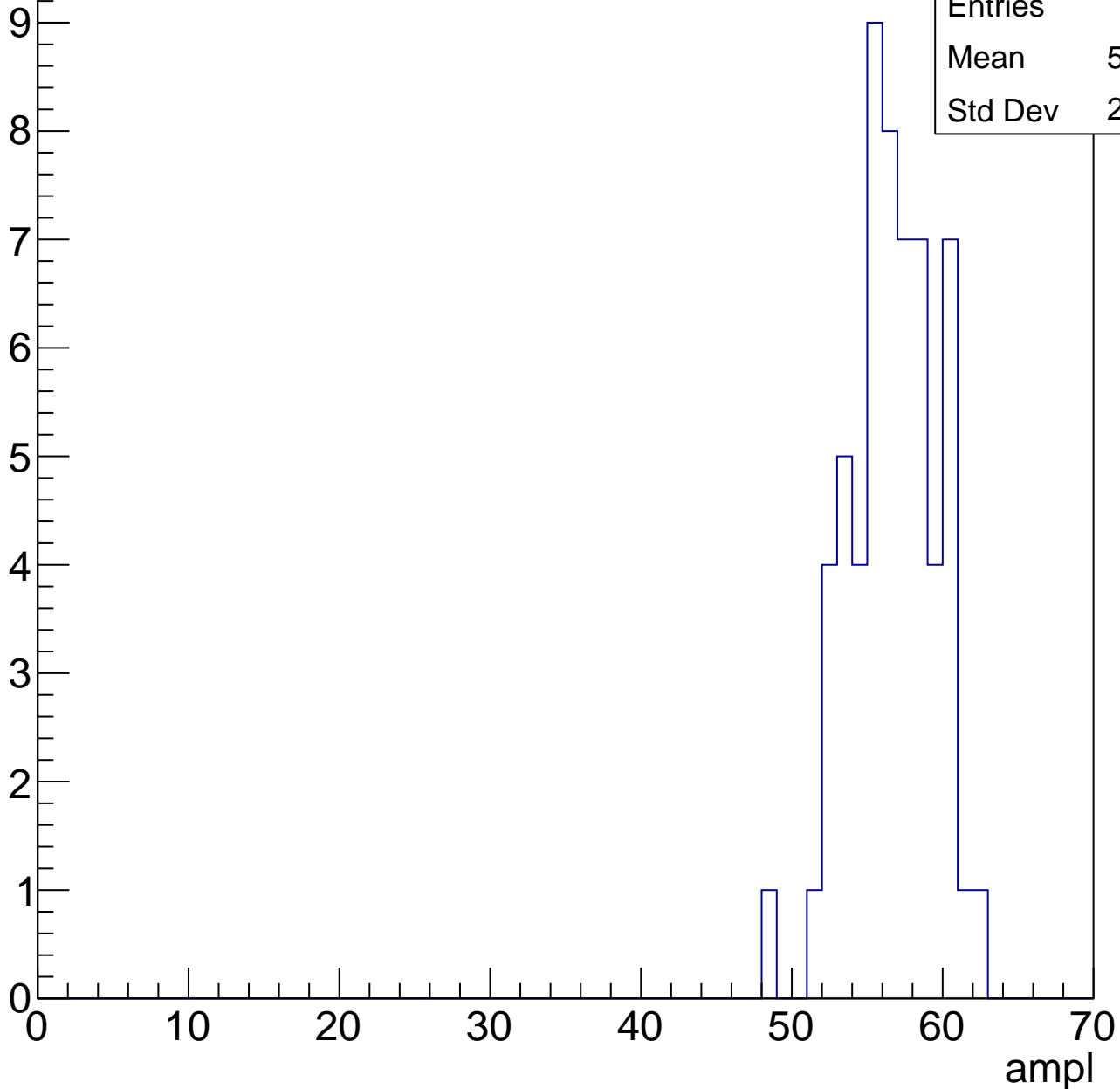


B0L102S, U20-ch101, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.19 |
| Std Dev | 2.807 |

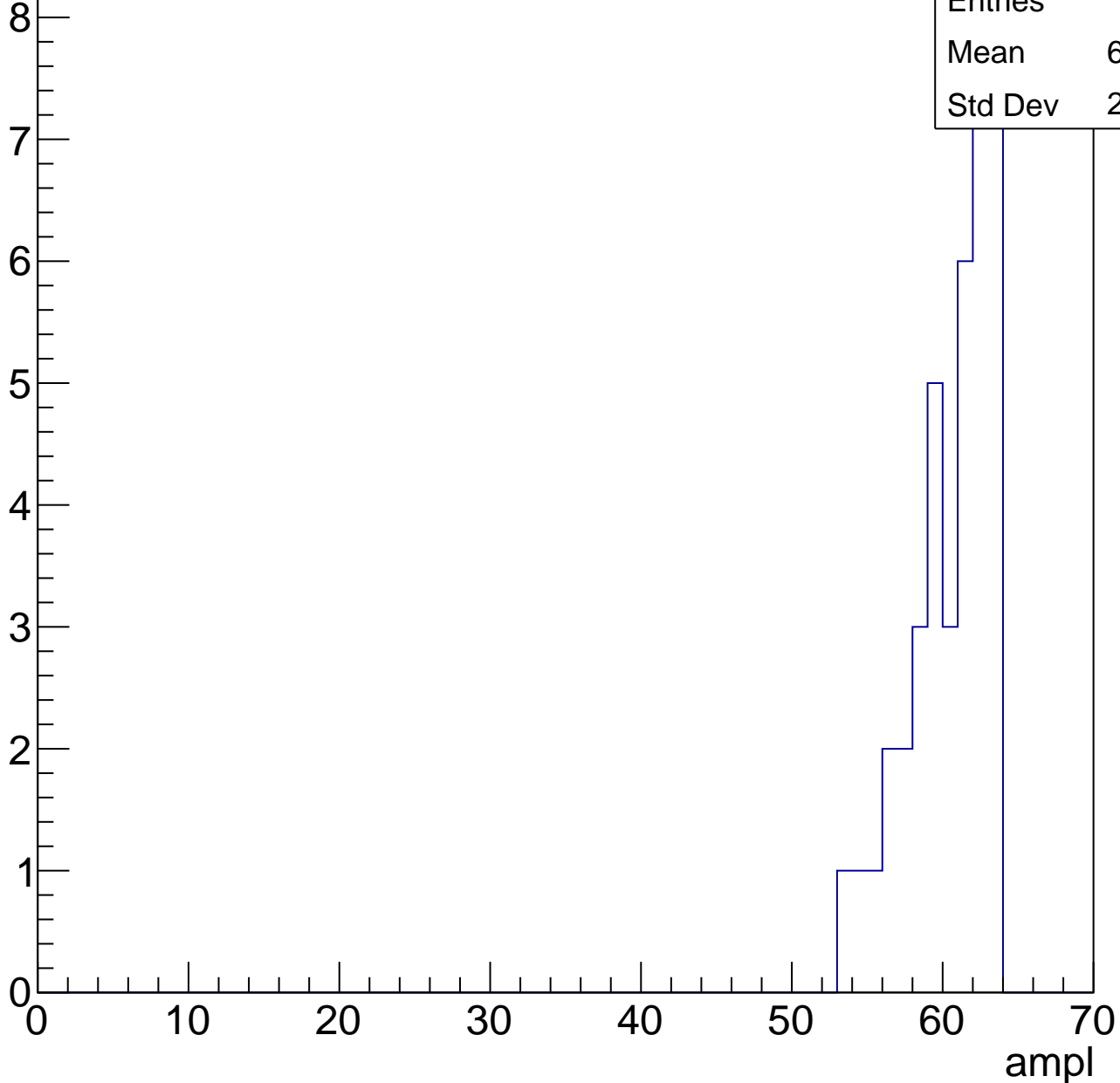


B0L102S, U20-ch101, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

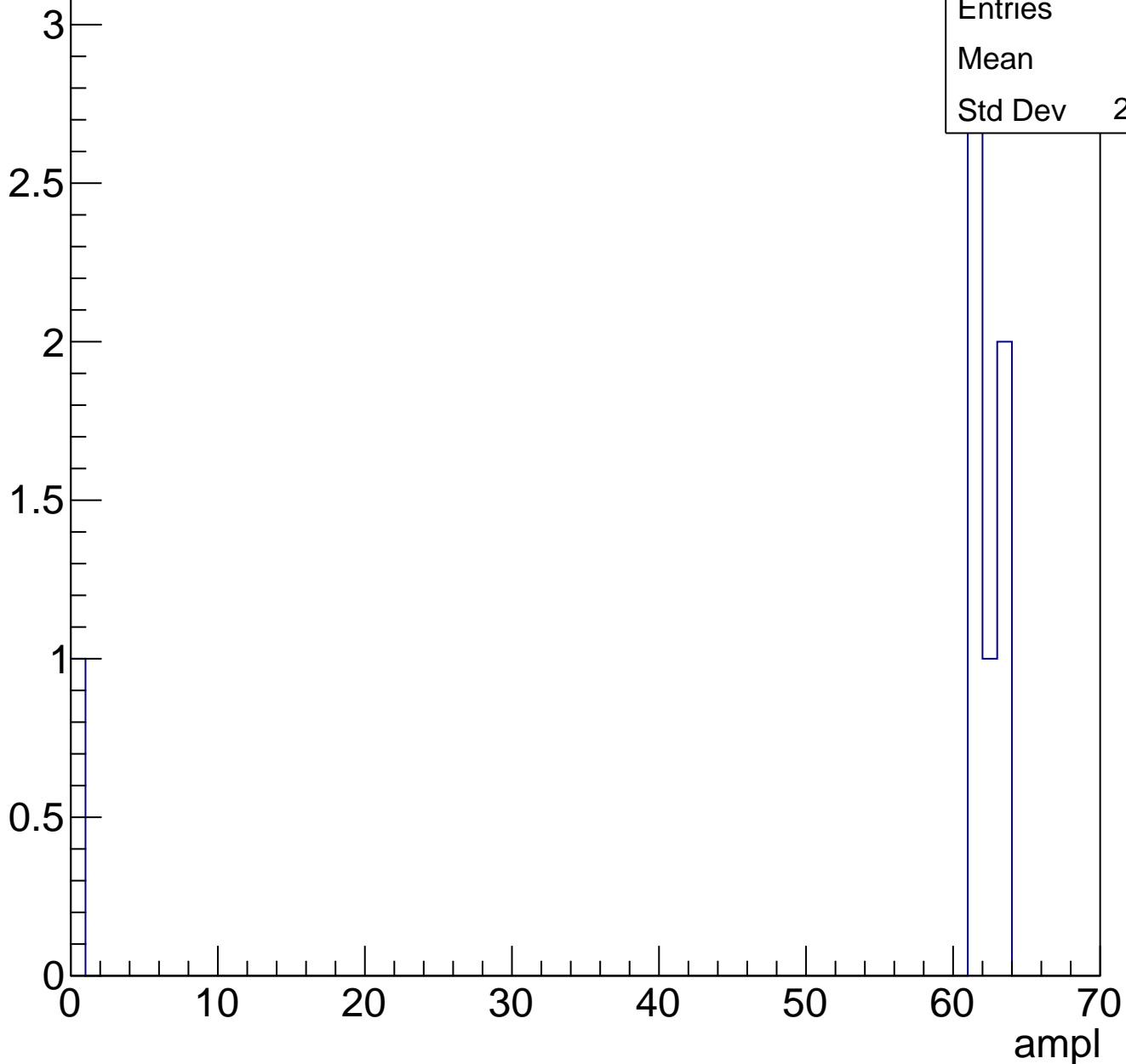
| | |
|---------|-------|
| Entries | 40 |
| Mean | 60.08 |
| Std Dev | 2.678 |



B0L102S, U20-ch101, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

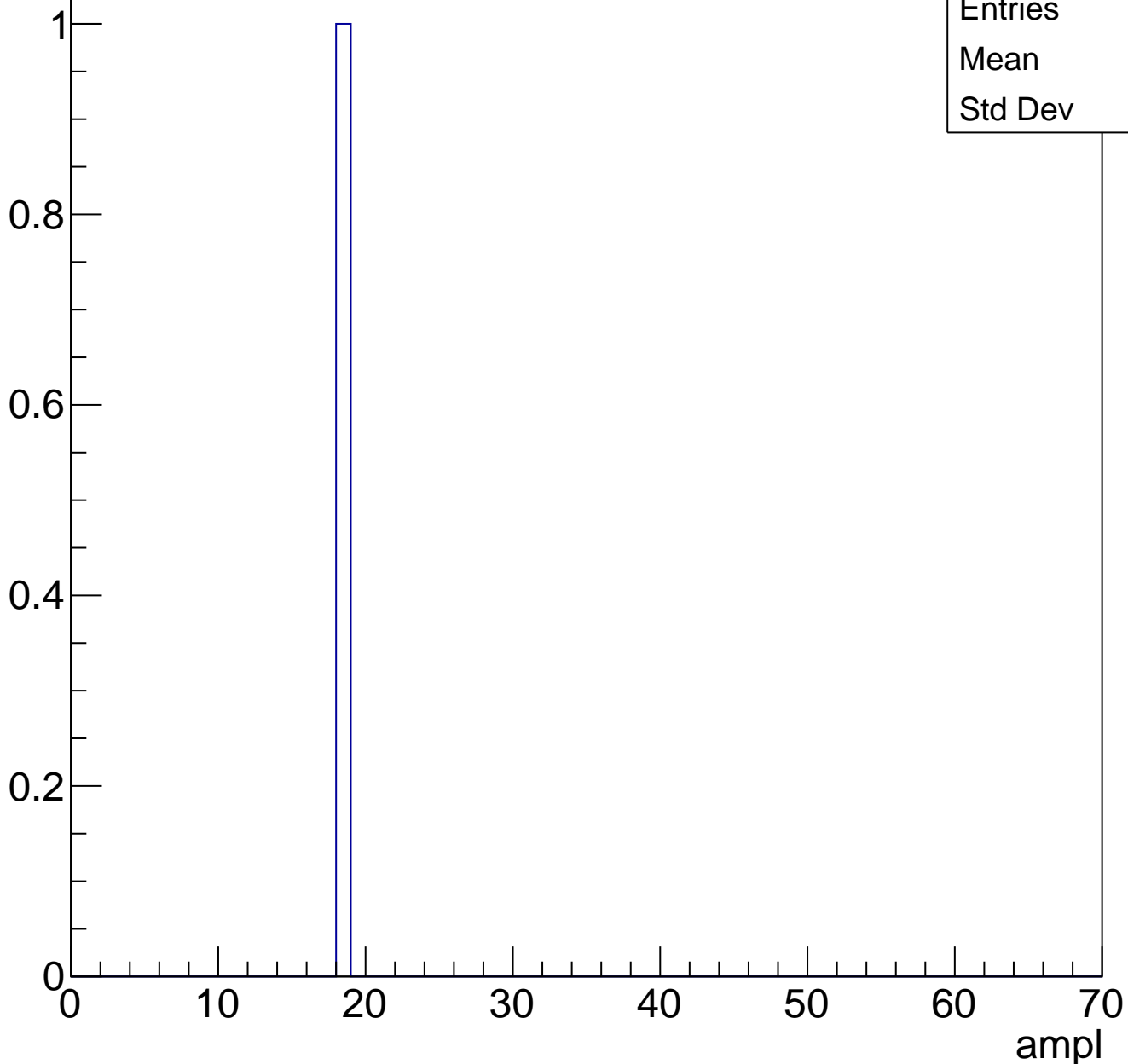


| | |
|---------|-------|
| Entries | 7 |
| Mean | 53 |
| Std Dev | 21.65 |

B0L102S, U20-ch101, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 18 |
| Std Dev | 0 |

B0L102S, U20-ch102, adc0

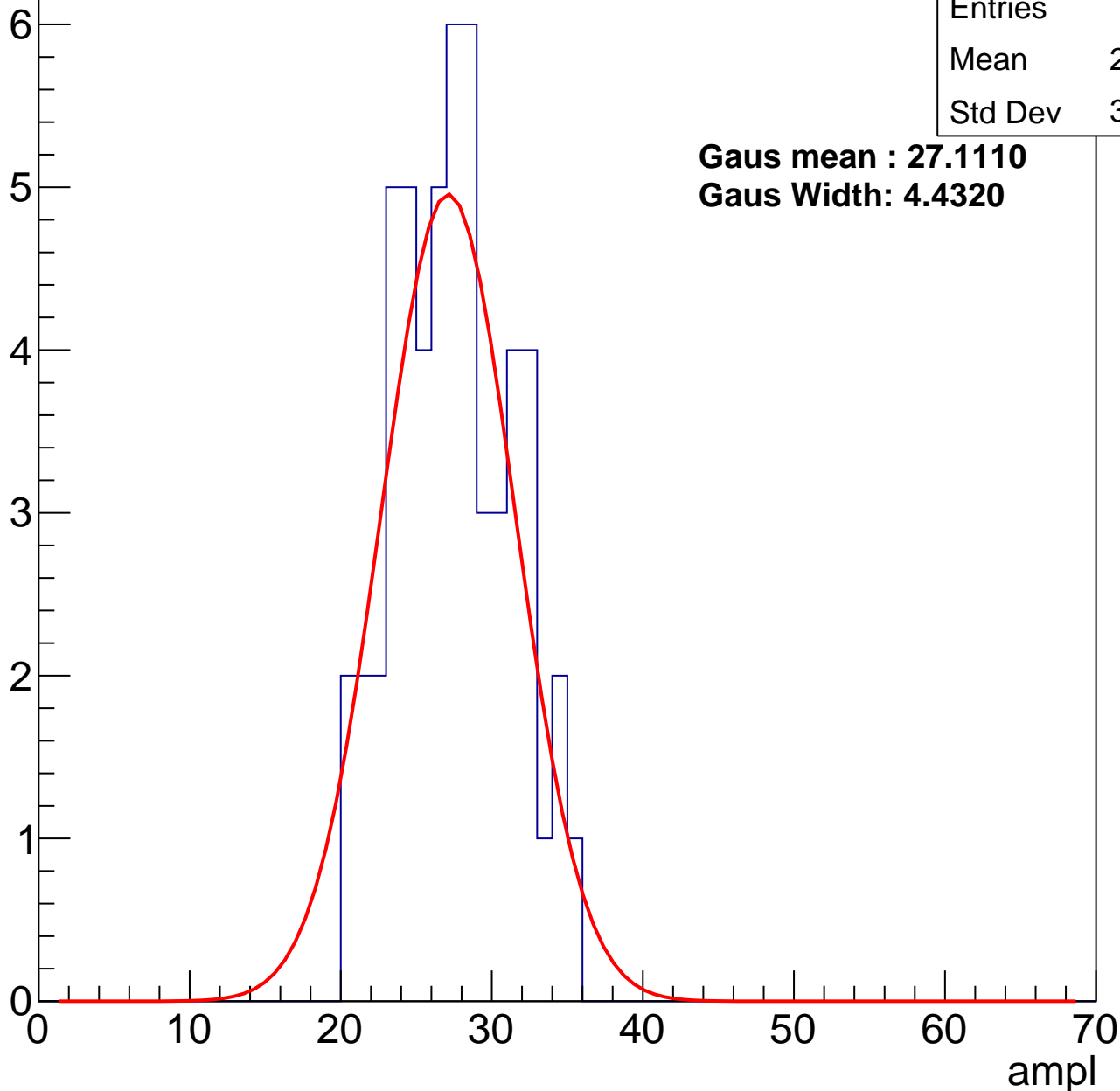
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 27.02 |
| Std Dev | 3.749 |

Gaus mean : 27.1110

Gaus Width: 4.4320



B0L102S, U20-ch102, adc1

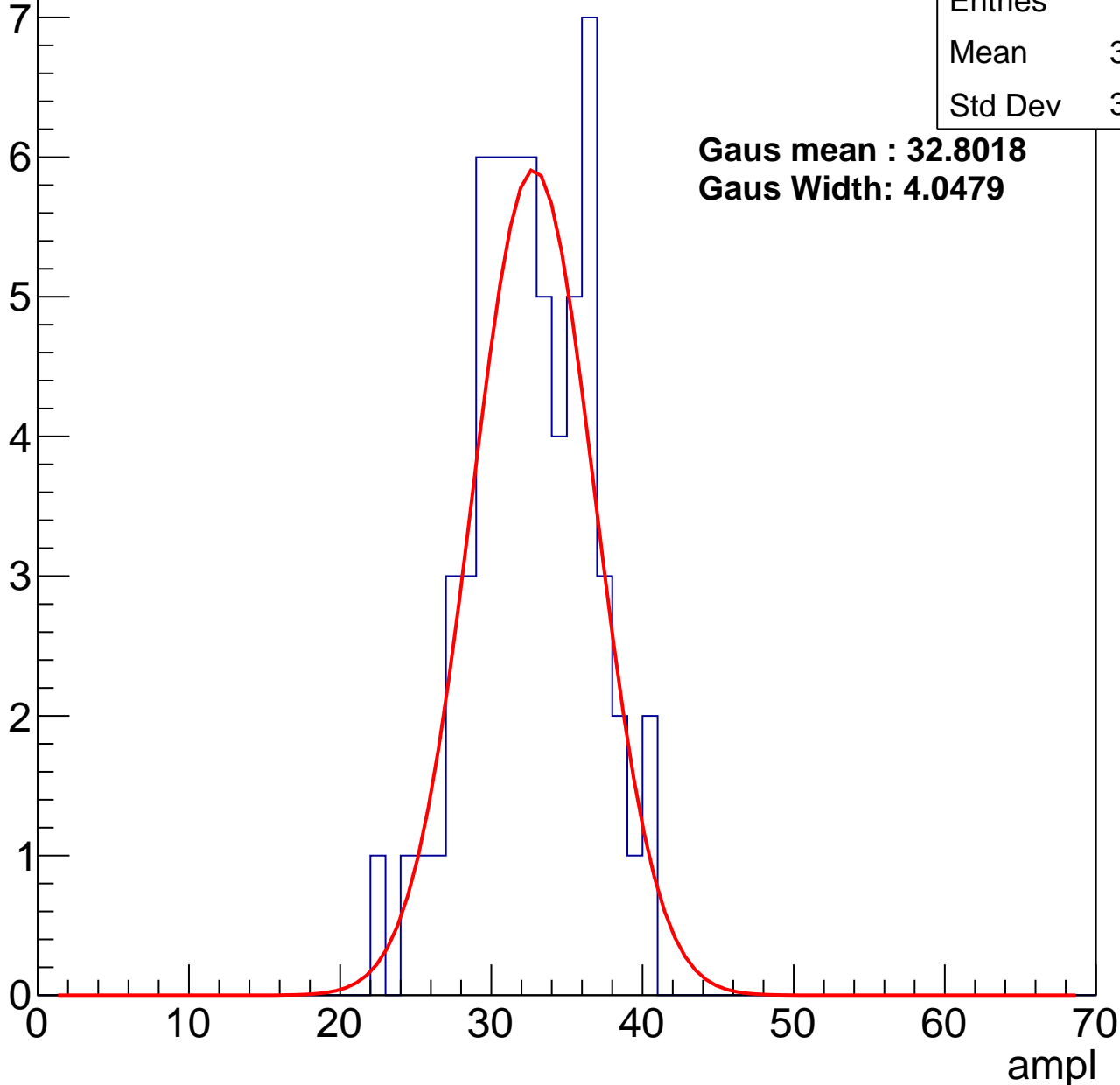
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 32.19 |
| Std Dev | 3.895 |

Gaus mean : 32.8018

Gaus Width: 4.0479



B0L102S, U20-ch102, adc2

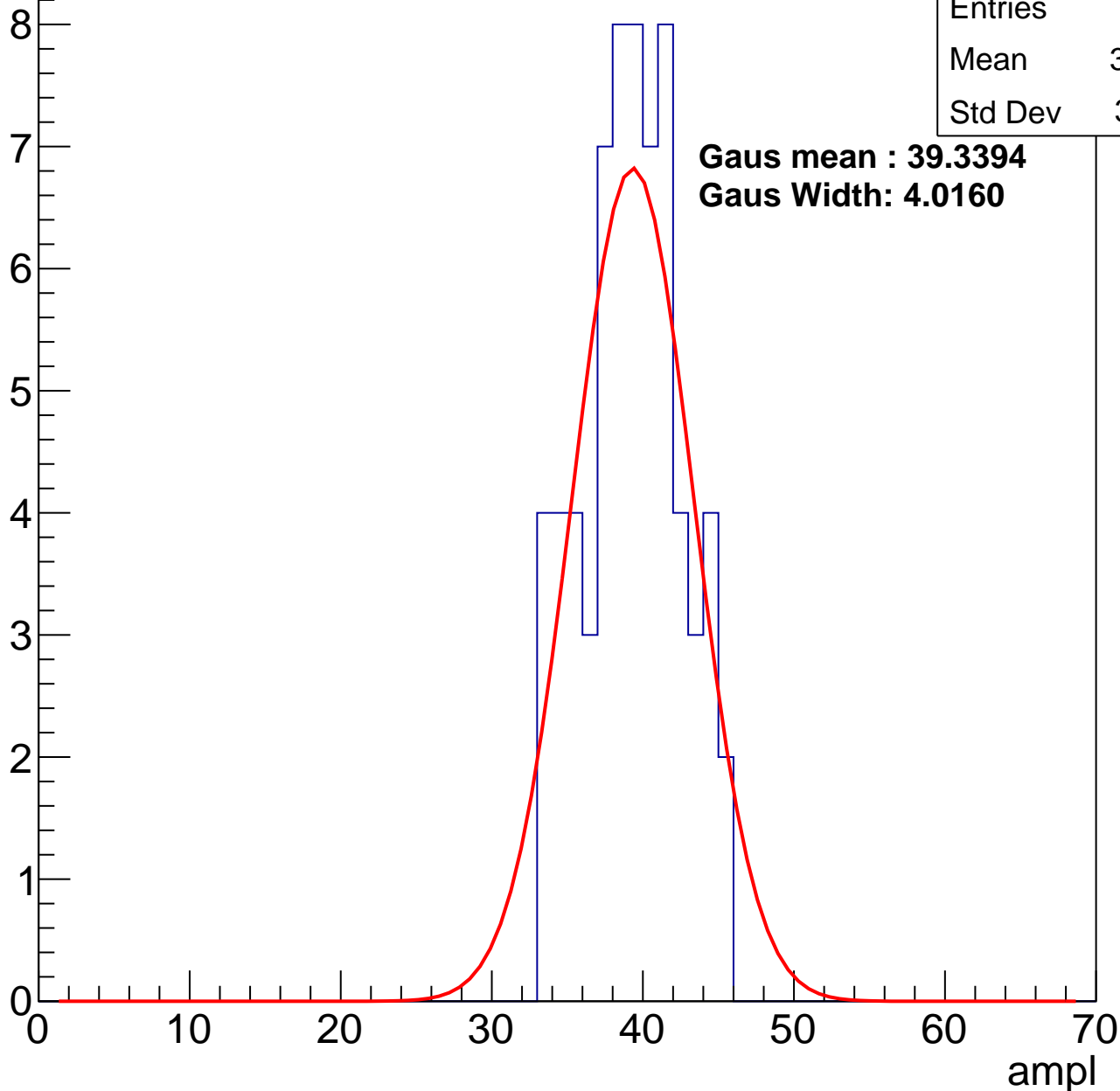
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 38.82 |
| Std Dev | 3.171 |

Gaus mean : 39.3394

Gaus Width: 4.0160



B0L102S, U20-ch102, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 78 |
| Mean | 46.31 |
| Std Dev | 3.155 |

Entry

10

8

6

4

2

0

0

10

20

30

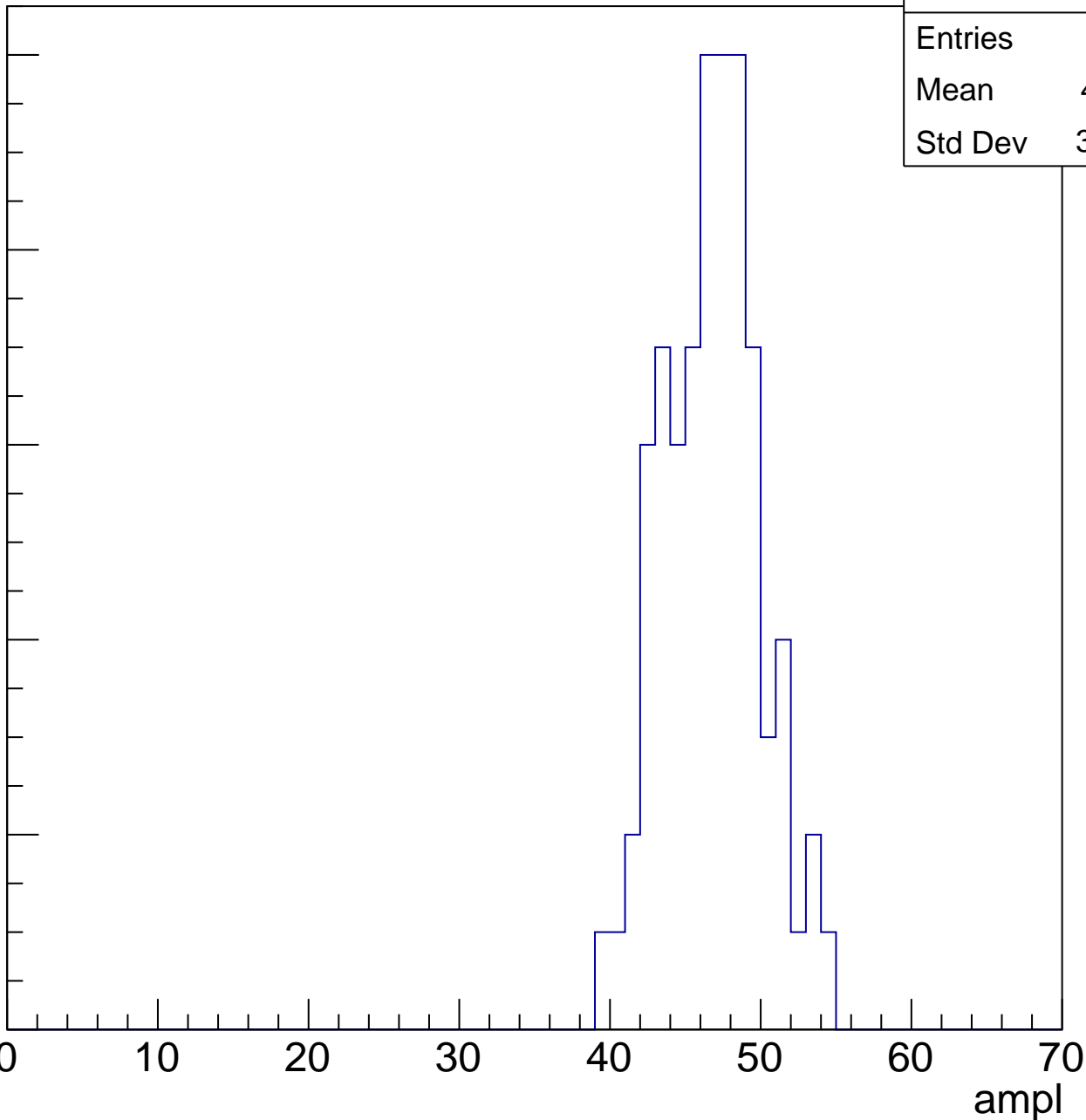
40

50

60

70

ampl

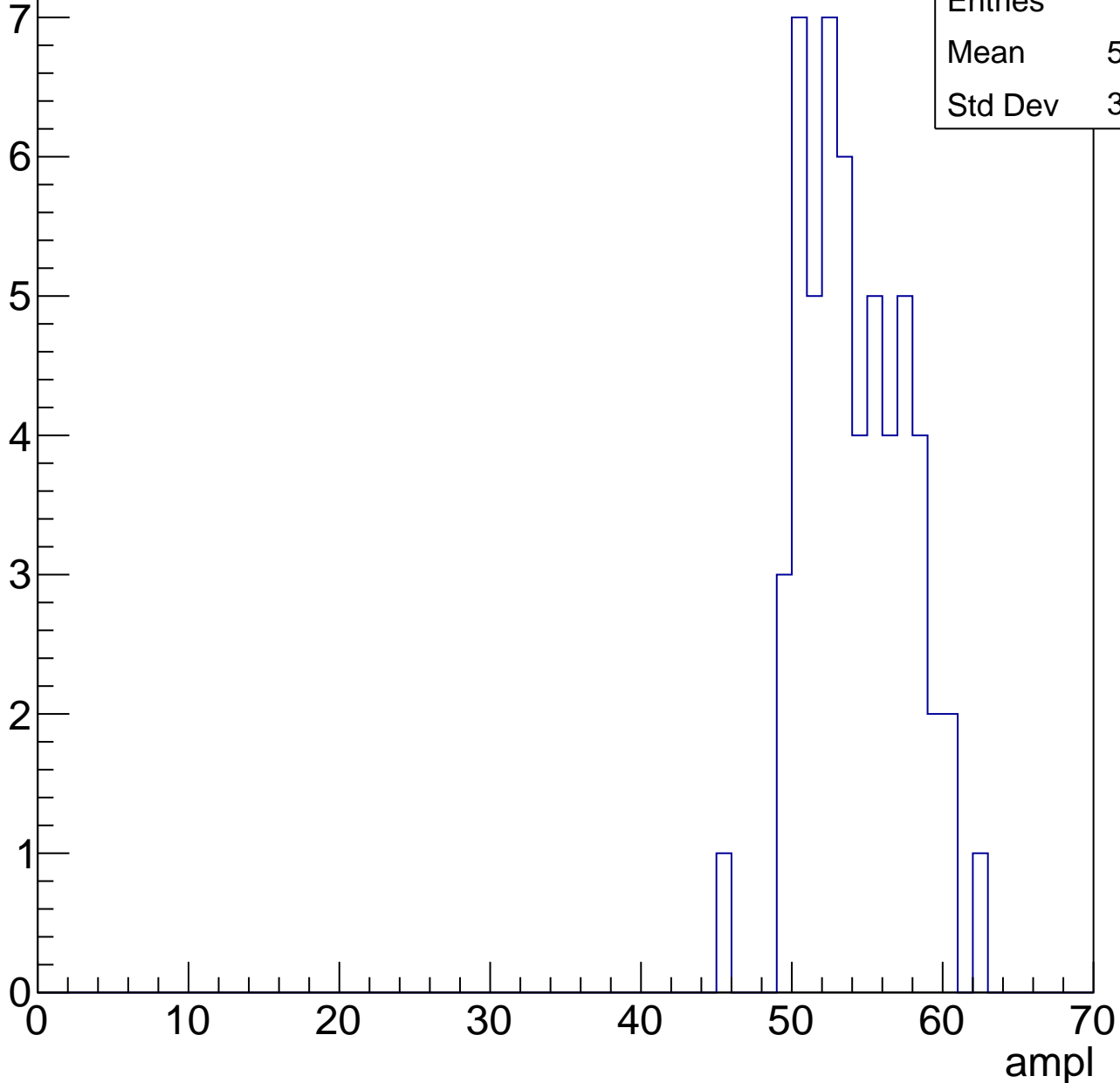


B0L102S, U20-ch102, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 53.77 |
| Std Dev | 3.433 |



B0L102S, U20-ch102, adc5

calib_packv5_042523_0143.root, FC#12, port B1

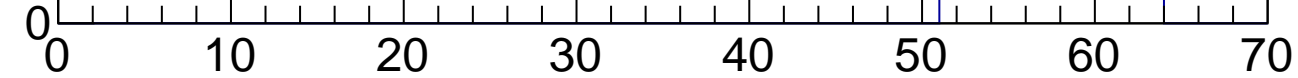
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 58.05 |
| Std Dev | 3.129 |

ampl

0 10 20 30 40 50 60 70

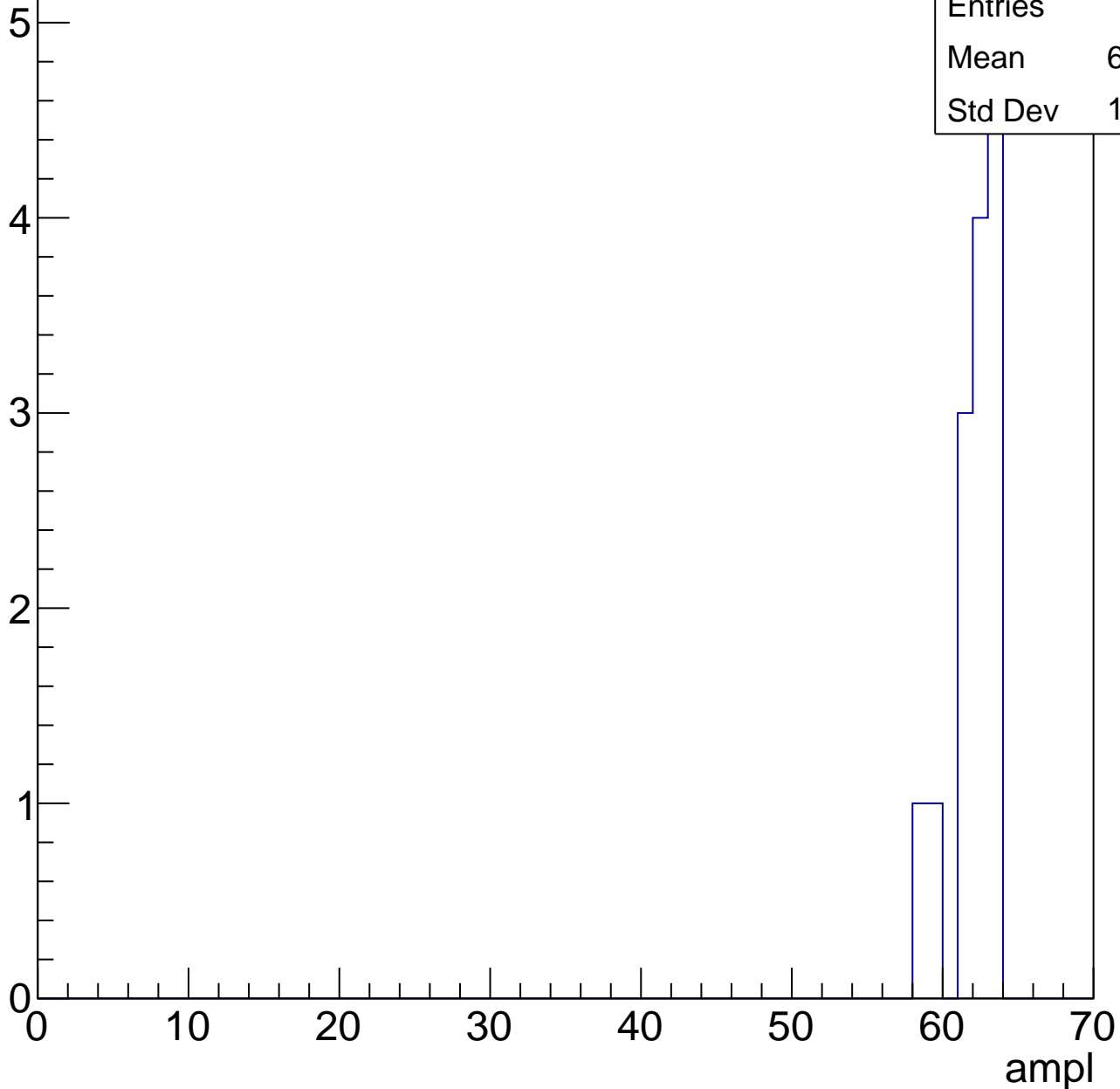


B0L102S, U20-ch102, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.64 |
| Std Dev | 1.493 |



B0L102S, U20-ch102, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch103, adc0

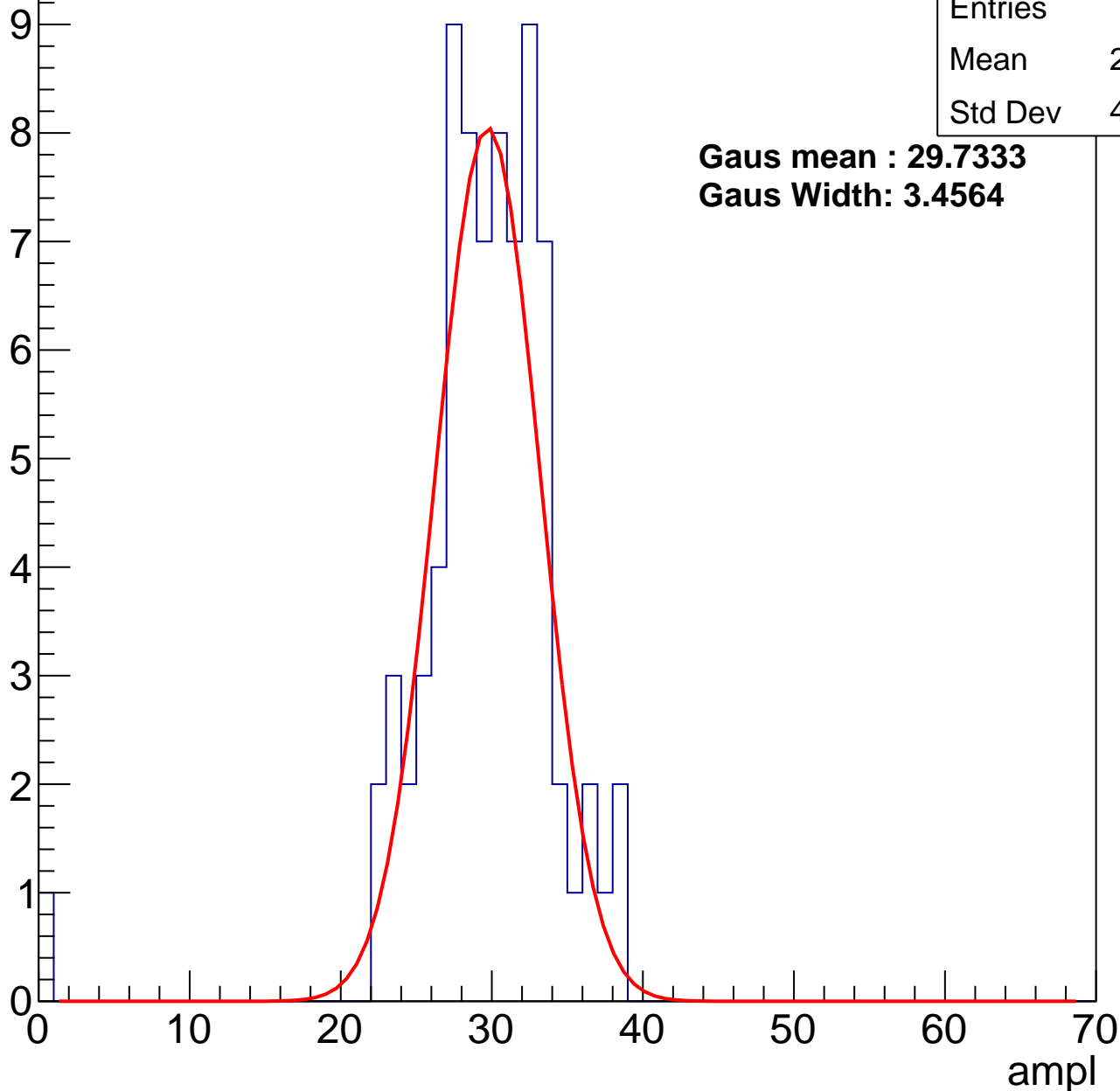
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 29.15 |
| Std Dev | 4.886 |

Gaus mean : 29.7333

Gaus Width: 3.4564



B0L102S, U20-ch103, adc1

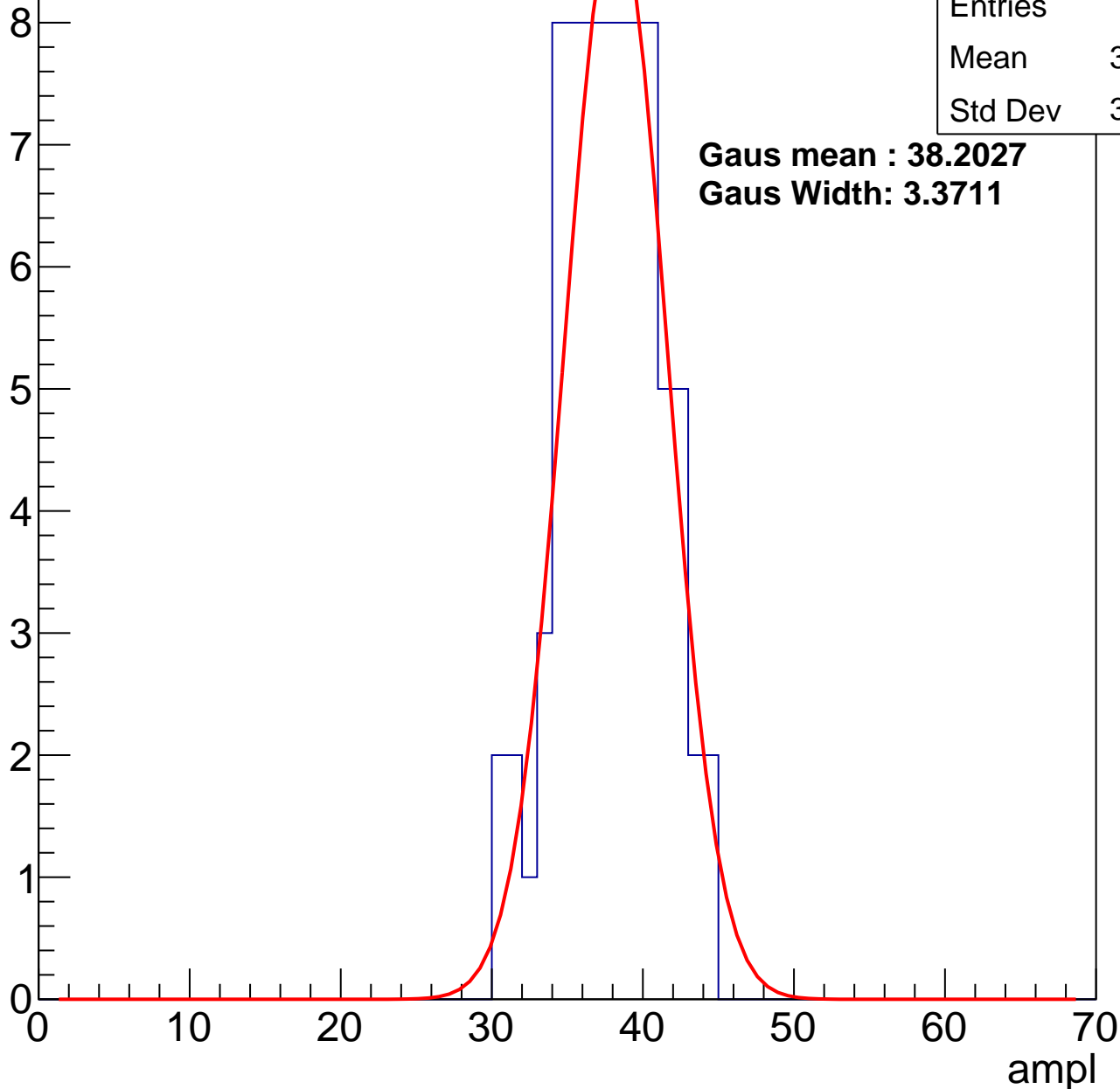
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 37.36 |
| Std Dev | 3.266 |

Gaus mean : 38.2027

Gaus Width: 3.3711



B0L102S, U20-ch103, adc2

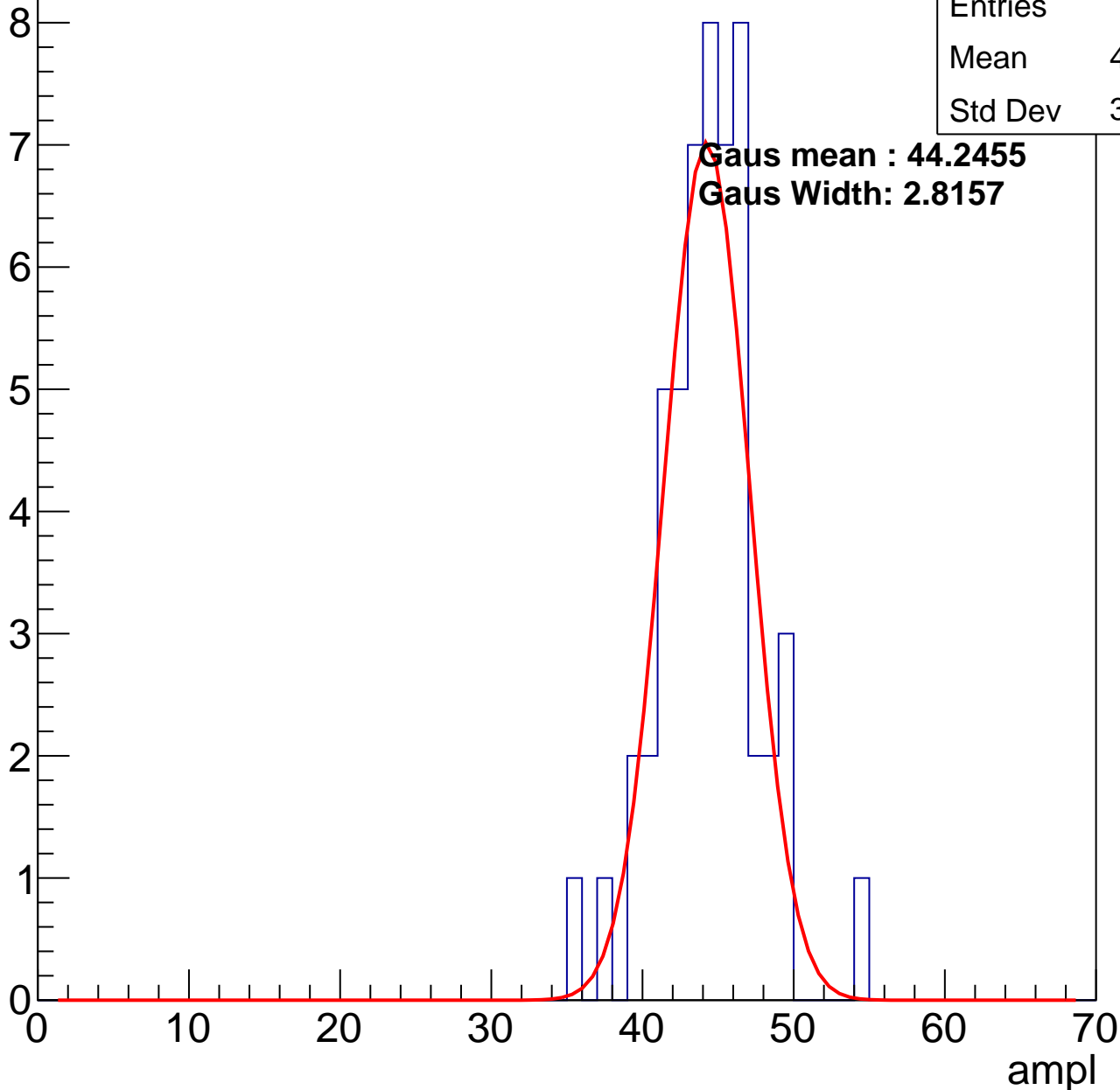
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.93 |
| Std Dev | 3.185 |

Gaus mean : 44.2455

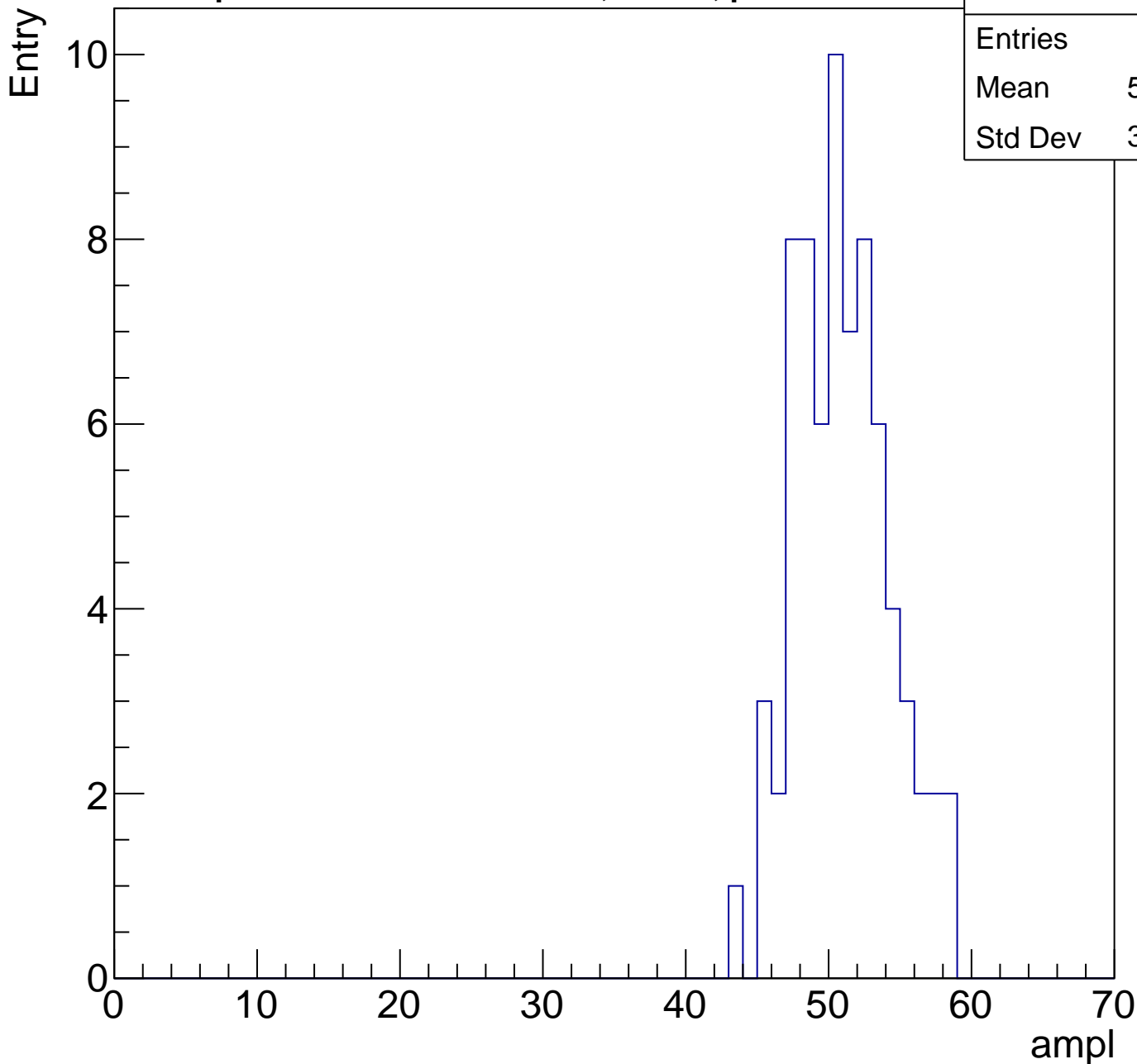
Gaus Width: 2.8157



B0L102S, U20-ch103, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 50.53 |
| Std Dev | 3.283 |

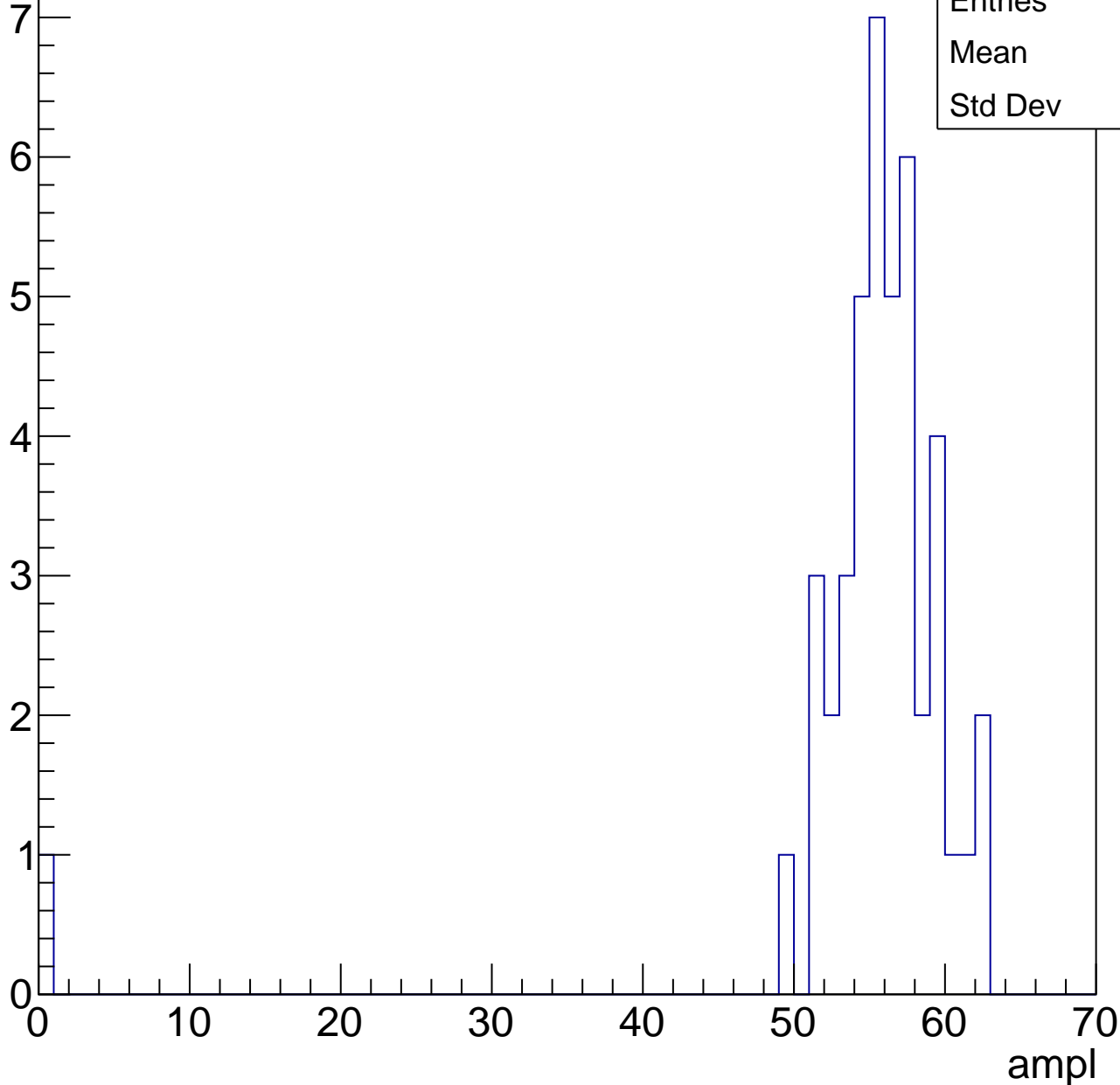


B0L102S, U20-ch103, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

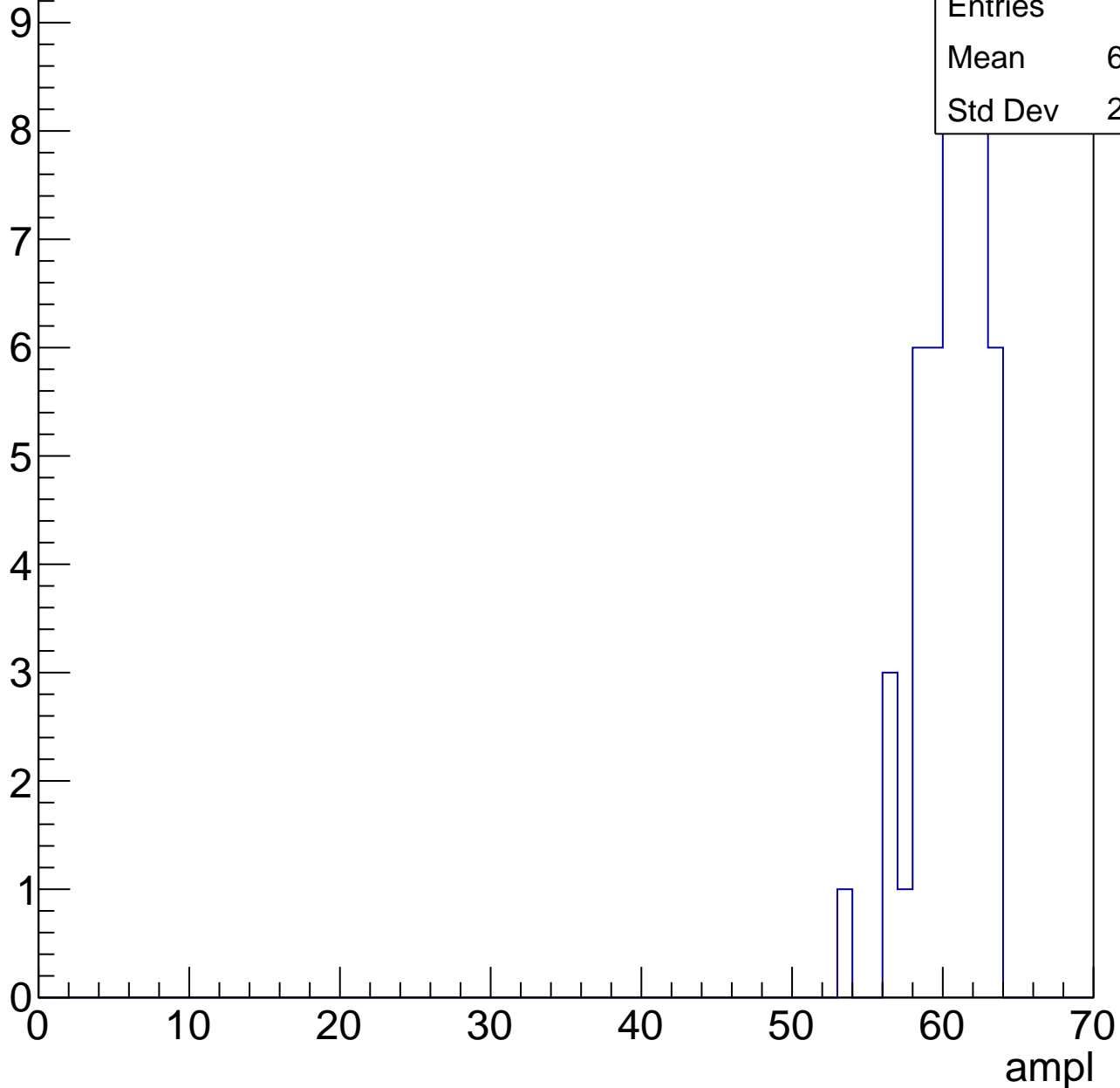
| | |
|---------|------|
| Entries | 43 |
| Mean | 54.4 |
| Std Dev | 8.89 |



B0L102S, U20-ch103, adc5

calib_packv5_042523_0143.root, FC#12, port B1

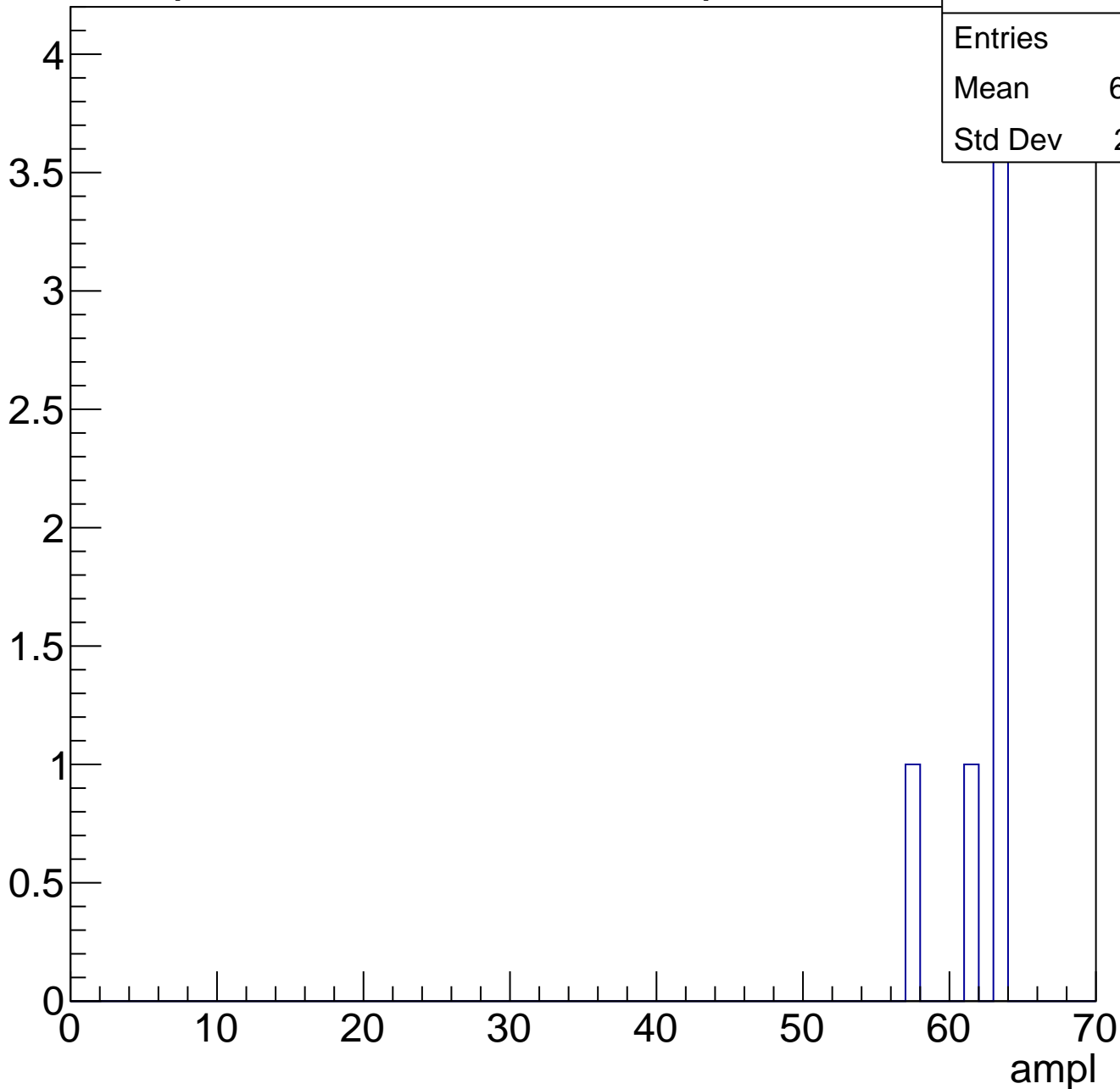
Entry



B0L102S, U20-ch103, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch103, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

ampl

B0L102S, U20-ch104, adc0

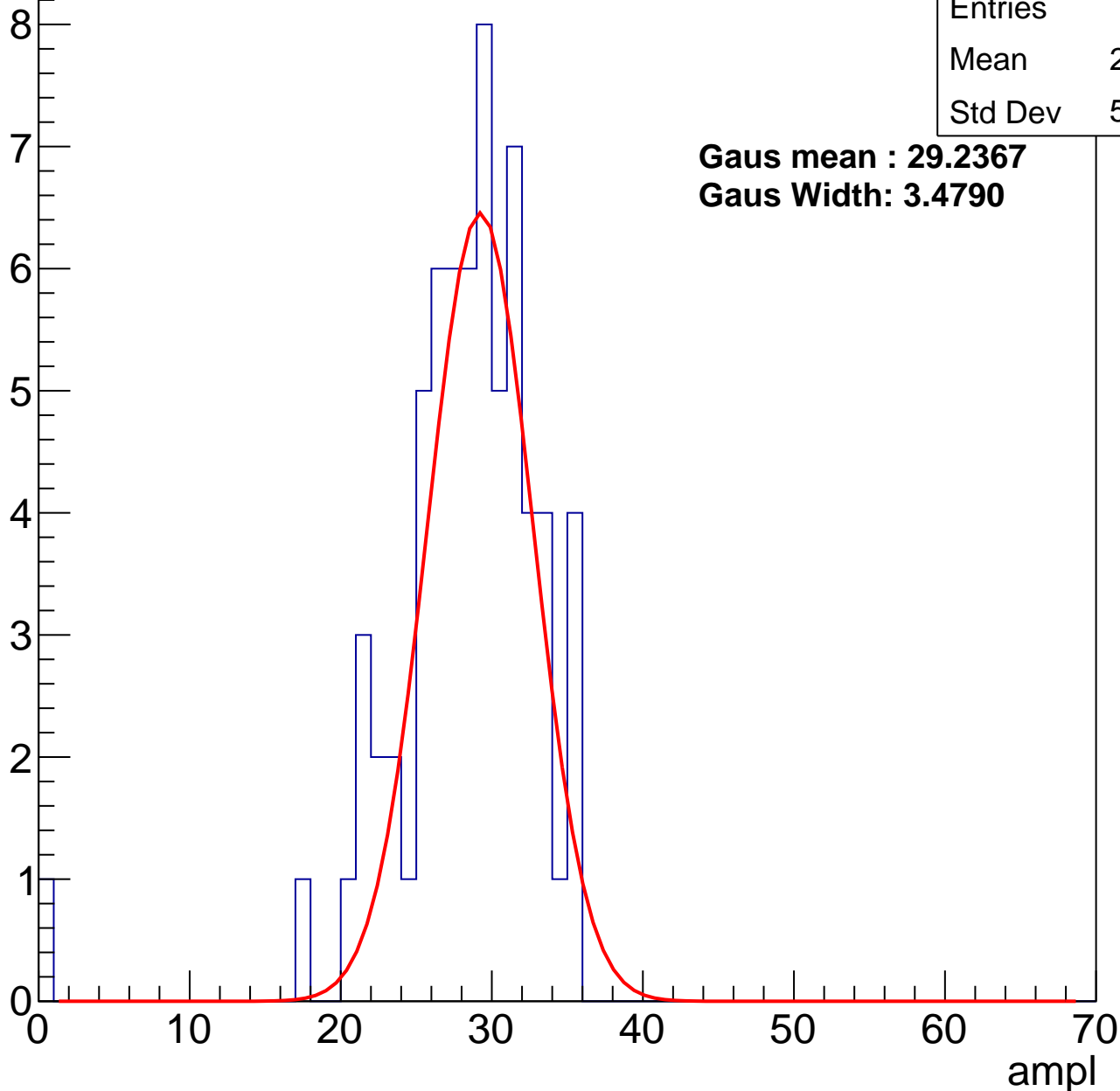
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.73 |
| Std Dev | 5.199 |

Gaus mean : 29.2367

Gaus Width: 3.4790



B0L102S, U20-ch104, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.04 |
| Std Dev | 3.407 |

Gaus mean : 35.3520

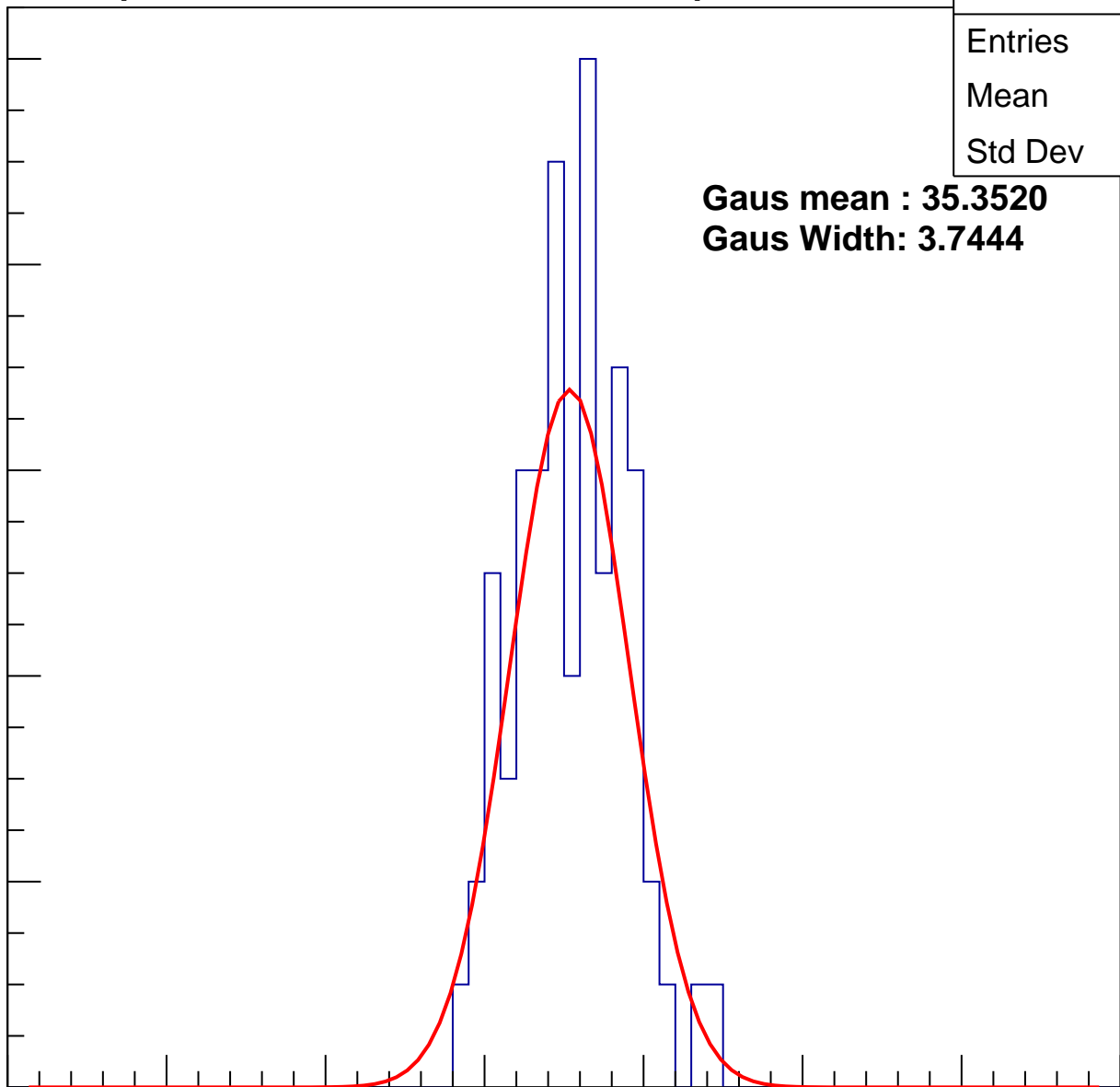
Gaus Width: 3.7444

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch104, adc2

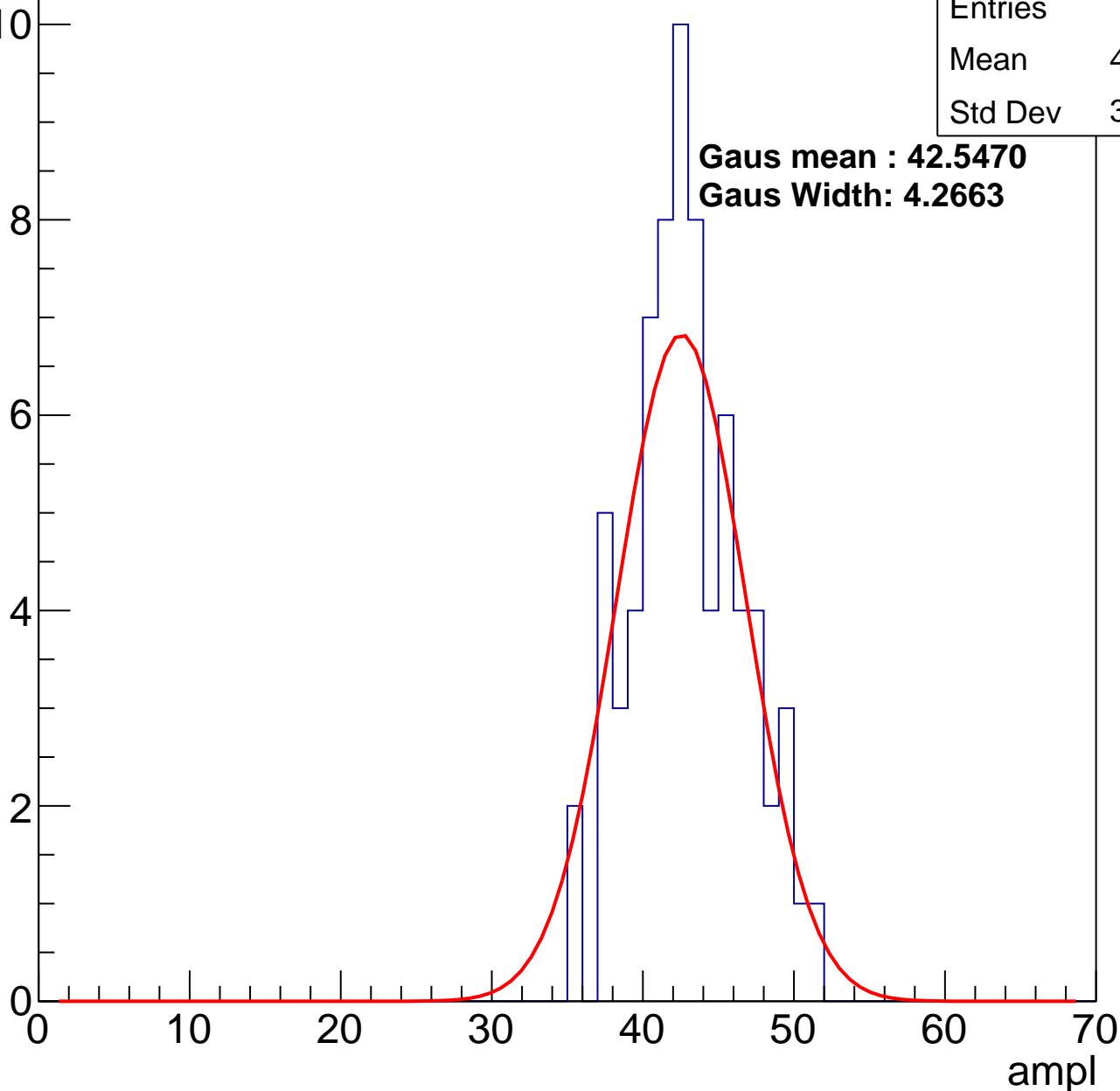
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 42.49 |
| Std Dev | 3.598 |

Gaus mean : 42.5470

Gaus Width: 4.2663

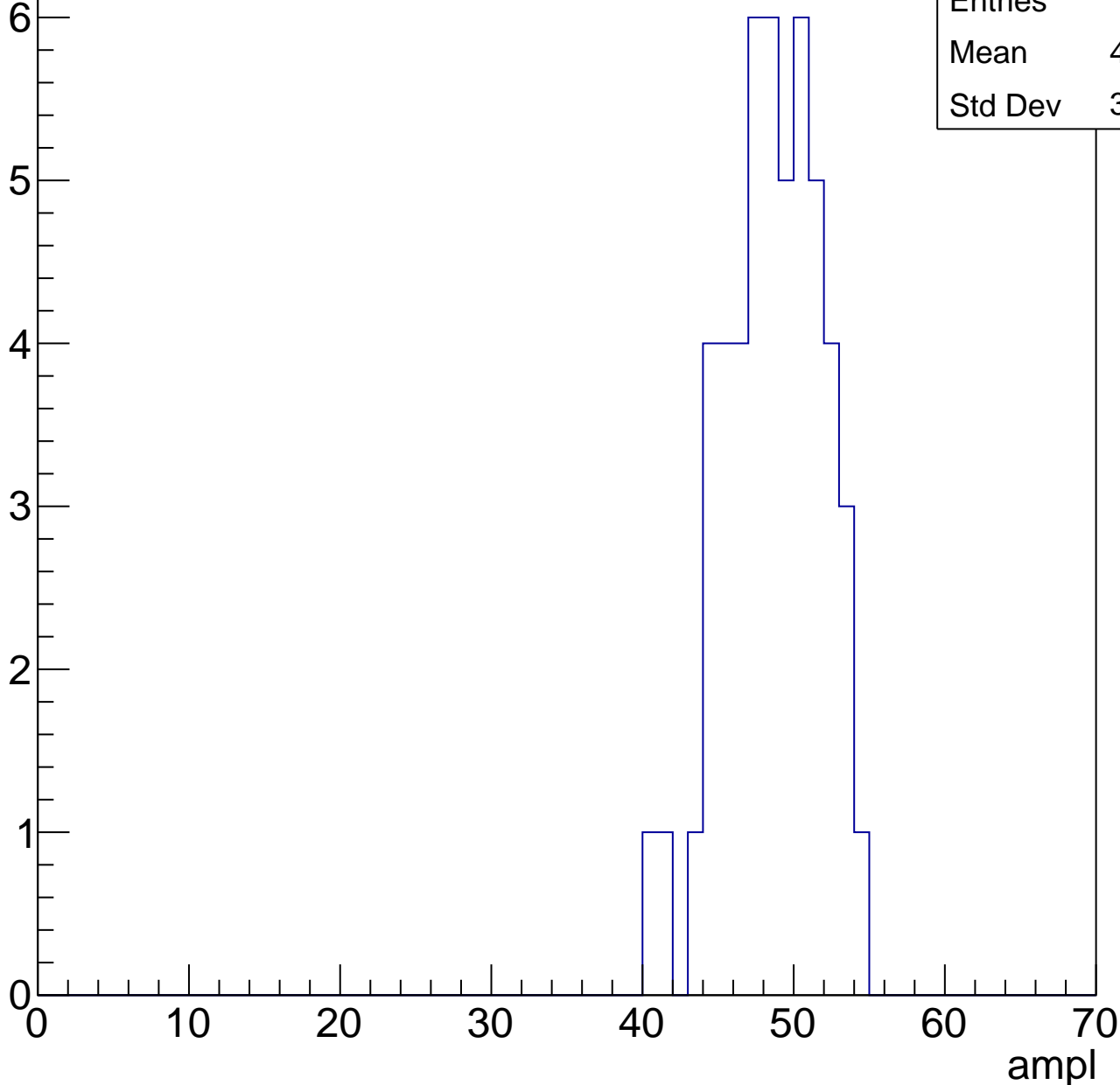


B0L102S, U20-ch104, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 48.14 |
| Std Dev | 3.156 |

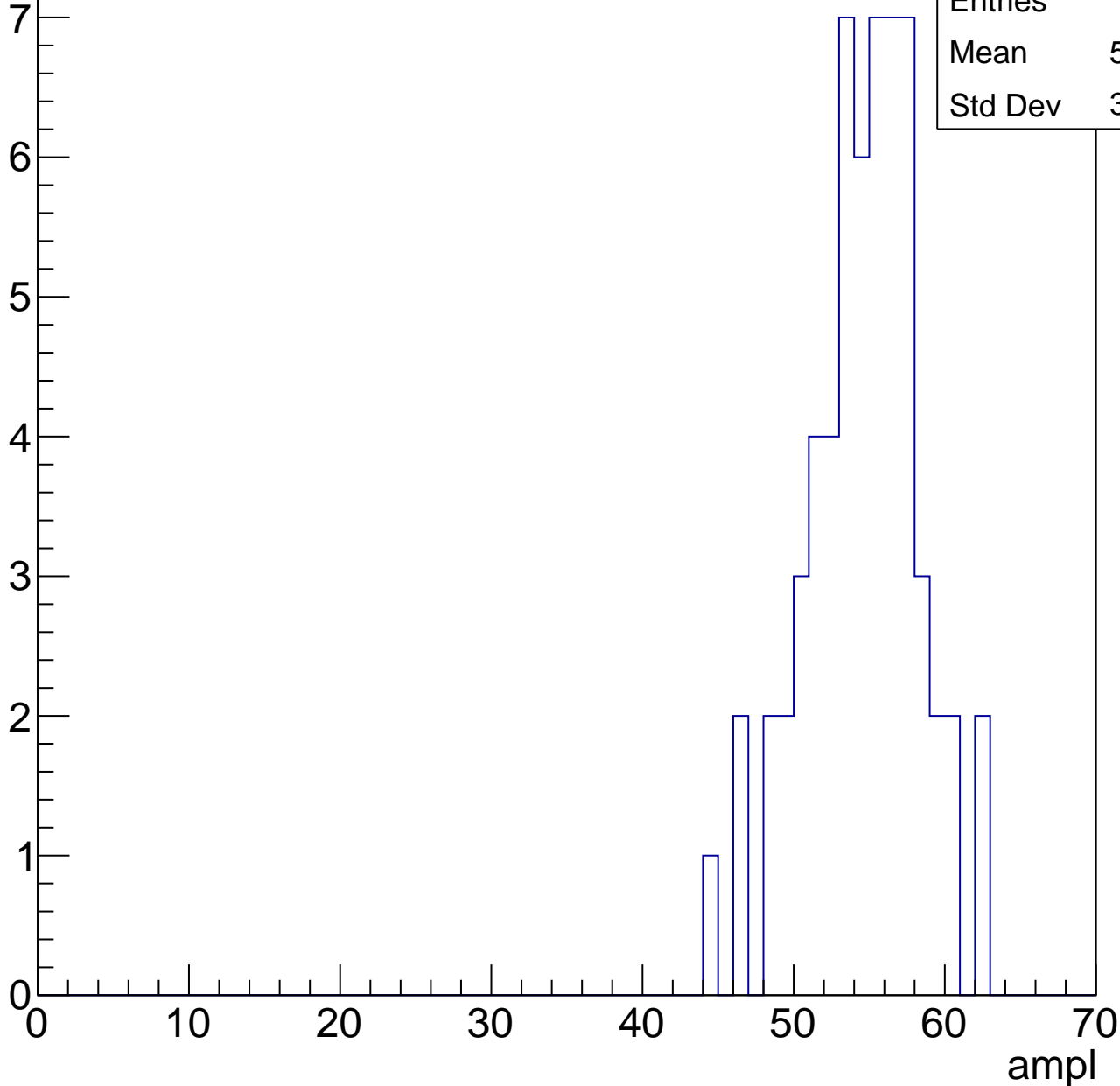


B0L102S, U20-ch104, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

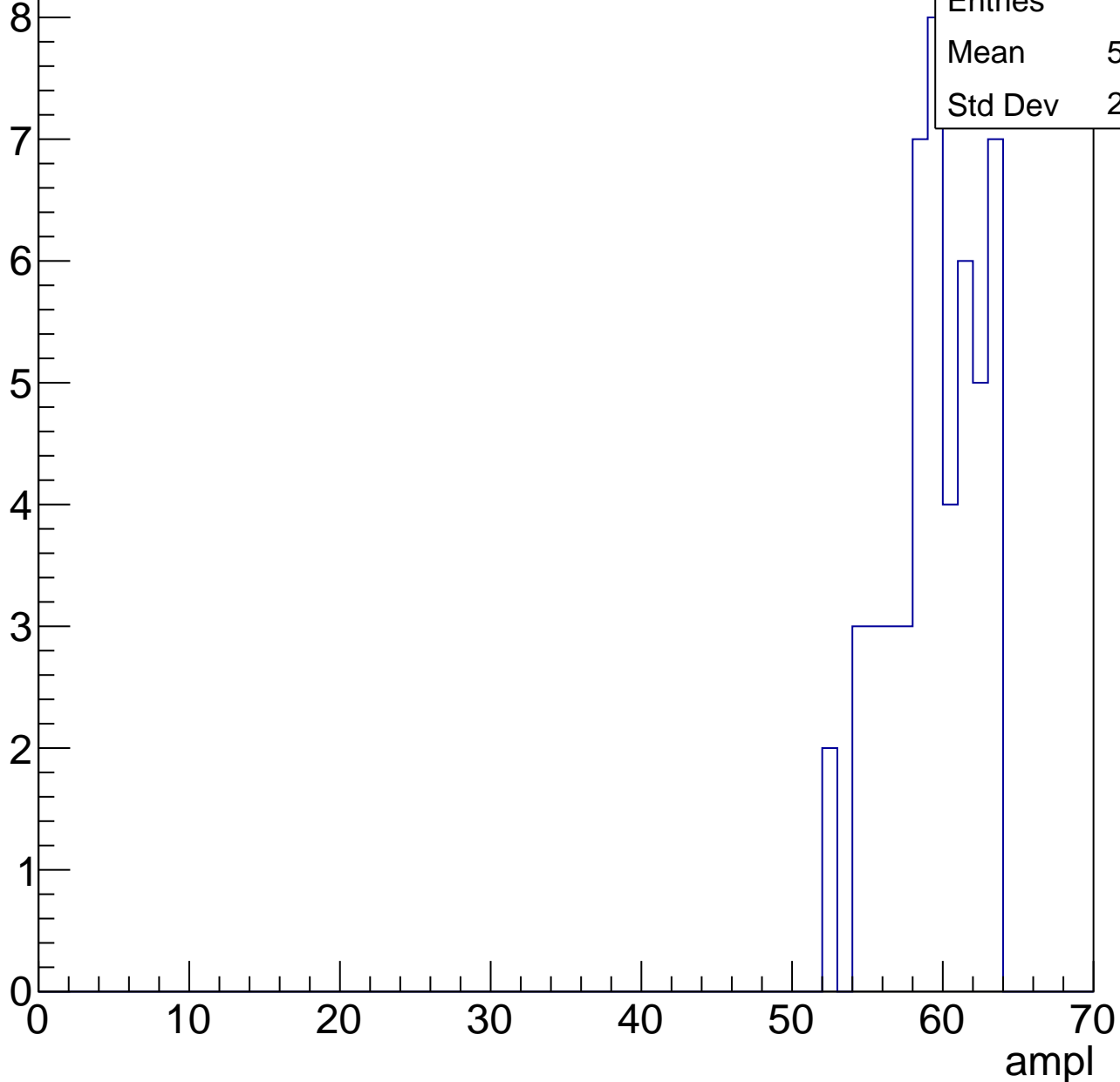
| | |
|---------|-------|
| Entries | 61 |
| Mean | 54.08 |
| Std Dev | 3.739 |



B0L102S, U20-ch104, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

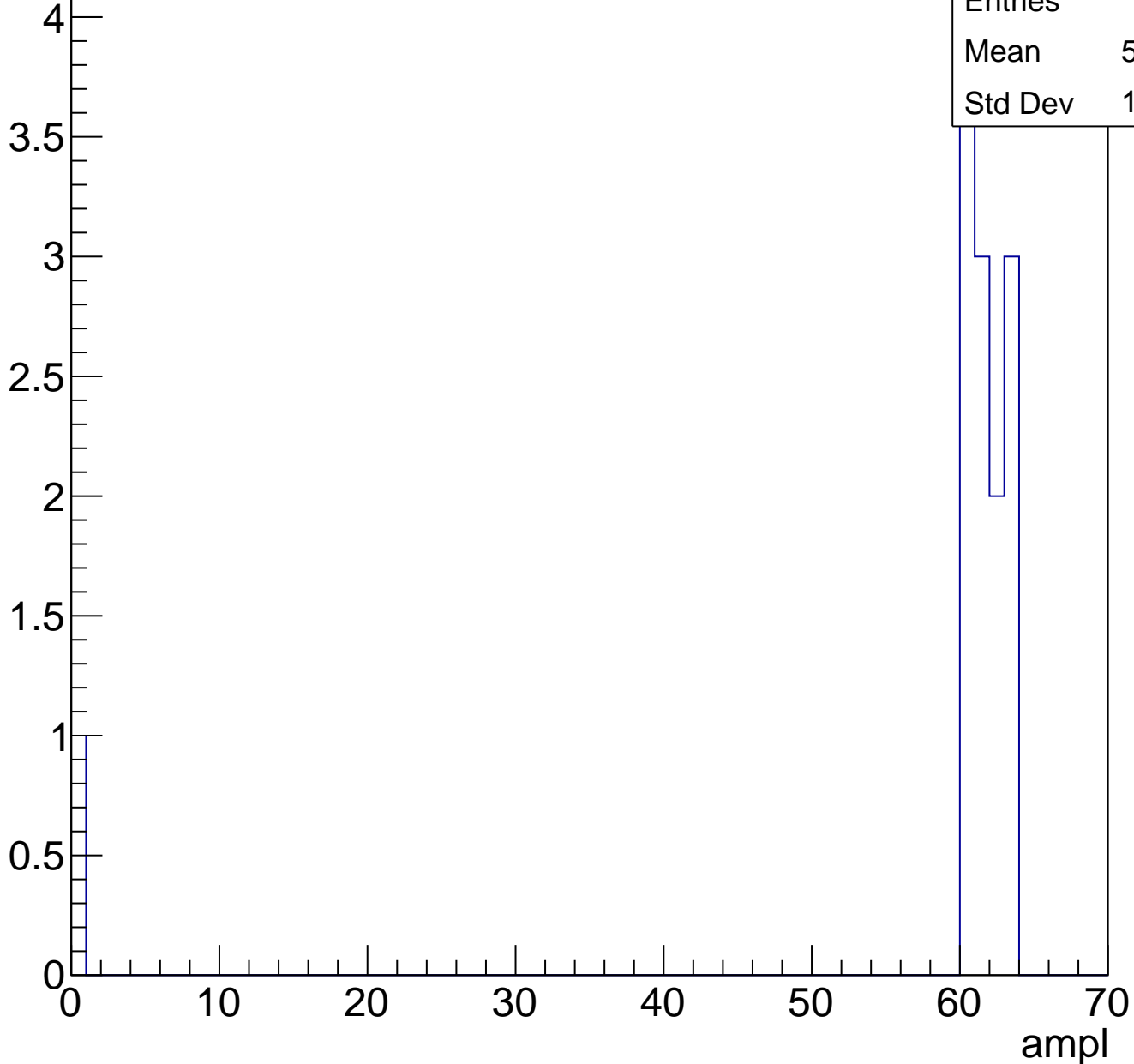


| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.92 |
| Std Dev | 2.976 |

B0L102S, U20-ch104, adc6

calib_packv5_042523_0143.root, FC#12, port B1

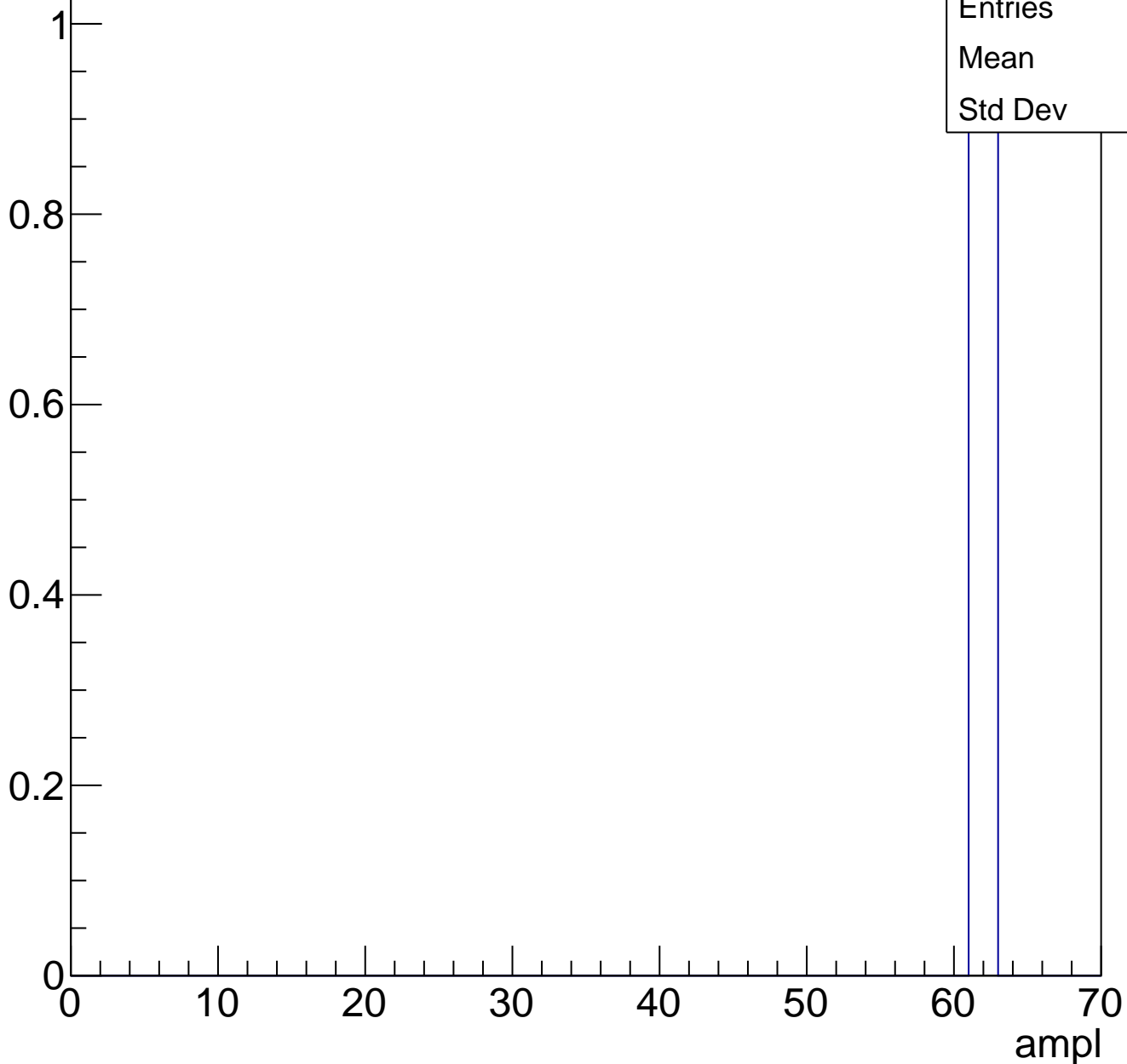
Entry



B0L102S, U20-ch104, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch105, adc0

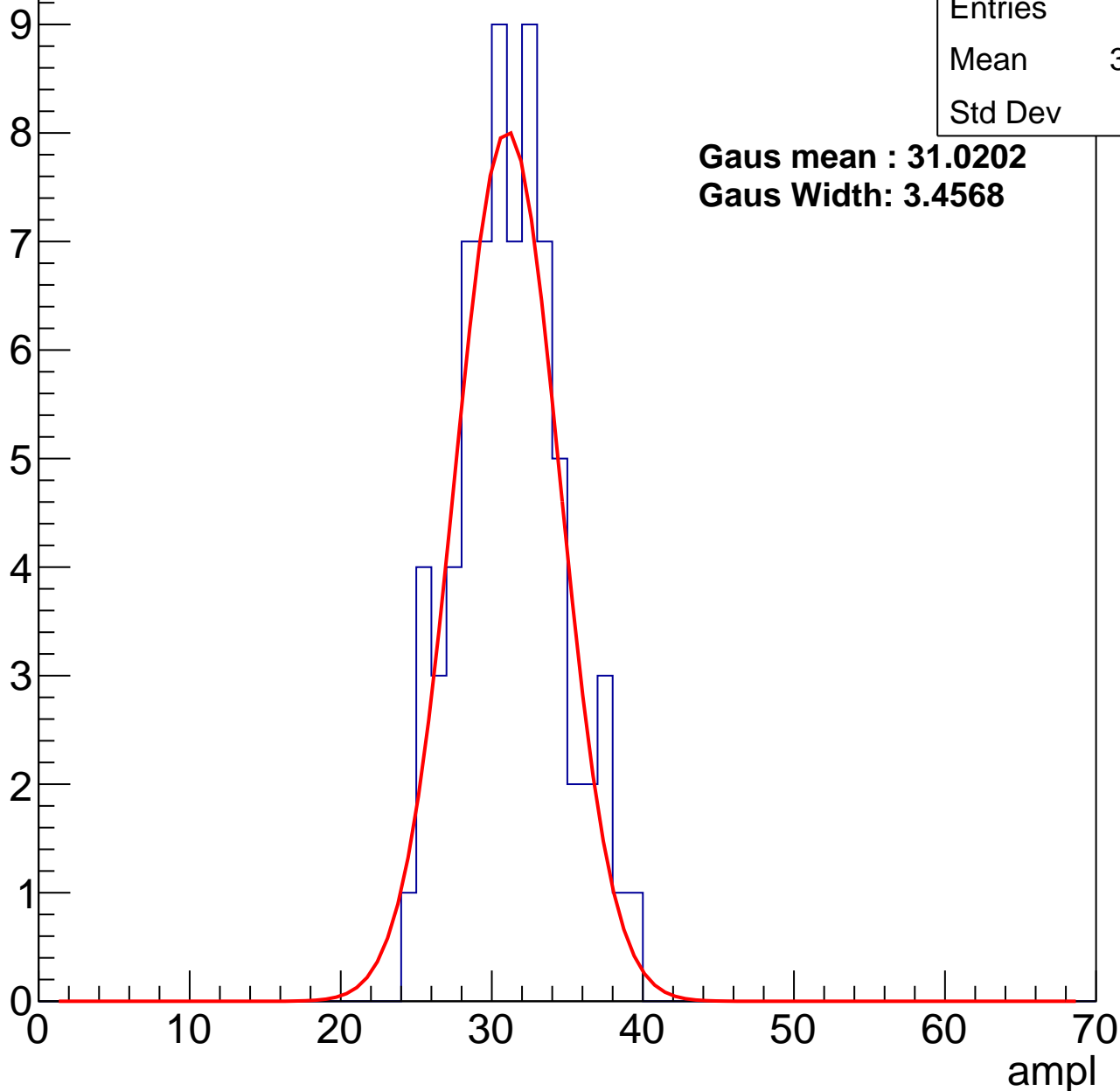
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 30.76 |
| Std Dev | 3.36 |

Gaus mean : 31.0202

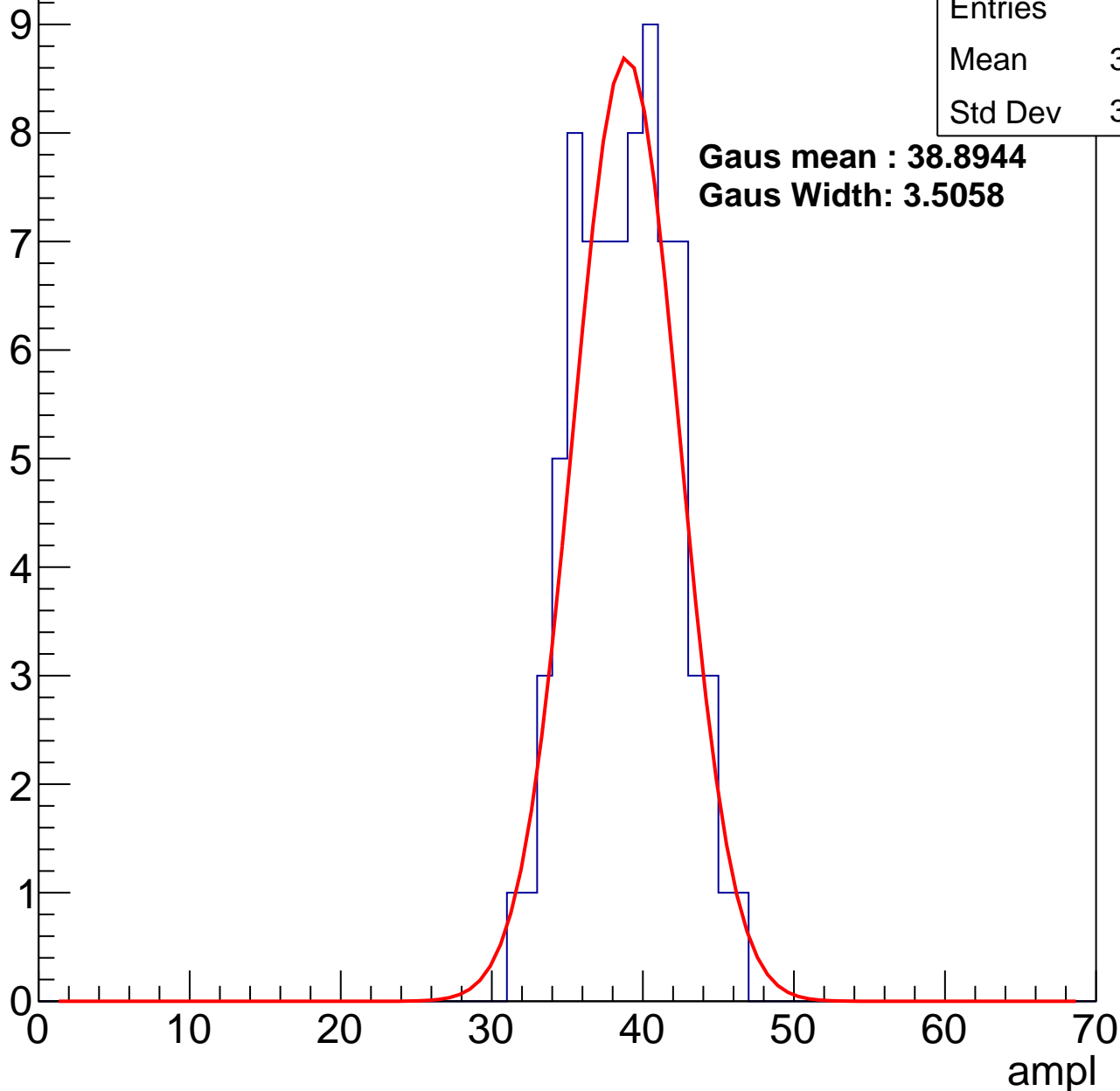
Gaus Width: 3.4568



B0L102S, U20-ch105, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch105, adc2

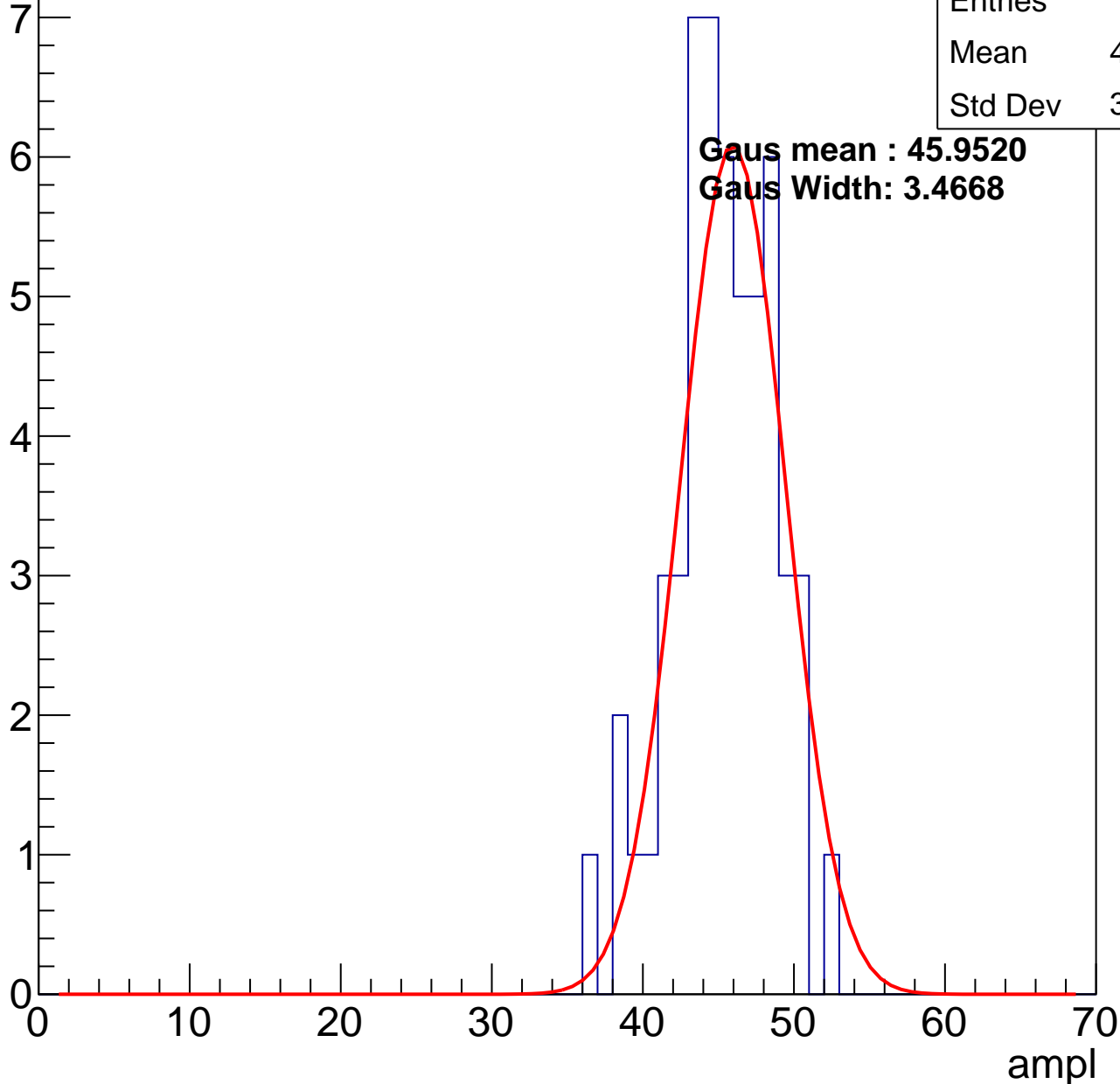
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 44.83 |
| Std Dev | 3.332 |

Gaus mean : 45.9520

Gaus Width: 3.4668

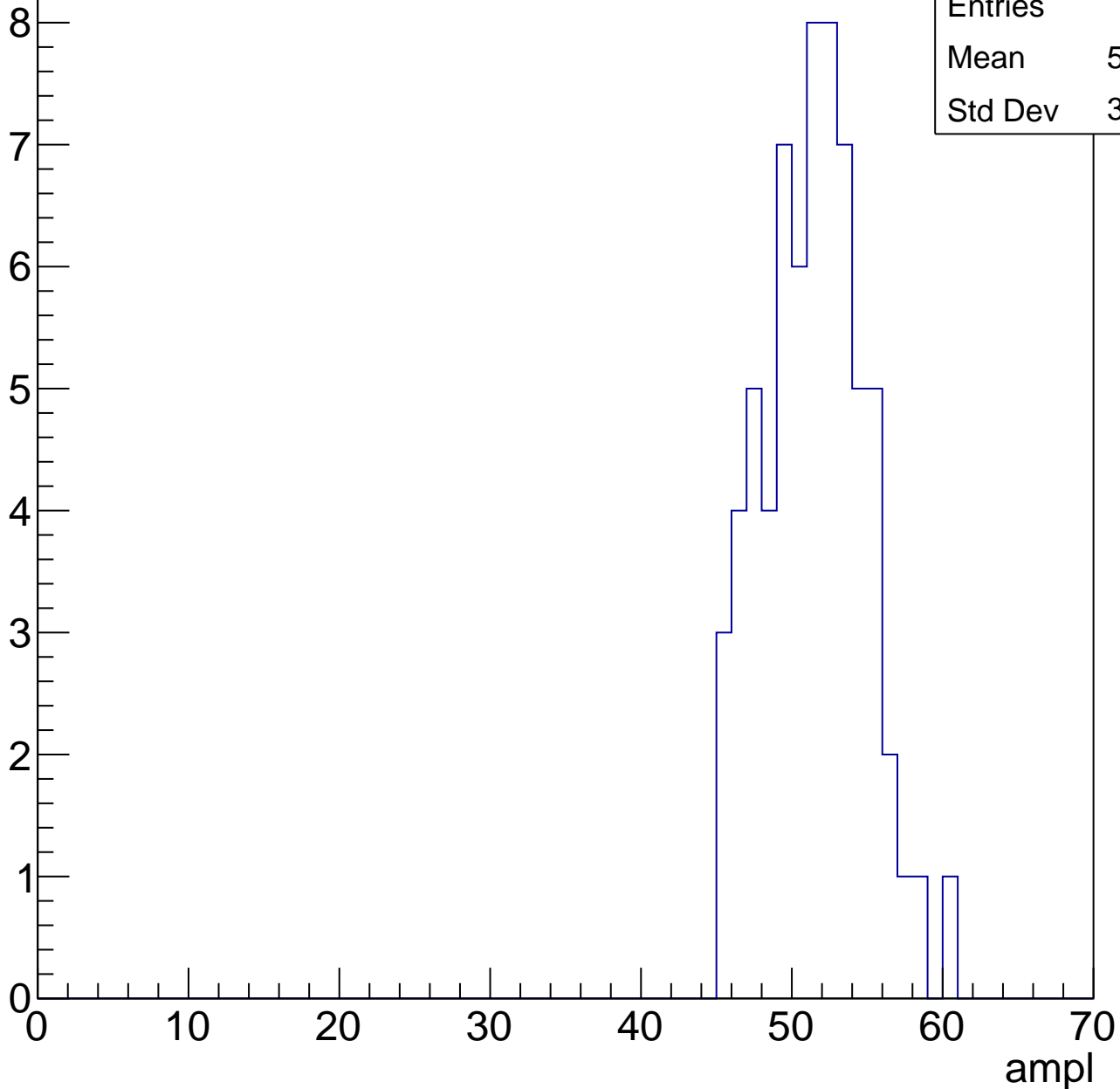


B0L102S, U20-ch105, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50.99 |
| Std Dev | 3.312 |

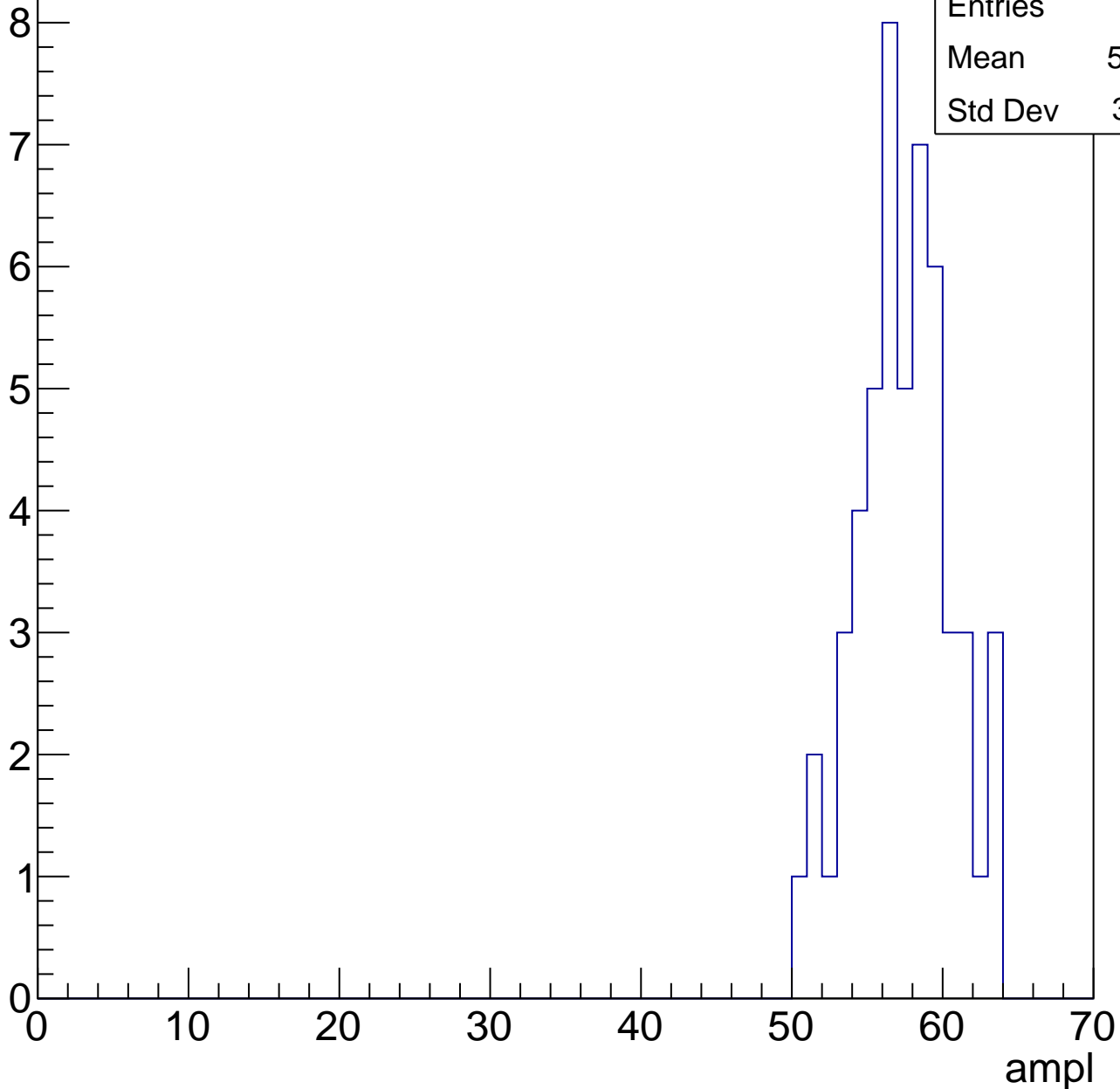


B0L102S, U20-ch105, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 56.94 |
| Std Dev | 3.091 |

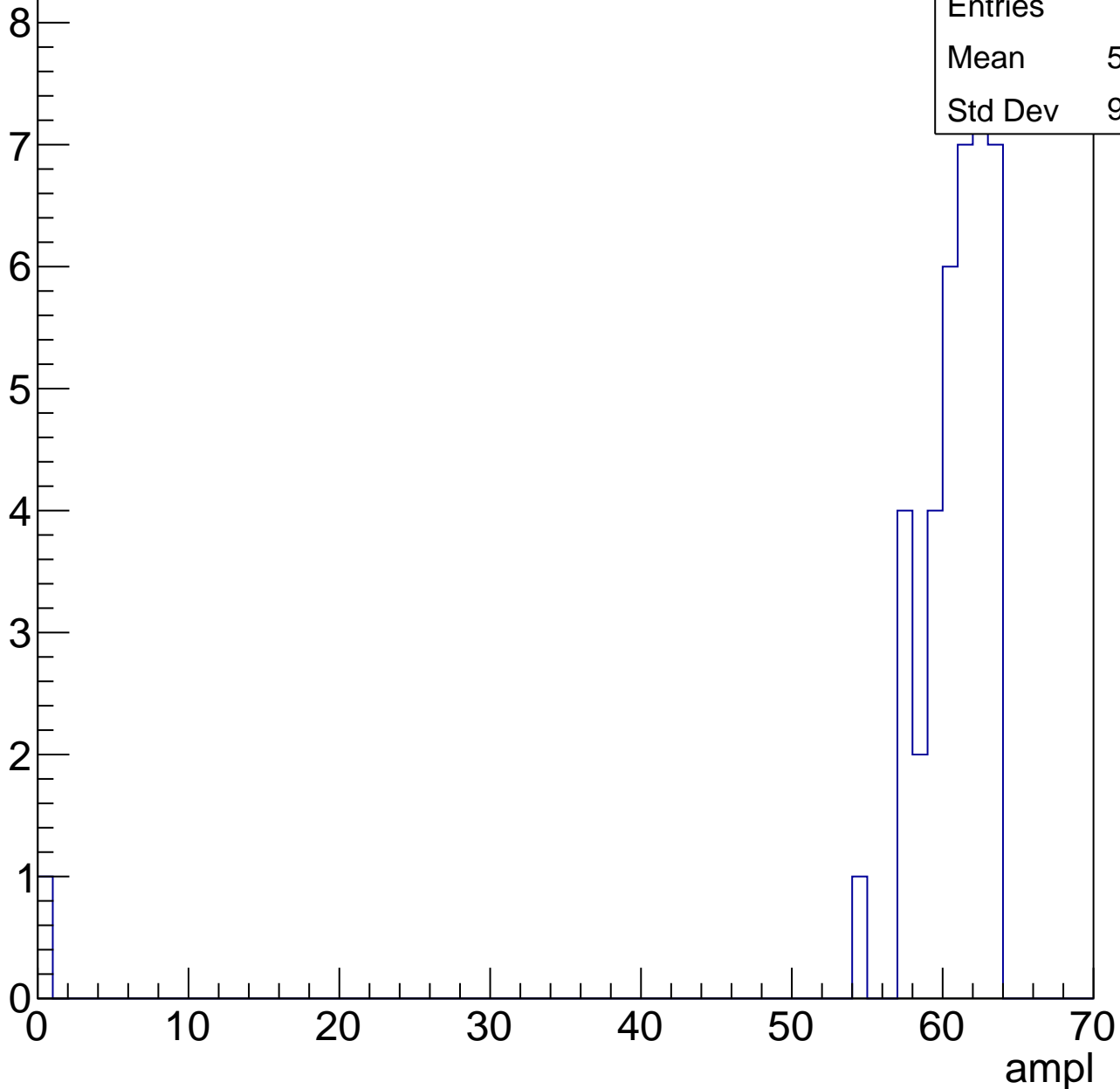


B0L102S, U20-ch105, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.95 |
| Std Dev | 9.672 |



B0L102S, U20-ch105, adc6

calib_packv5_042523_0143.root, FC#12, port B1

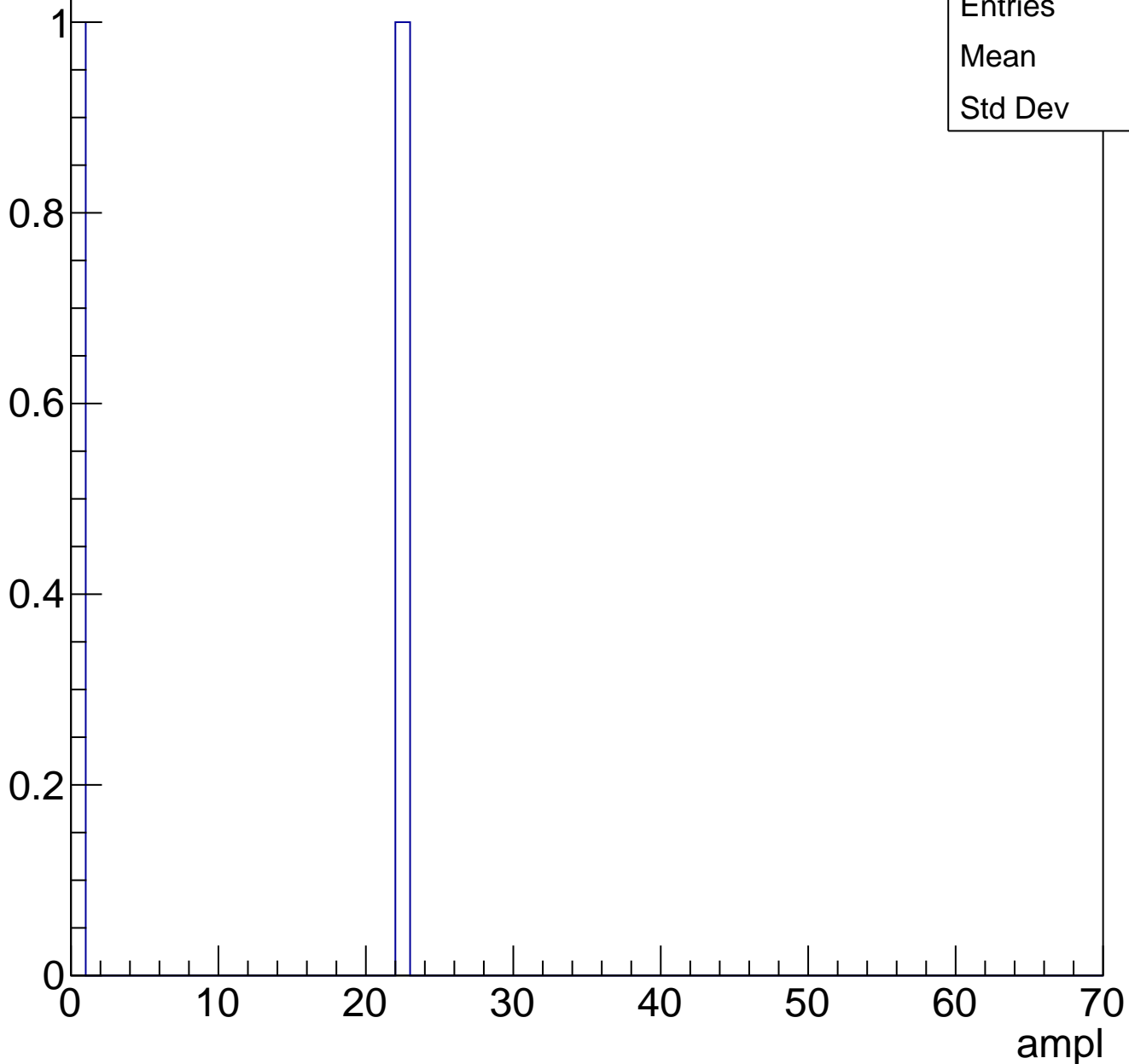
Entry



B0L102S, U20-ch105, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch106, adc0

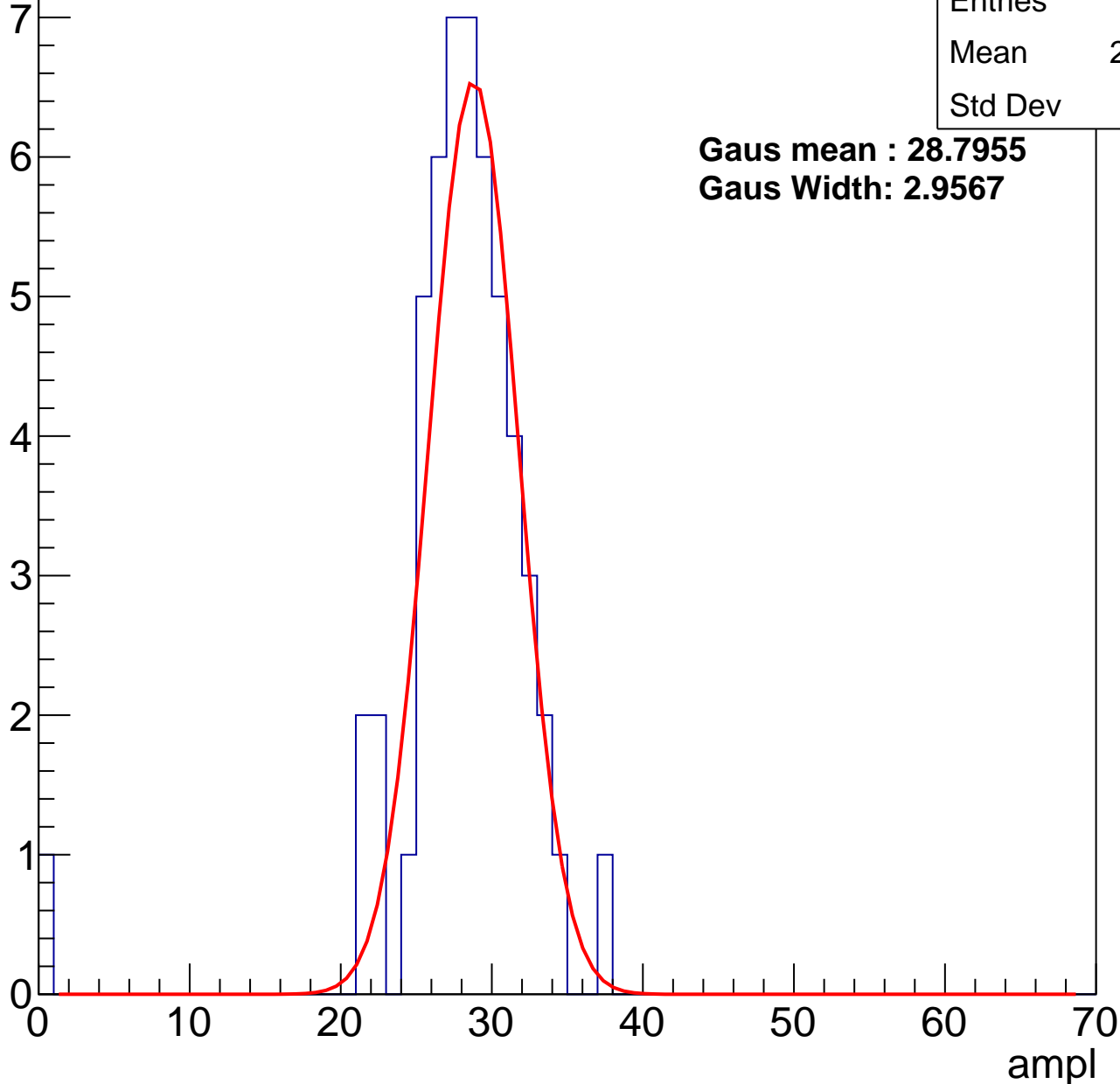
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 27.49 |
| Std Dev | 4.97 |

Gaus mean : 28.7955

Gaus Width: 2.9567



B0L102S, U20-ch106, adc1

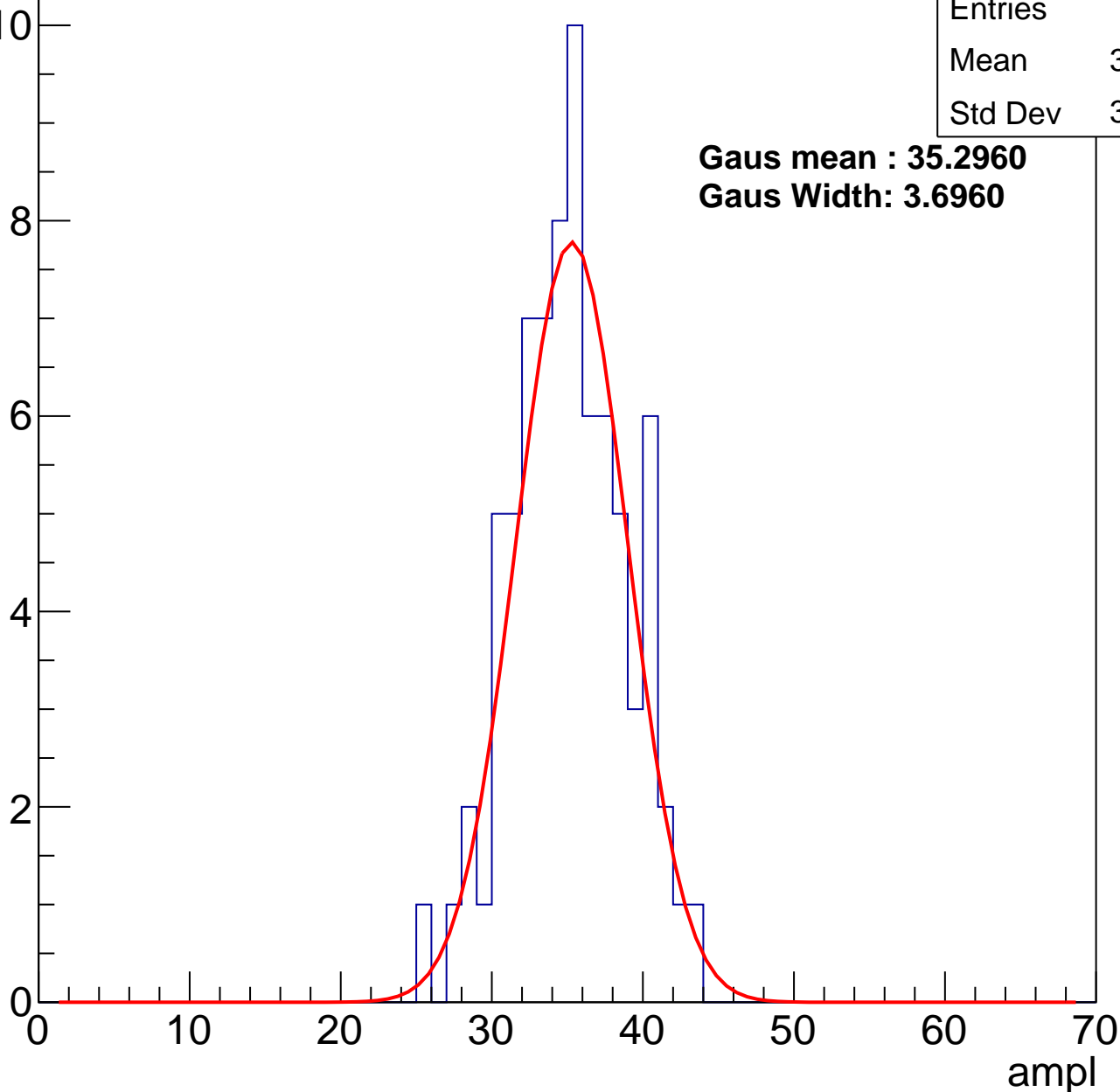
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 34.69 |
| Std Dev | 3.697 |

Gaus mean : 35.2960

Gaus Width: 3.6960



B0L102S, U20-ch106, adc2

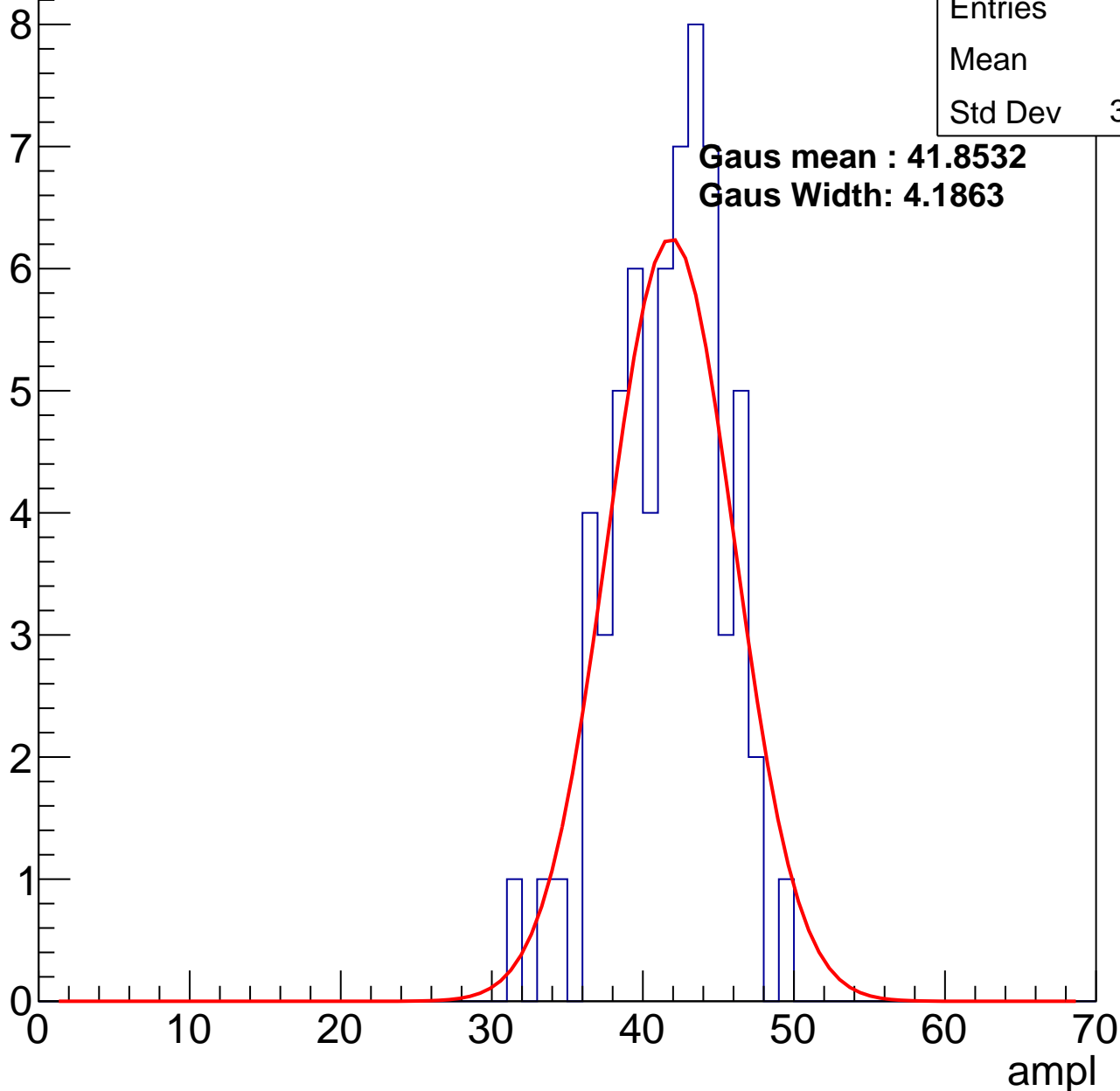
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 41.2 |
| Std Dev | 3.628 |

Gaus mean : 41.8532

Gaus Width: 4.1863

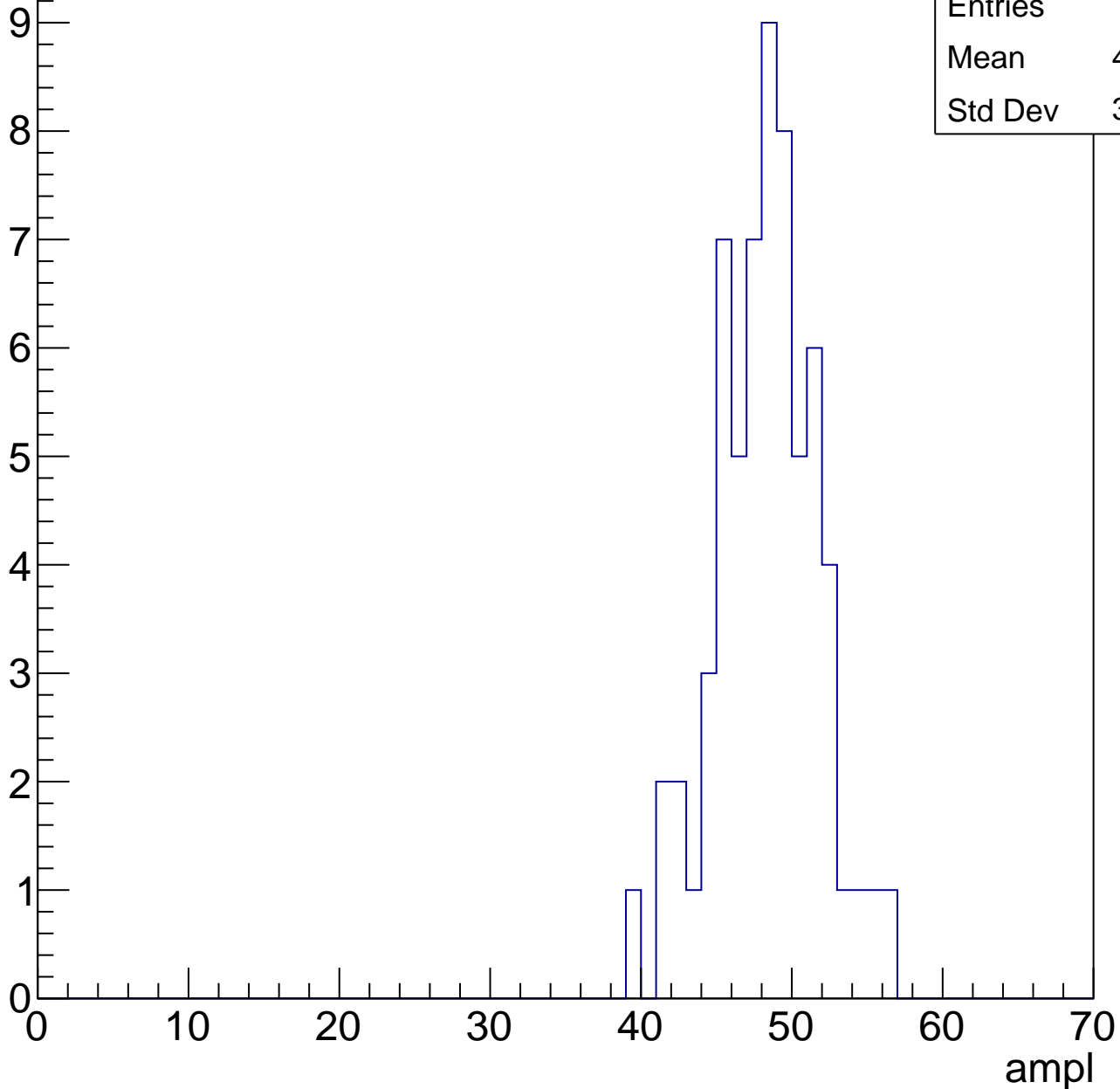


B0L102S, U20-ch106, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 47.81 |
| Std Dev | 3.381 |

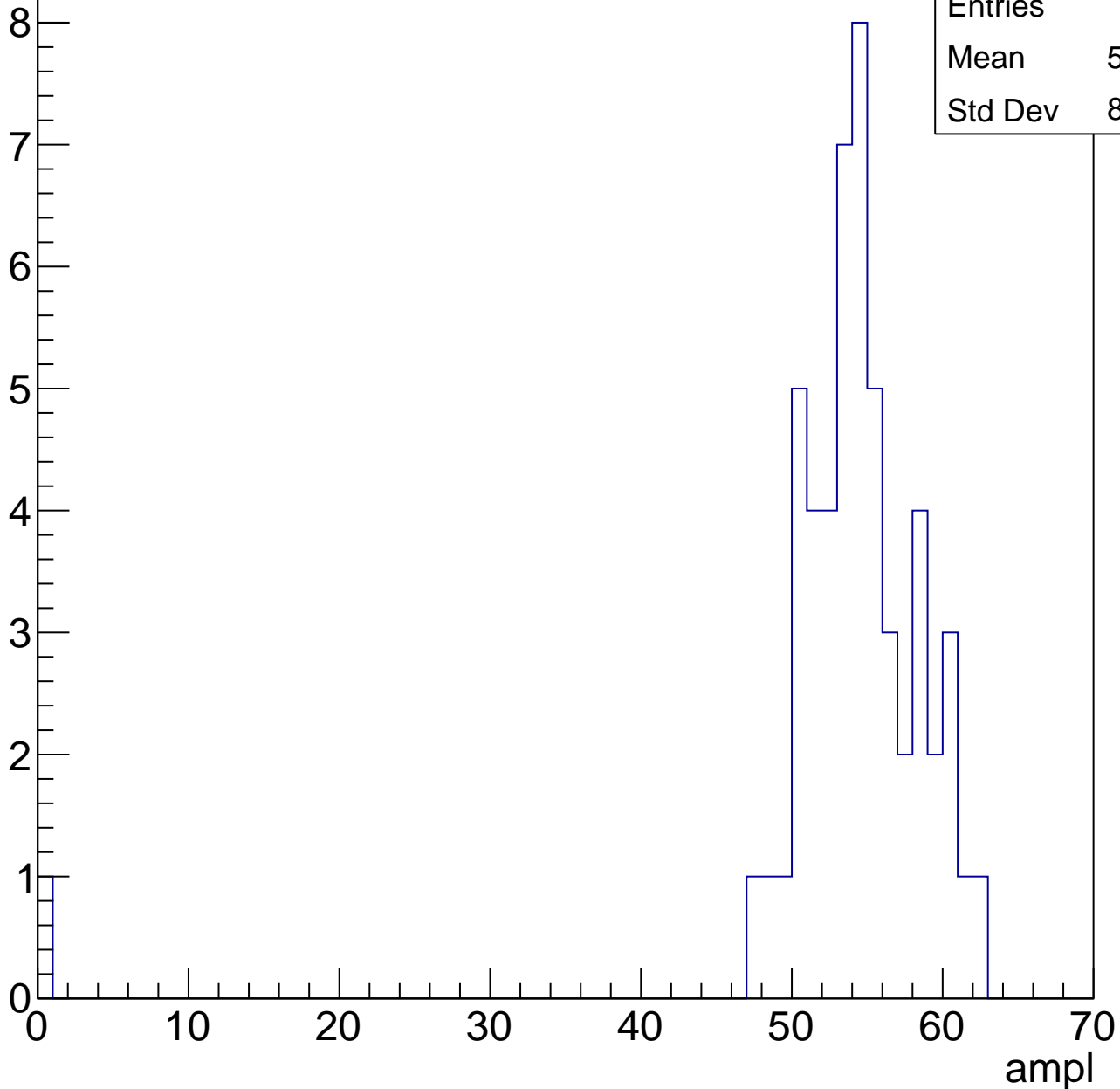


B0L102S, U20-ch106, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

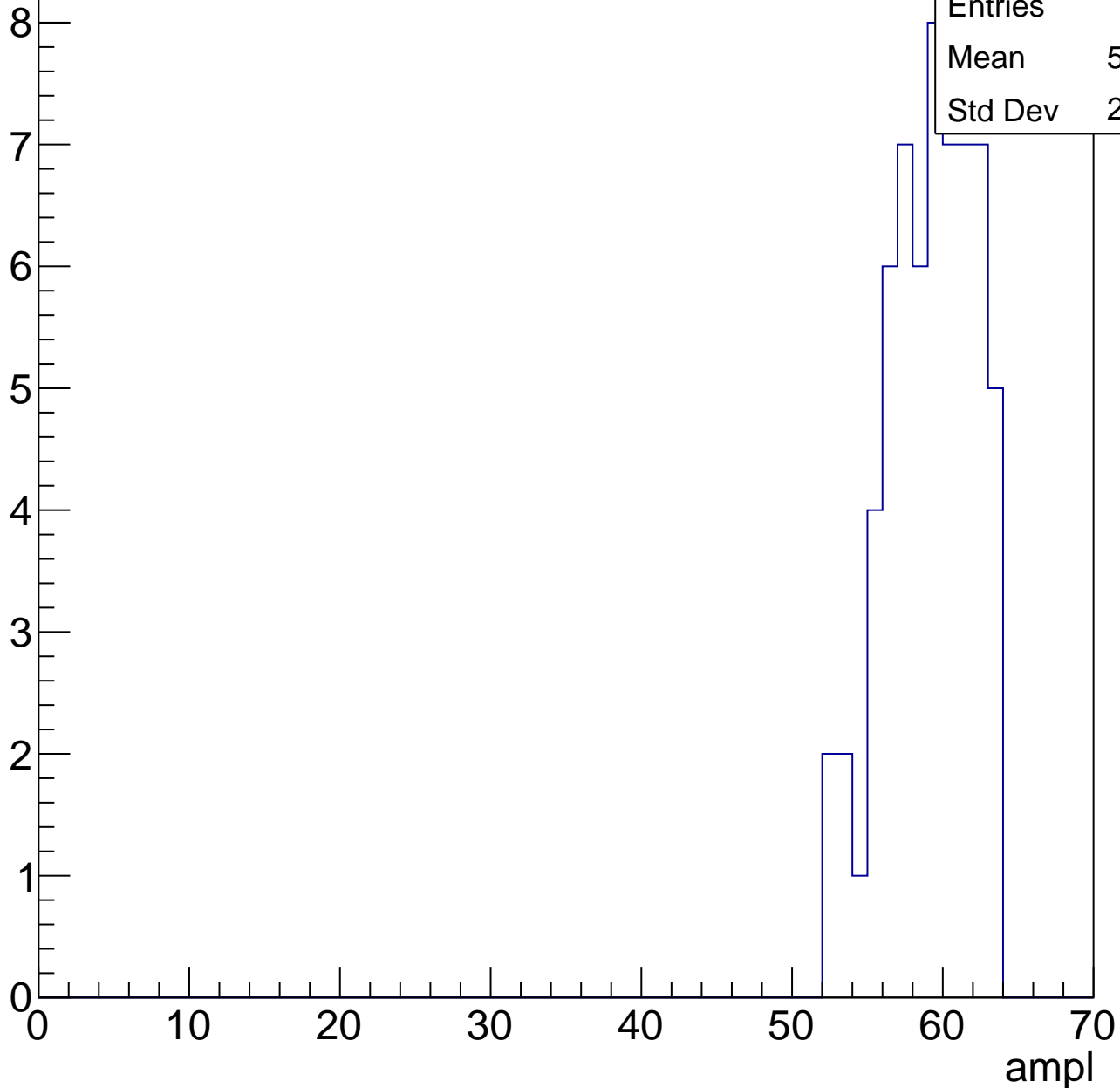
| | |
|---------|-------|
| Entries | 53 |
| Mean | 53.19 |
| Std Dev | 8.126 |



B0L102S, U20-ch106, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

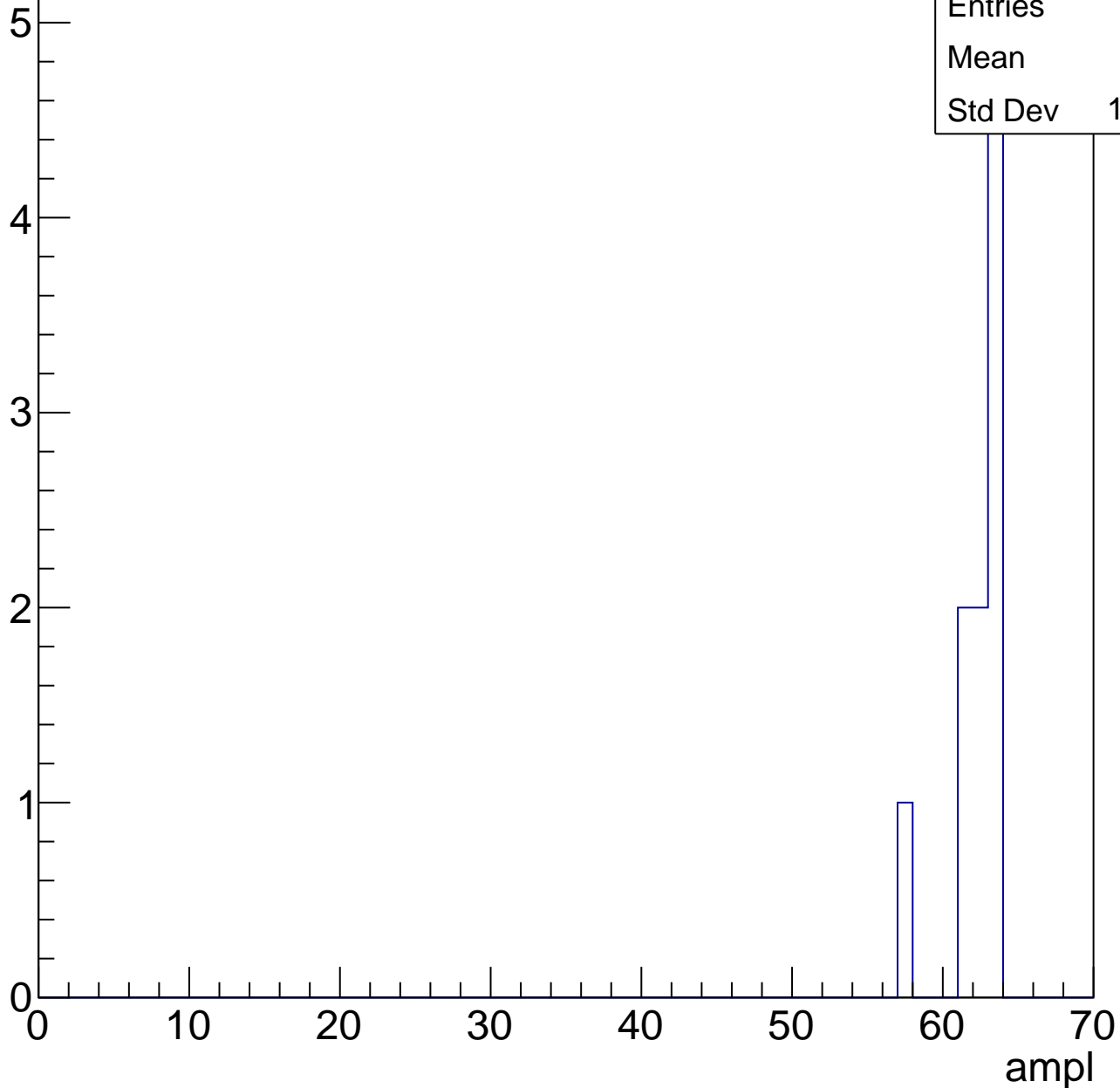


B0L102S, U20-ch106, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 61.8 |
| Std Dev | 1.778 |



B0L102S, U20-ch106, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch107, adc0

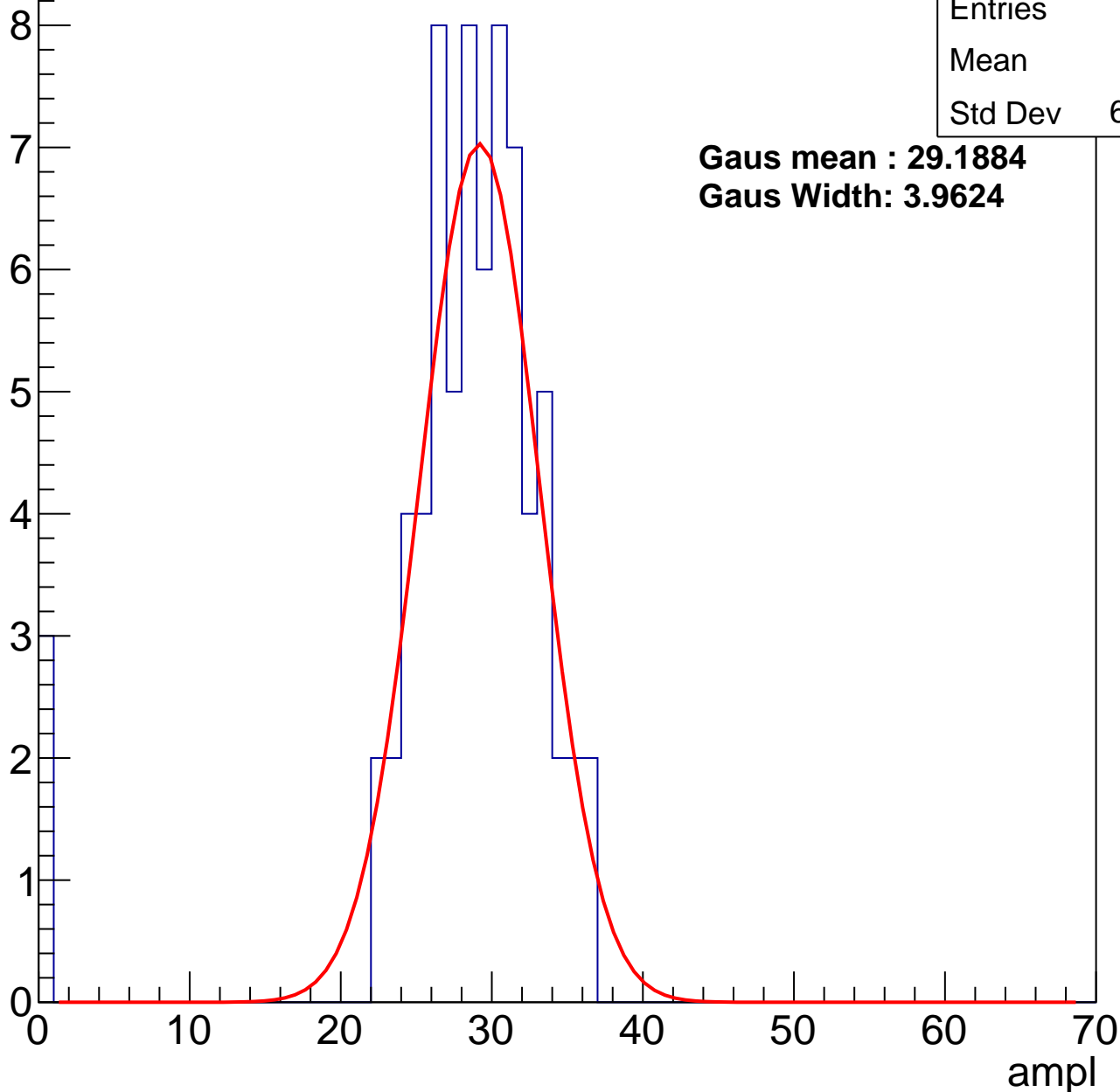
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 27.6 |
| Std Dev | 6.653 |

Gaus mean : 29.1884

Gaus Width: 3.9624



B0L102S, U20-ch107, adc1

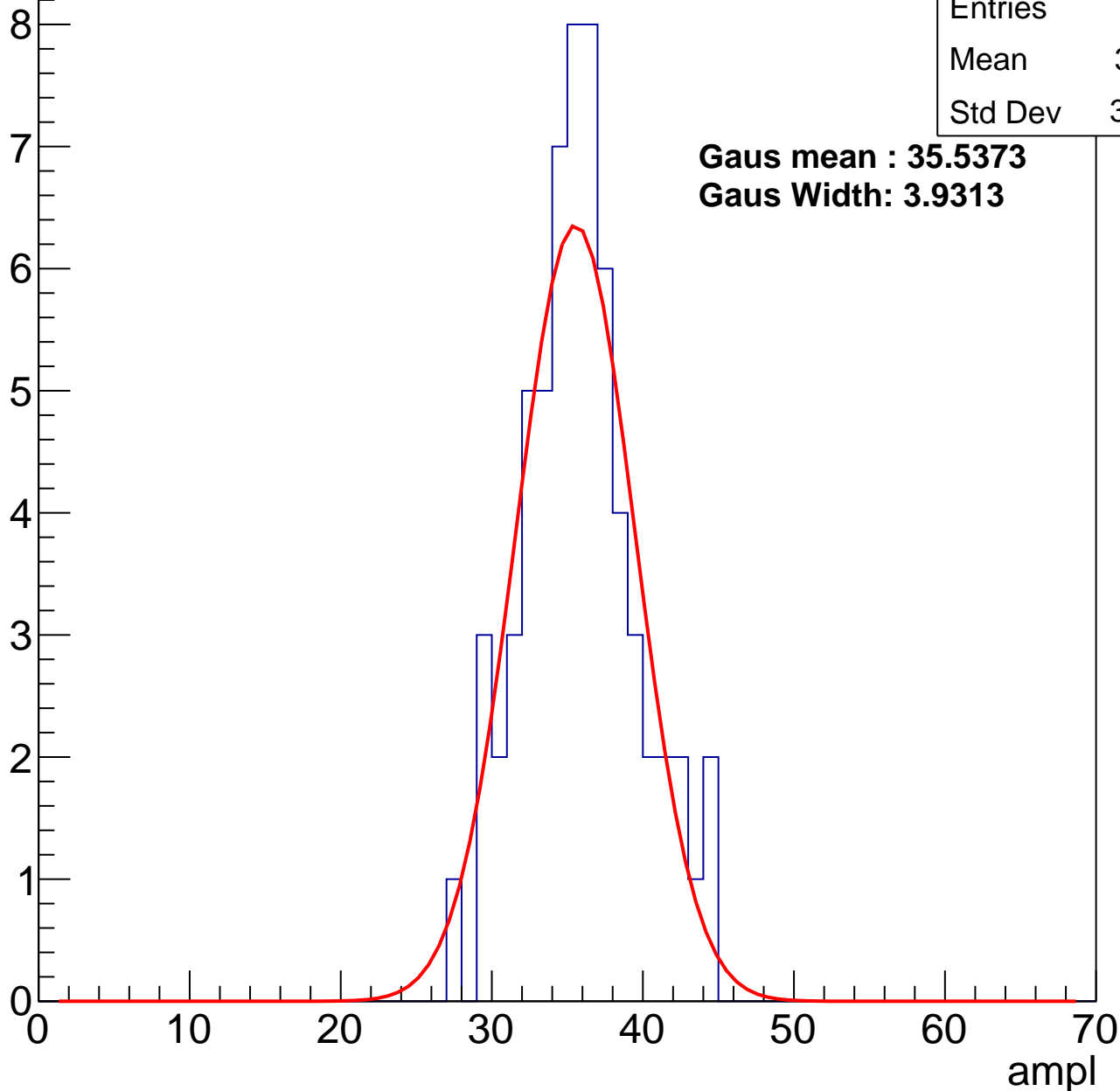
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.41 |
| Std Dev | 3.736 |

Gaus mean : 35.5373

Gaus Width: 3.9313



B0L102S, U20-ch107, adc2

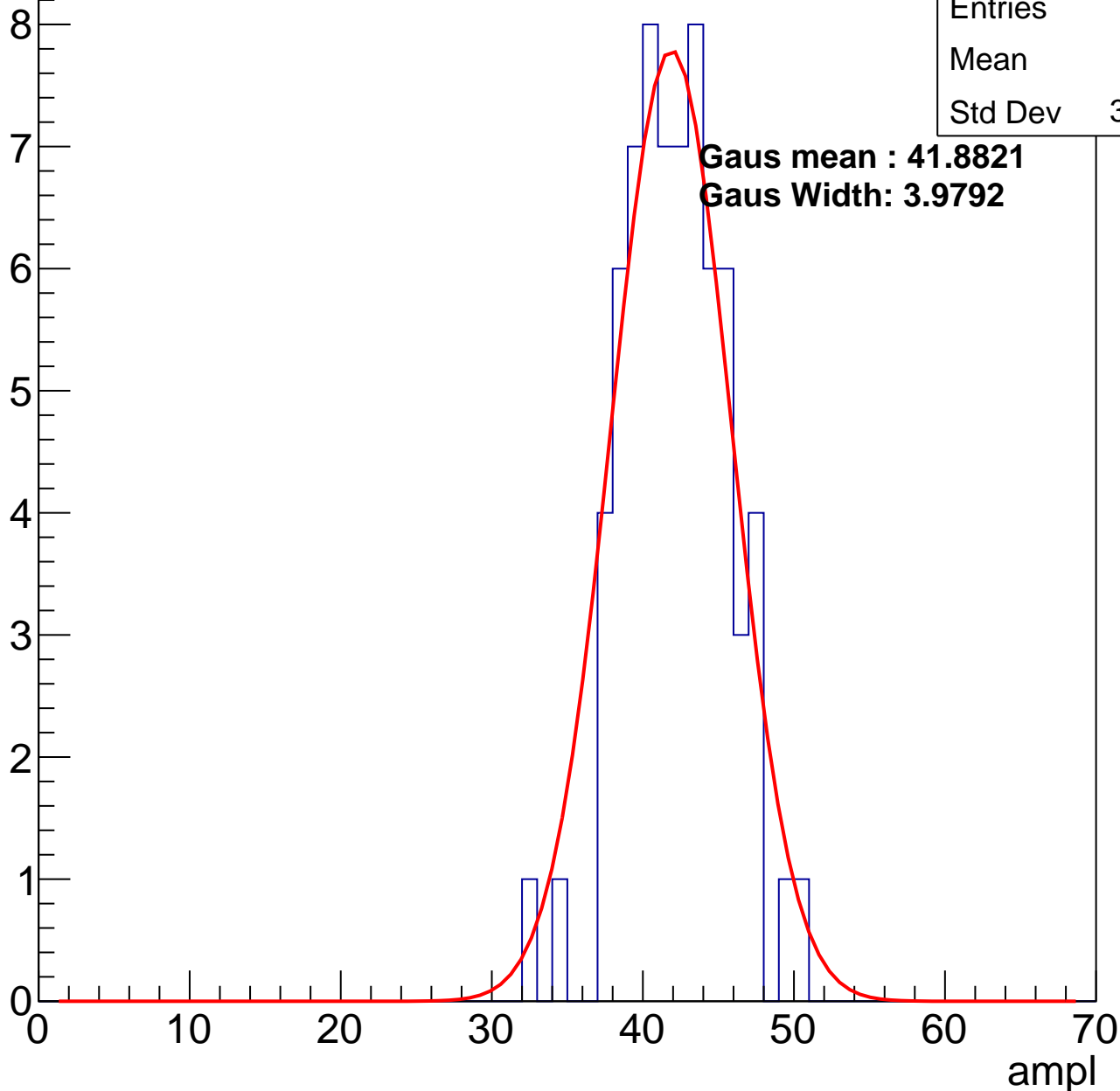
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 41.7 |
| Std Dev | 3.386 |

Gaus mean : 41.8821

Gaus Width: 3.9792



B0L102S, U20-ch107, adc3

calib_packv5_042523_0143.root, FC#12, port B1

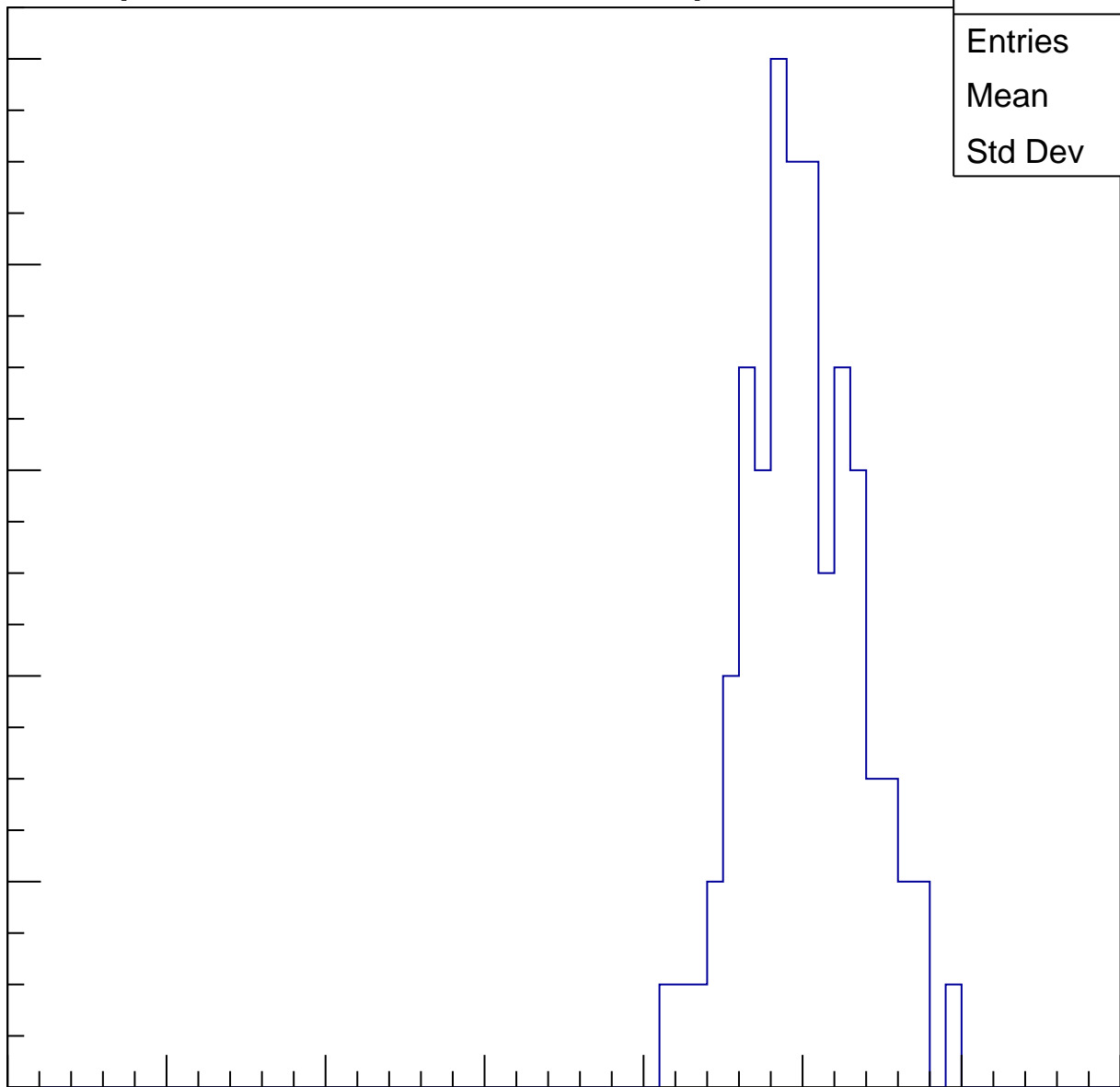
| | |
|---------|-------|
| Entries | 79 |
| Mean | 49.59 |
| Std Dev | 3.609 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

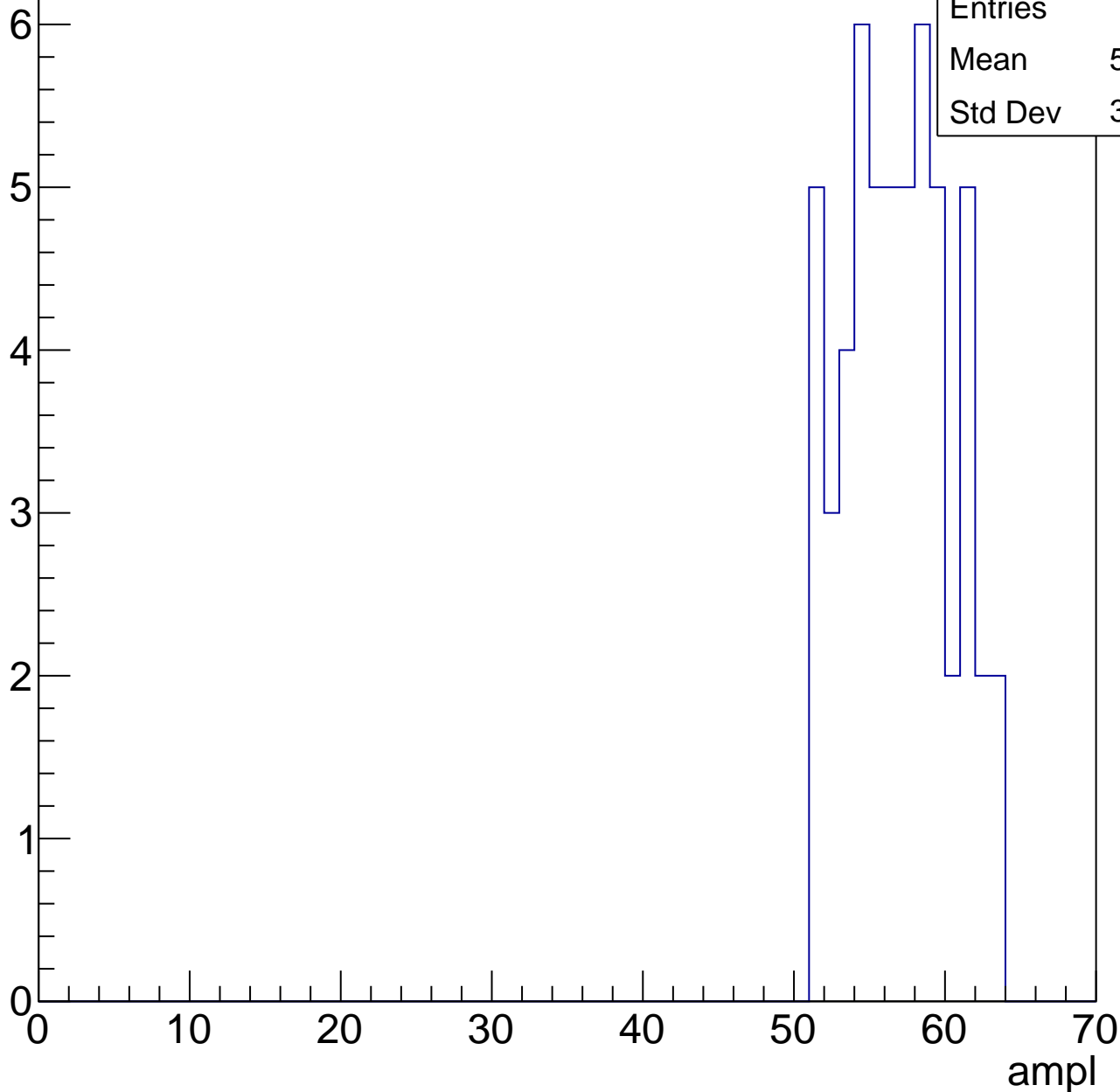


B0L102S, U20-ch107, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 56.45 |
| Std Dev | 3.378 |

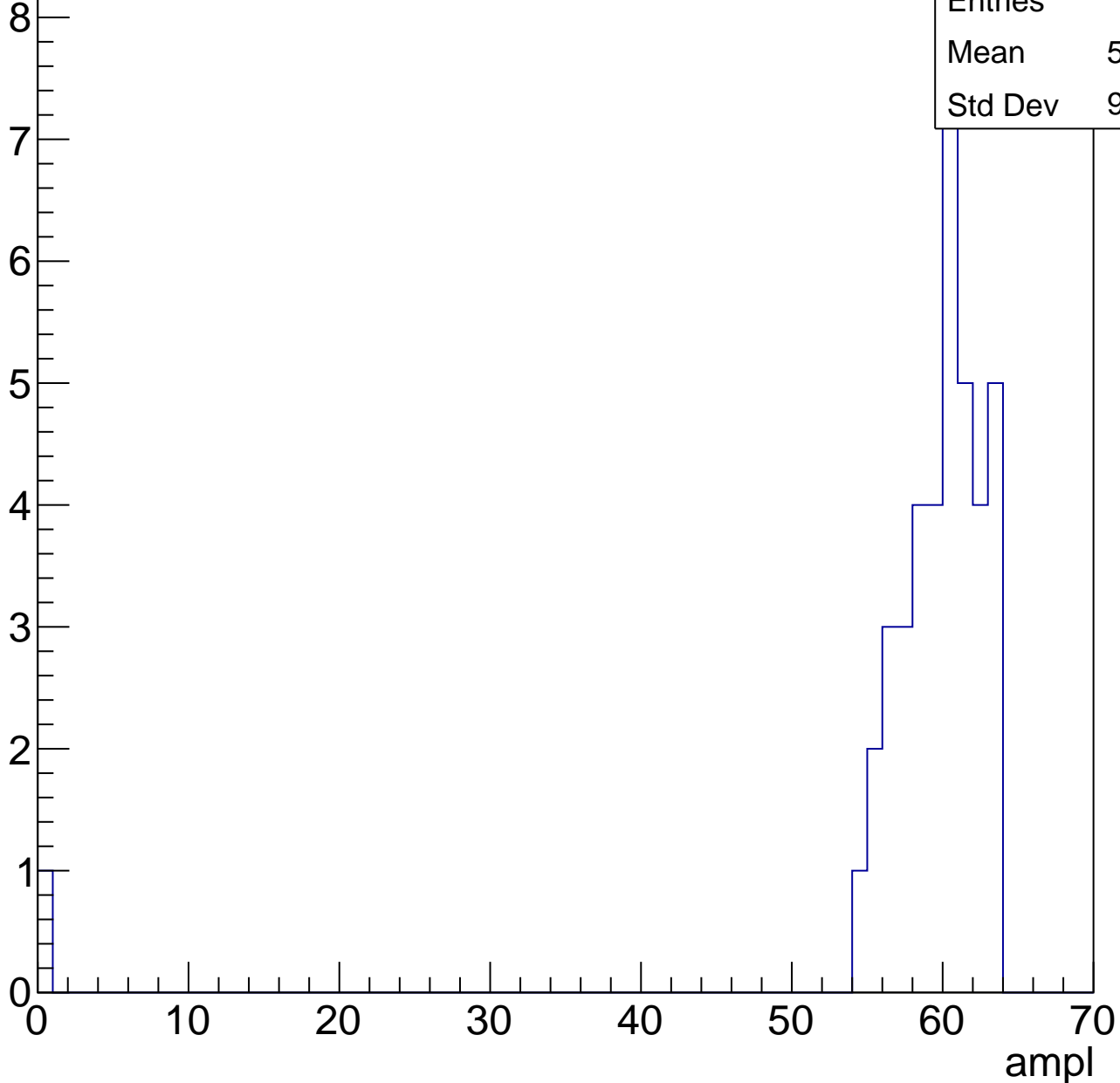


B0L102S, U20-ch107, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

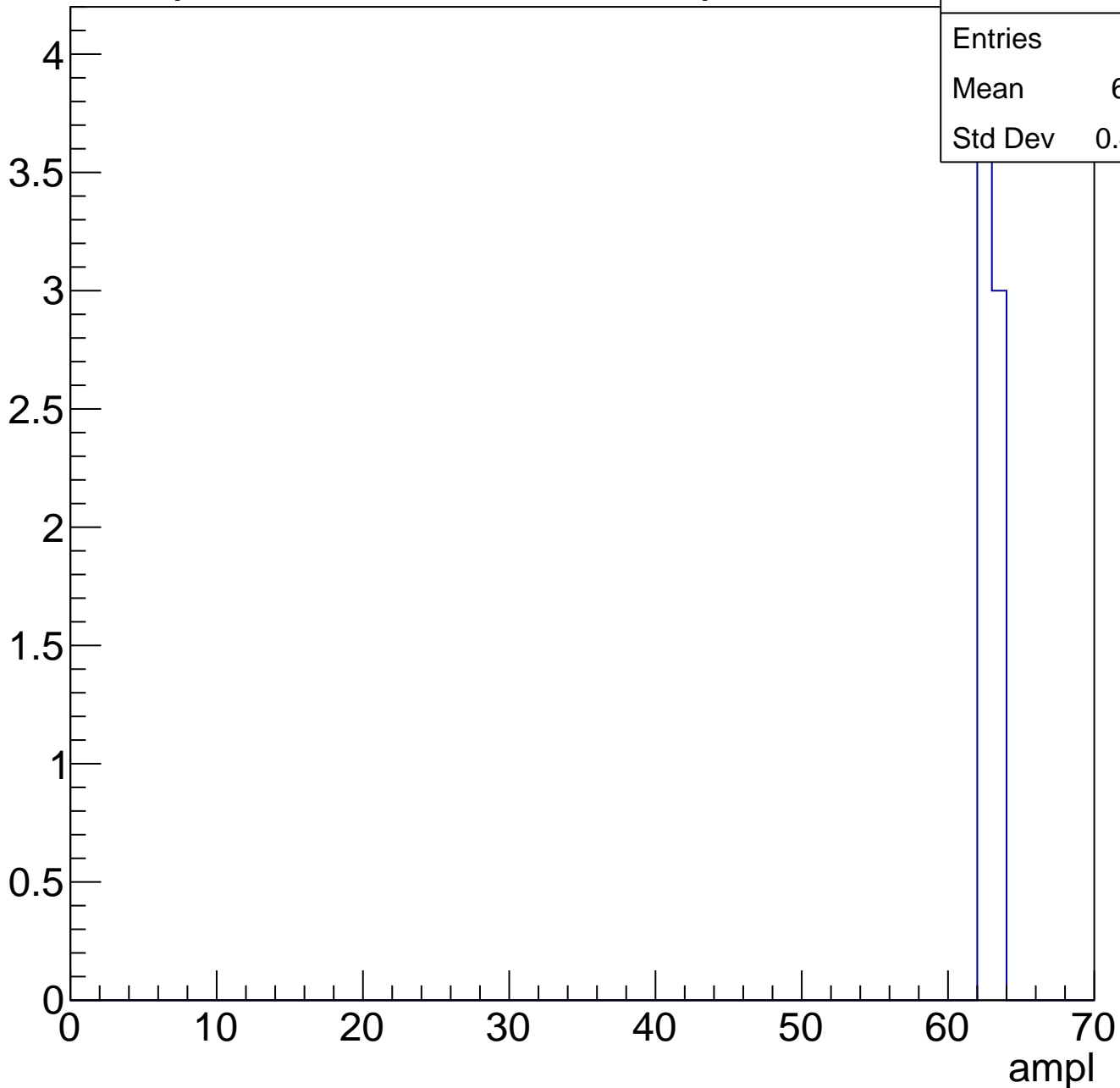
| | |
|---------|-------|
| Entries | 40 |
| Mean | 57.98 |
| Std Dev | 9.596 |



B0L102S, U20-ch107, adc6

calib_packv5_042523_0143.root, FC#12, port B1

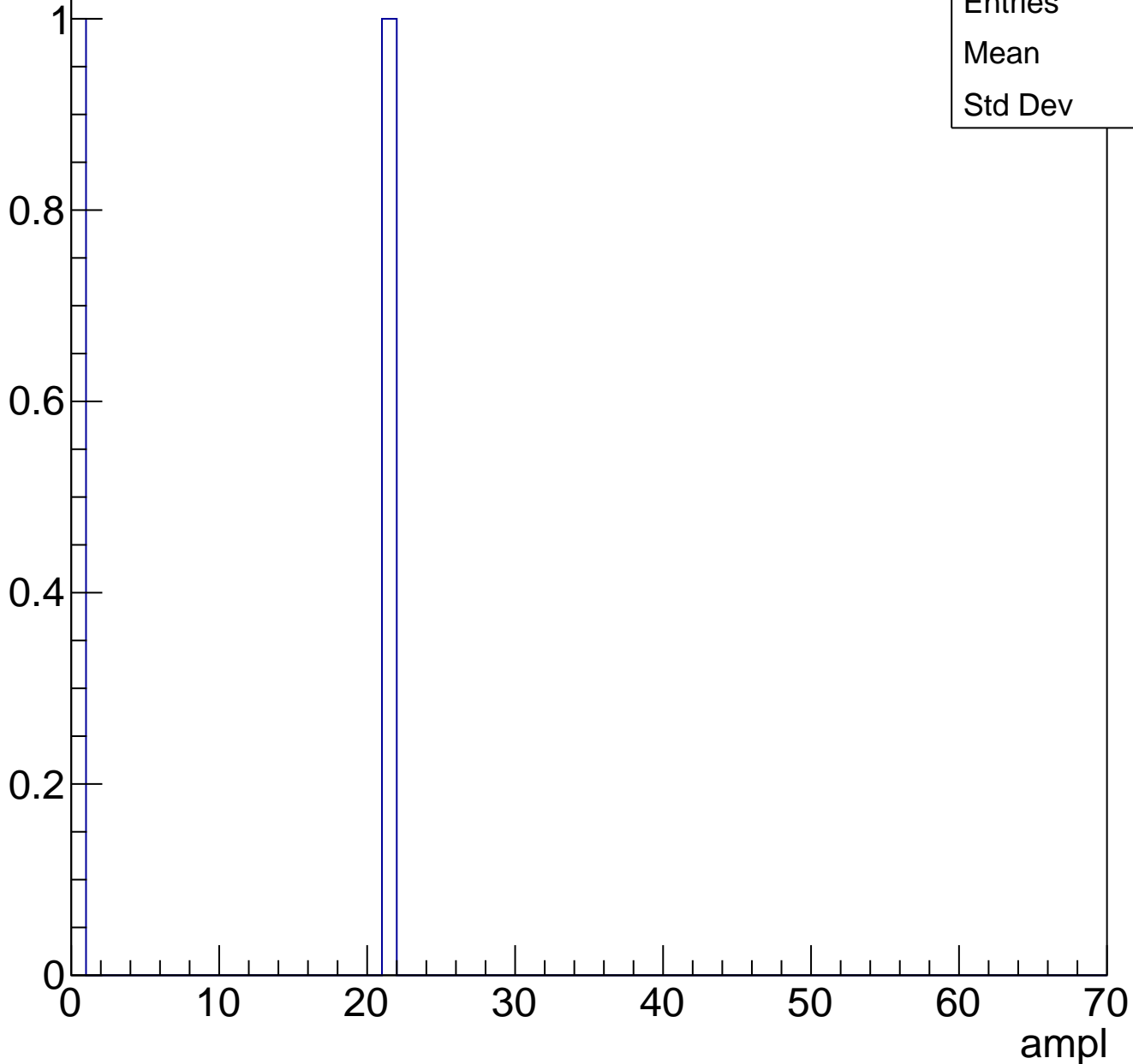
Entry



B0L102S, U20-ch107, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch108, adc0

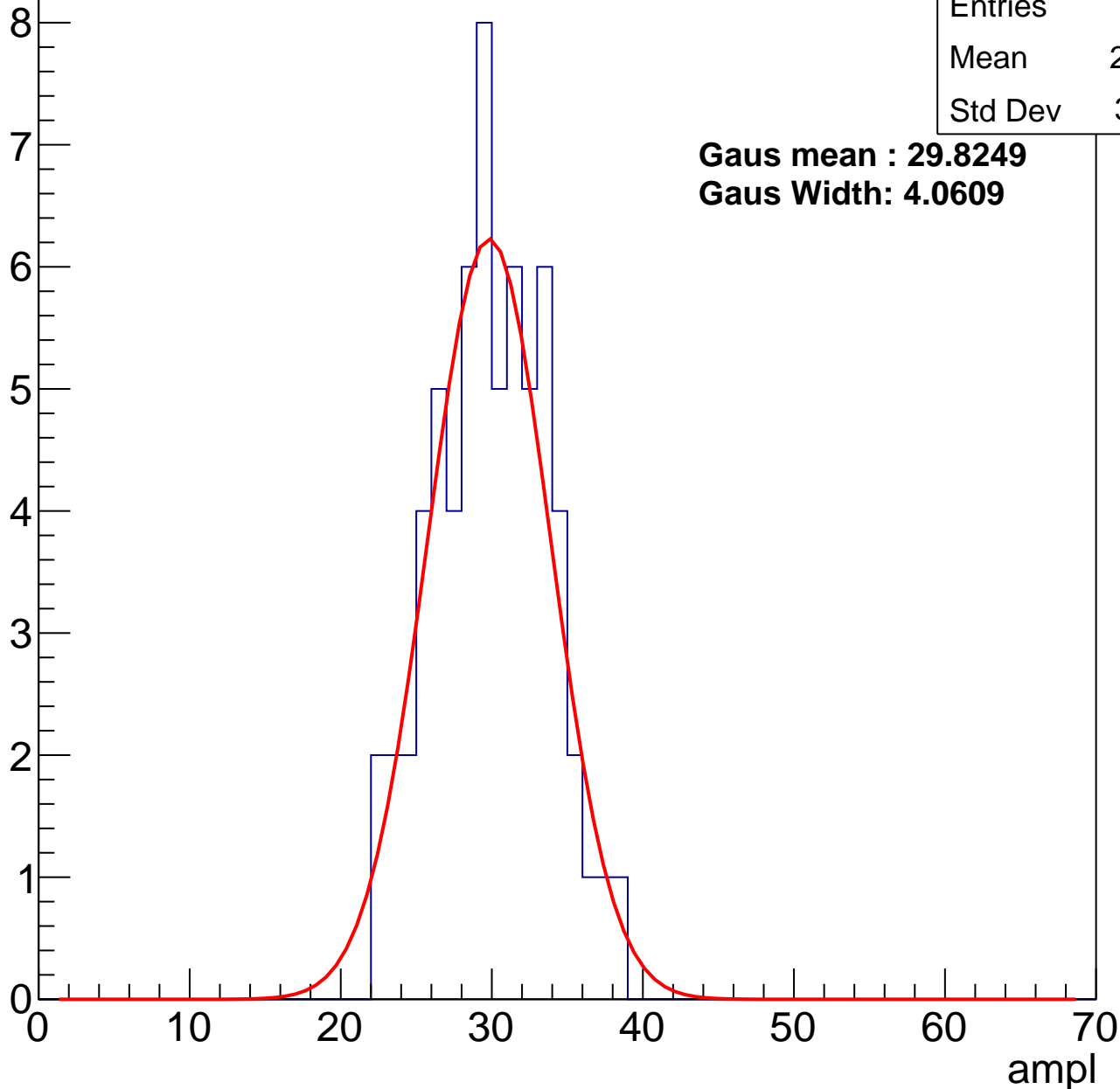
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.48 |
| Std Dev | 3.691 |

Gaus mean : 29.8249

Gaus Width: 4.0609



B0L102S, U20-ch108, adc1

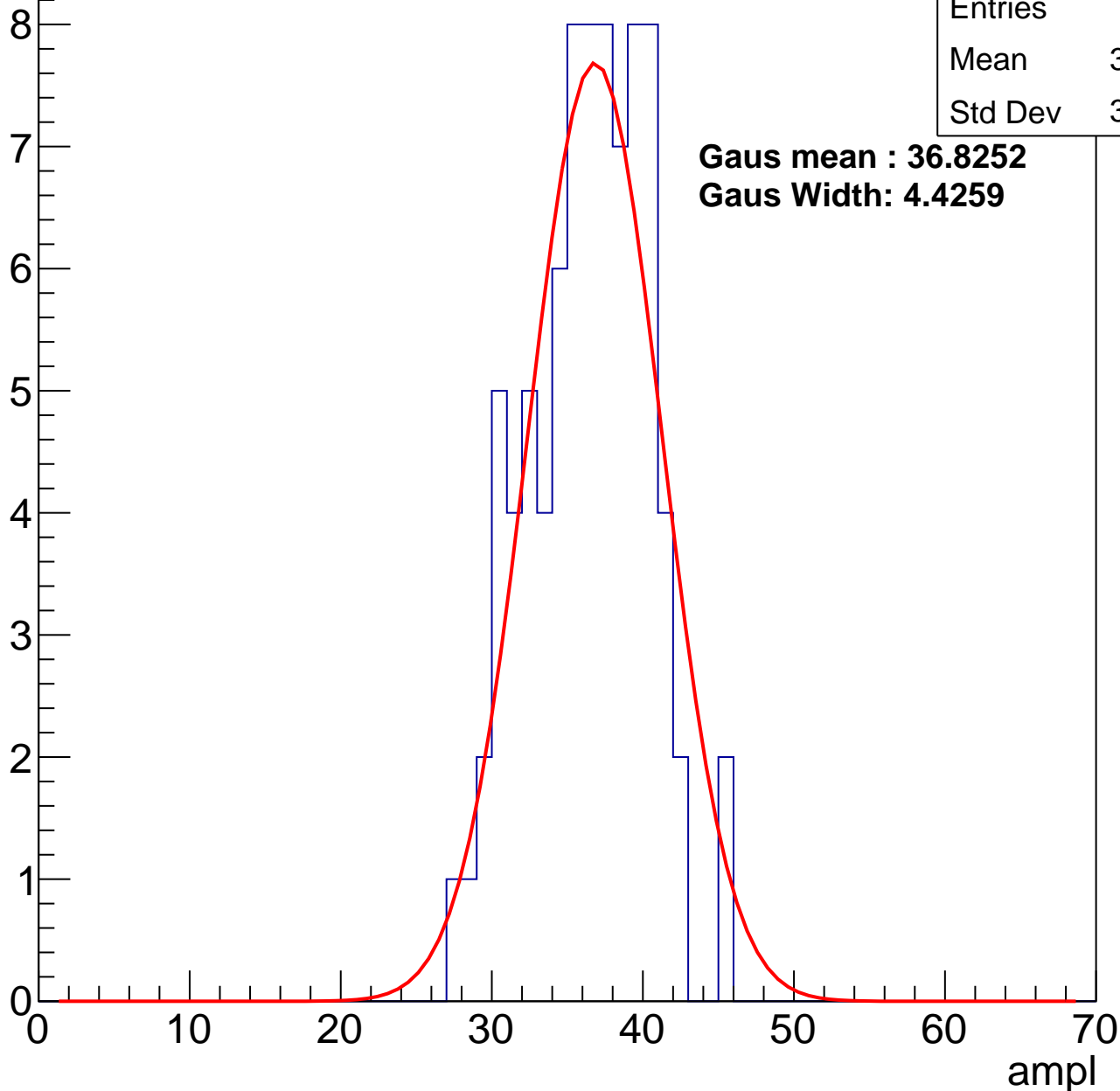
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 35.94 |
| Std Dev | 3.866 |

Gaus mean : 36.8252

Gaus Width: 4.4259



B0L102S, U20-ch108, adc2

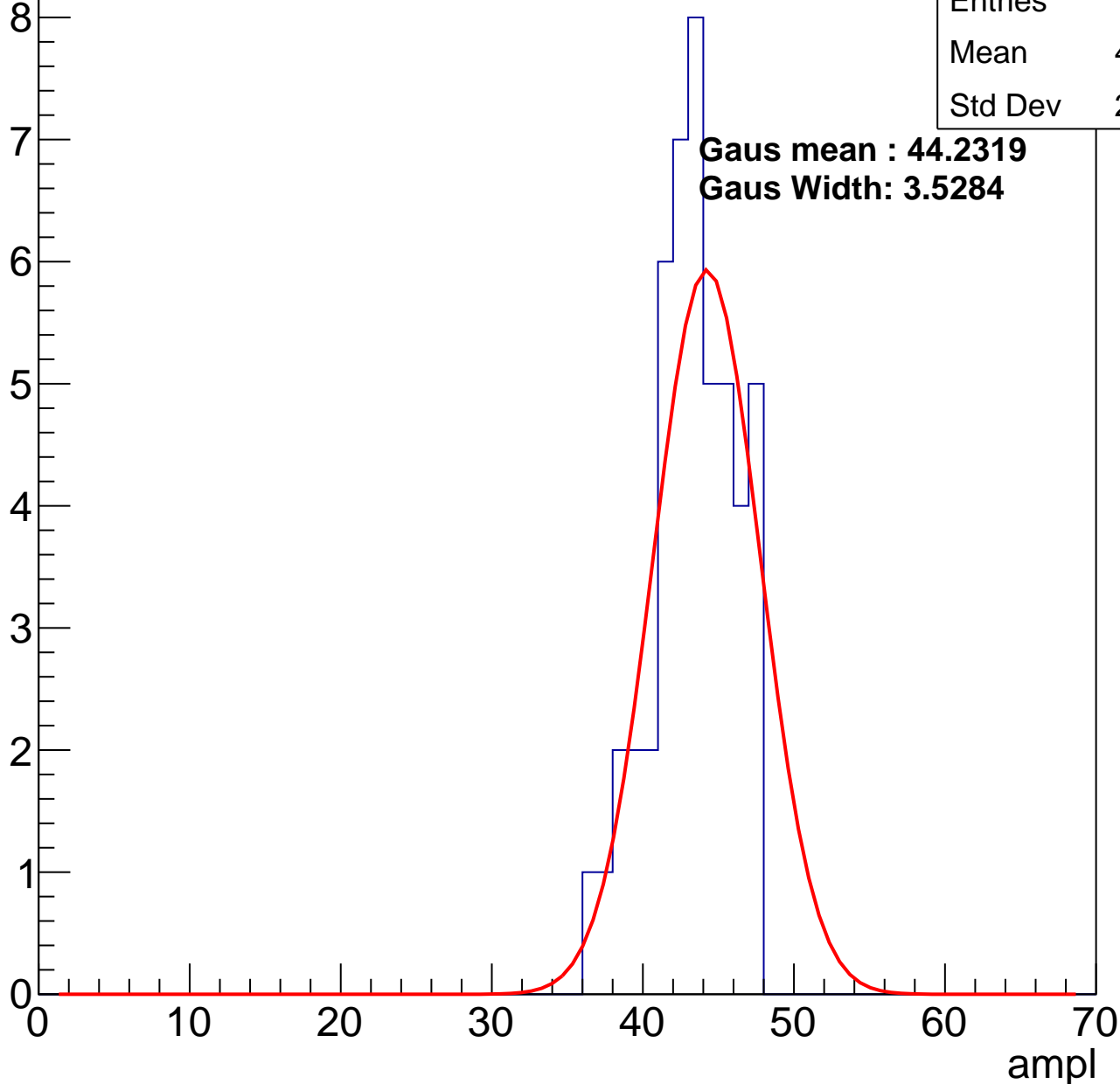
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 42.81 |
| Std Dev | 2.721 |

Gaus mean : 44.2319

Gaus Width: 3.5284

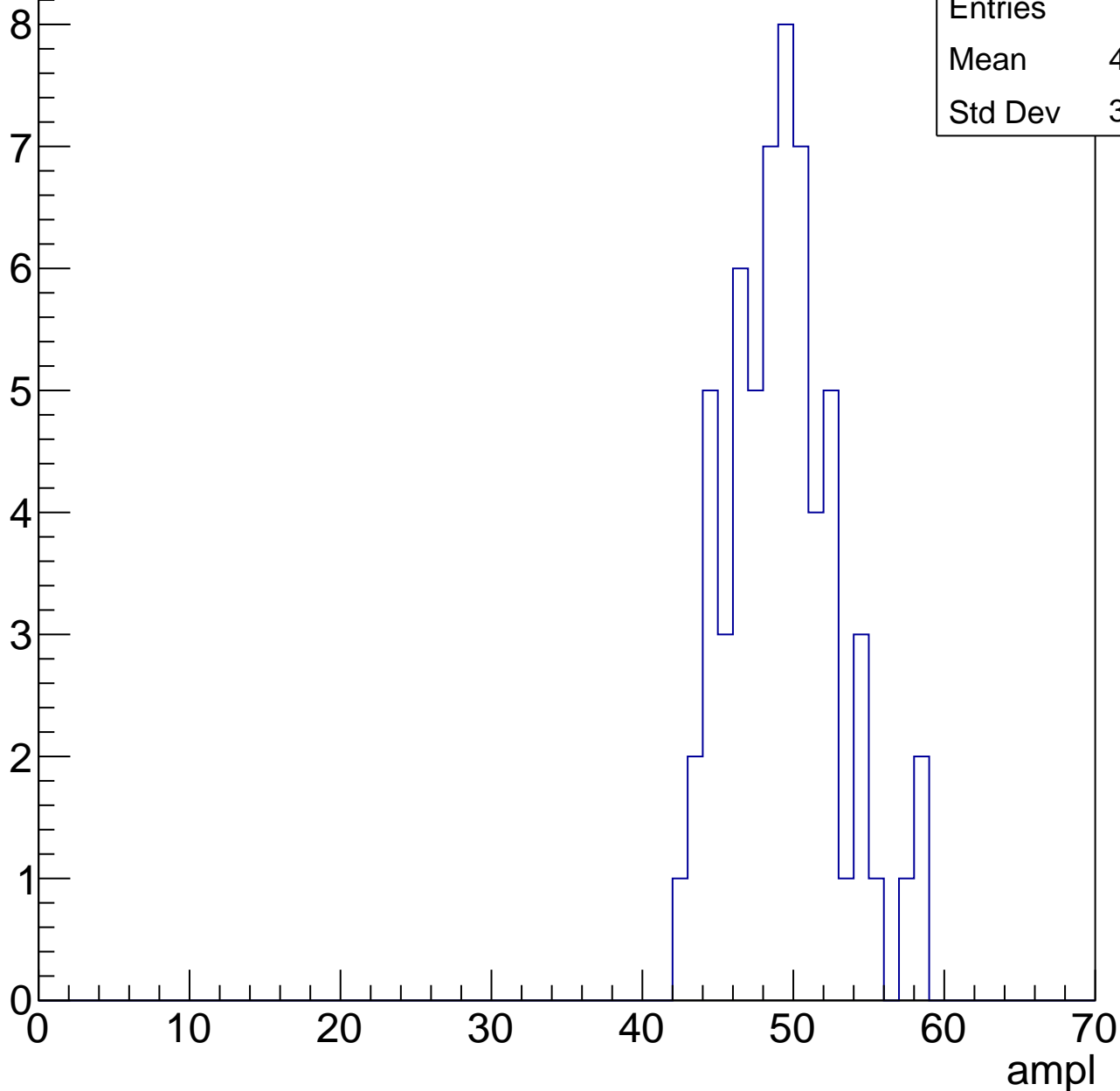


B0L102S, U20-ch108, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 48.84 |
| Std Dev | 3.609 |

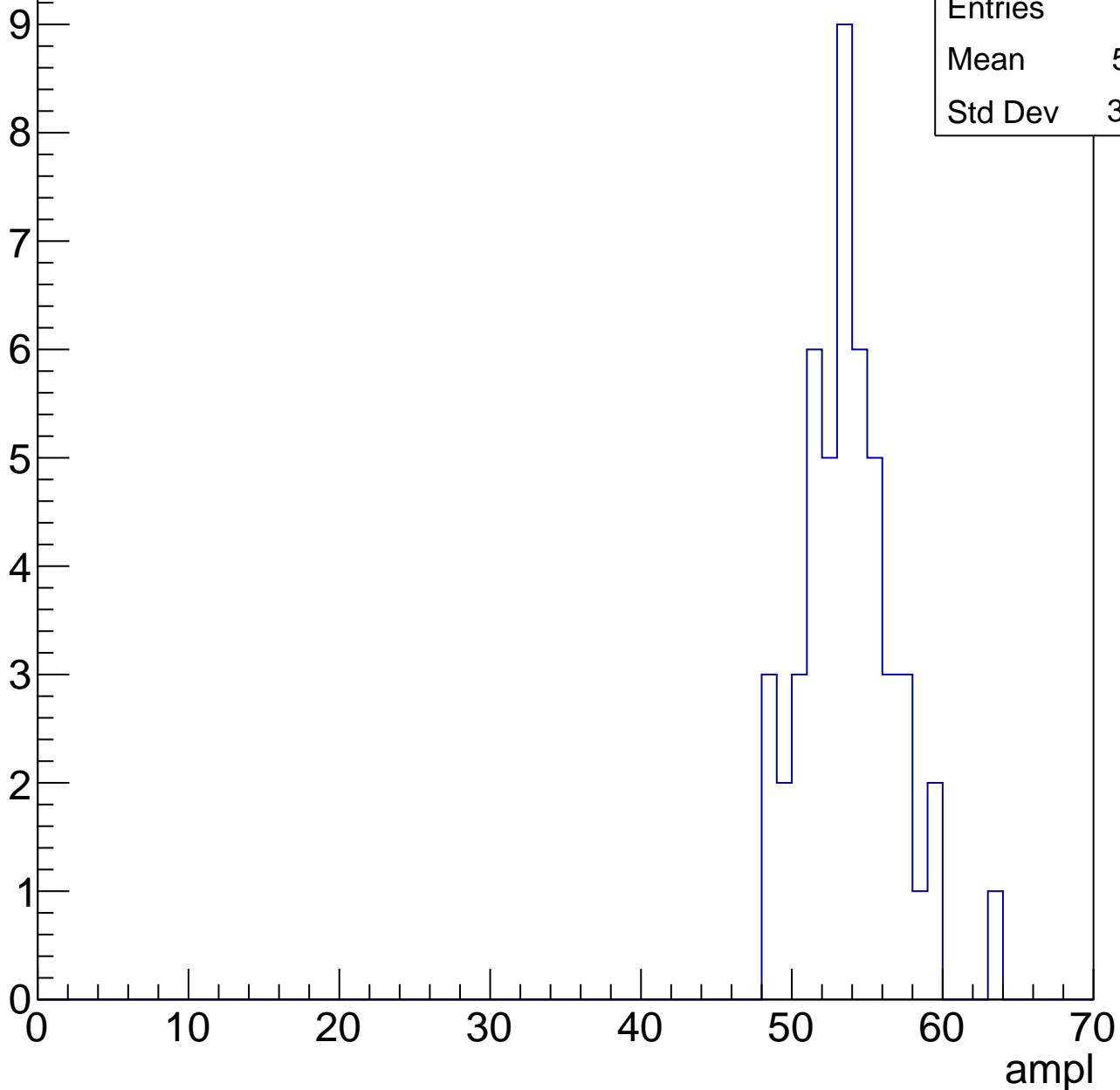


B0L102S, U20-ch108, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 53.31 |
| Std Dev | 3.052 |

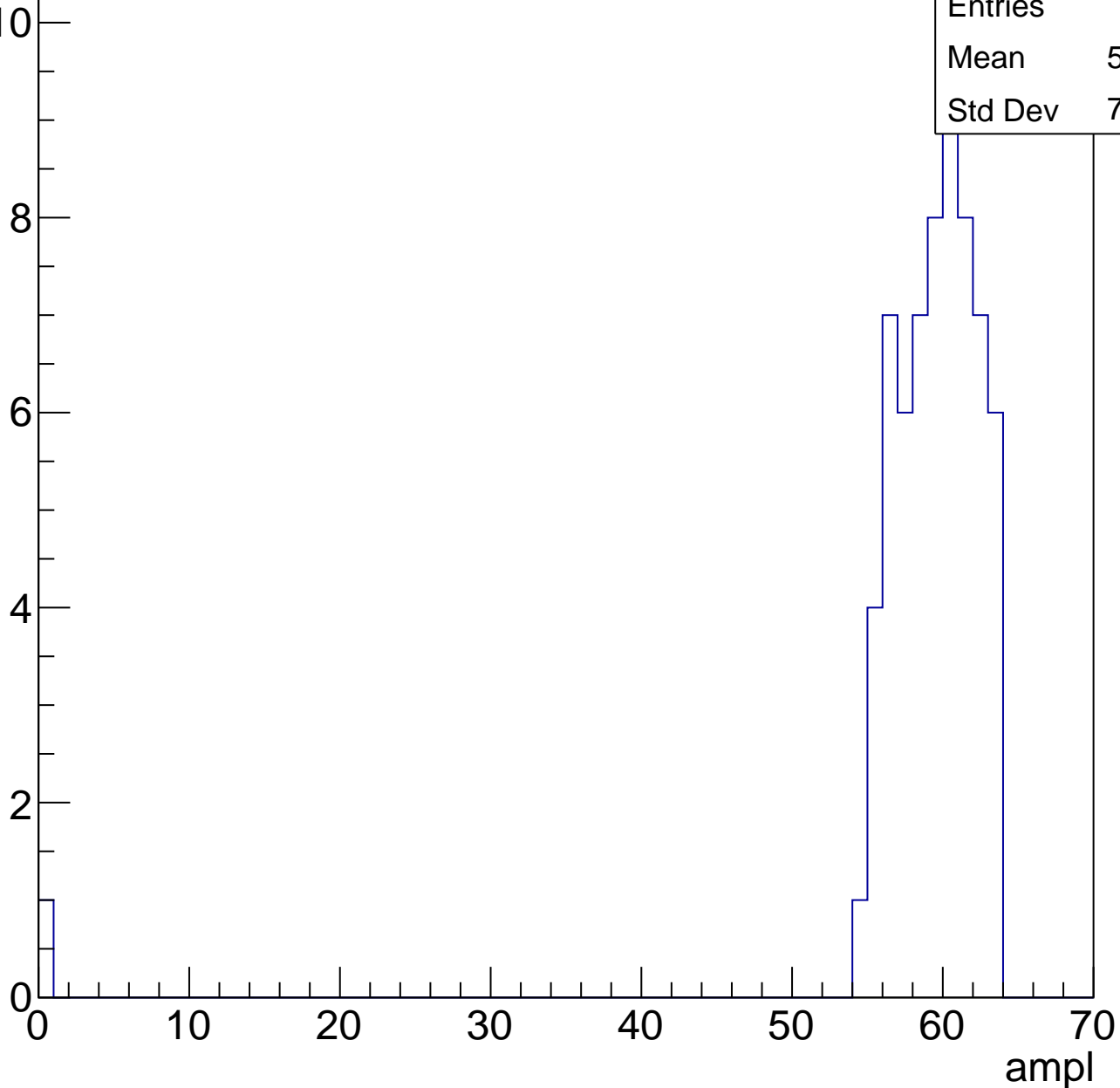


B0L102S, U20-ch108, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

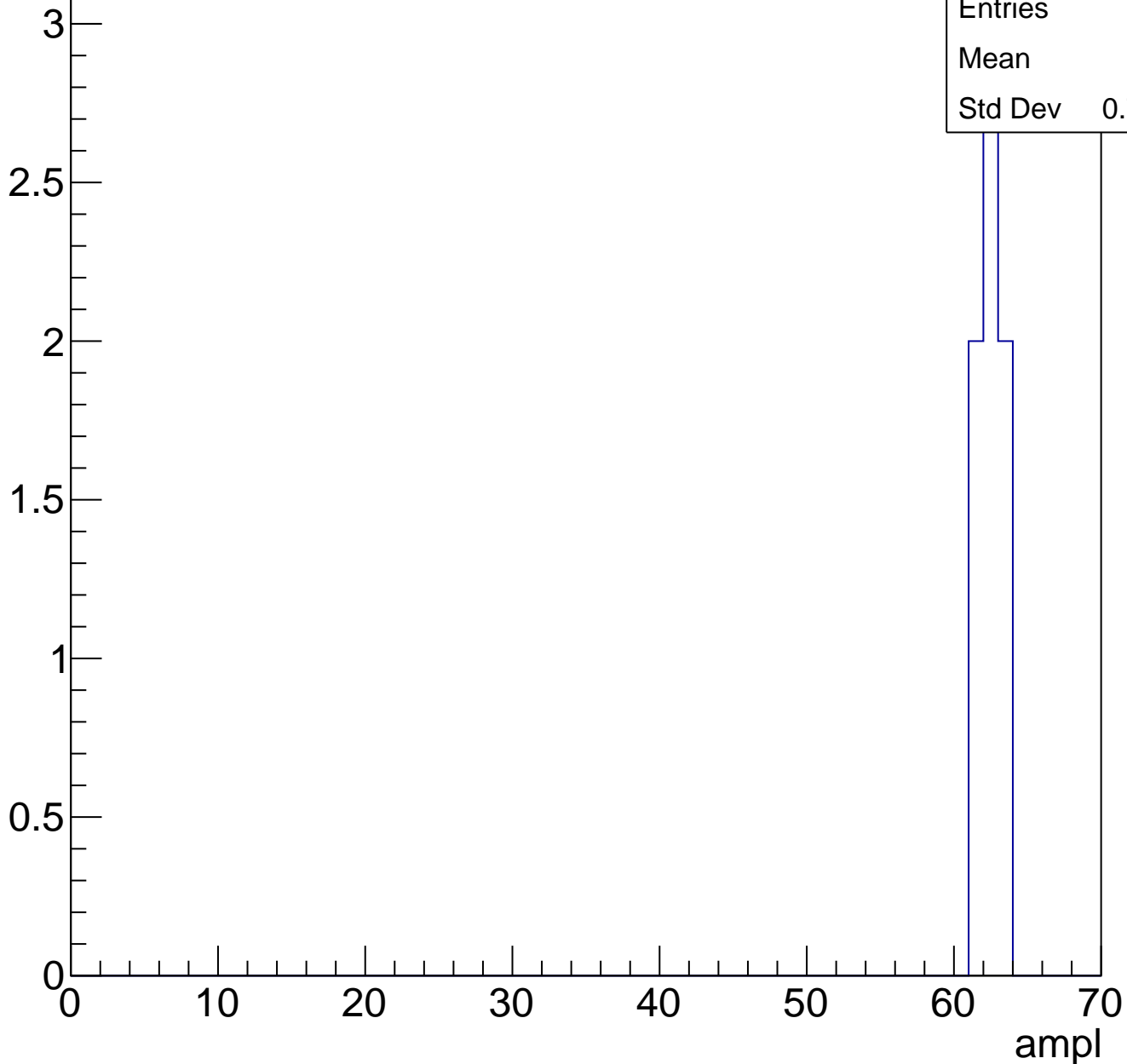
| | |
|---------|-------|
| Entries | 65 |
| Mean | 58.25 |
| Std Dev | 7.674 |



B0L102S, U20-ch108, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch108, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch109, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 83 |
| Mean | 29.86 |
| Std Dev | 4.84 |

Gaus mean : 30.0222

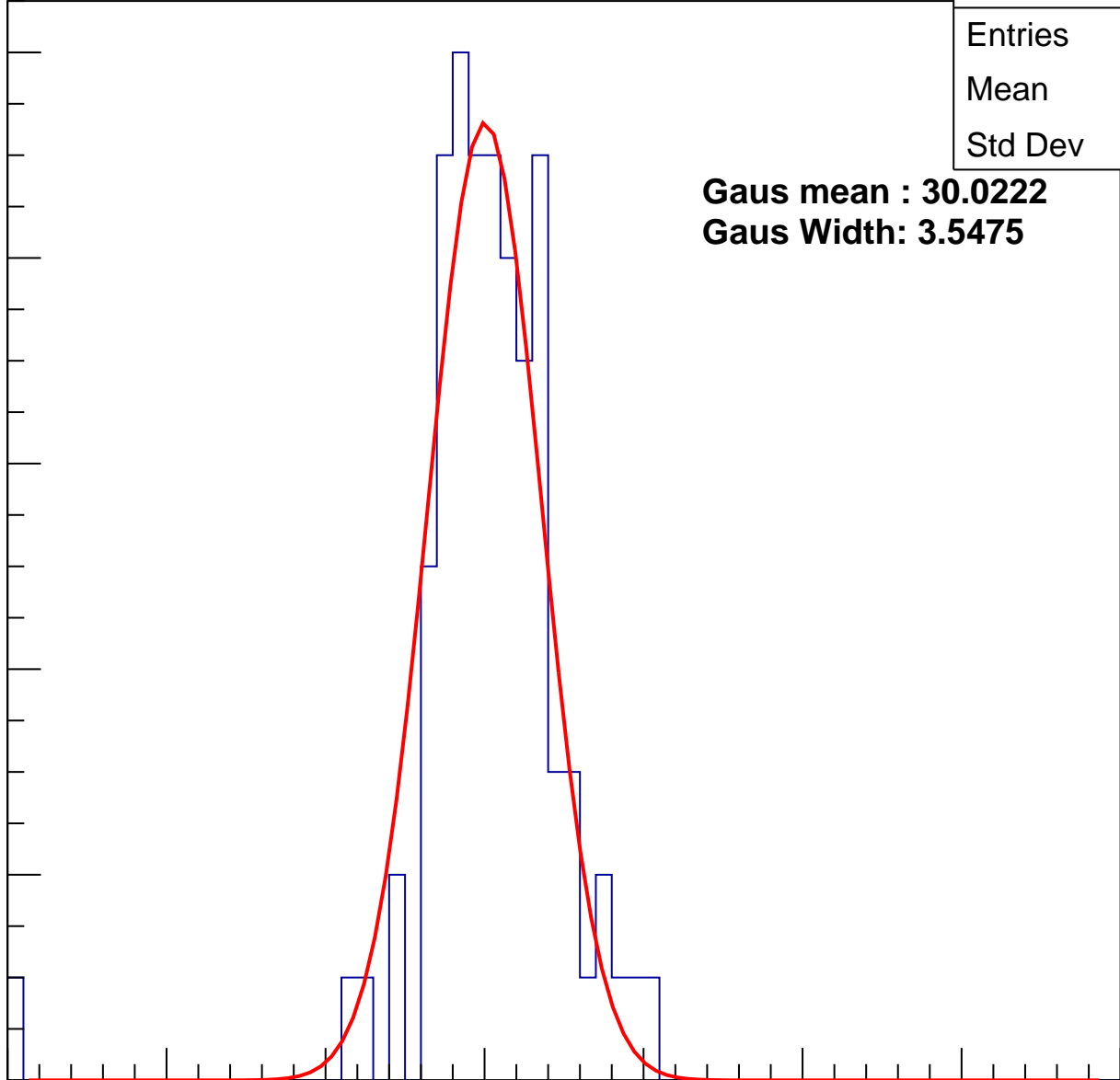
Gaus Width: 3.5475

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch109, adc1

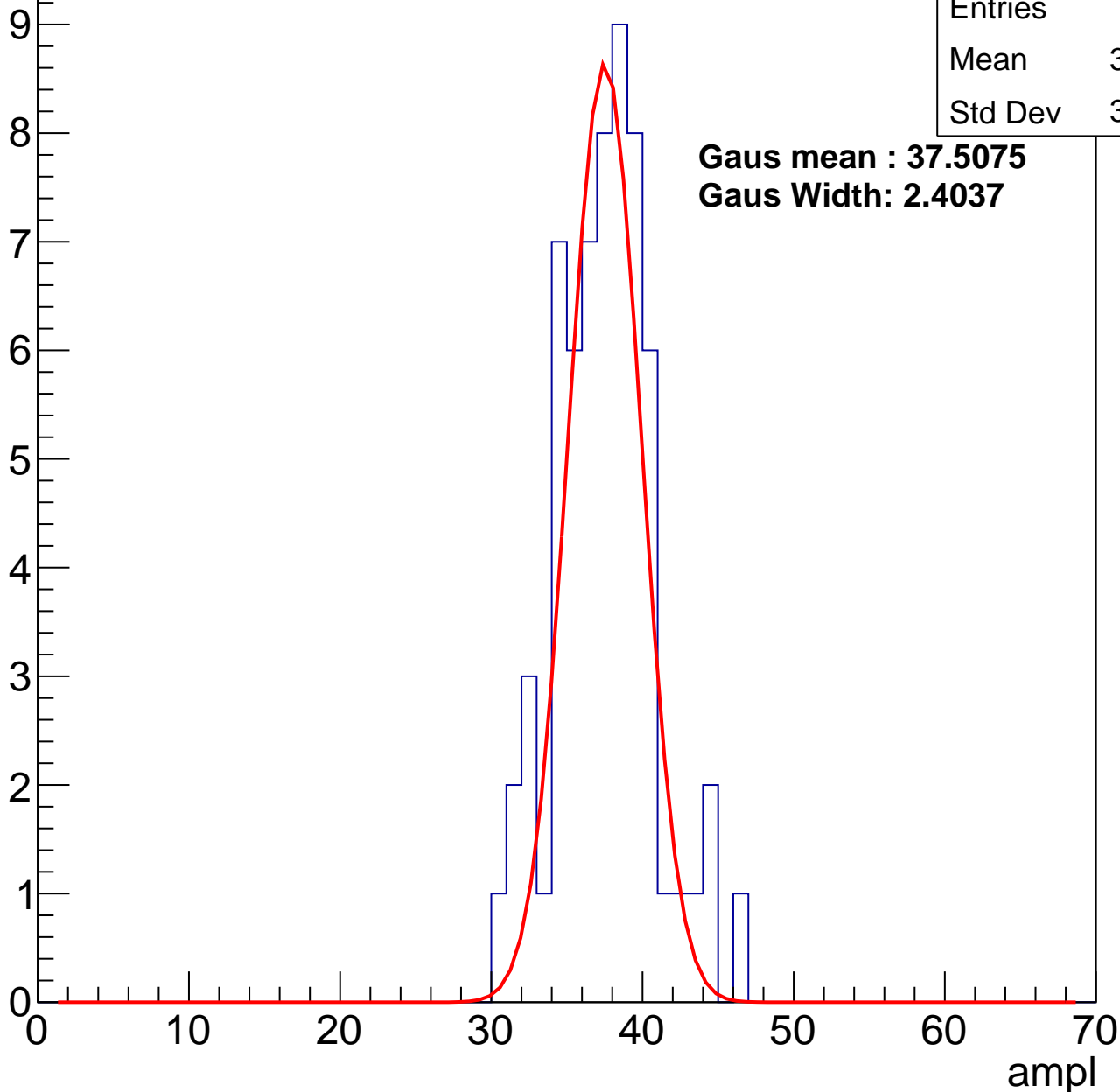
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37.05 |
| Std Dev | 3.204 |

Gaus mean : 37.5075

Gaus Width: 2.4037



B0L102S, U20-ch109, adc2

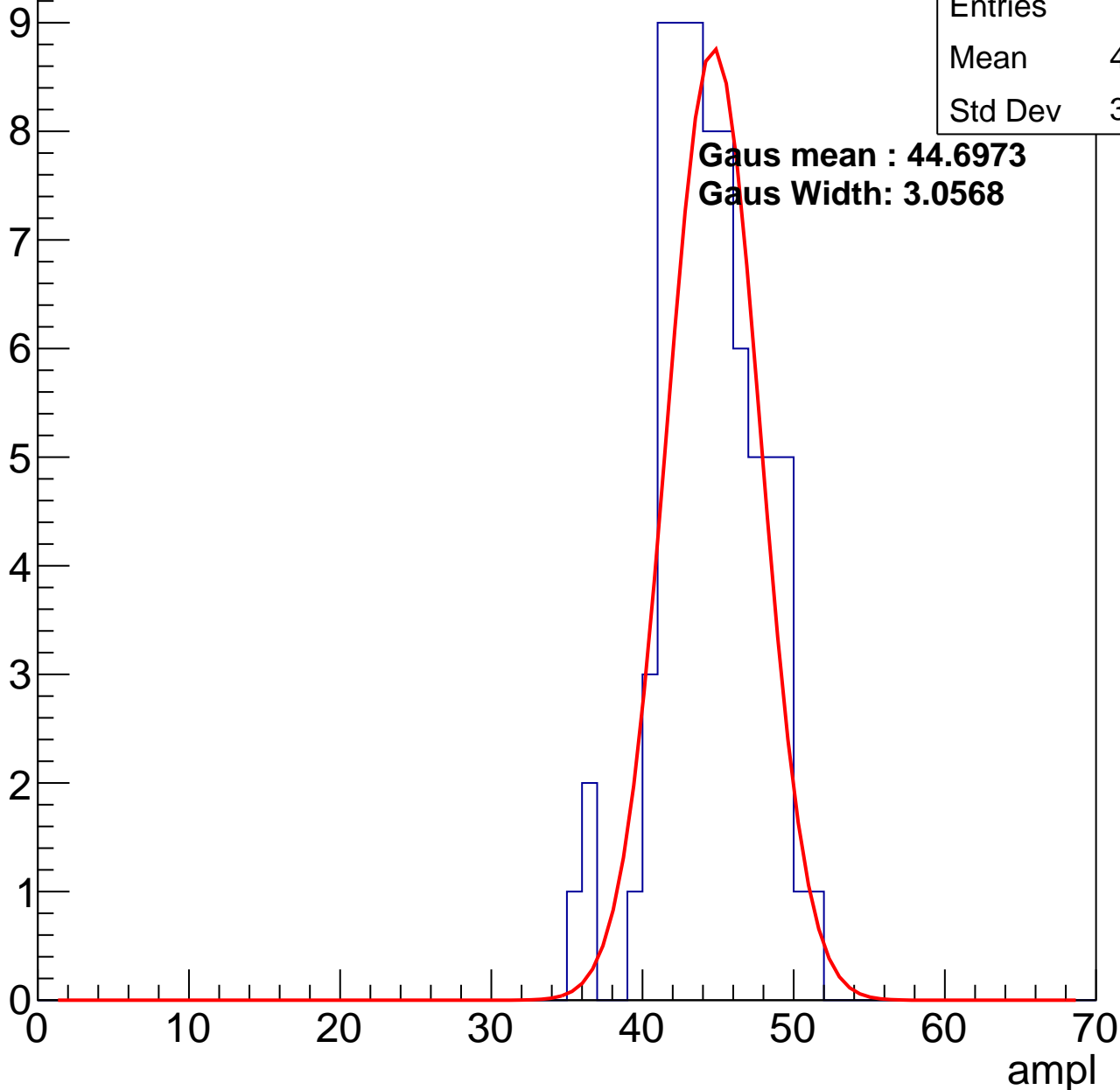
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.96 |
| Std Dev | 3.258 |

Gaus mean : 44.6973

Gaus Width: 3.0568

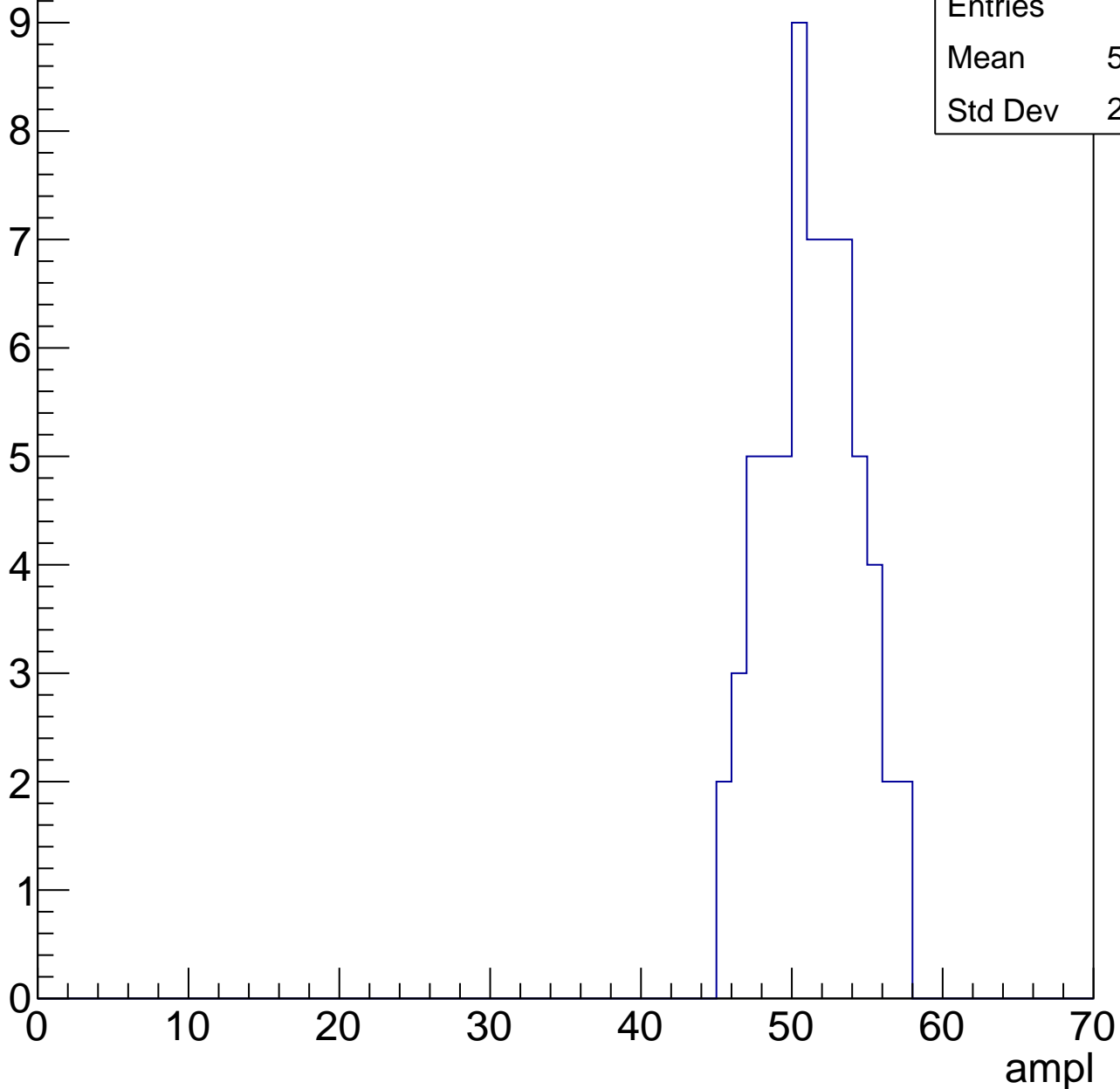


B0L102S, U20-ch109, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

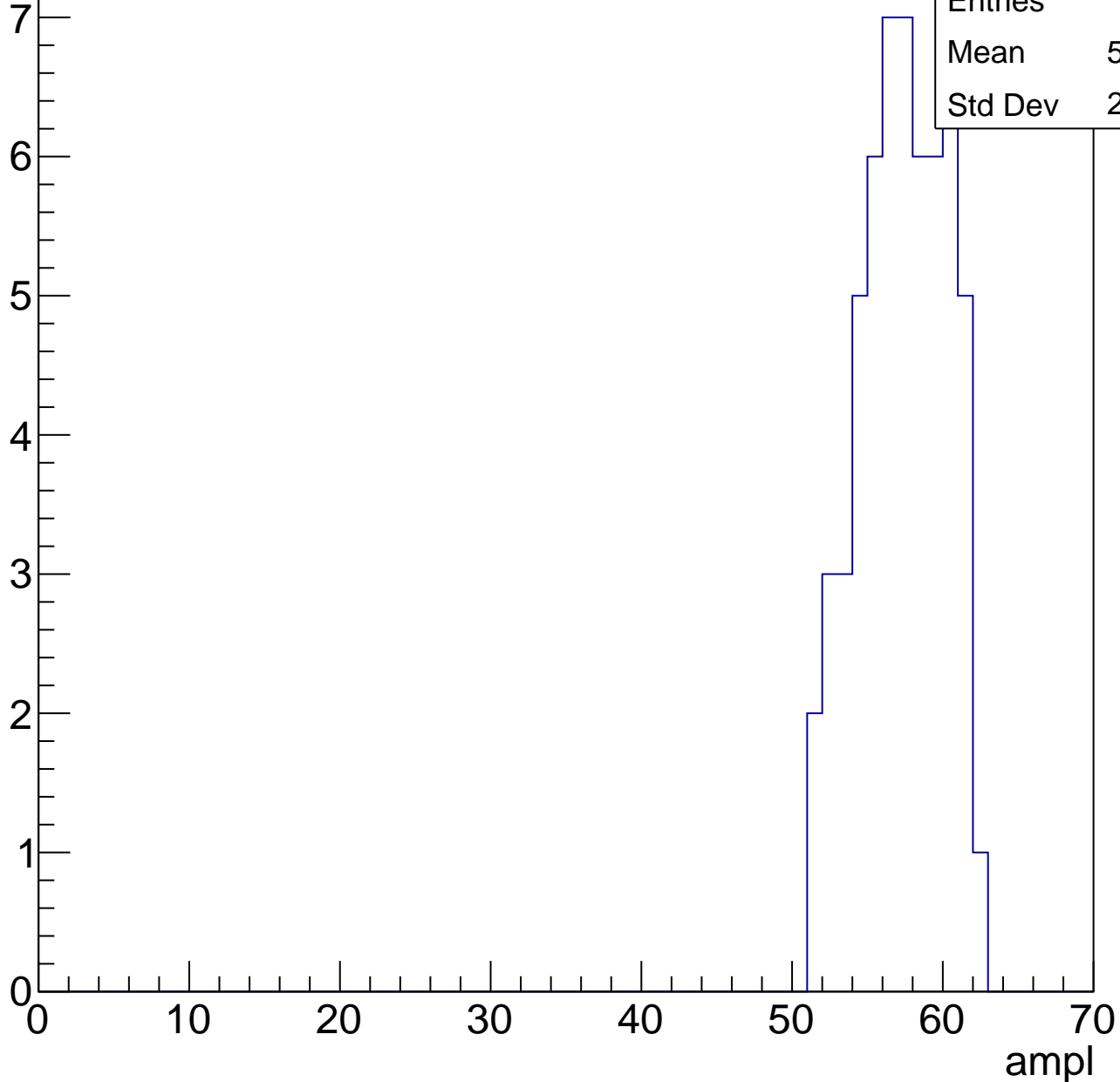
| | |
|---------|-------|
| Entries | 63 |
| Mean | 50.89 |
| Std Dev | 2.998 |



B0L102S, U20-ch109, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

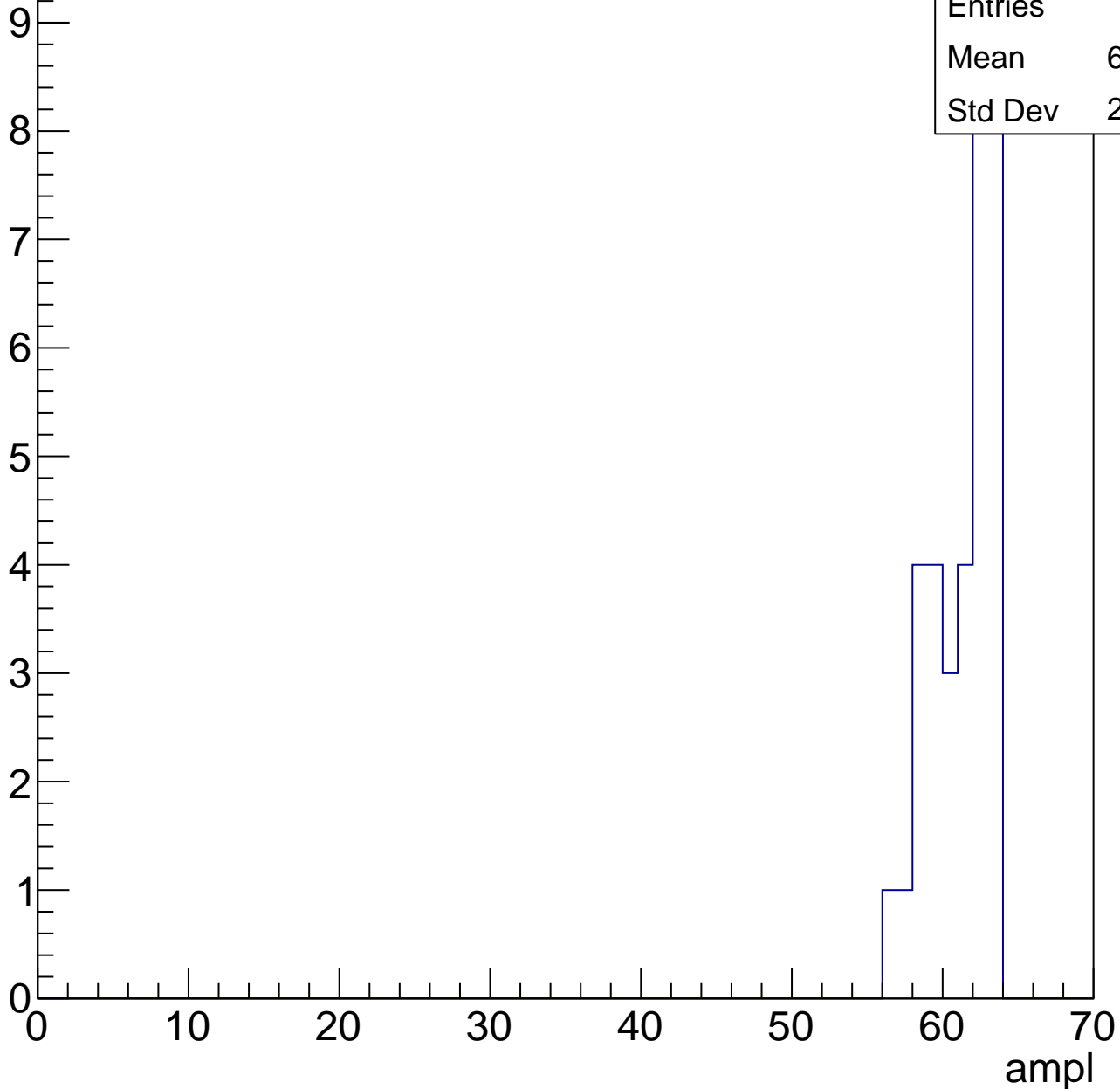


B0L102S, U20-ch109, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 60.82 |
| Std Dev | 2.022 |



B0L102S, U20-ch109, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch109, adc7

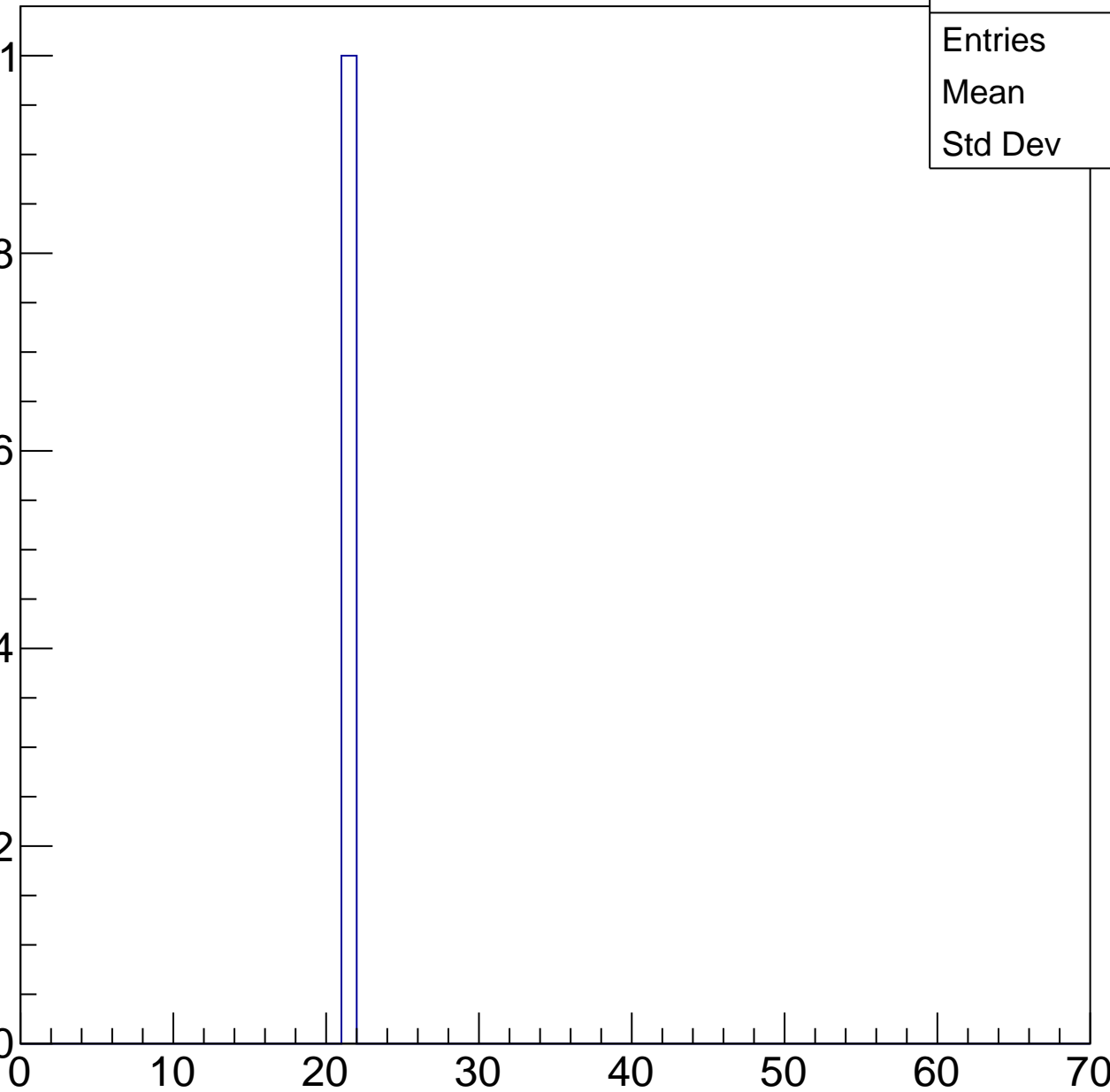
calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl



B0L102S, U20-ch110, adc0

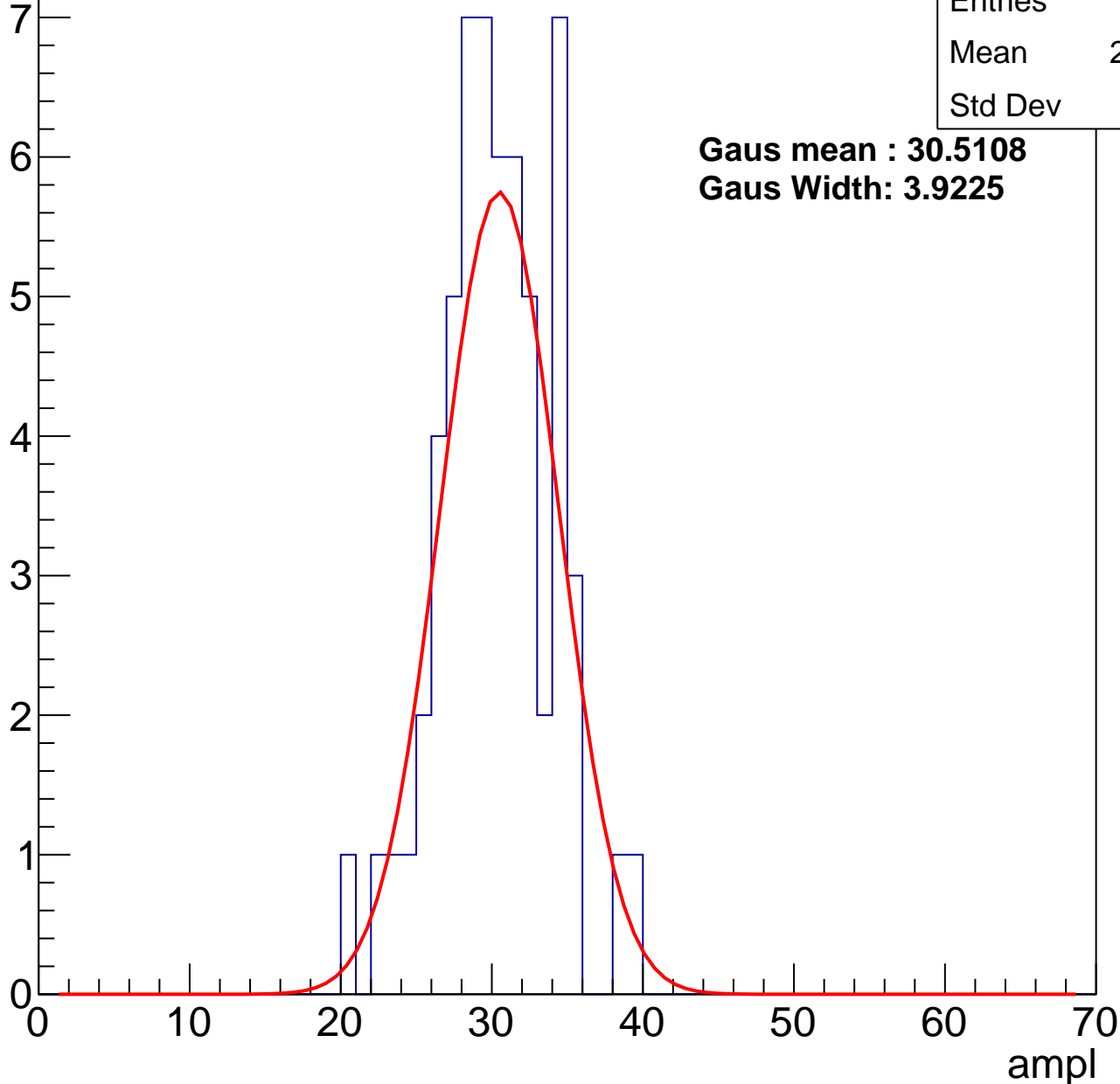
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 29.82 |
| Std Dev | 3.69 |

Gaus mean : 30.5108

Gaus Width: 3.9225



B0L102S, U20-ch110, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.77 |
| Std Dev | 3.619 |

Gaus mean : 37.3109

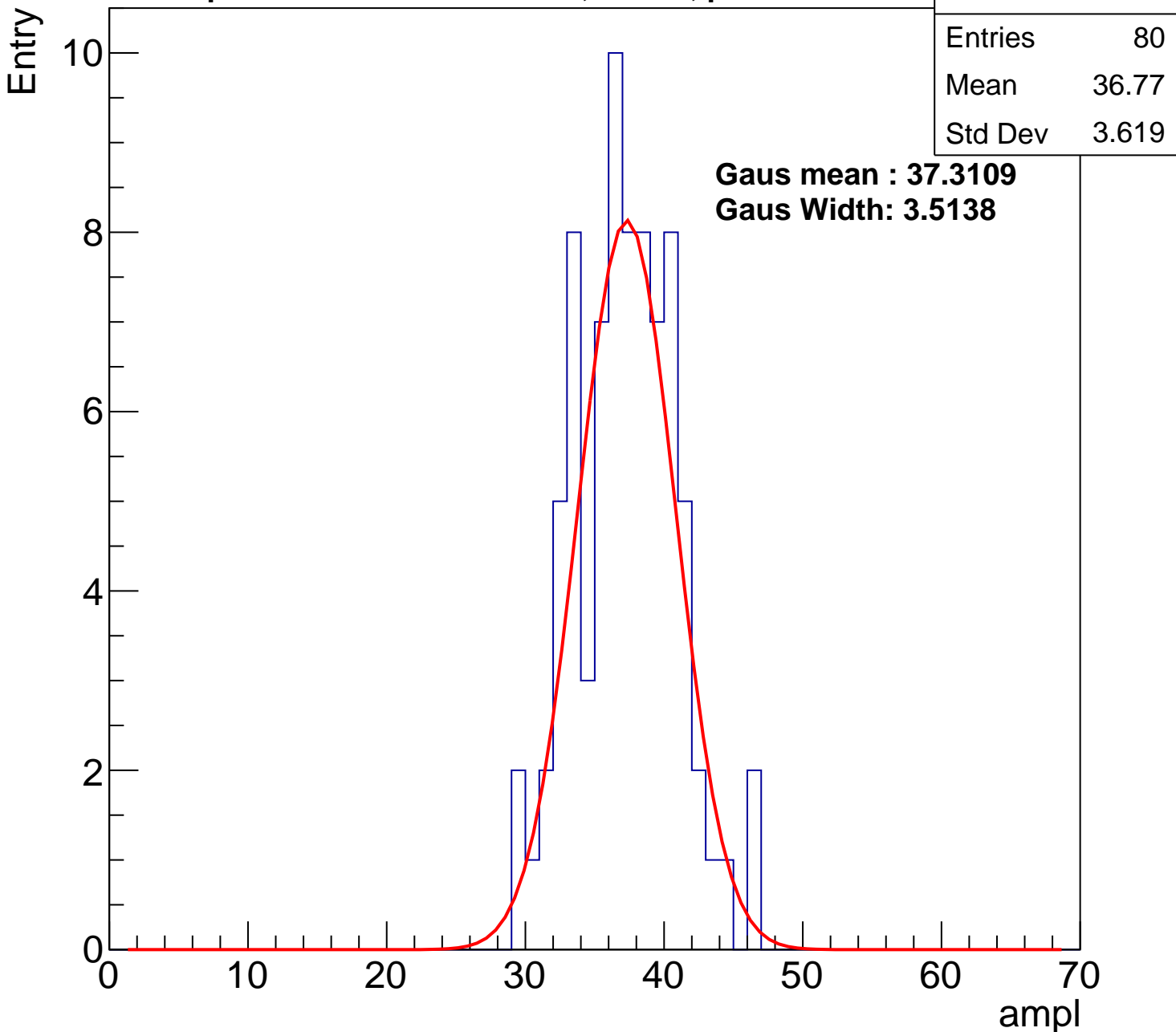
Gaus Width: 3.5138

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U20-ch110, adc2

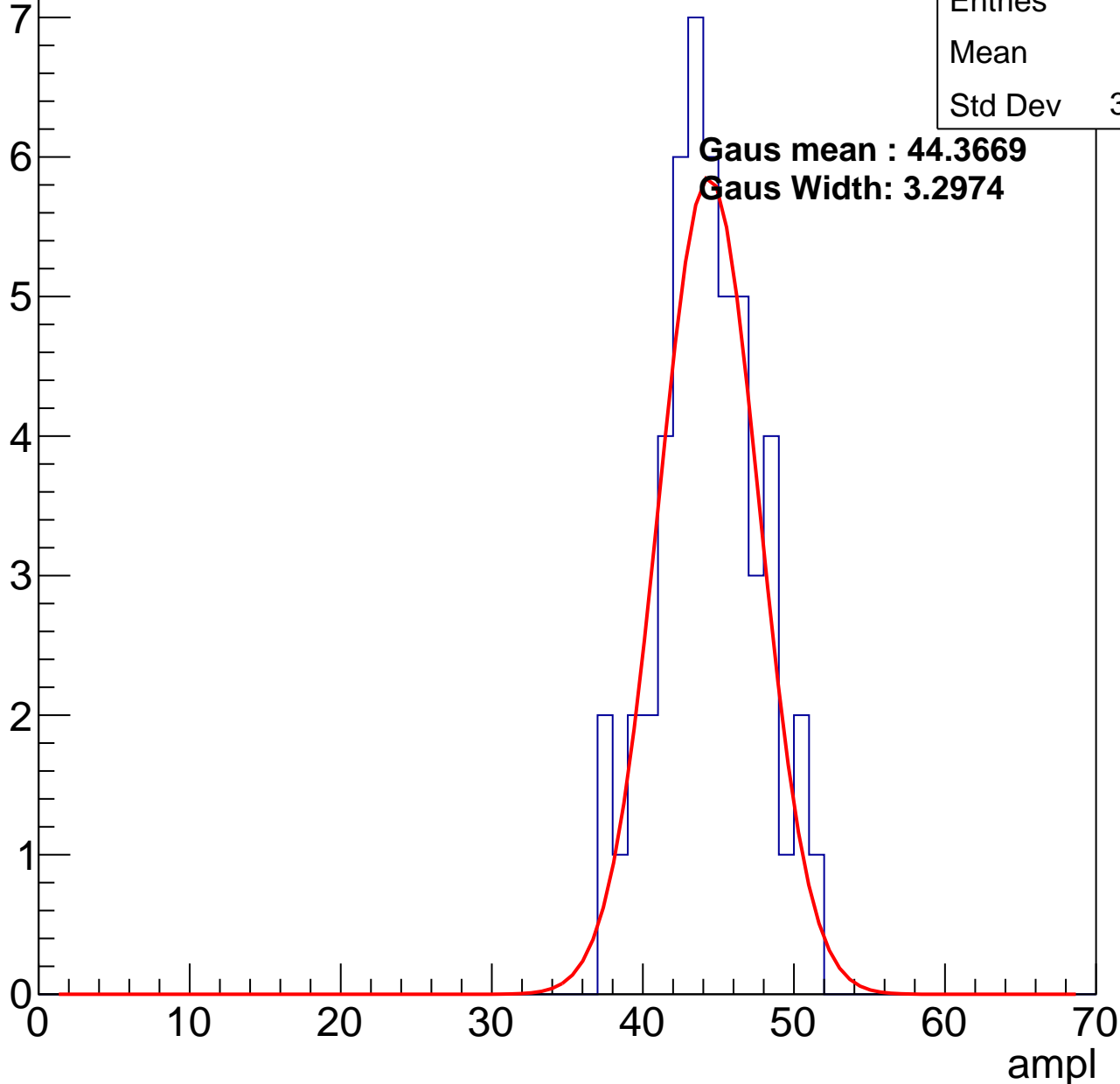
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 43.9 |
| Std Dev | 3.268 |

Gaus mean : 44.3669

Gaus Width: 3.2974

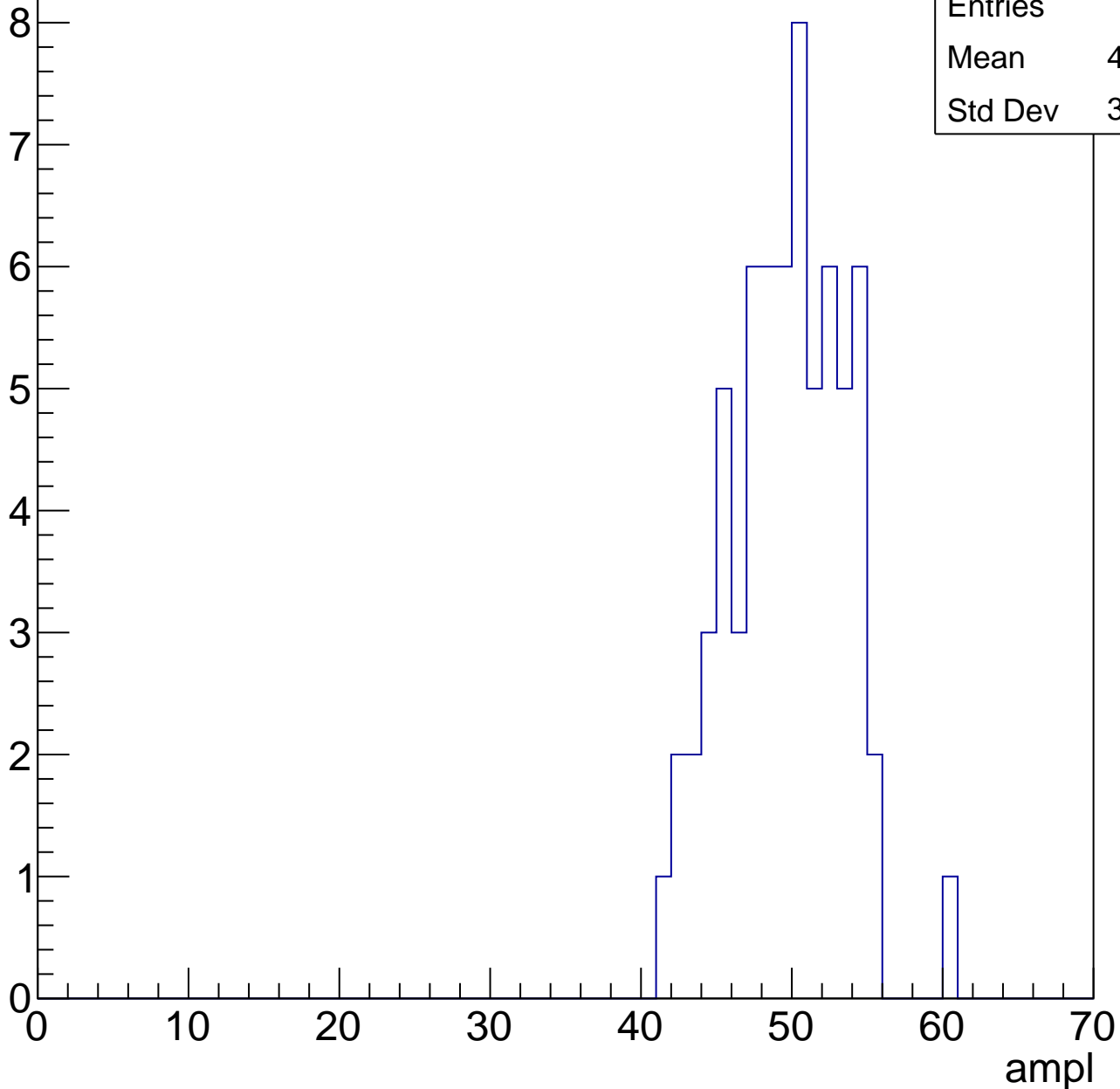


B0L102S, U20-ch110, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 49.19 |
| Std Dev | 3.762 |

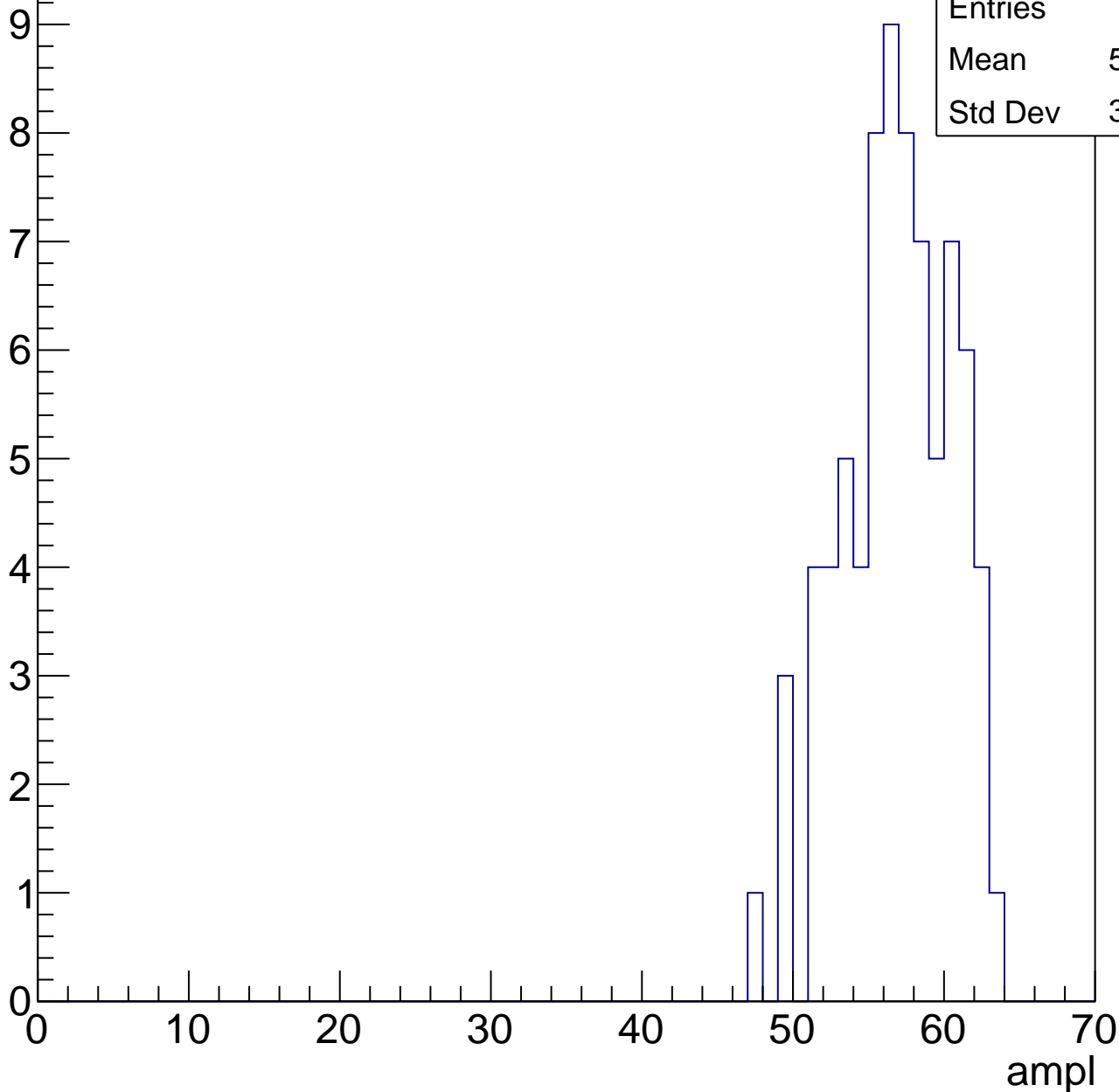


B0L102S, U20-ch110, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 56.38 |
| Std Dev | 3.602 |

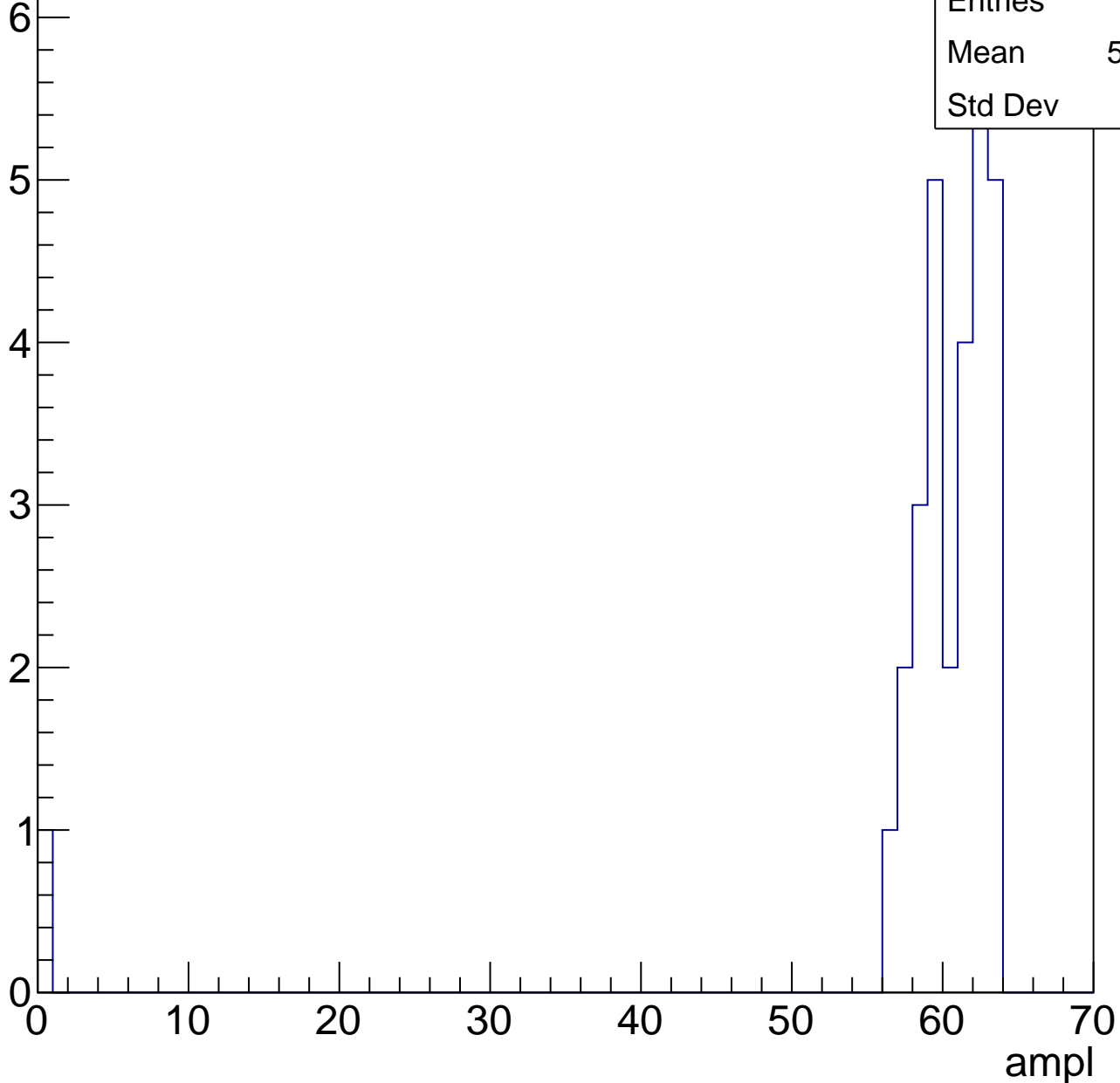


B0L102S, U20-ch110, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.28 |
| Std Dev | 11.2 |



B0L102S, U20-ch110, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch110, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch111, adc0

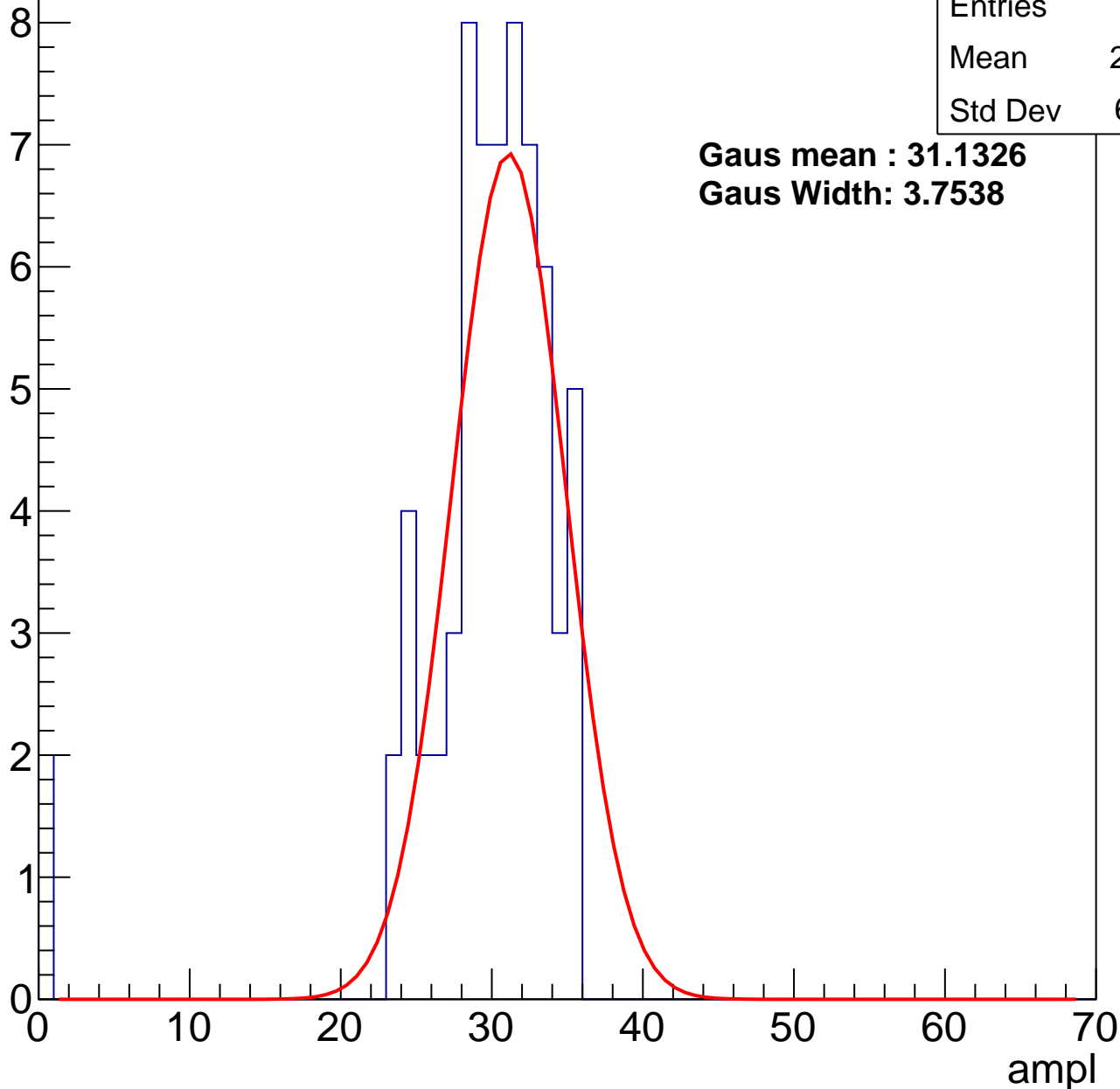
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.92 |
| Std Dev | 6.001 |

Gaus mean : 31.1326

Gaus Width: 3.7538



B0L102S, U20-ch111, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 37.52 |
| Std Dev | 3.667 |

Gaus mean : 37.7239

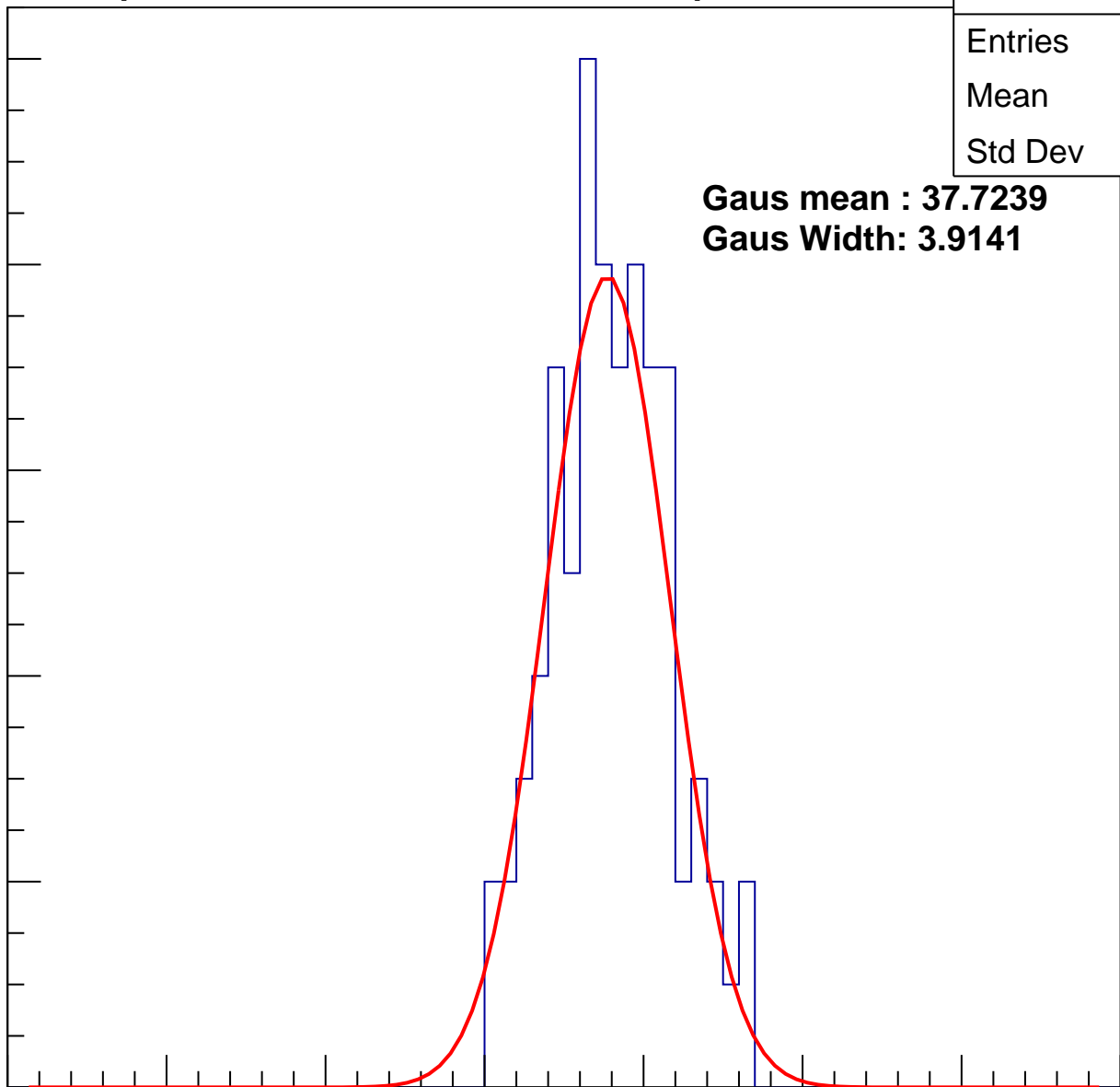
Gaus Width: 3.9141

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch111, adc2

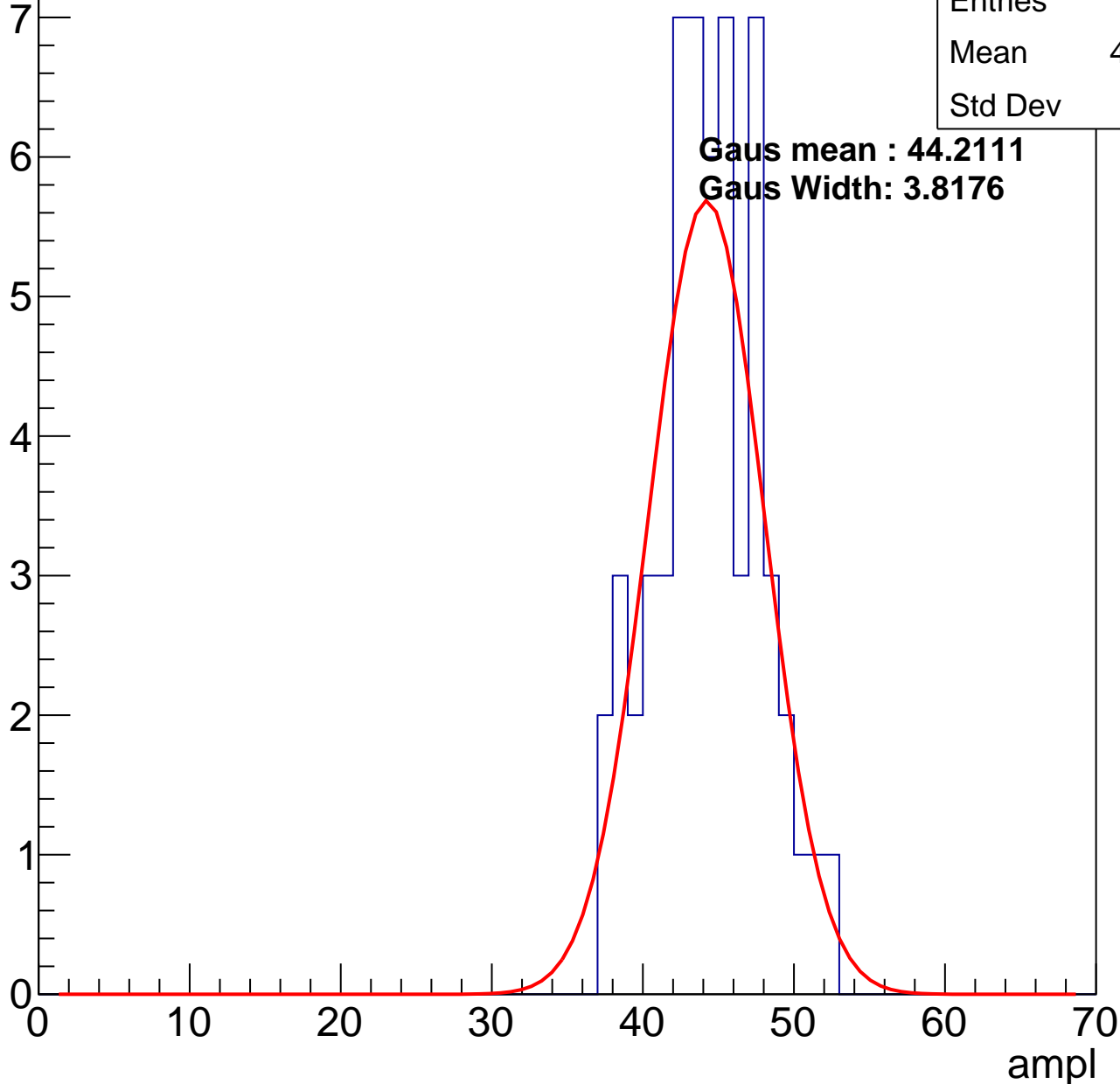
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 43.88 |
| Std Dev | 3.46 |

Gaus mean : 44.2111

Gaus Width: 3.8176

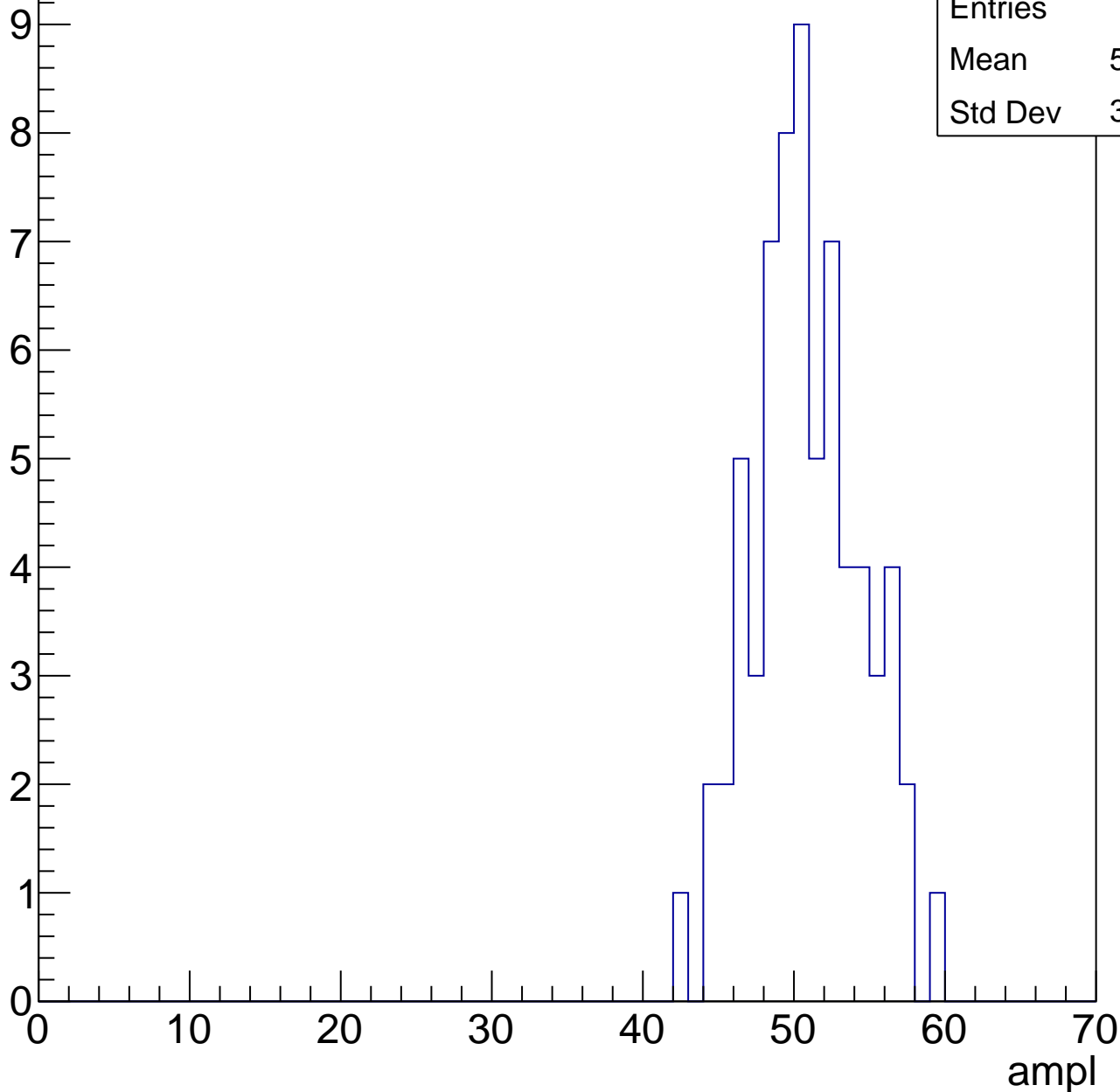


B0L102S, U20-ch111, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50.42 |
| Std Dev | 3.558 |



B0L102S, U20-ch111, adc4

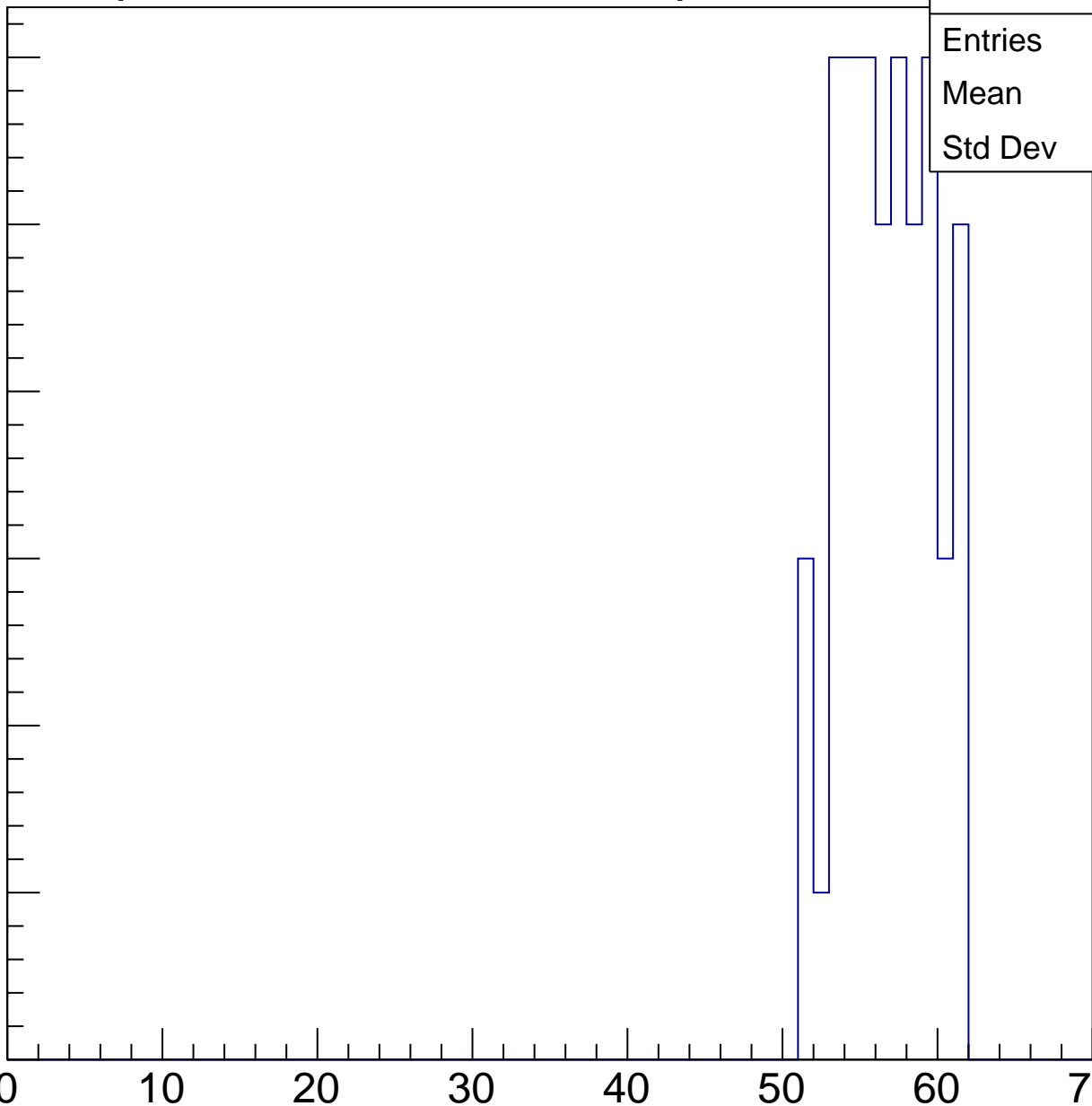
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 56.31 |
| Std Dev | 2.852 |

ampl

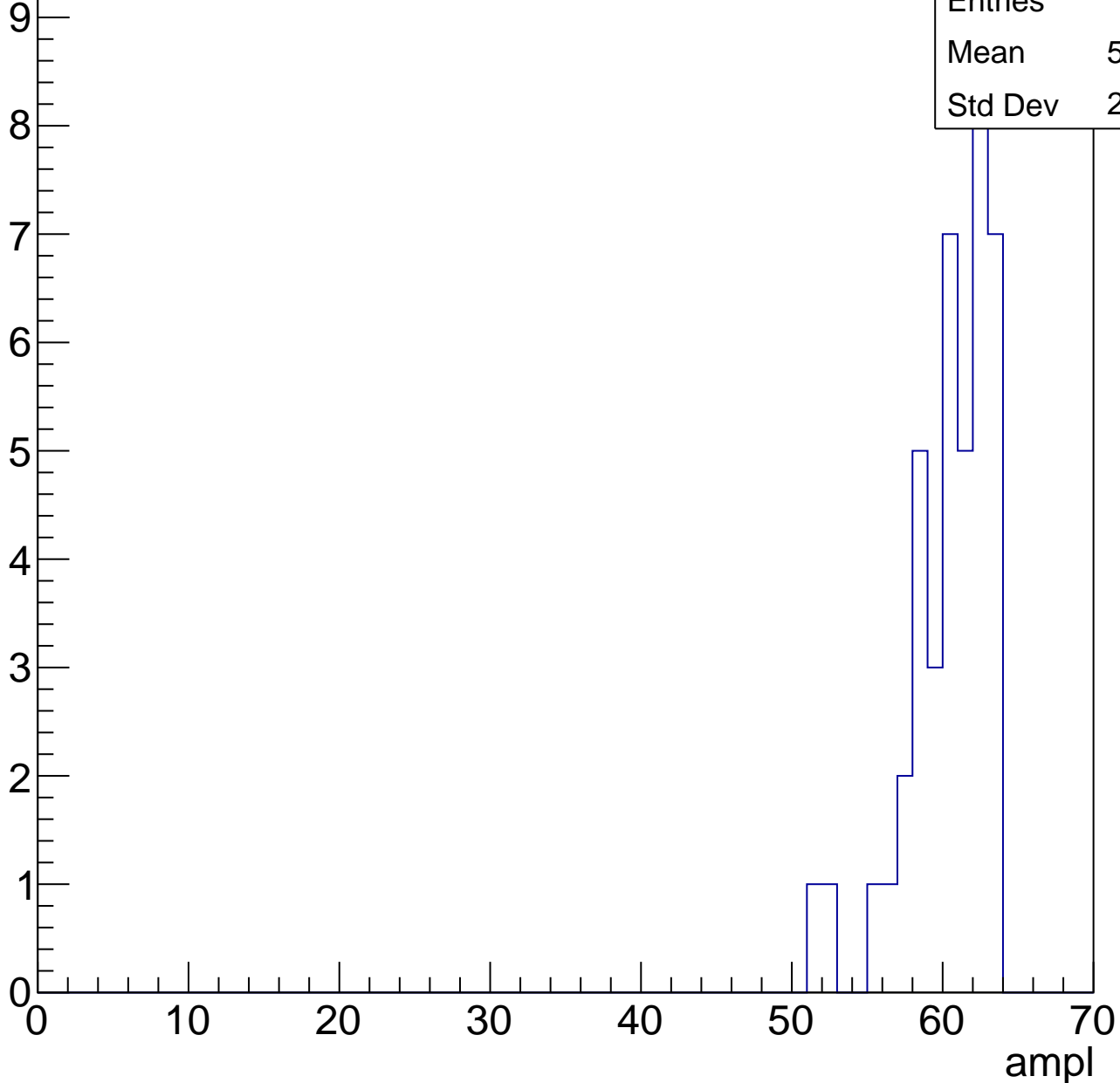


B0L102S, U20-ch111, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.98 |
| Std Dev | 2.807 |



B0L102S, U20-ch111, adc6

calib_packv5_042523_0143.root, FC#12, port B1

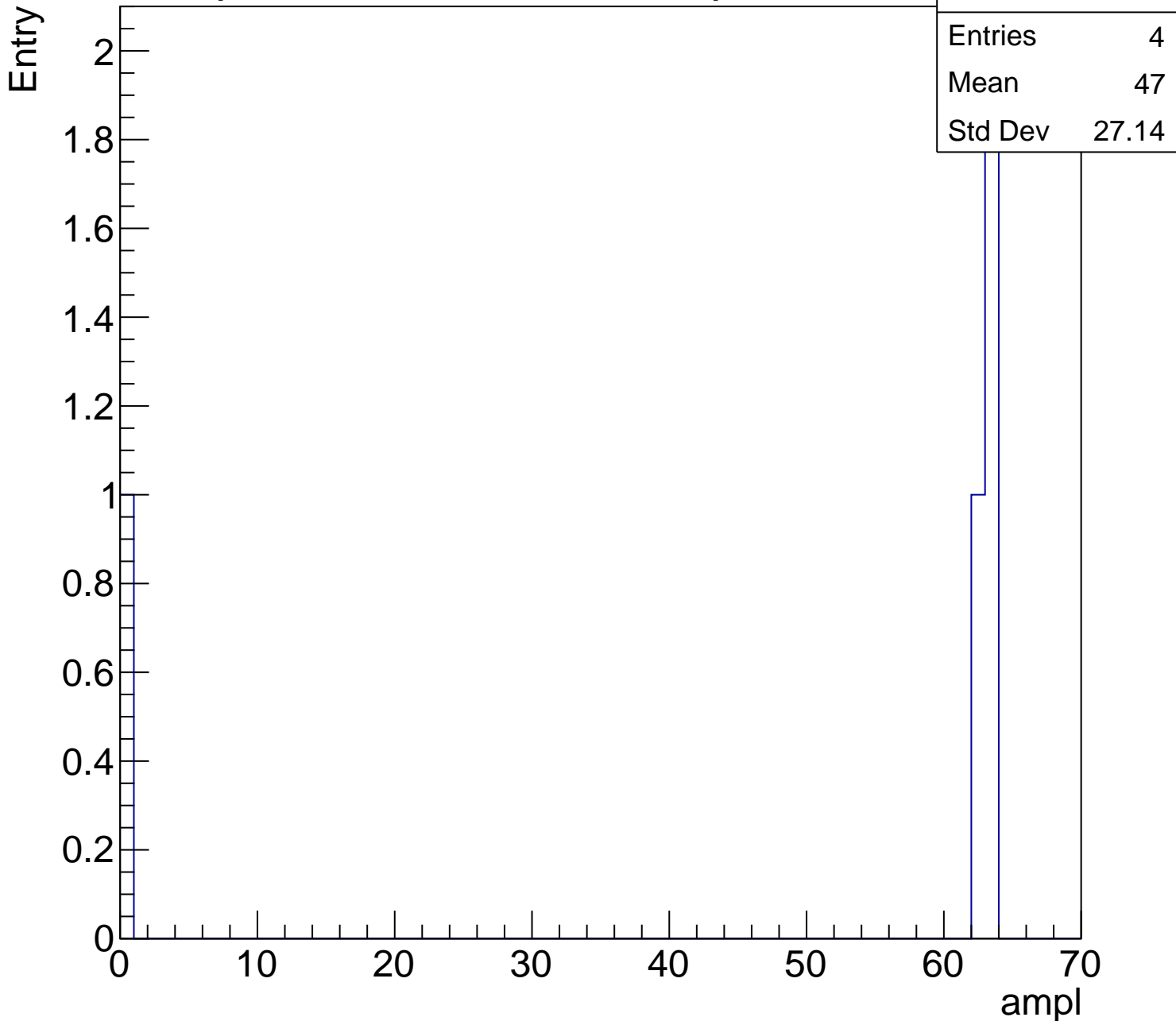
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 47 |
| Std Dev | 27.14 |

0 10 20 30 40 50 60 70

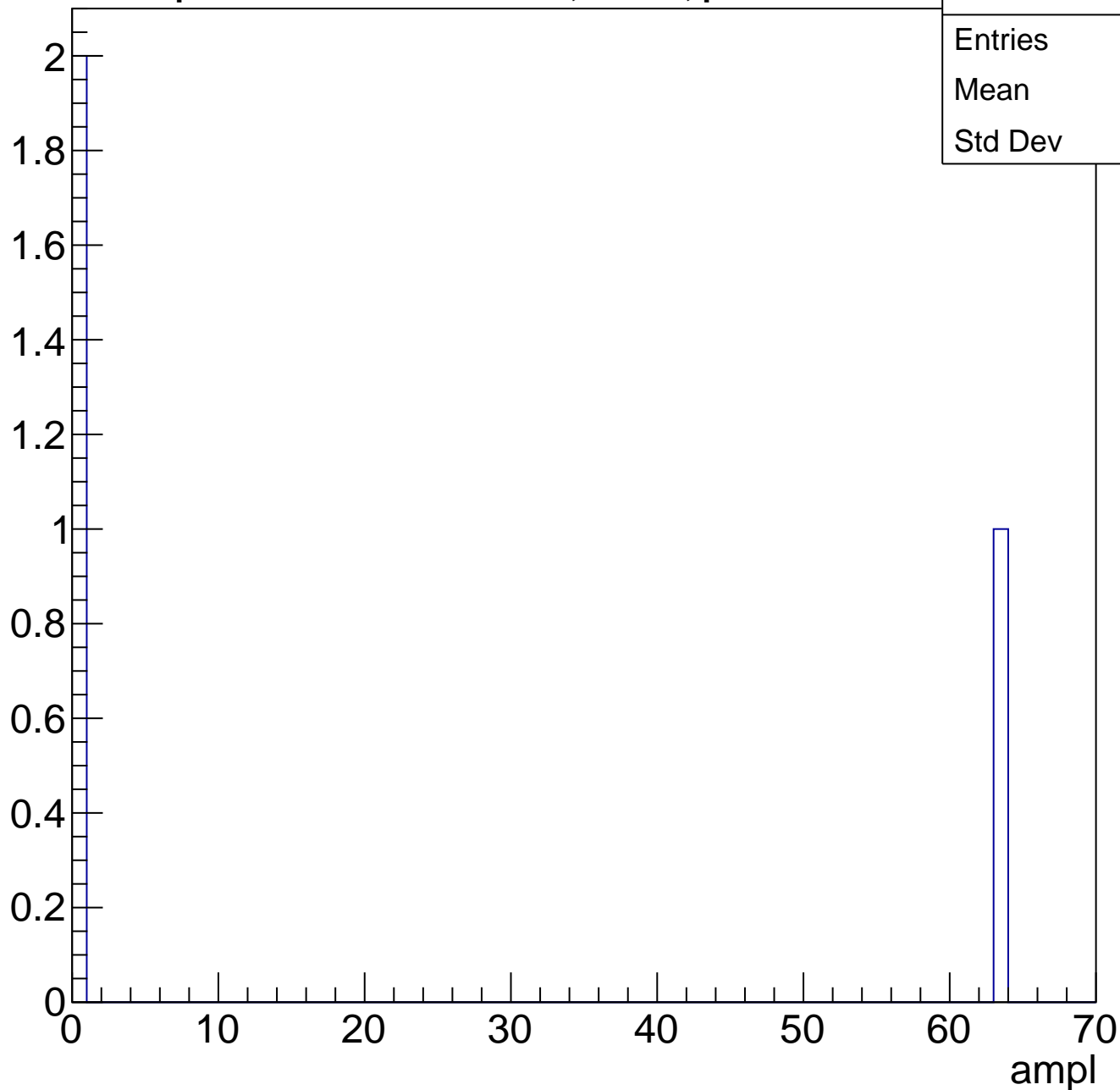
ampl



B0L102S, U20-ch111, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch112, adc0

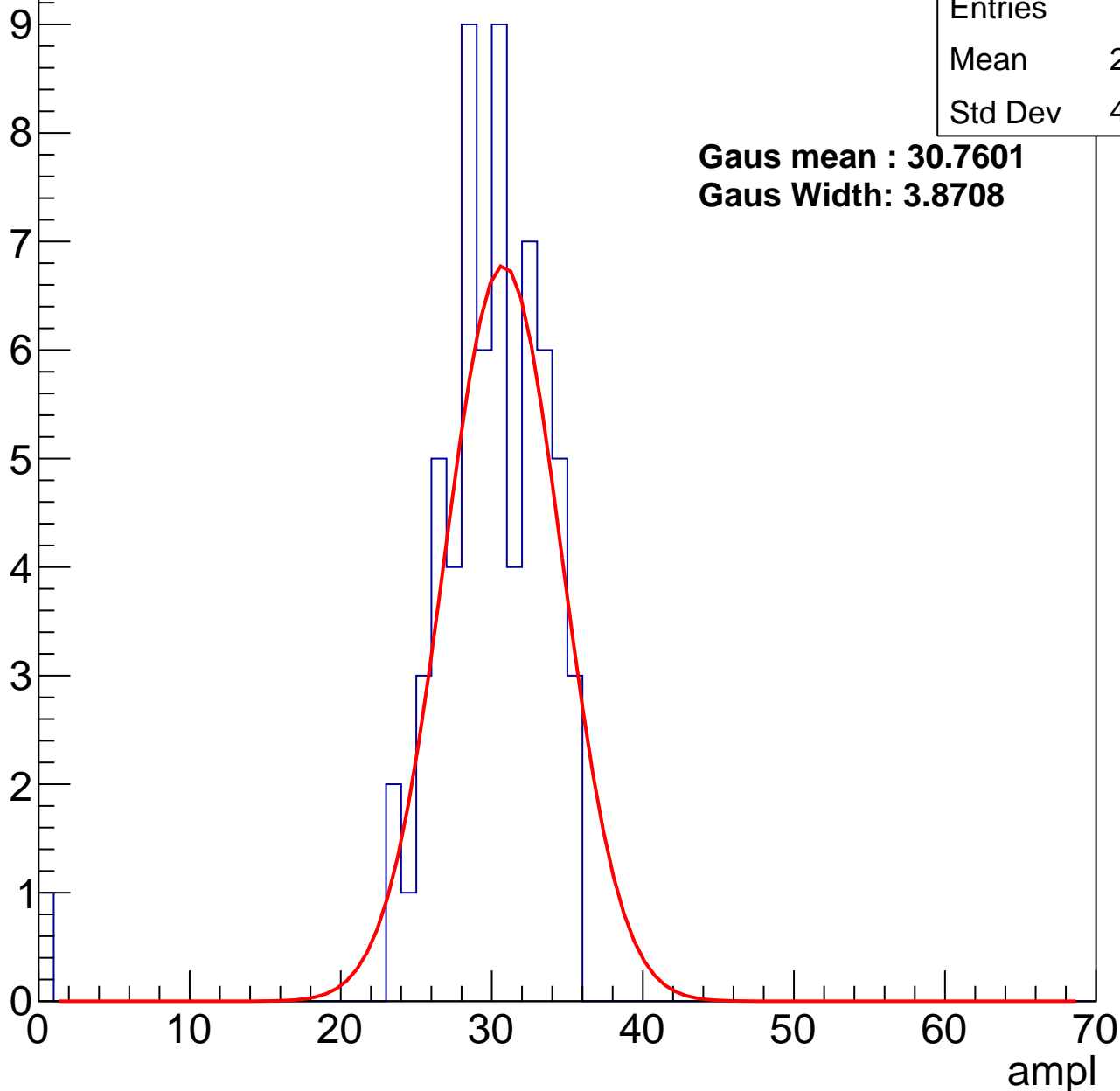
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 29.23 |
| Std Dev | 4.755 |

Gaus mean : 30.7601

Gaus Width: 3.8708

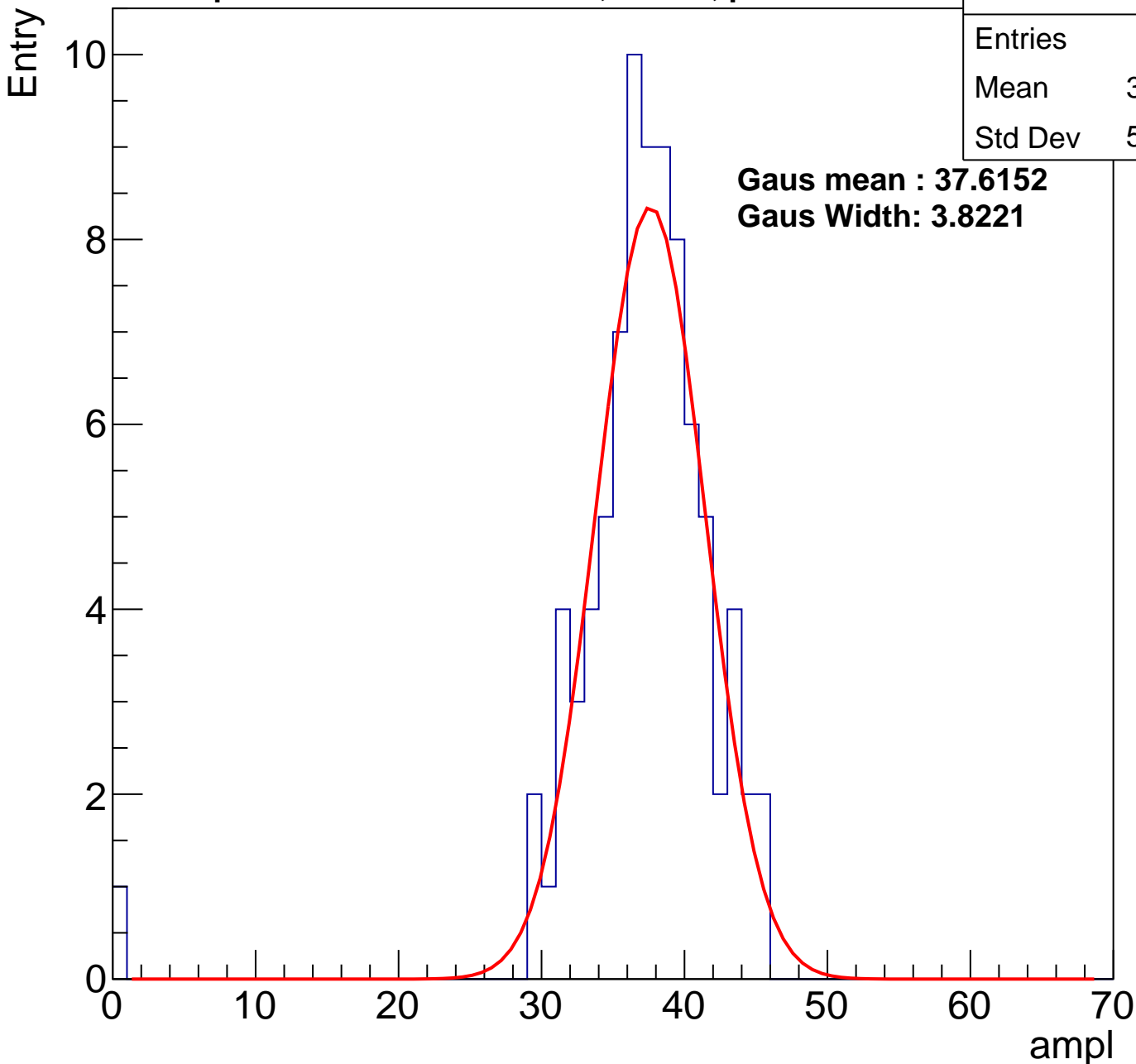


B0L102S, U20-ch112, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 84 |
| Mean | 36.68 |
| Std Dev | 5.454 |

Gaus mean : 37.6152
Gaus Width: 3.8221



B0L102S, U20-ch112, adc2

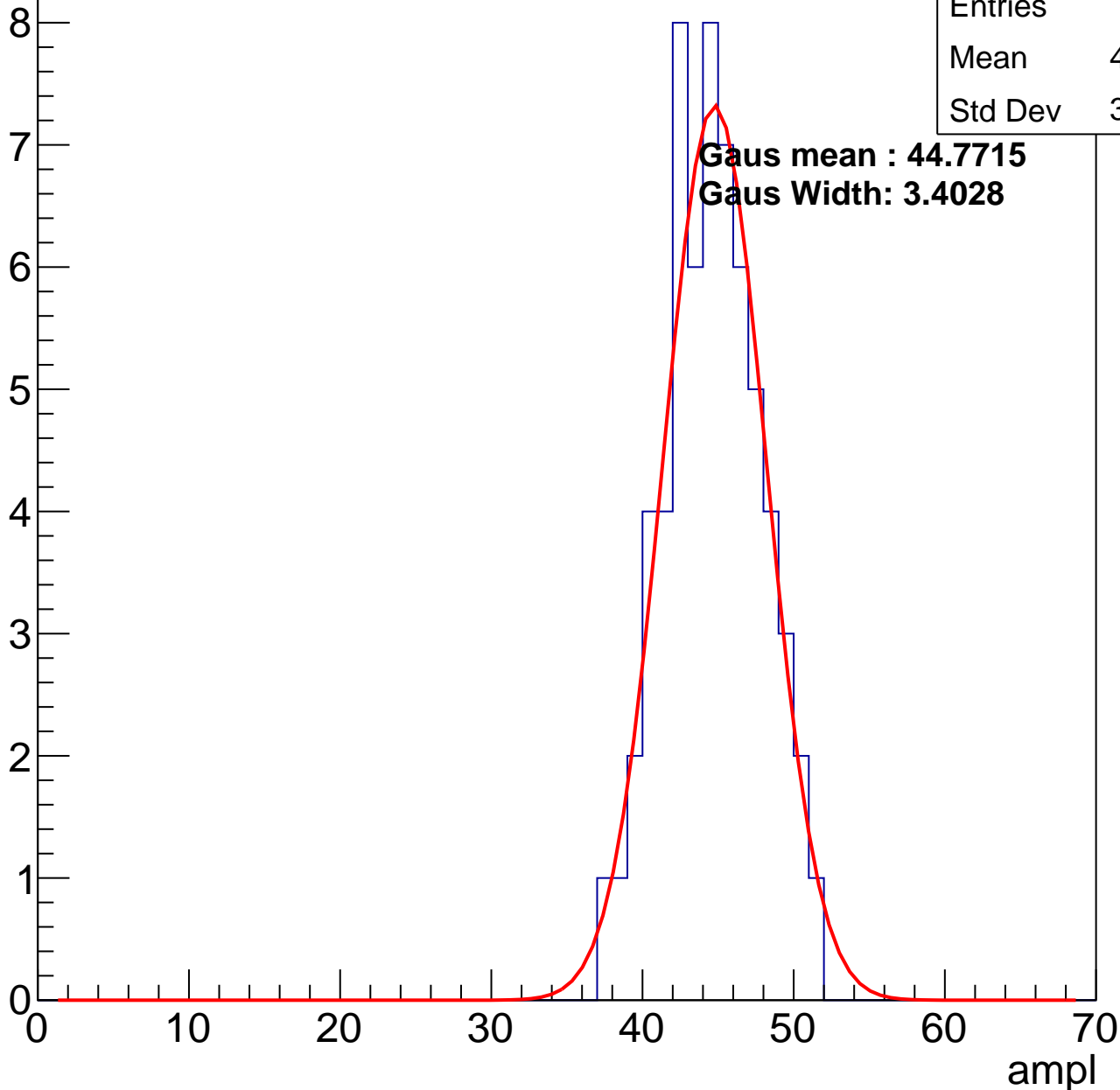
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 44.18 |
| Std Dev | 3.129 |

Gaus mean : 44.7715

Gaus Width: 3.4028

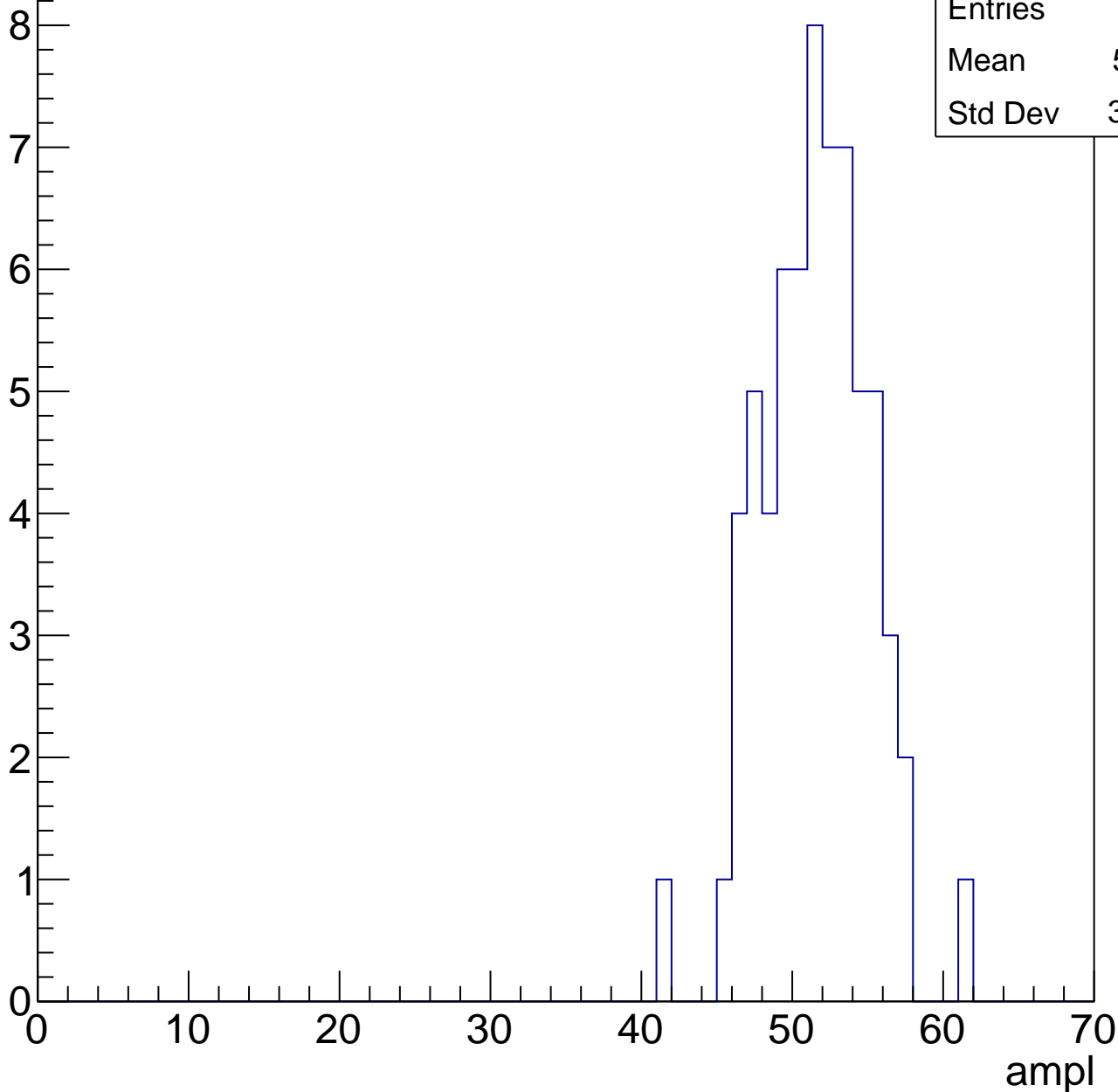


B0L102S, U20-ch112, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

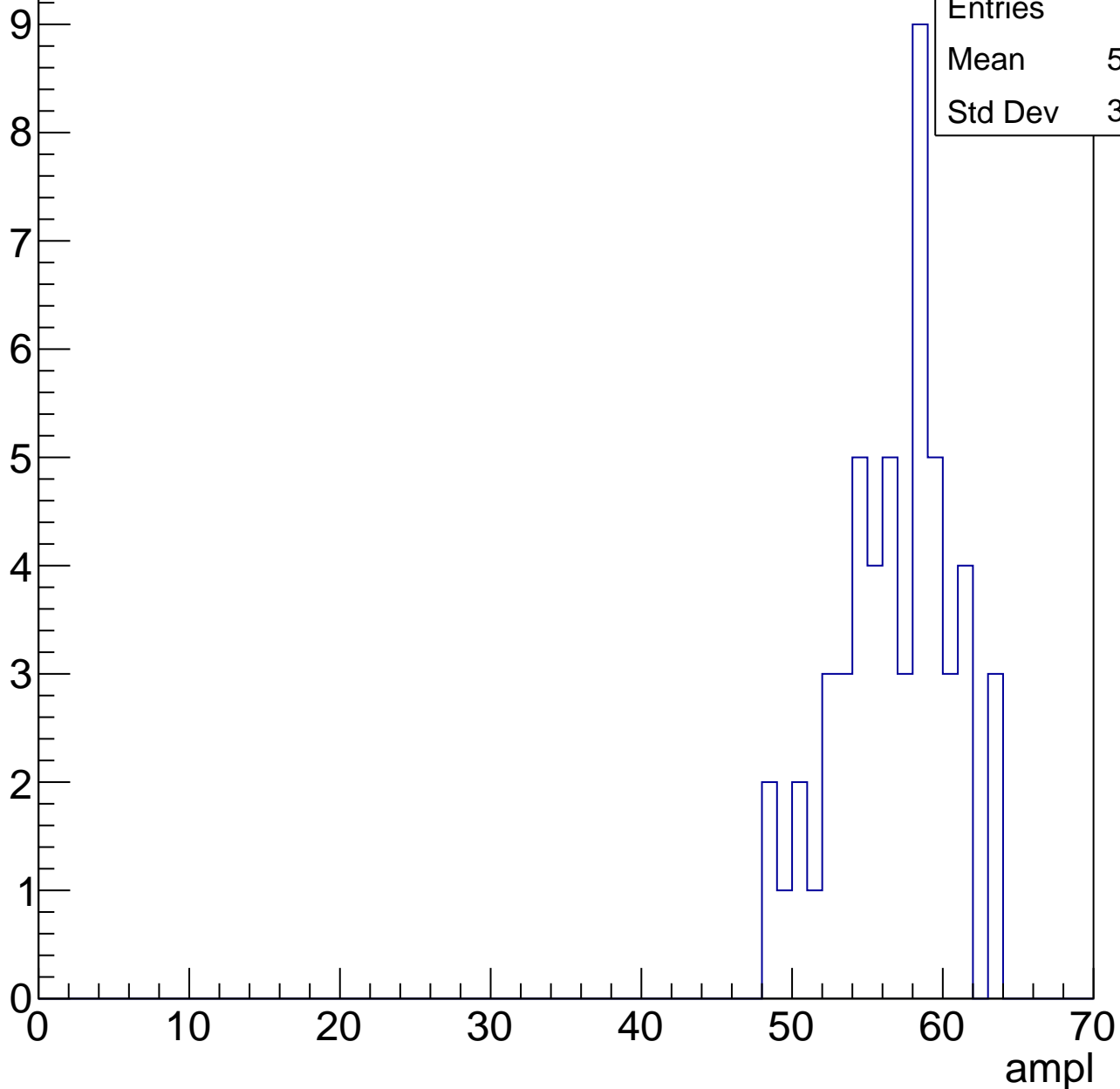
| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.11 |
| Std Dev | 3.482 |



B0L102S, U20-ch112, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

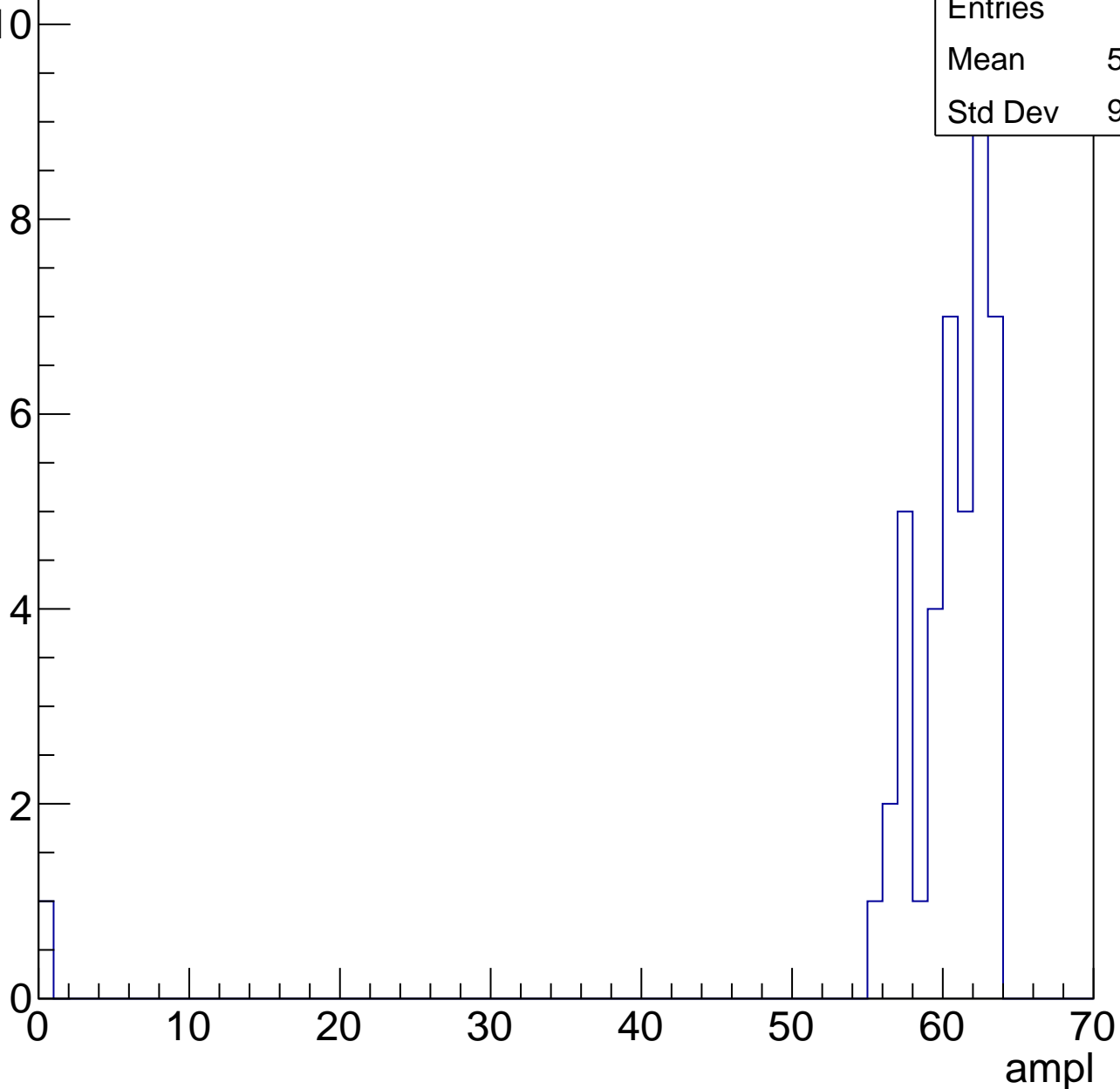


B0L102S, U20-ch112, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.88 |
| Std Dev | 9.357 |



B0L102S, U20-ch112, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 59 |
| Std Dev | 0 |

B0L102S, U20-ch112, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch113, adc0

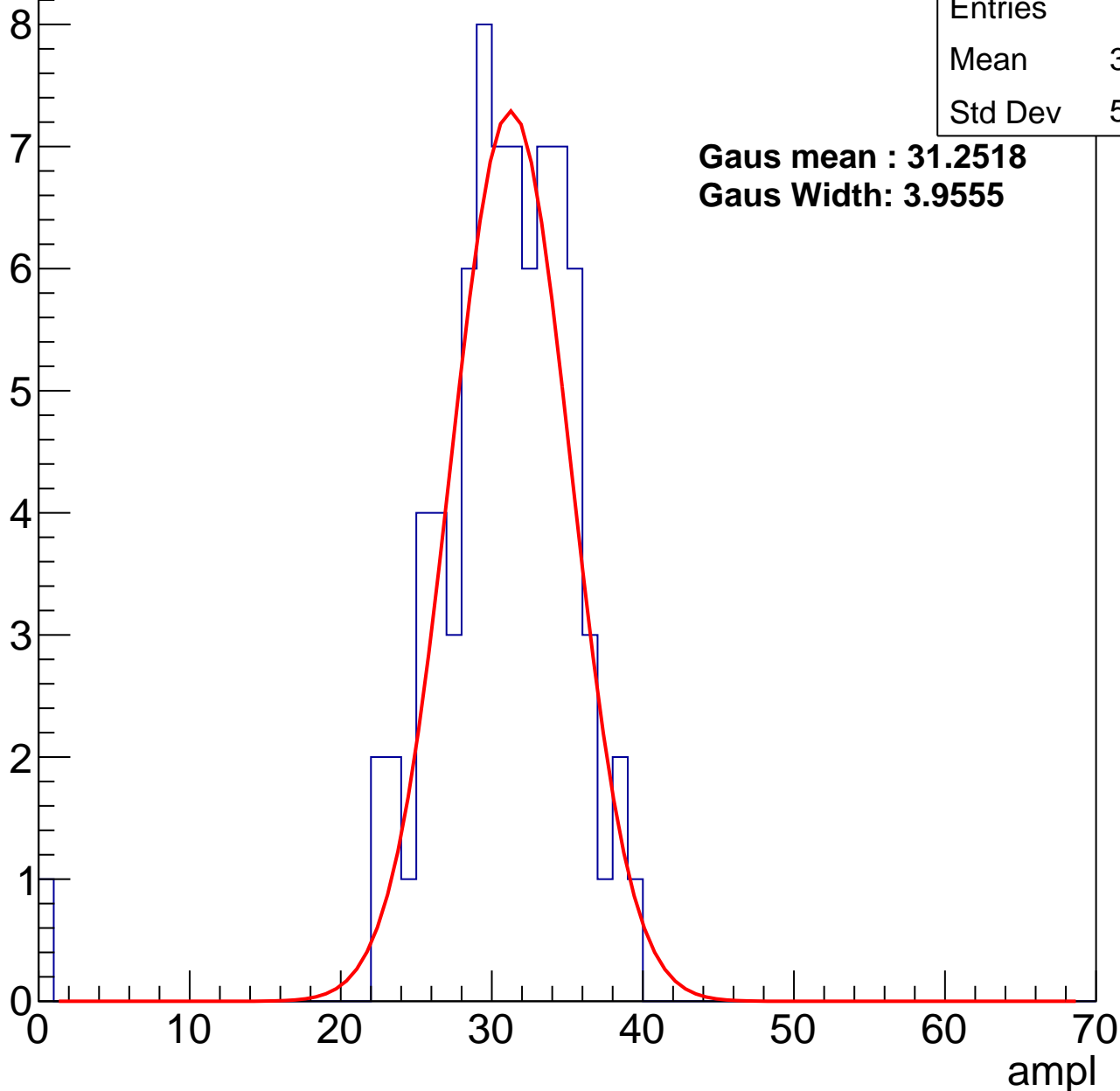
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 30.22 |
| Std Dev | 5.193 |

Gaus mean : 31.2518

Gaus Width: 3.9555



B0L102S, U20-ch113, adc1

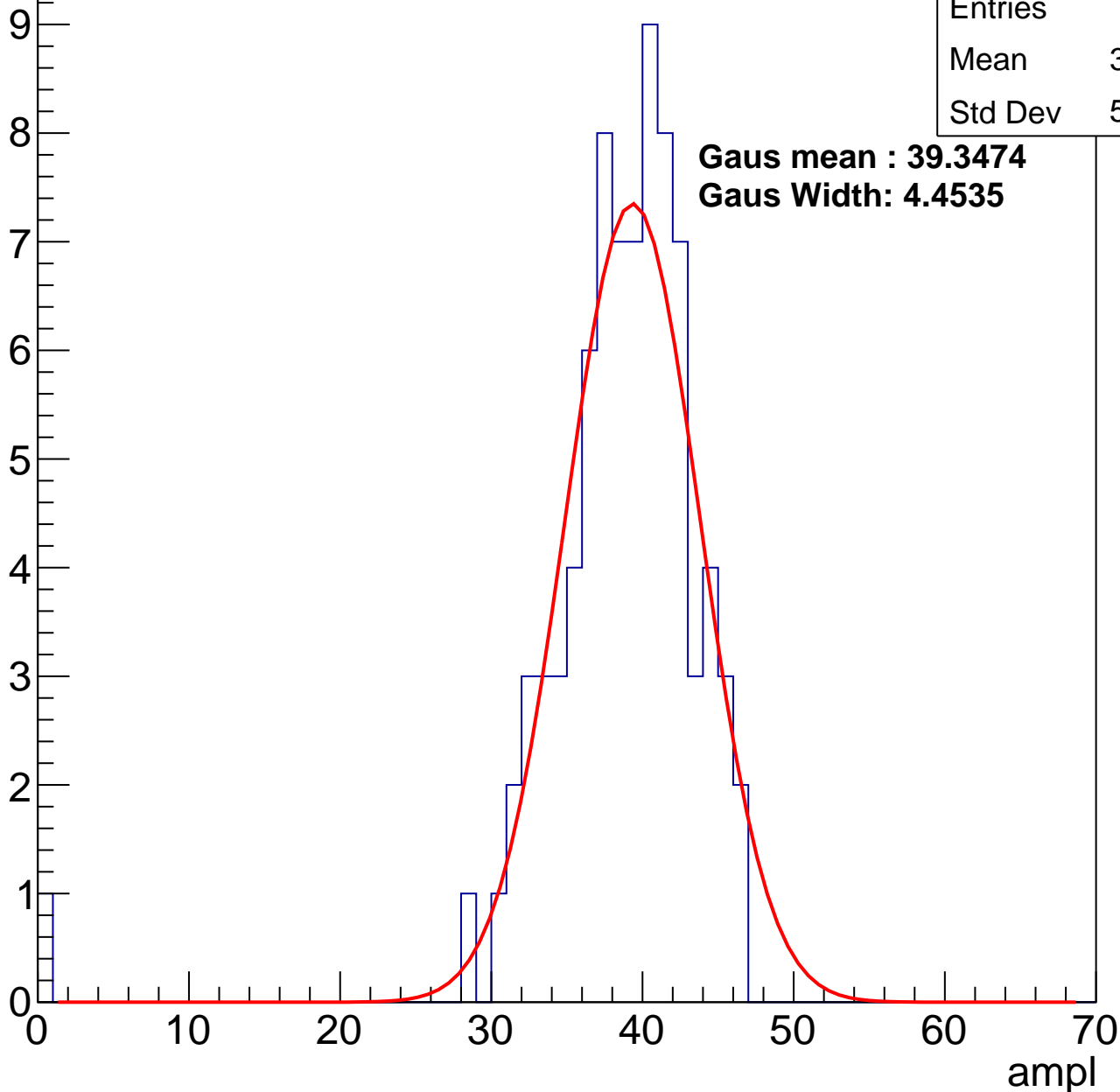
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 38.07 |
| Std Dev | 5.765 |

Gaus mean : 39.3474

Gaus Width: 4.4535



B0L102S, U20-ch113, adc2

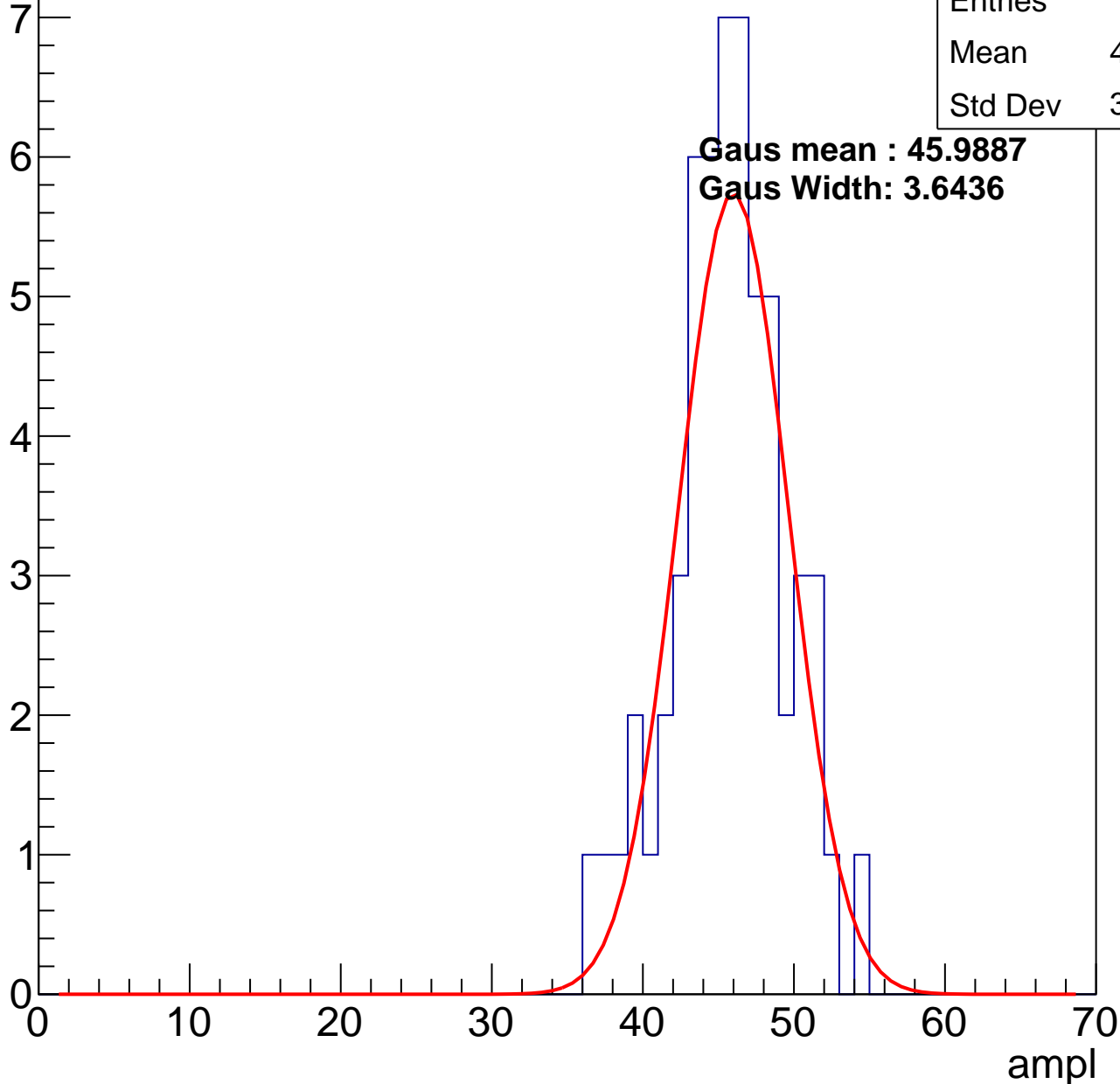
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 45.23 |
| Std Dev | 3.732 |

Gaus mean : 45.9887

Gaus Width: 3.6436

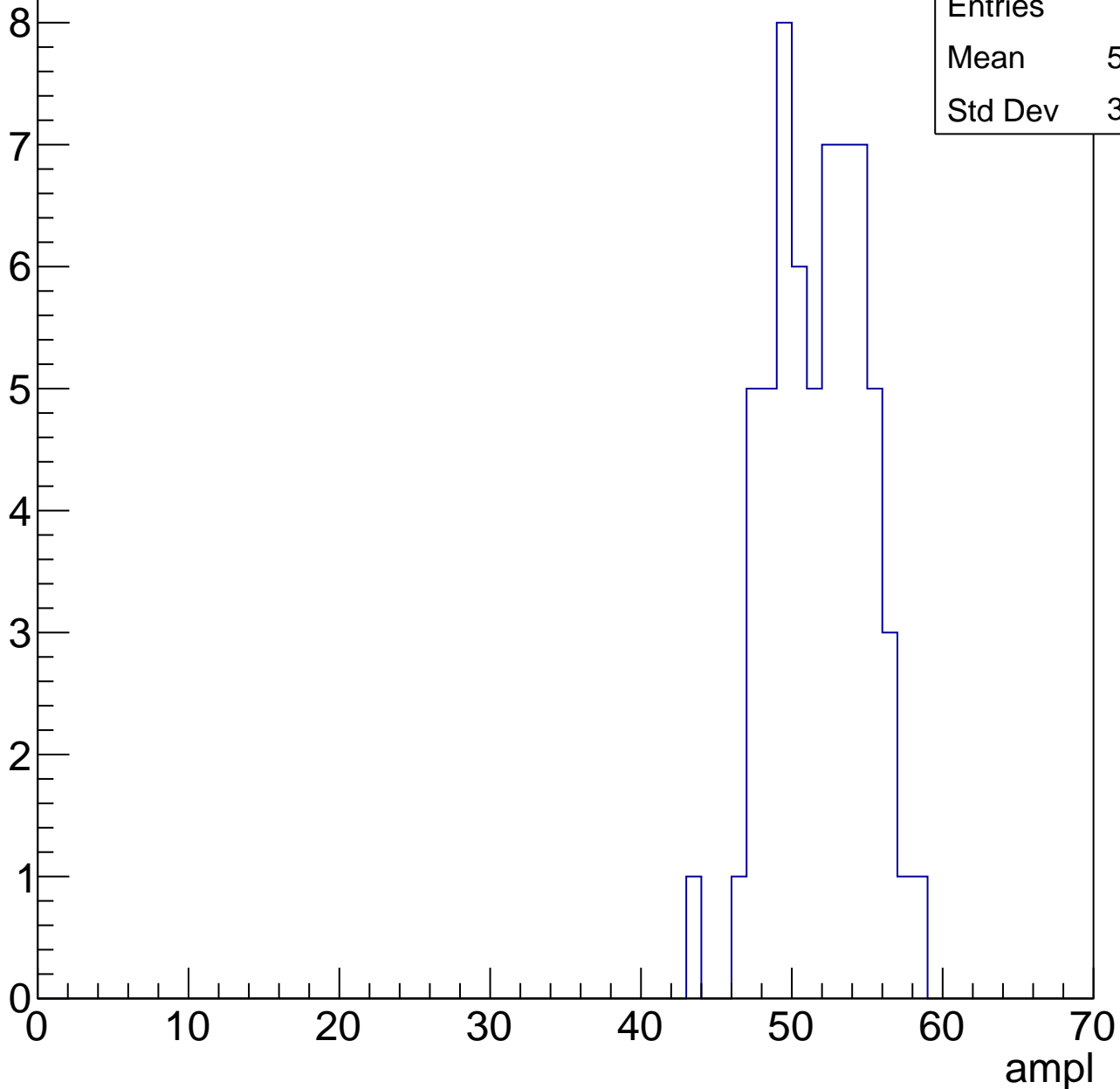


B0L102S, U20-ch113, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 51.32 |
| Std Dev | 3.068 |

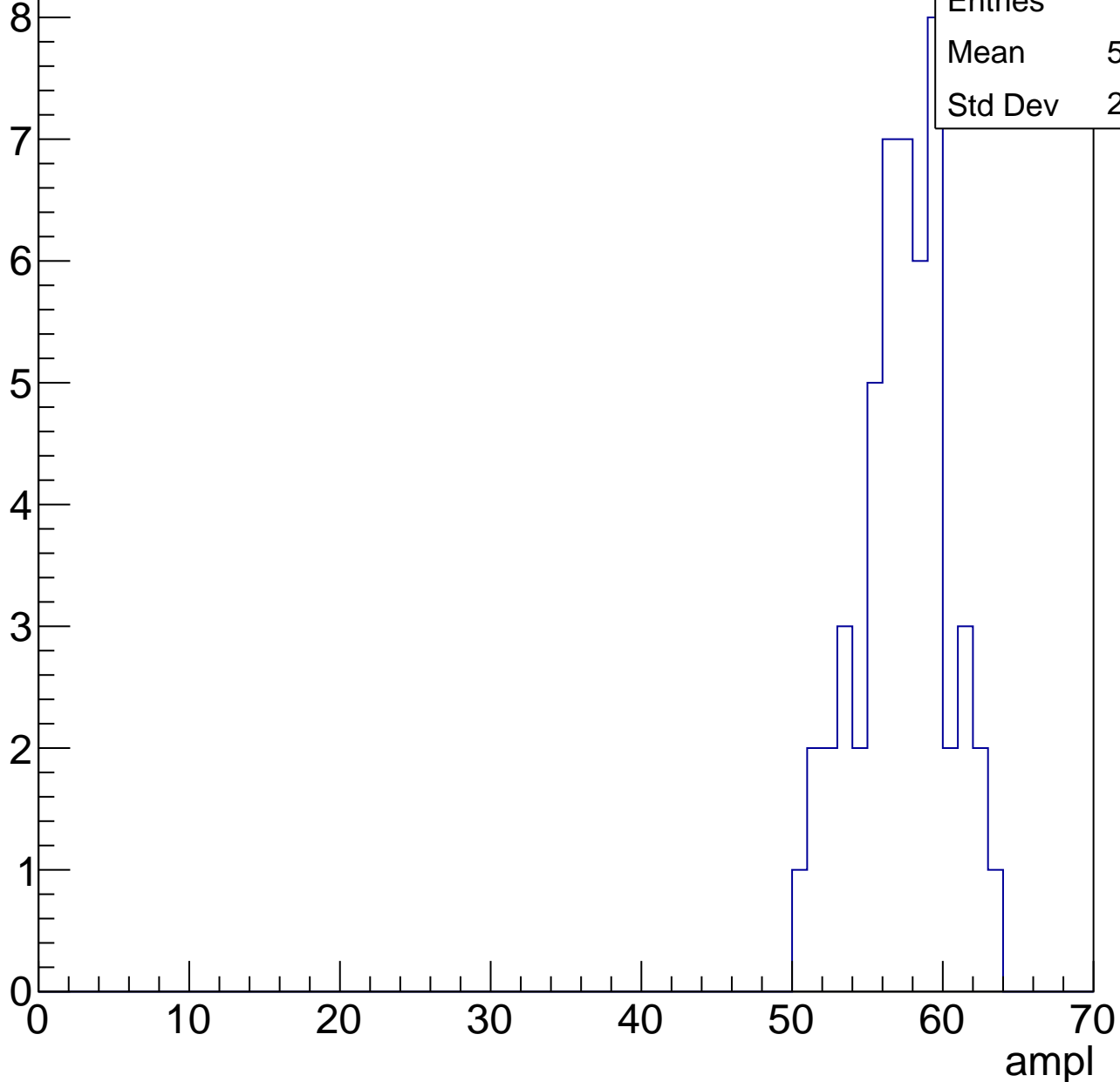


B0L102S, U20-ch113, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

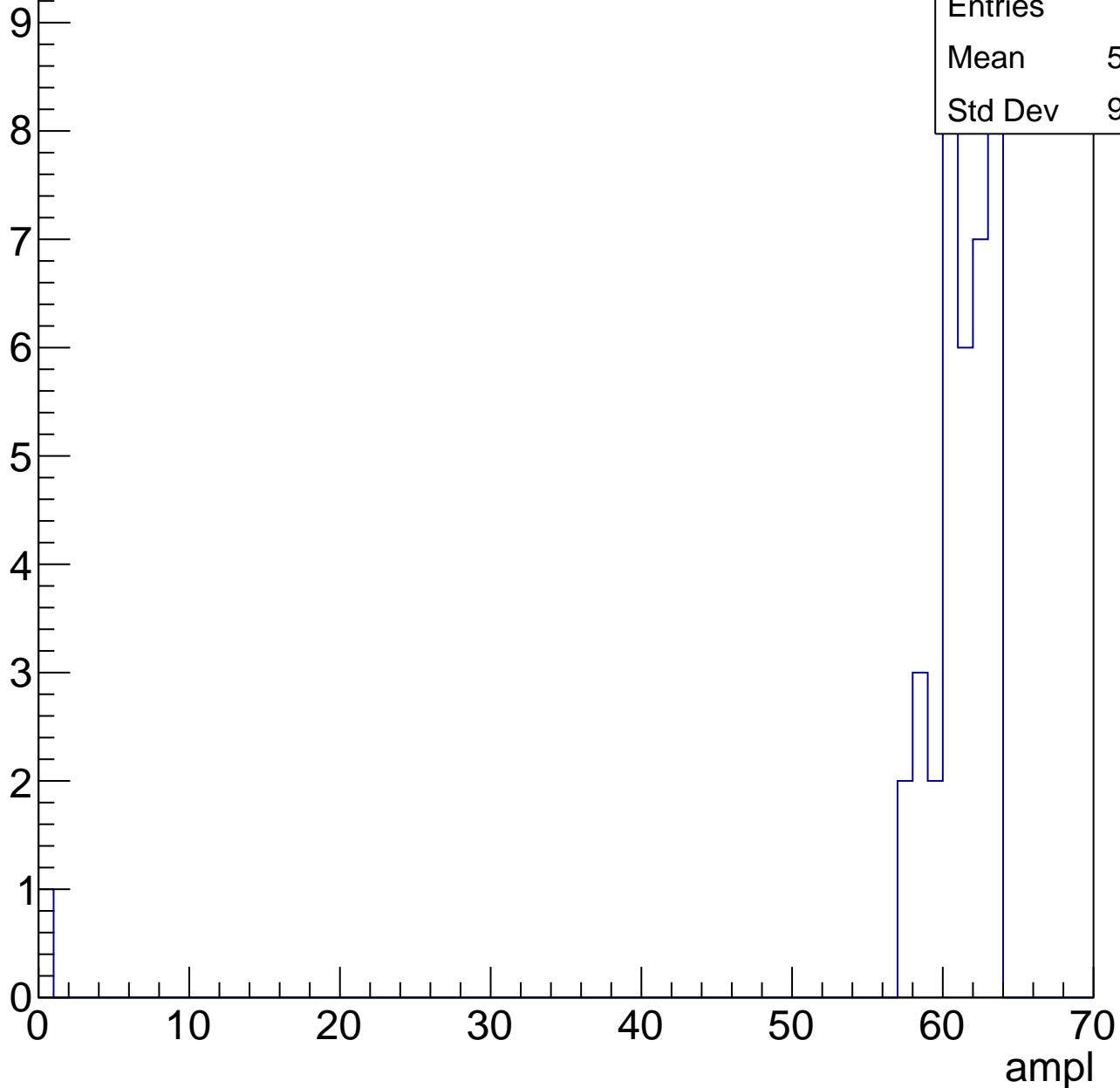
| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.84 |
| Std Dev | 2.979 |



B0L102S, U20-ch113, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch113, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch113, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch114, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 83 |
| Mean | 28.69 |
| Std Dev | 3.663 |

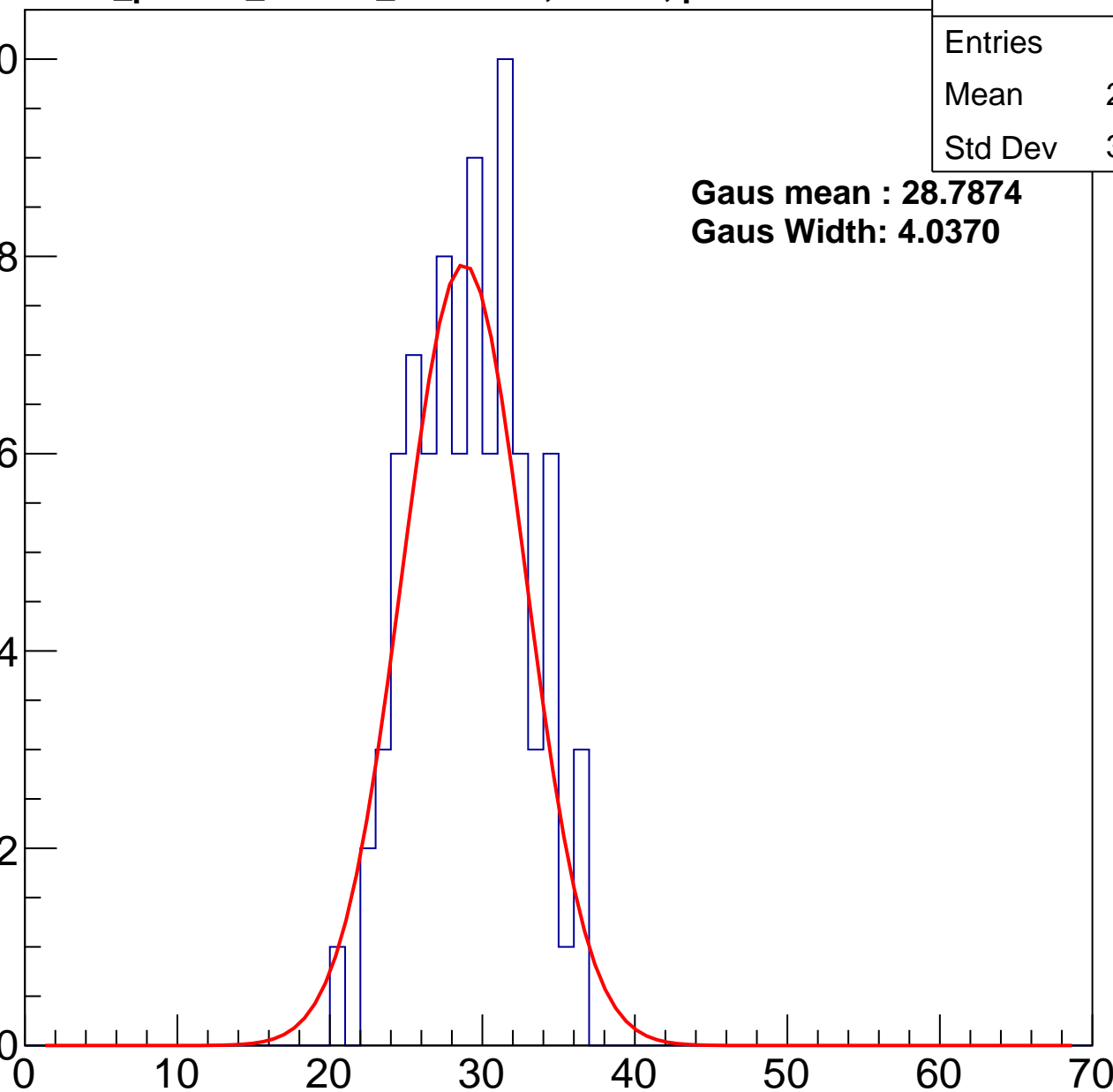
Gaus mean : 28.7874

Gaus Width: 4.0370

Entry

10
8
6
4
2
0

ampl



B0L102S, U20-ch114, adc1

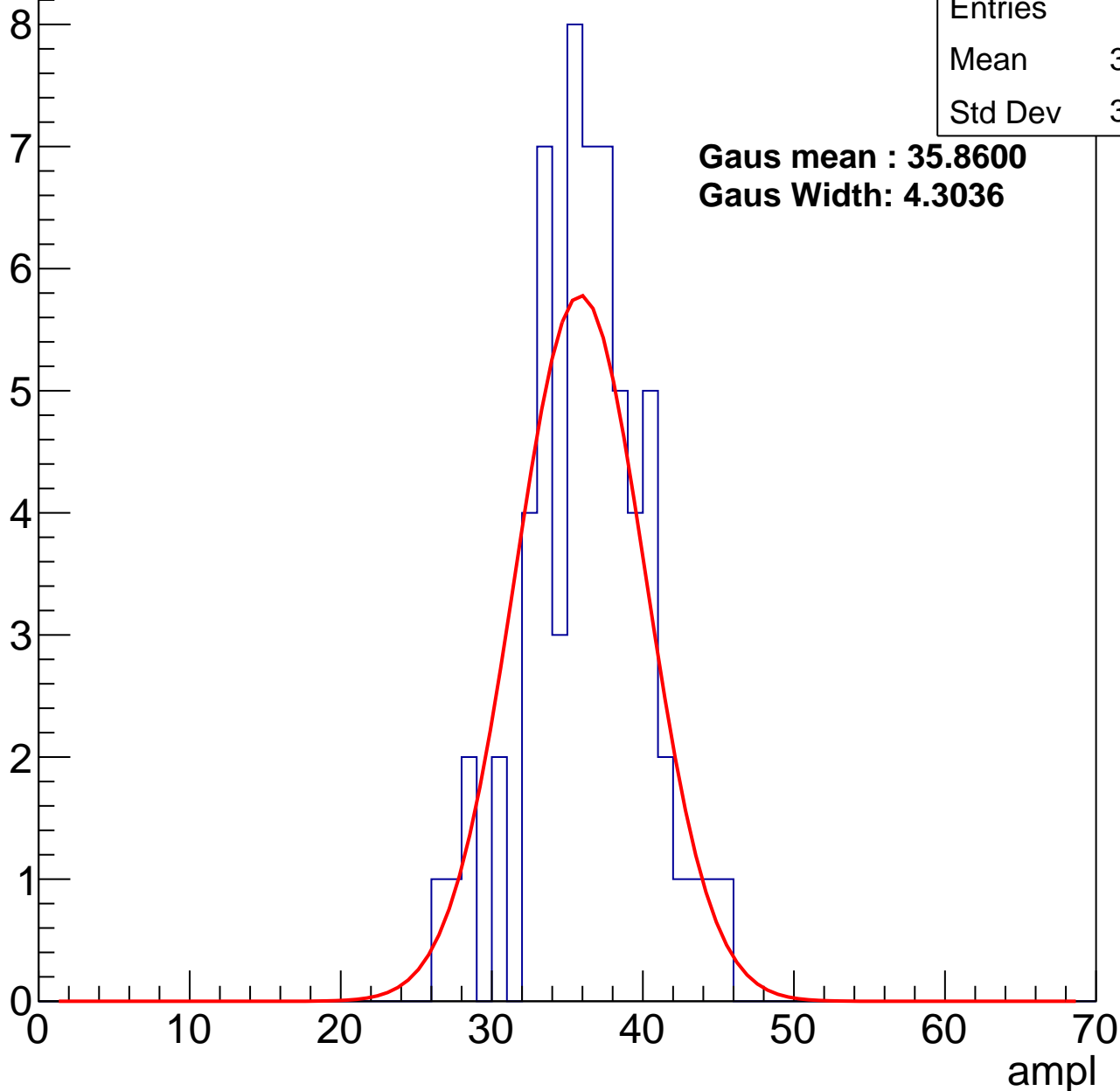
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.85 |
| Std Dev | 3.914 |

Gaus mean : 35.8600

Gaus Width: 4.3036



B0L102S, U20-ch114, adc2

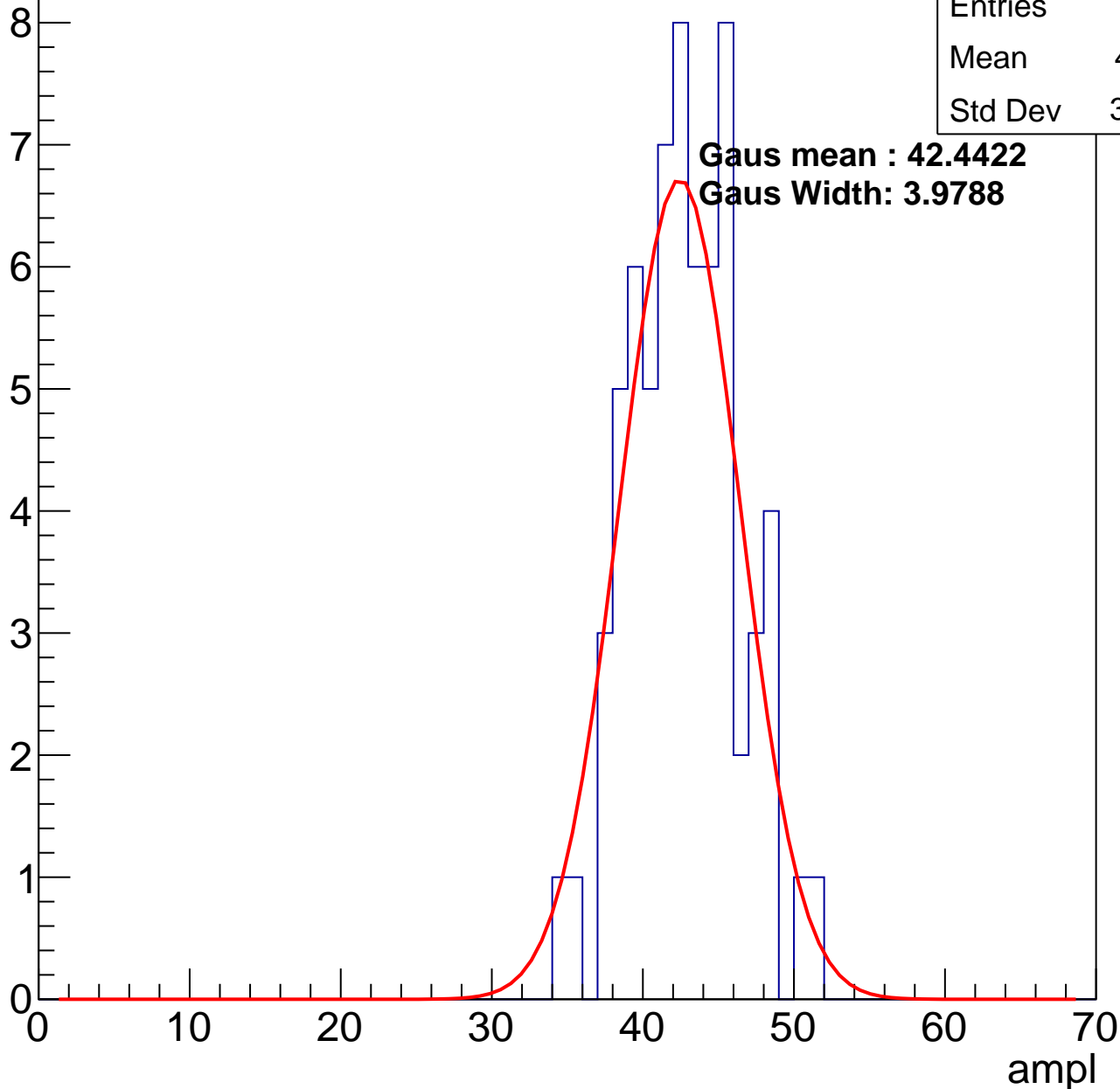
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 42.31 |
| Std Dev | 3.542 |

Gaus mean : 42.4422

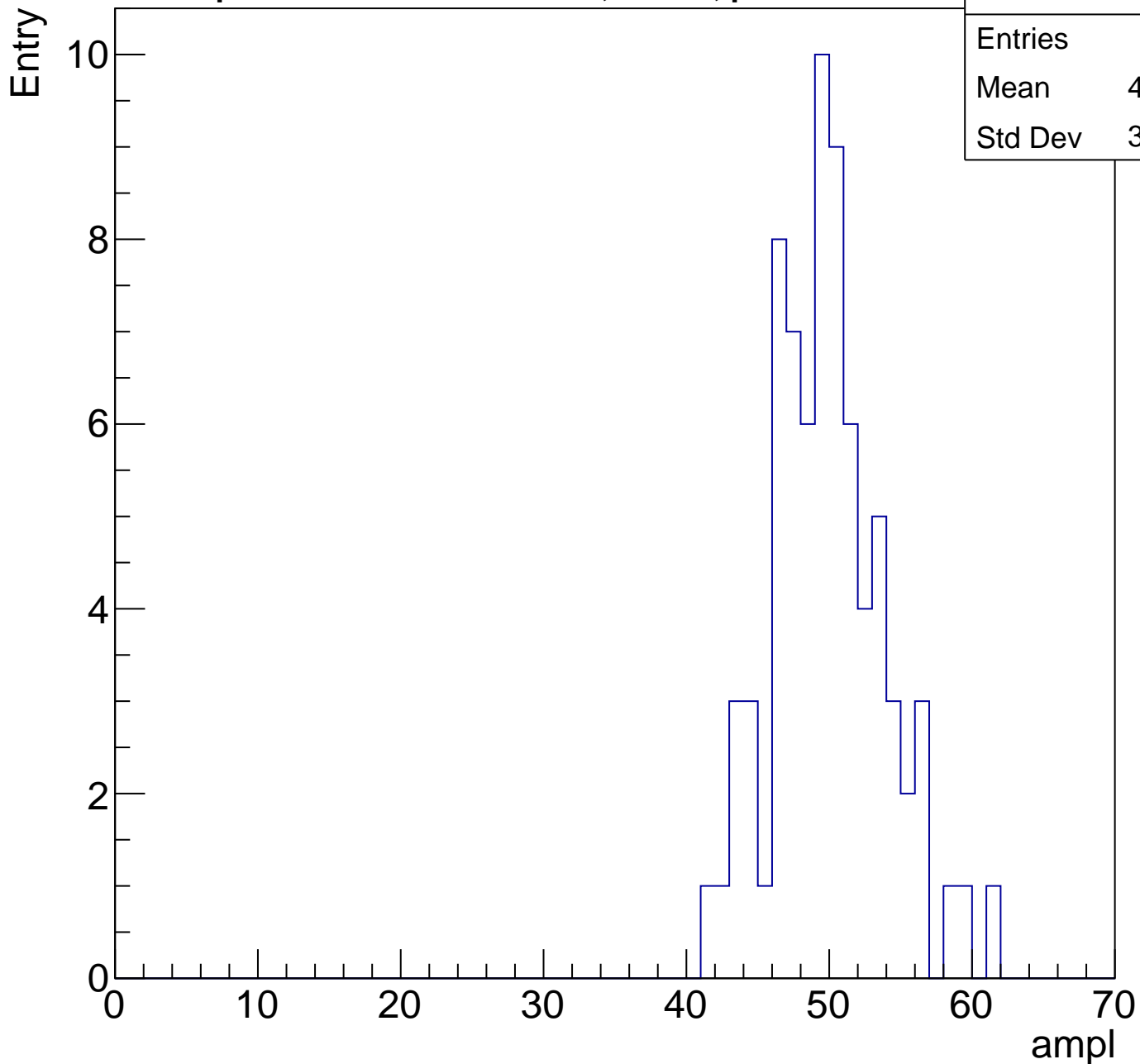
Gaus Width: 3.9788



B0L102S, U20-ch114, adc3

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.48 |
| Std Dev | 3.948 |

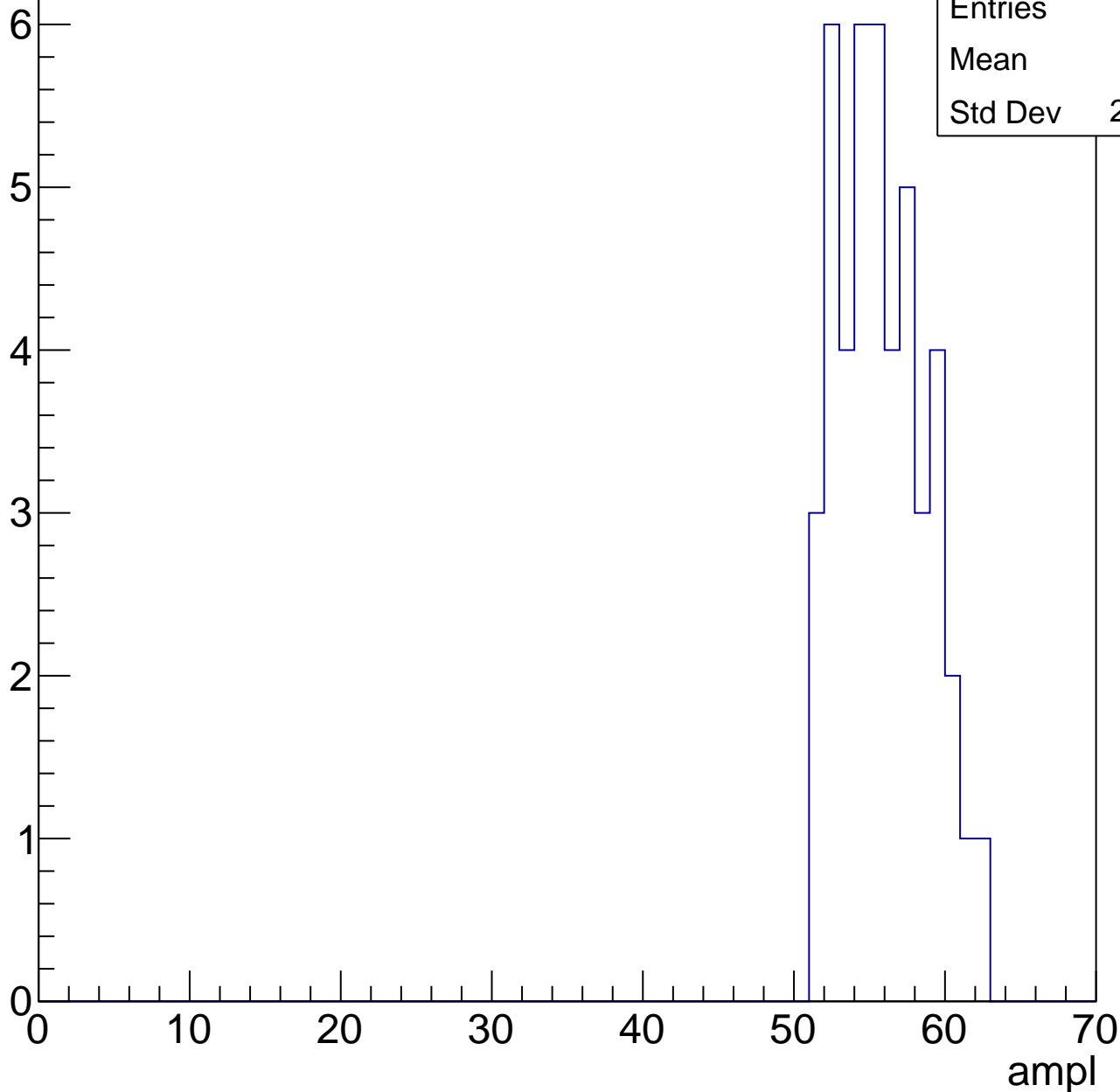


B0L102S, U20-ch114, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 55.4 |
| Std Dev | 2.855 |

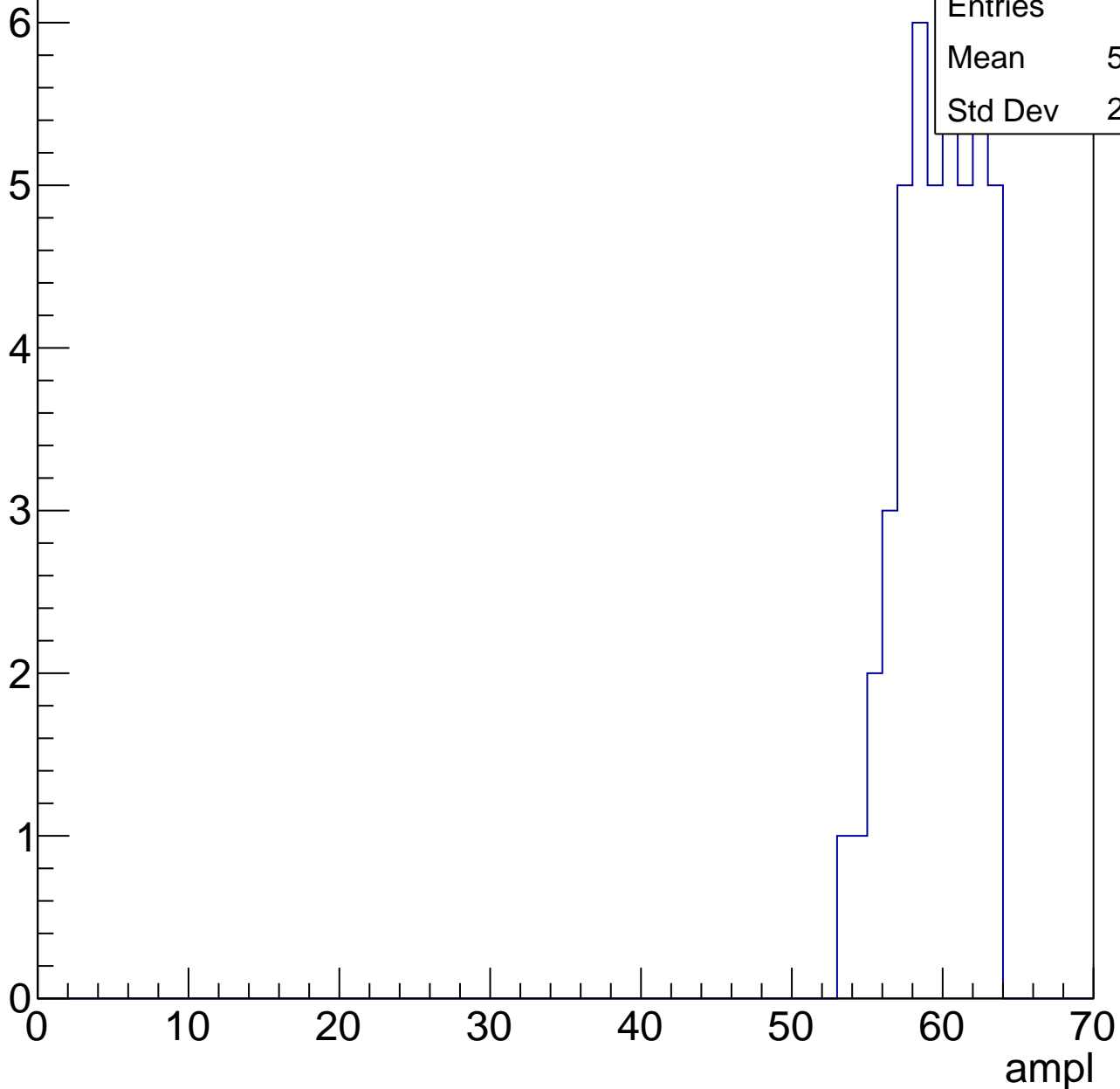


B0L102S, U20-ch114, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

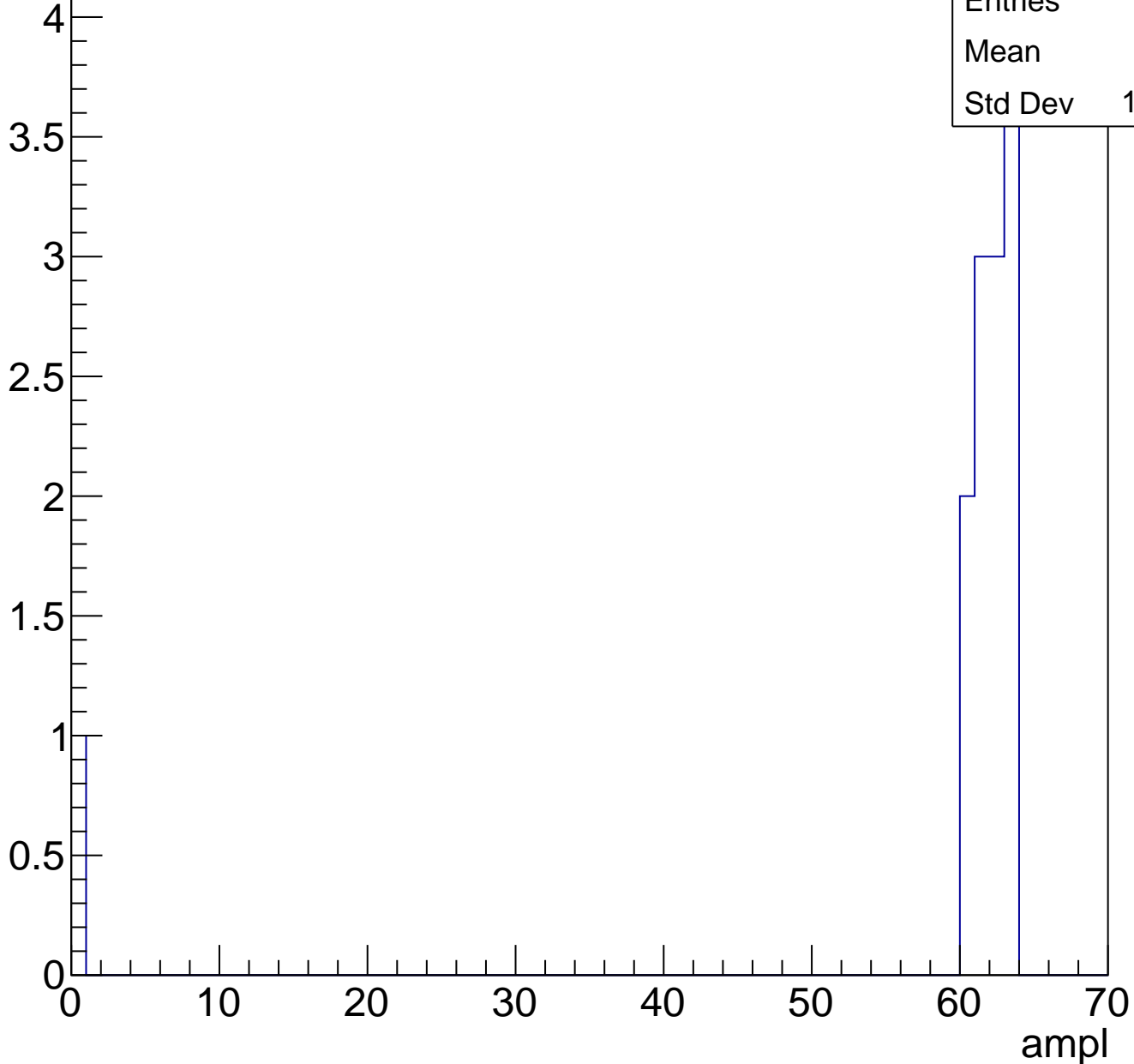
| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.22 |
| Std Dev | 2.598 |



B0L102S, U20-ch114, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch114, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch115, adc0

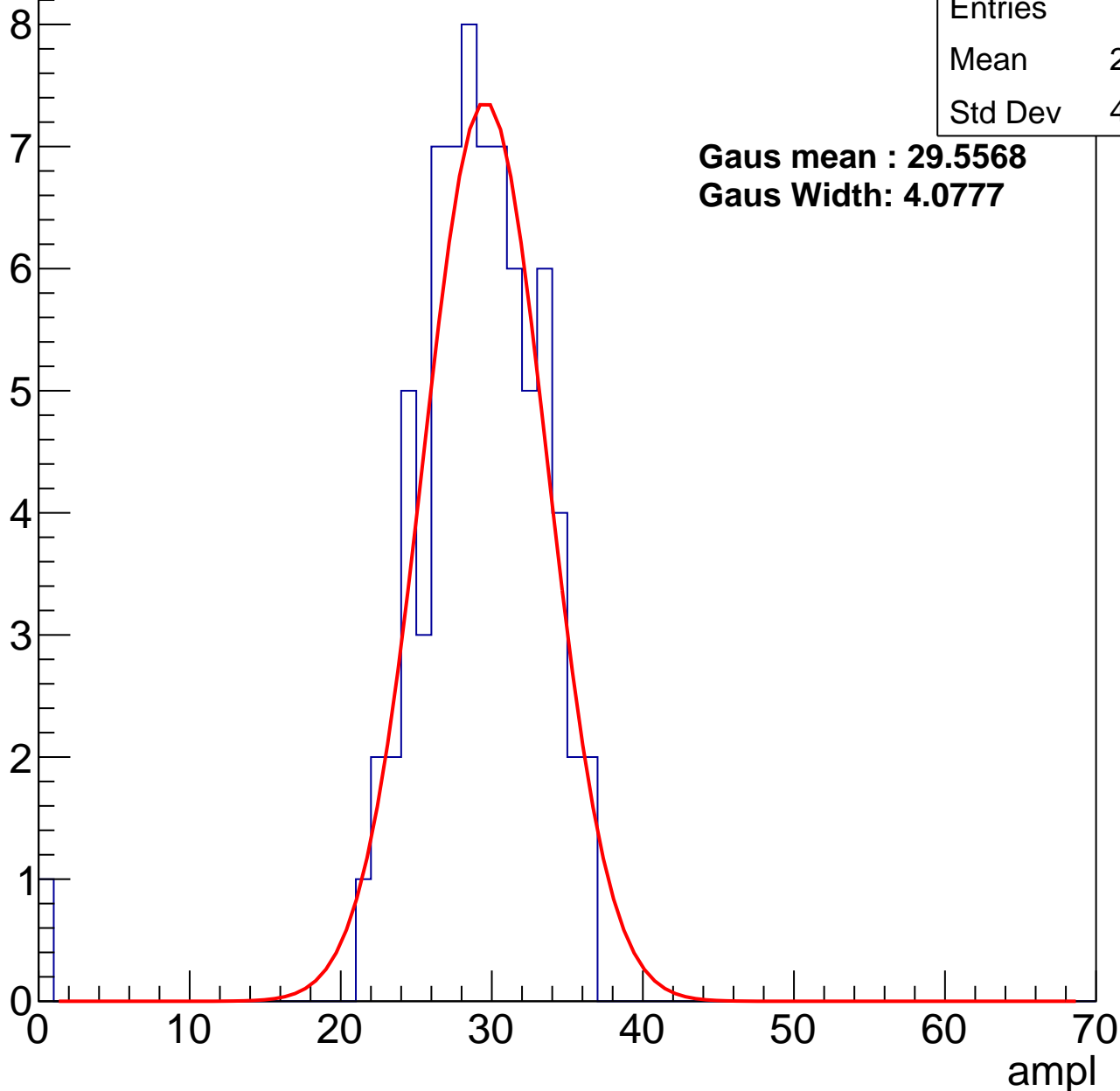
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 28.48 |
| Std Dev | 4.856 |

Gaus mean : 29.5568

Gaus Width: 4.0777



B0L102S, U20-ch115, adc1

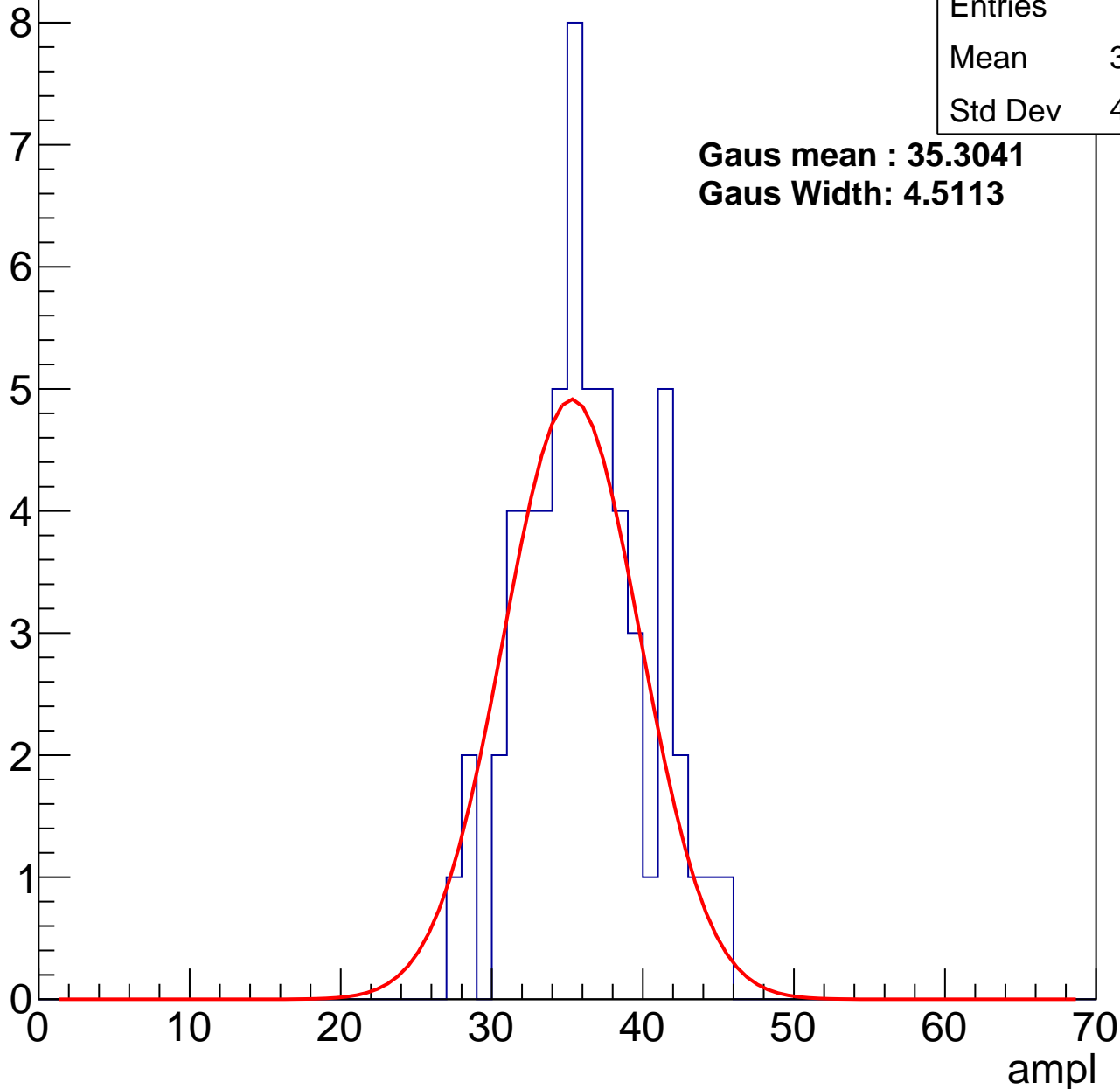
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 35.72 |
| Std Dev | 4.068 |

Gaus mean : 35.3041

Gaus Width: 4.5113



B0L102S, U20-ch115, adc2

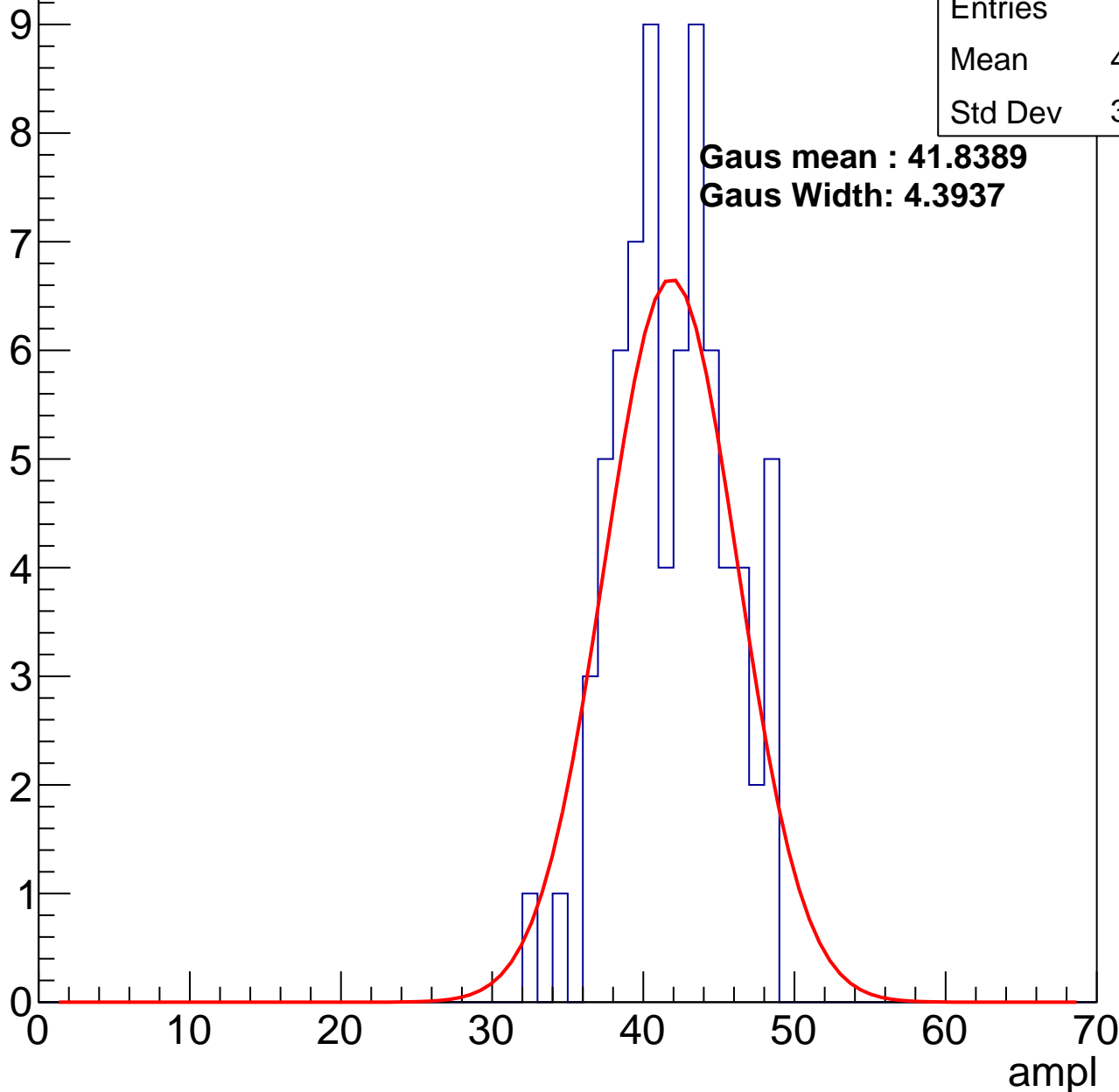
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 41.46 |
| Std Dev | 3.609 |

Gaus mean : 41.8389

Gaus Width: 4.3937

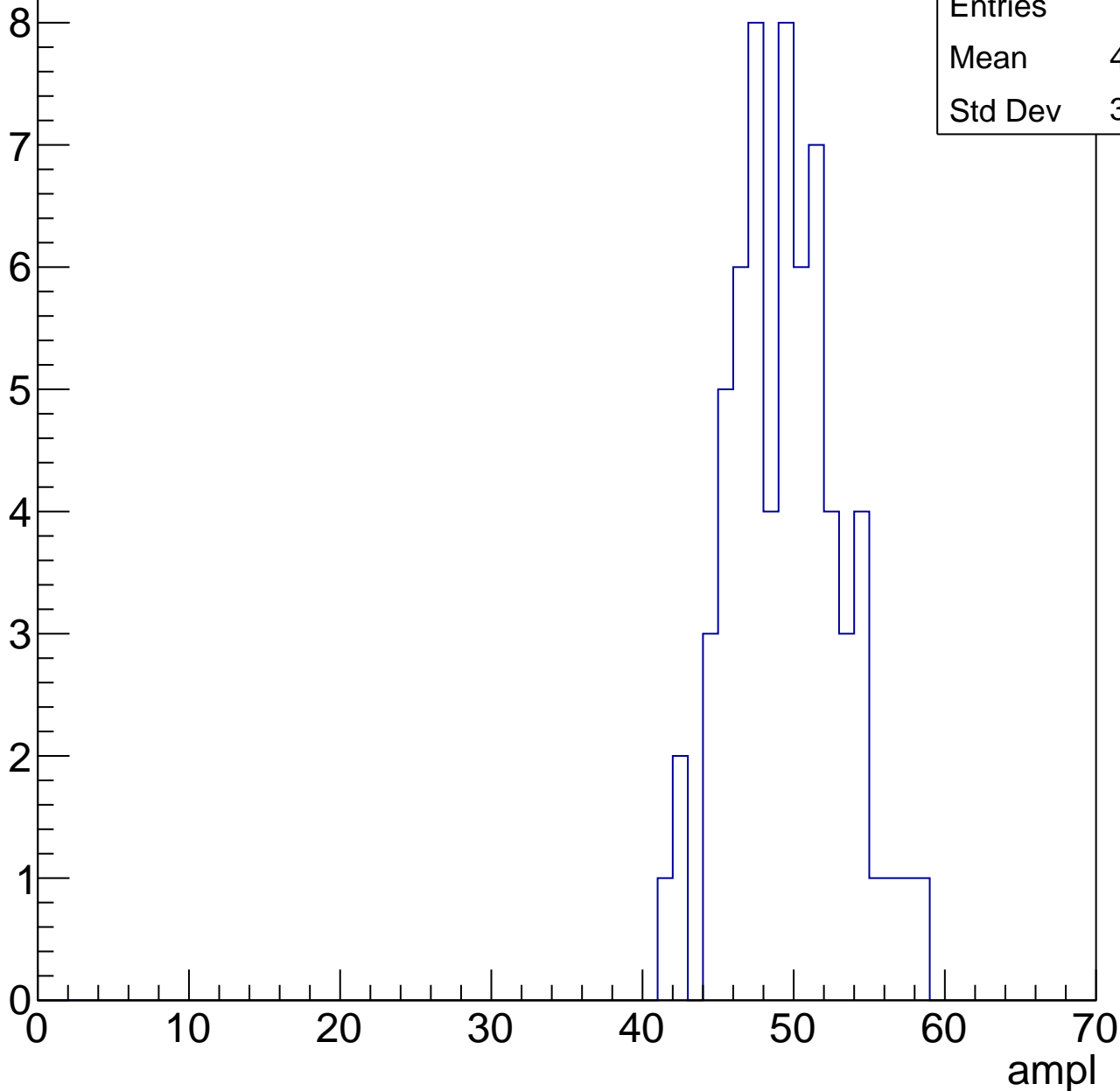


B0L102S, U20-ch115, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 48.98 |
| Std Dev | 3.627 |

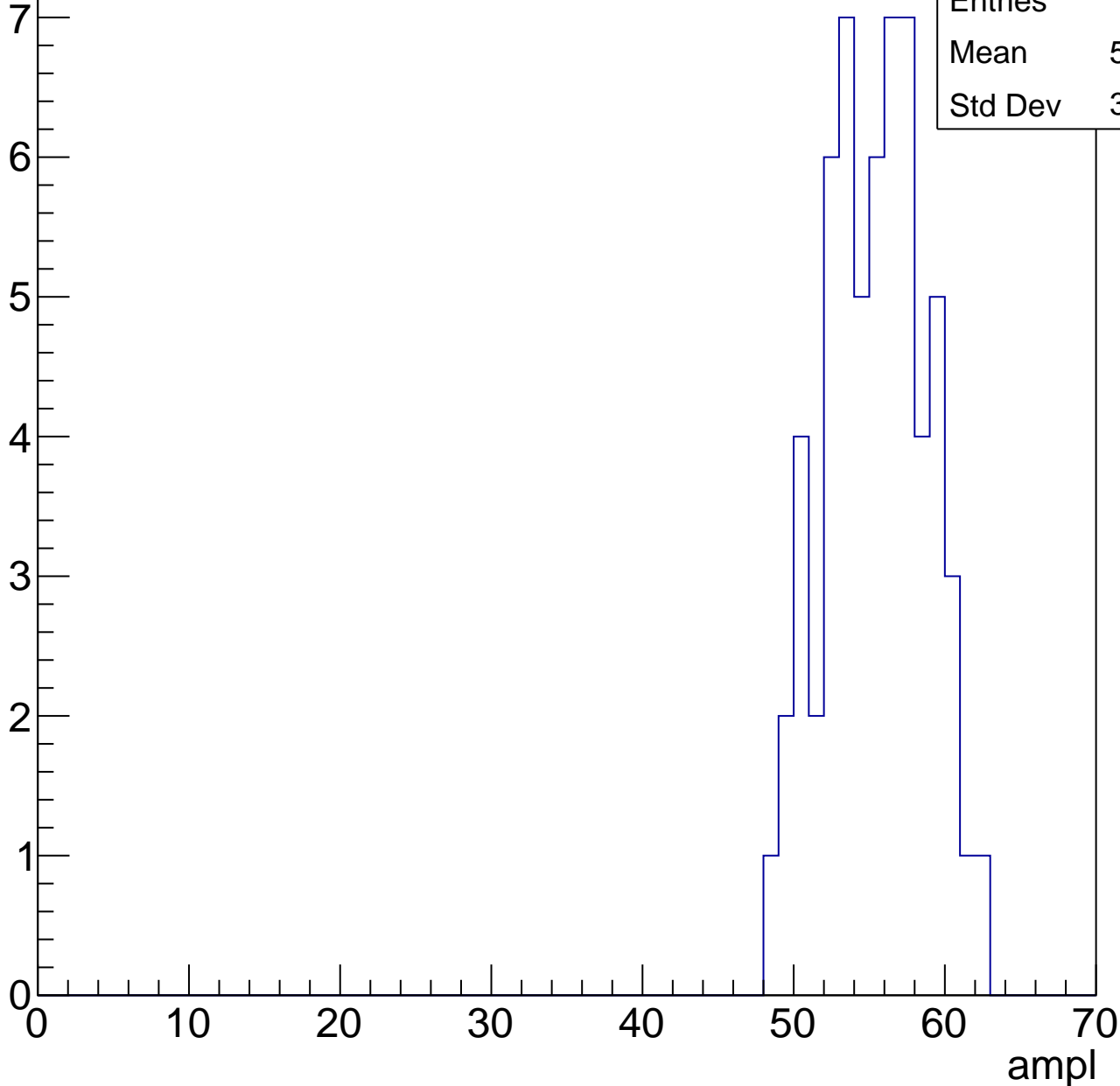


B0L102S, U20-ch115, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 54.95 |
| Std Dev | 3.266 |

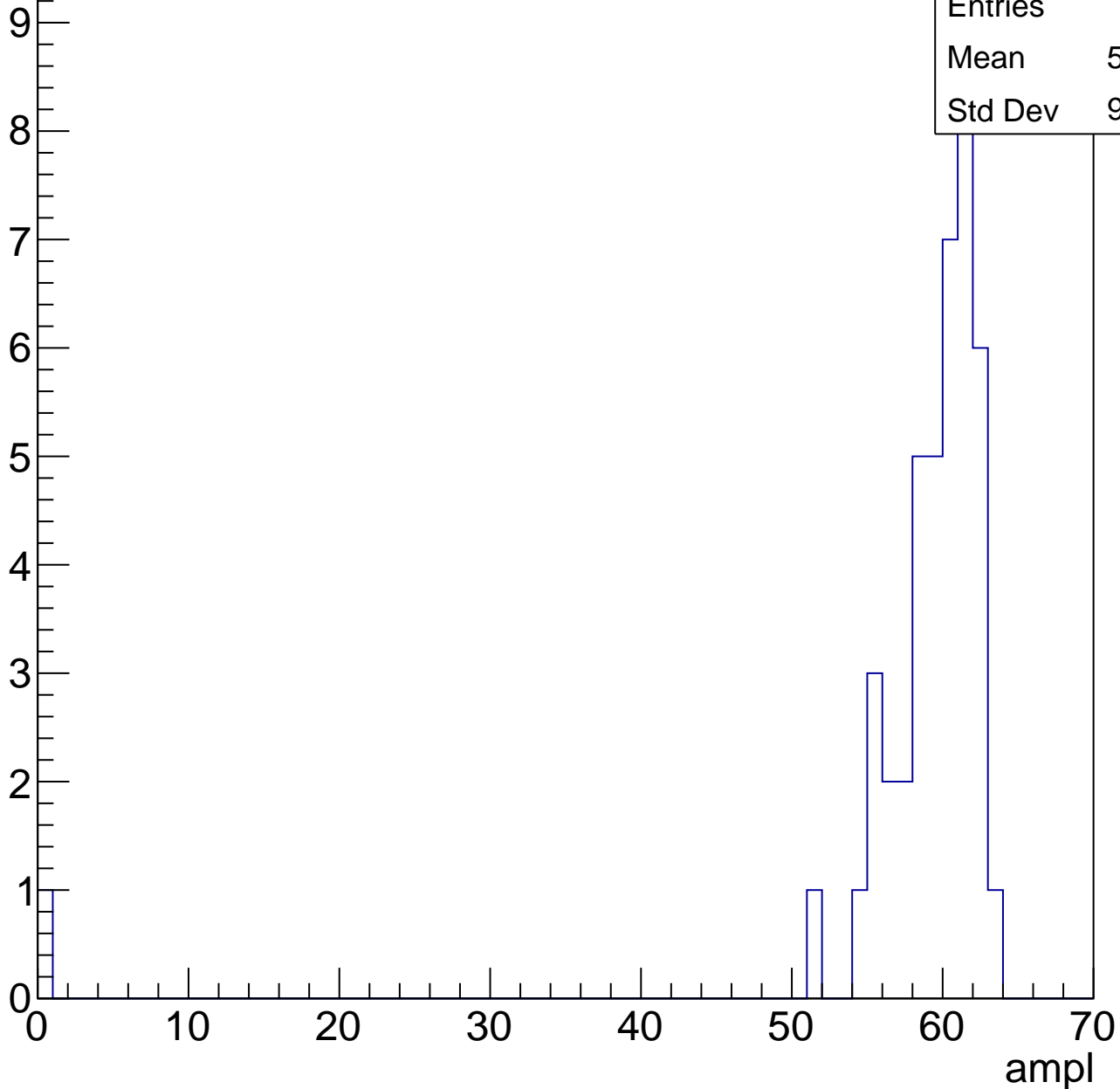


B0L102S, U20-ch115, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 57.79 |
| Std Dev | 9.275 |

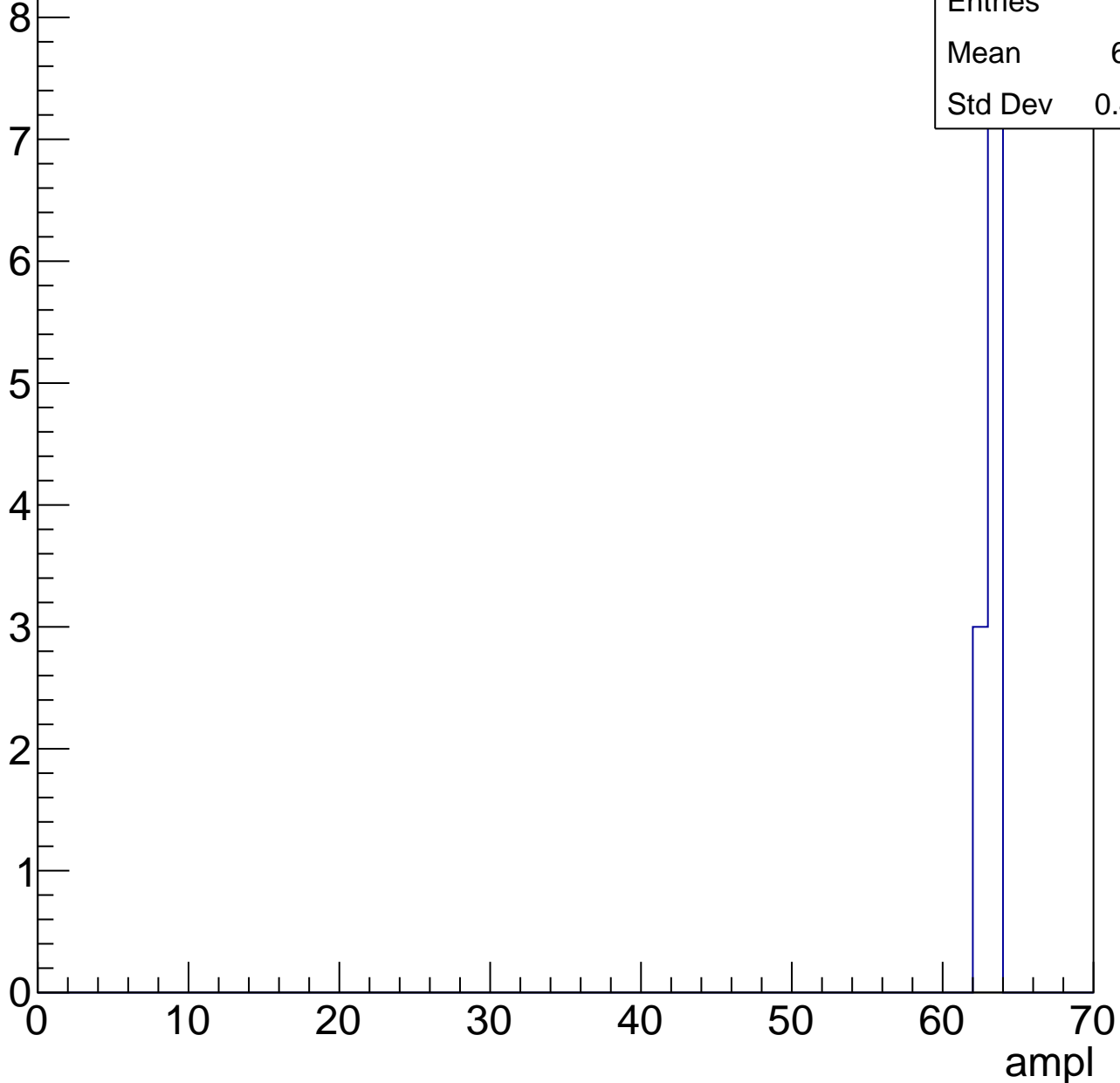


B0L102S, U20-ch115, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|--------|
| Entries | 11 |
| Mean | 62.73 |
| Std Dev | 0.4454 |



B0L102S, U20-ch115, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch116, adc0

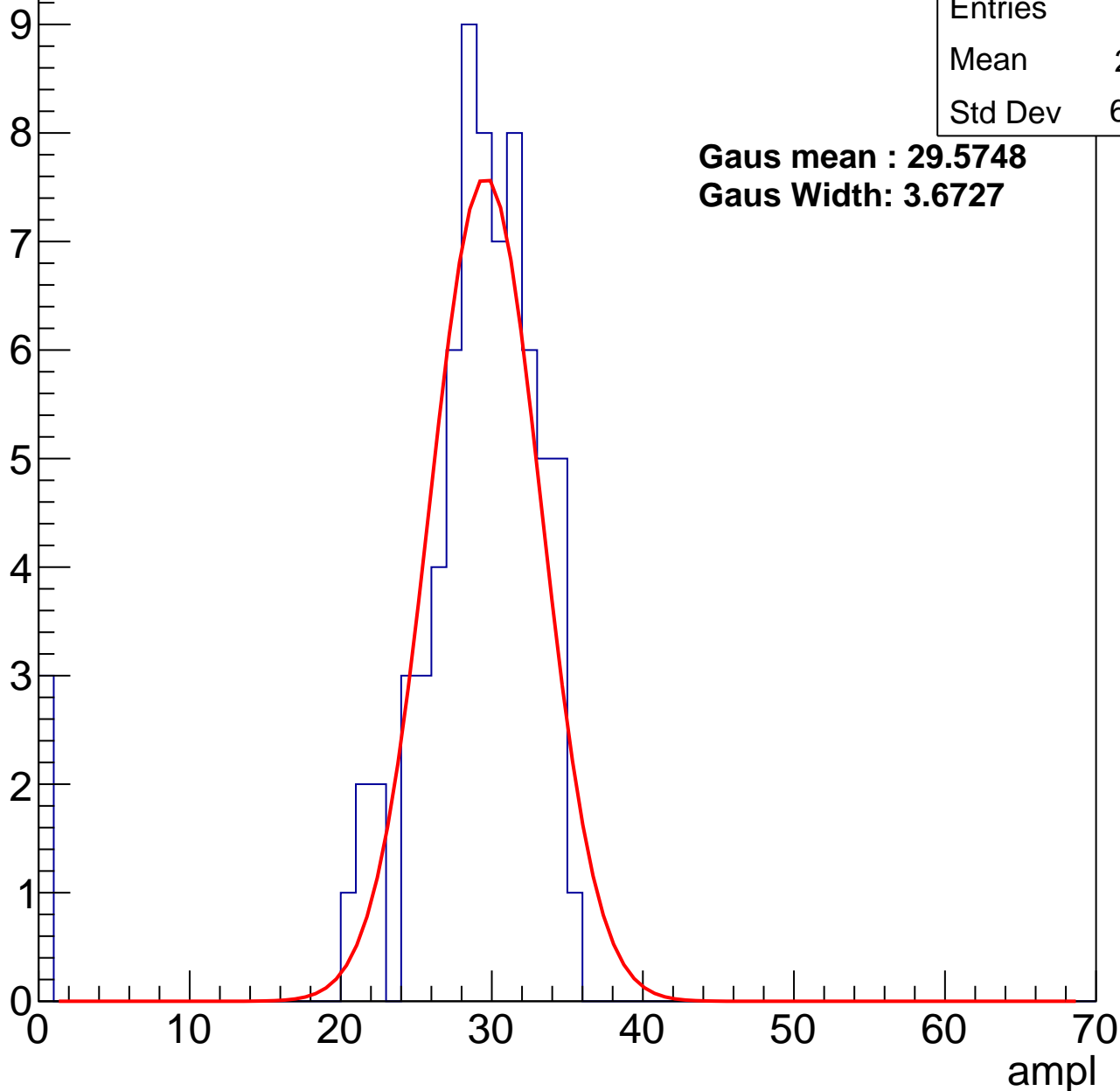
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 27.71 |
| Std Dev | 6.657 |

Gaus mean : 29.5748

Gaus Width: 3.6727



B0L102S, U20-ch116, adc1

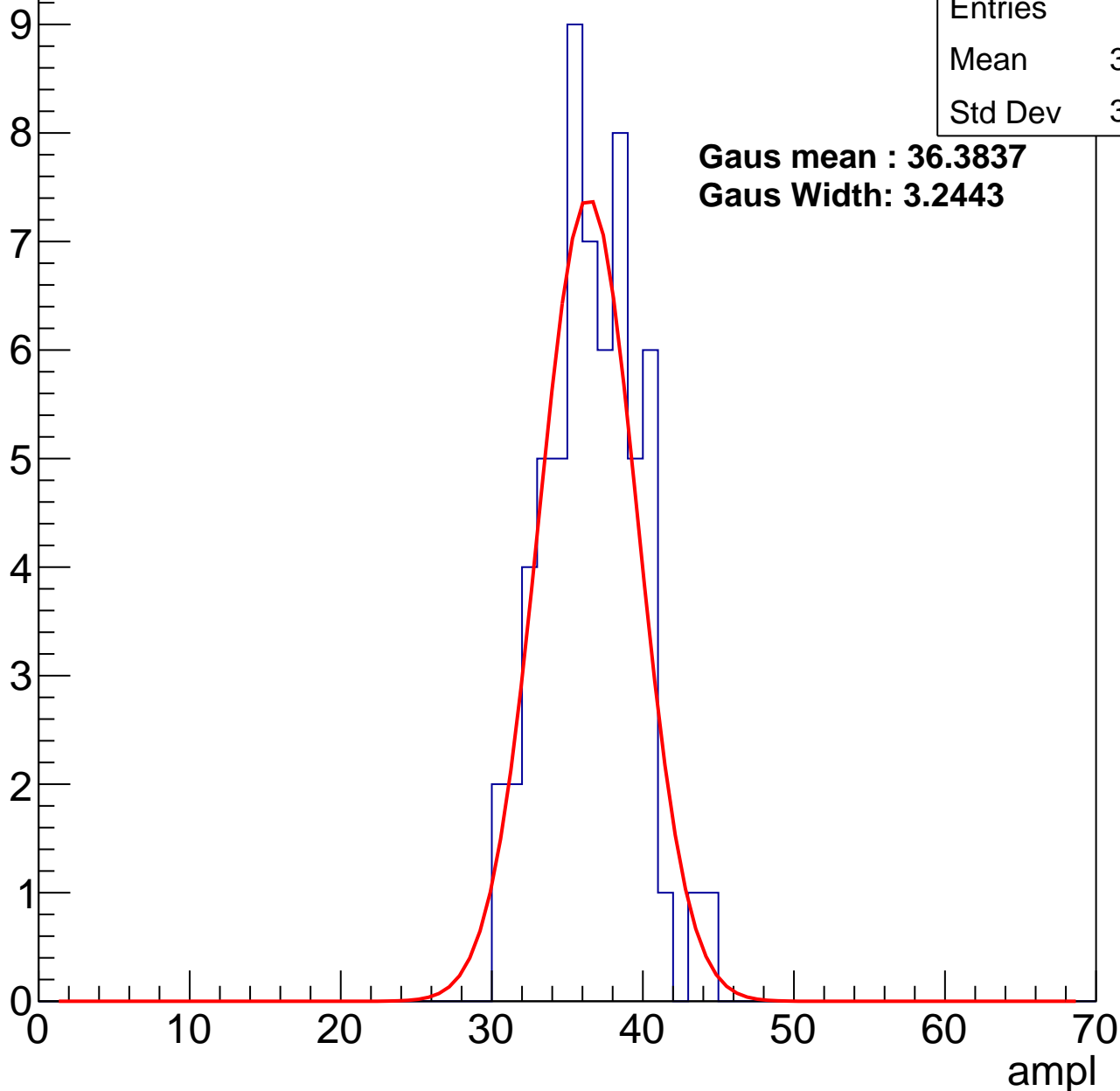
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 36.15 |
| Std Dev | 3.047 |

Gaus mean : 36.3837

Gaus Width: 3.2443



B0L102S, U20-ch116, adc2

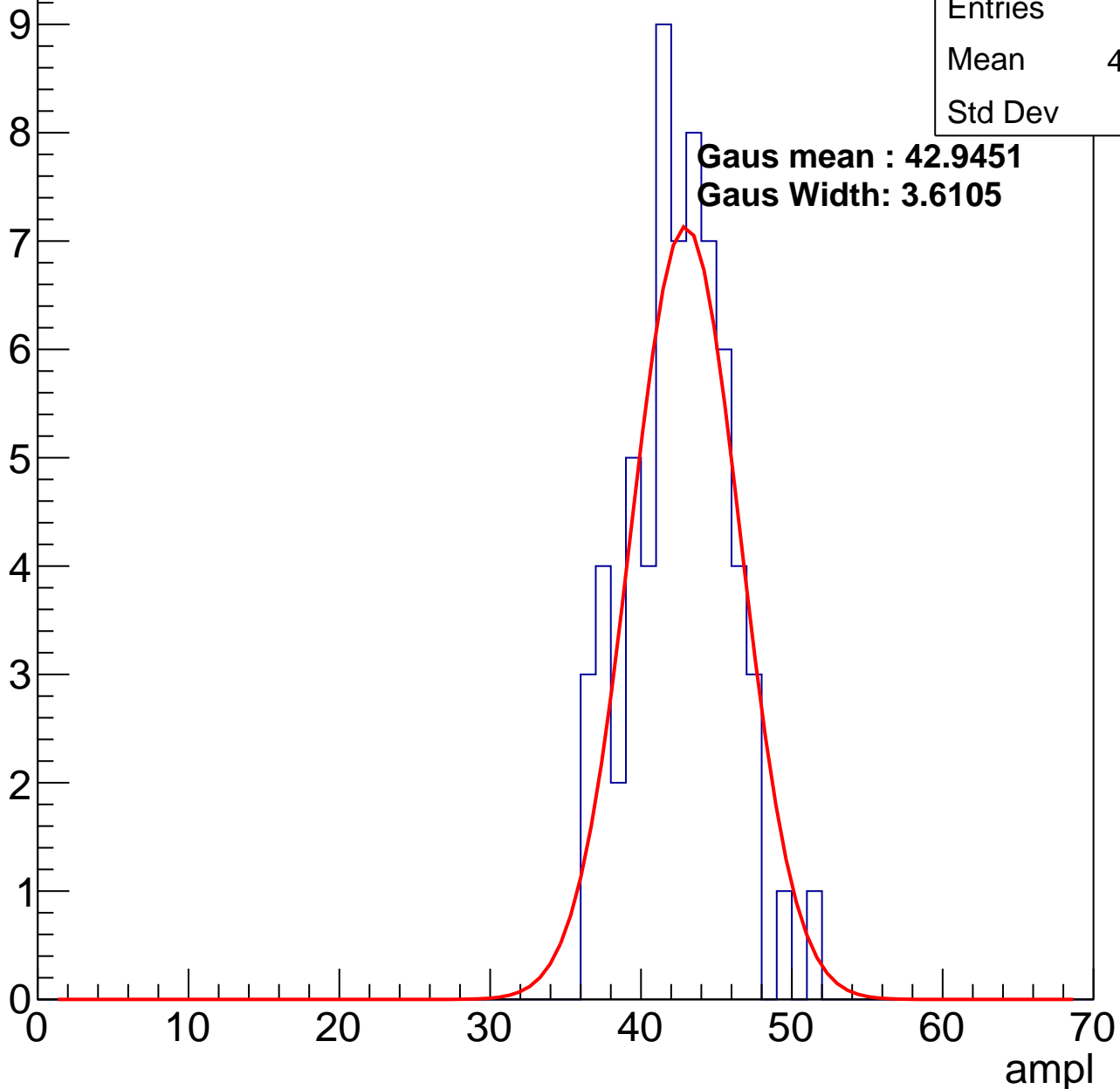
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.14 |
| Std Dev | 3.23 |

Gaus mean : 42.9451

Gaus Width: 3.6105

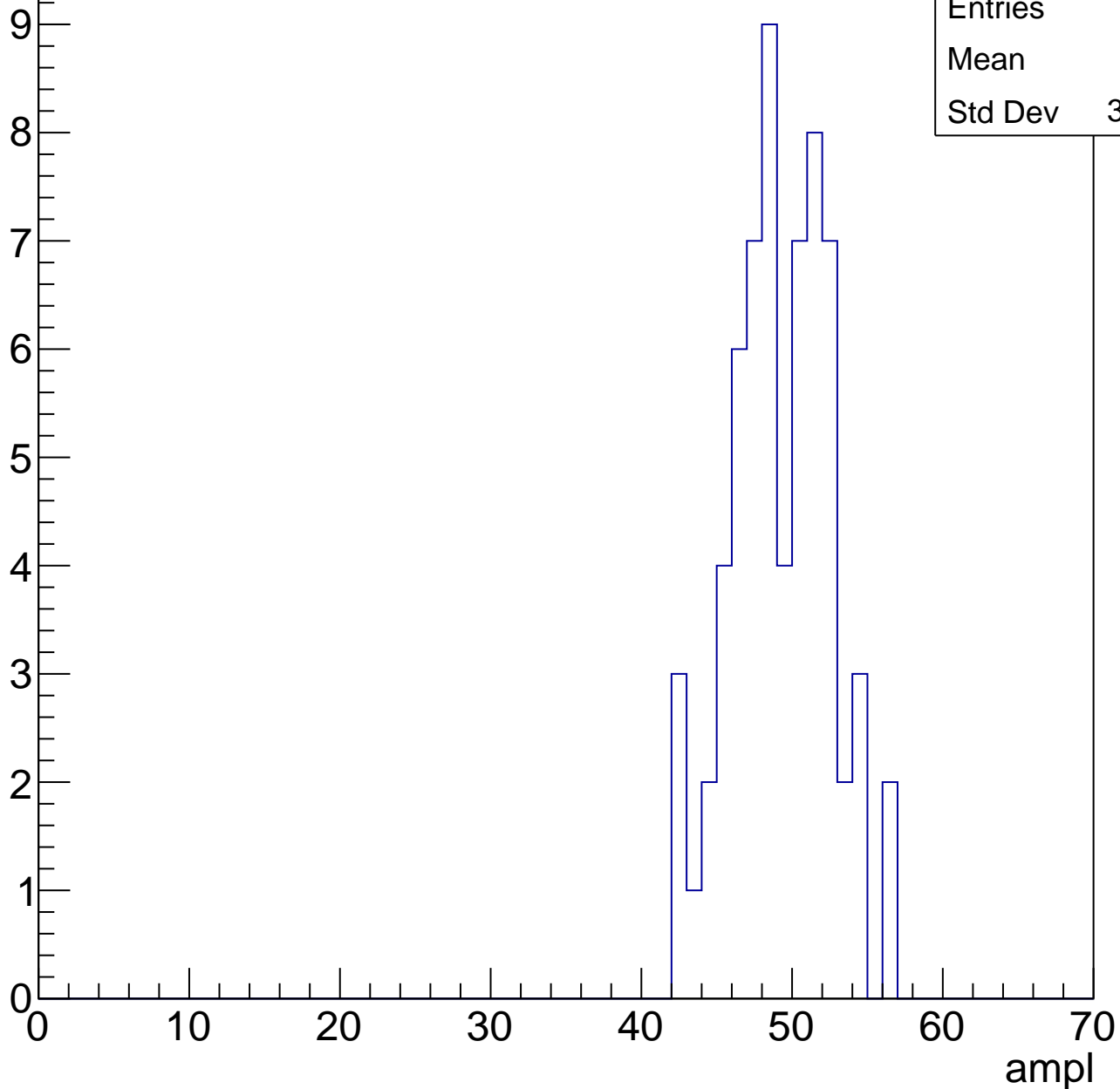


B0L102S, U20-ch116, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 48.8 |
| Std Dev | 3.264 |

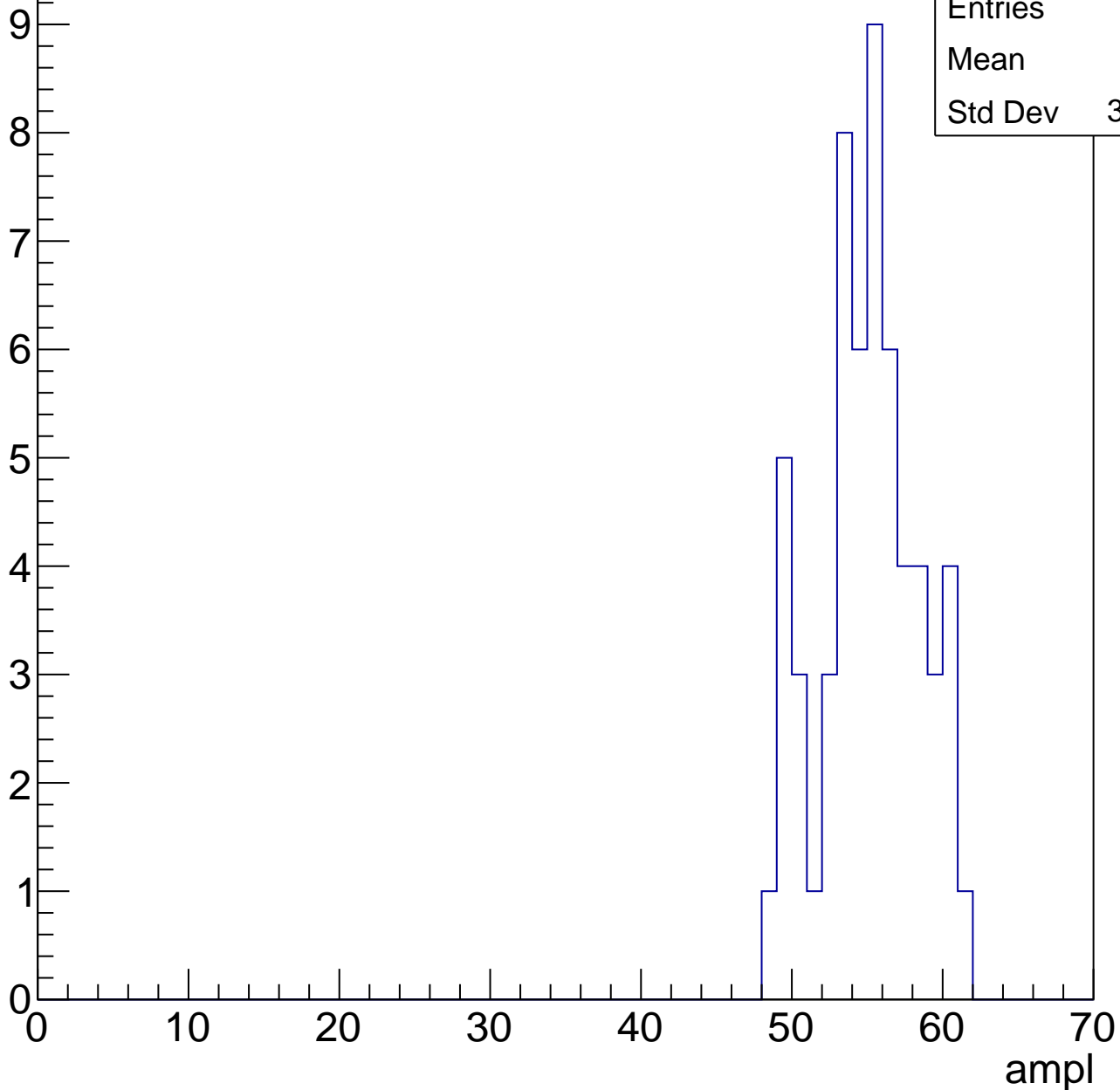


B0L102S, U20-ch116, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 54.6 |
| Std Dev | 3.264 |

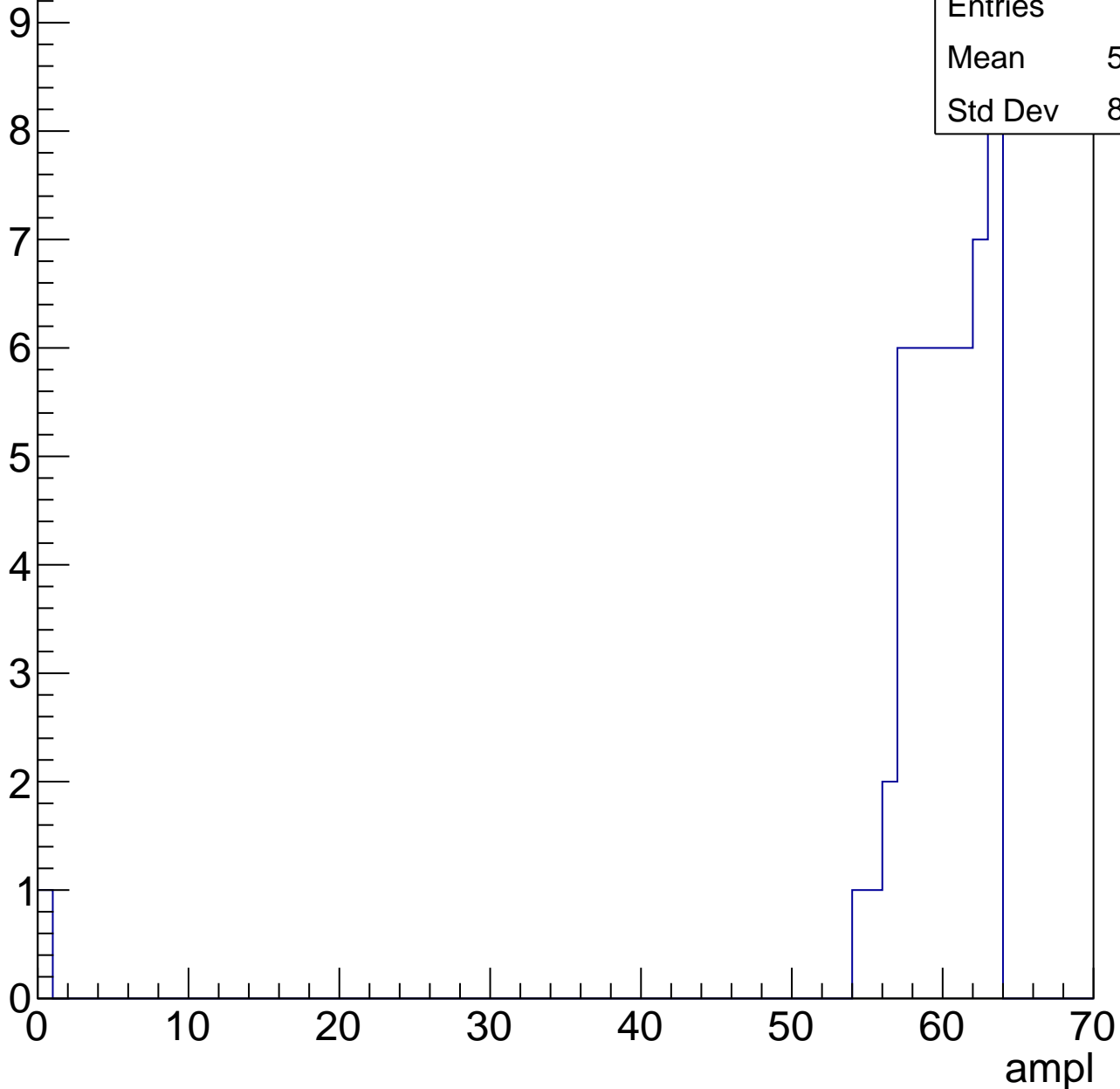


B0L102S, U20-ch116, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

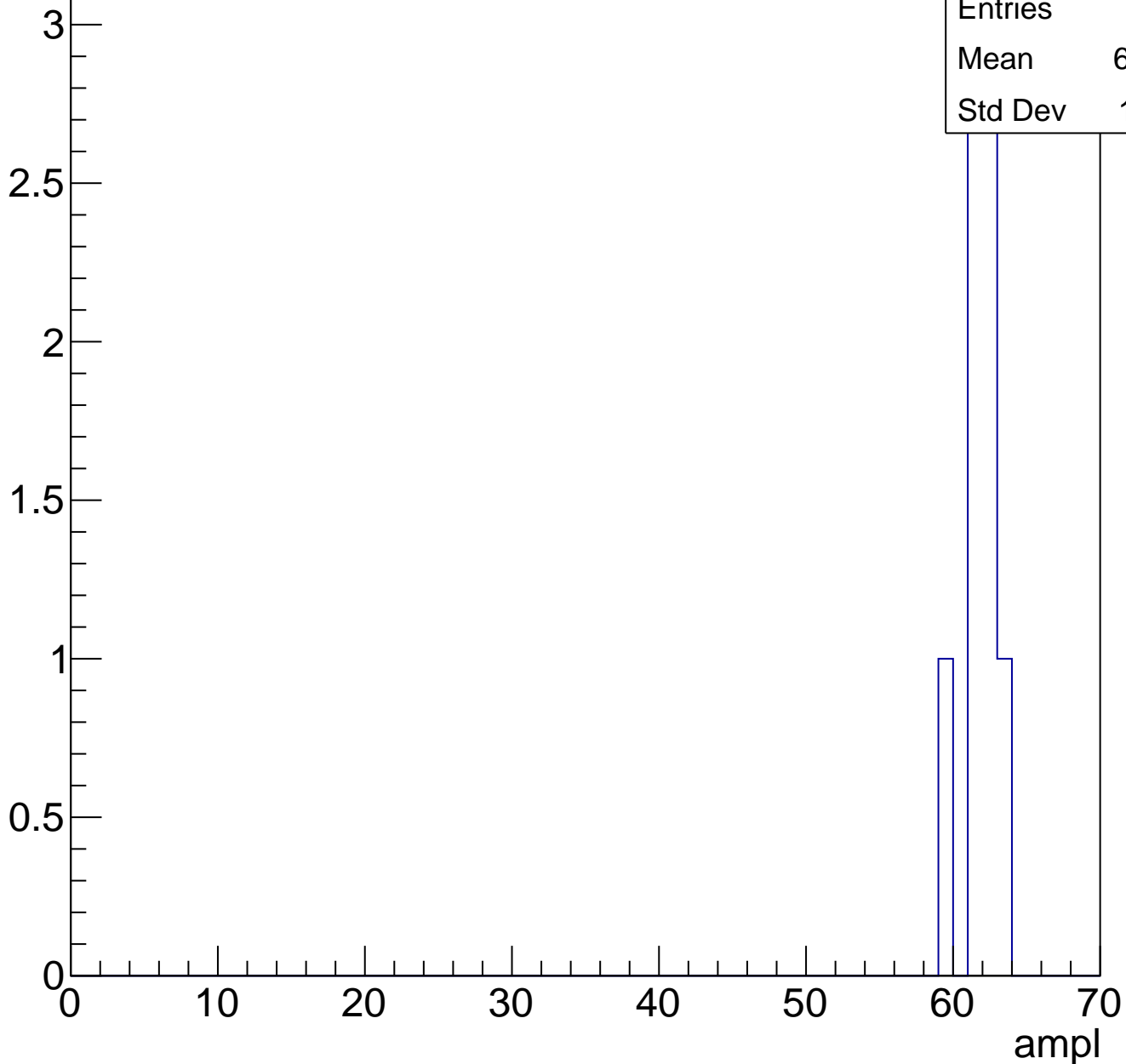
| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.67 |
| Std Dev | 8.633 |



B0L102S, U20-ch116, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch116, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch117, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 93 |
| Mean | 29.13 |
| Std Dev | 3.664 |

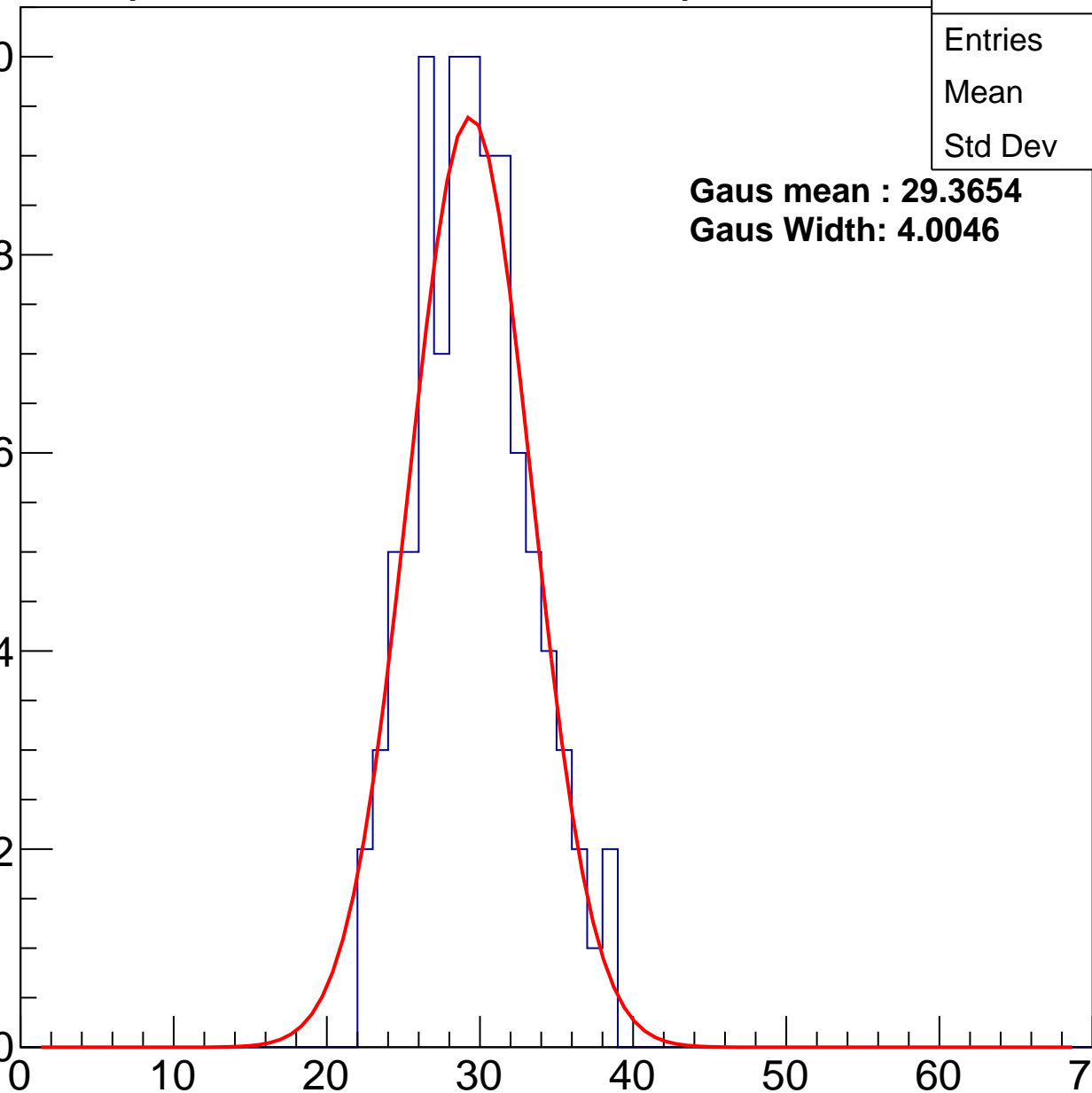
Gaus mean : 29.3654

Gaus Width: 4.0046

Entry

10
8
6
4
2
0

ampl



B0L102S, U20-ch117, adc1

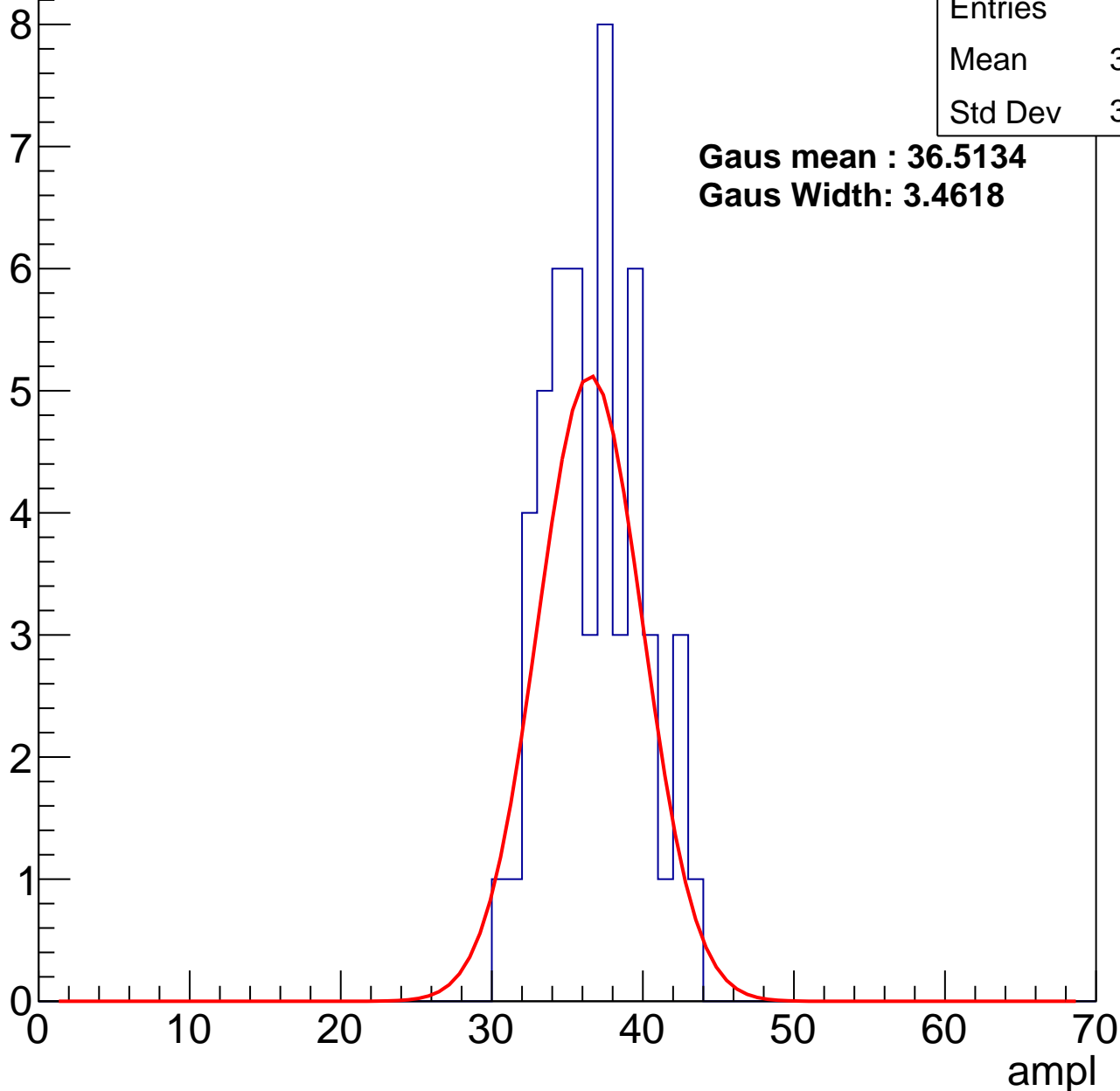
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 36.27 |
| Std Dev | 3.132 |

Gaus mean : 36.5134

Gaus Width: 3.4618



B0L102S, U20-ch117, adc2

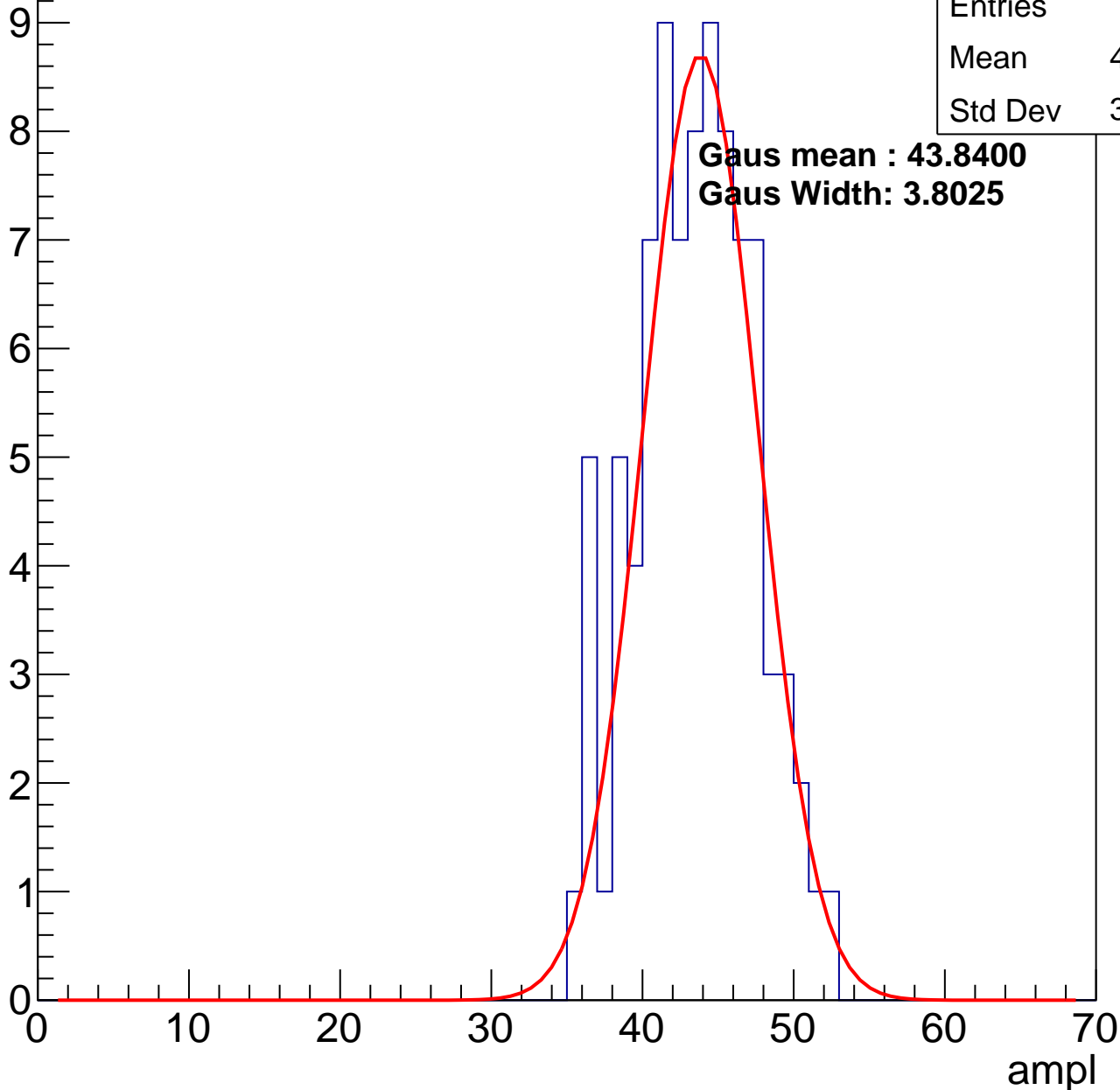
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 43.02 |
| Std Dev | 3.817 |

Gaus mean : 43.8400

Gaus Width: 3.8025

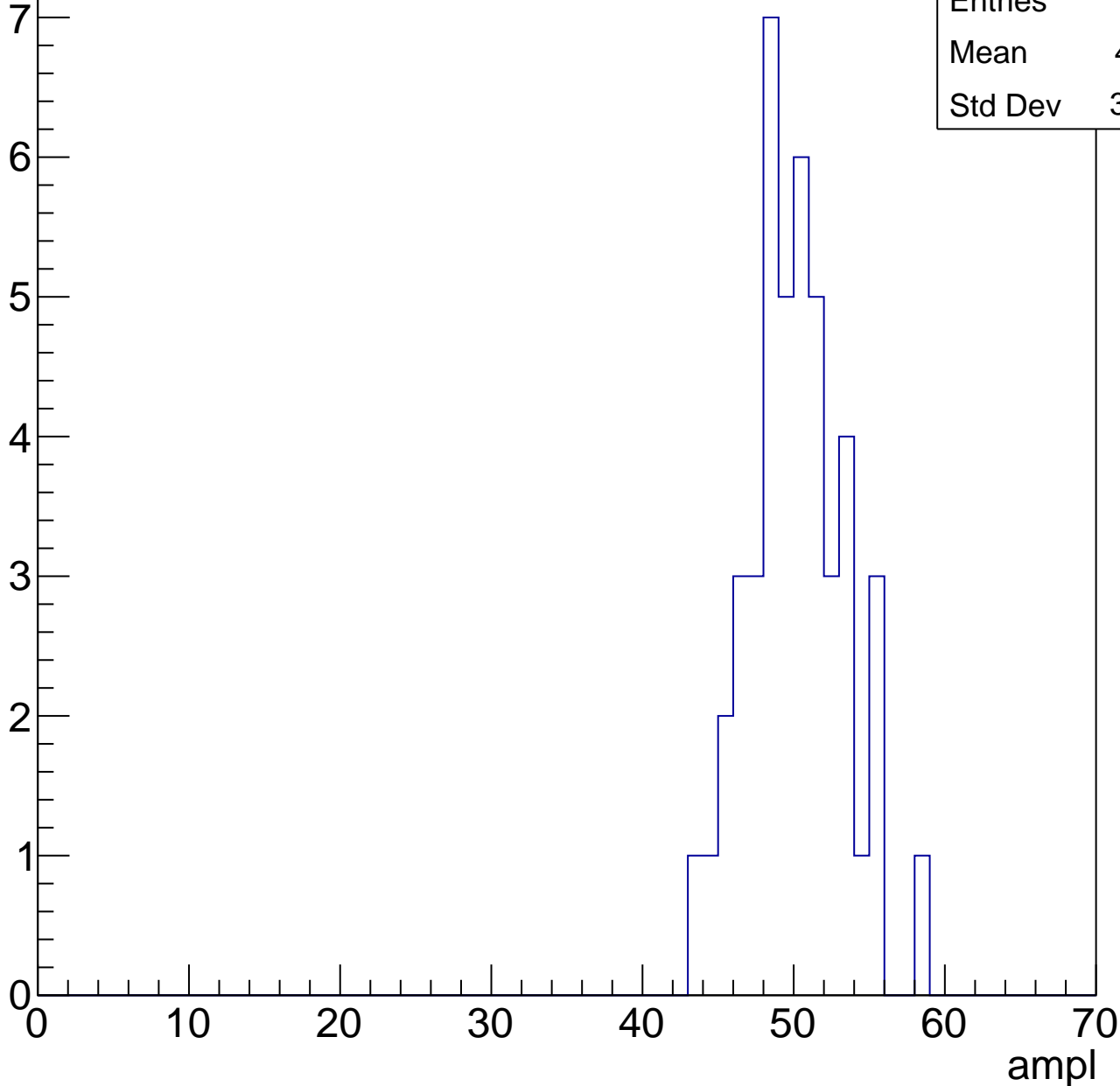


B0L102S, U20-ch117, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 49.71 |
| Std Dev | 3.153 |

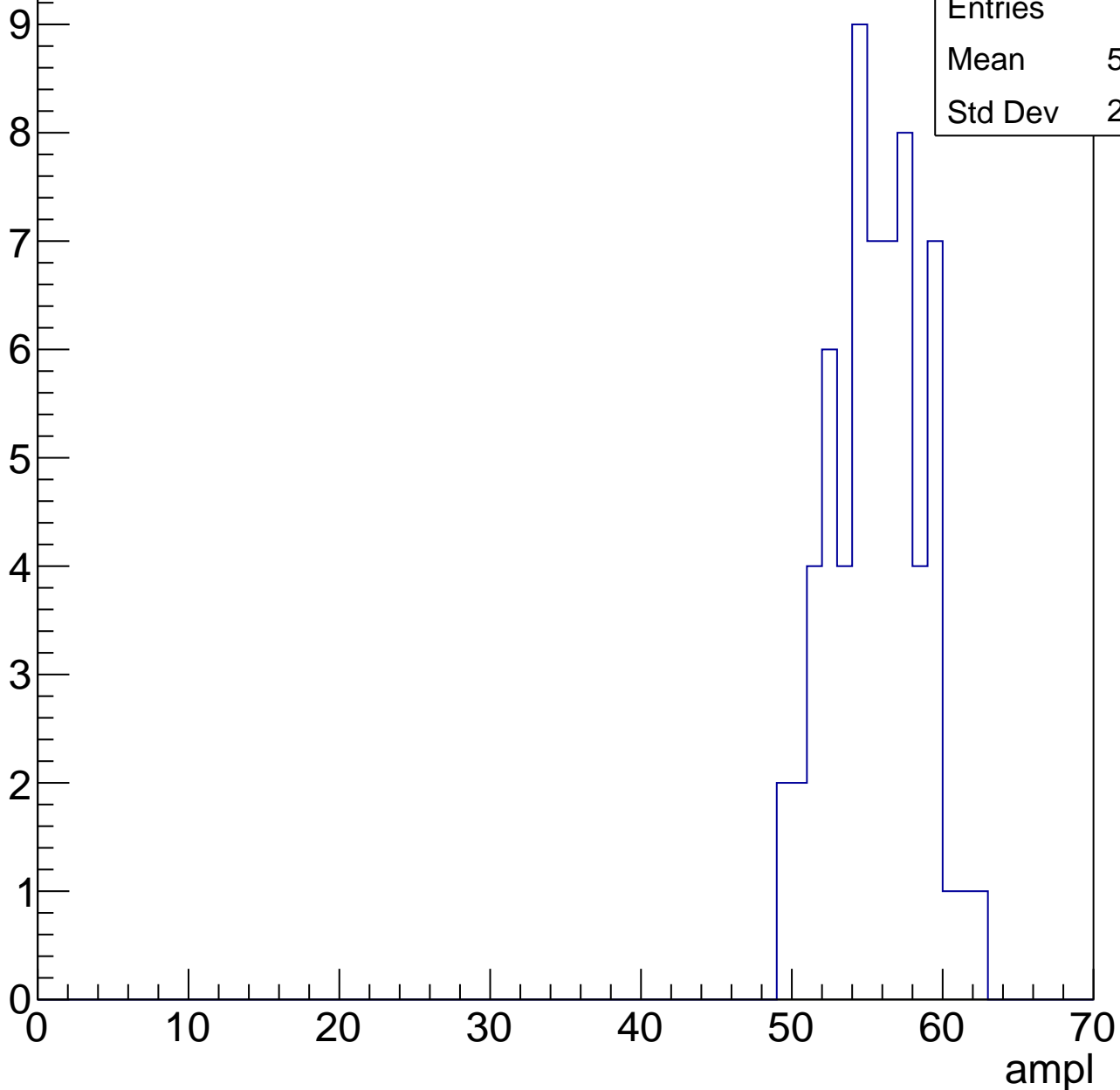


B0L102S, U20-ch117, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 55.13 |
| Std Dev | 2.984 |

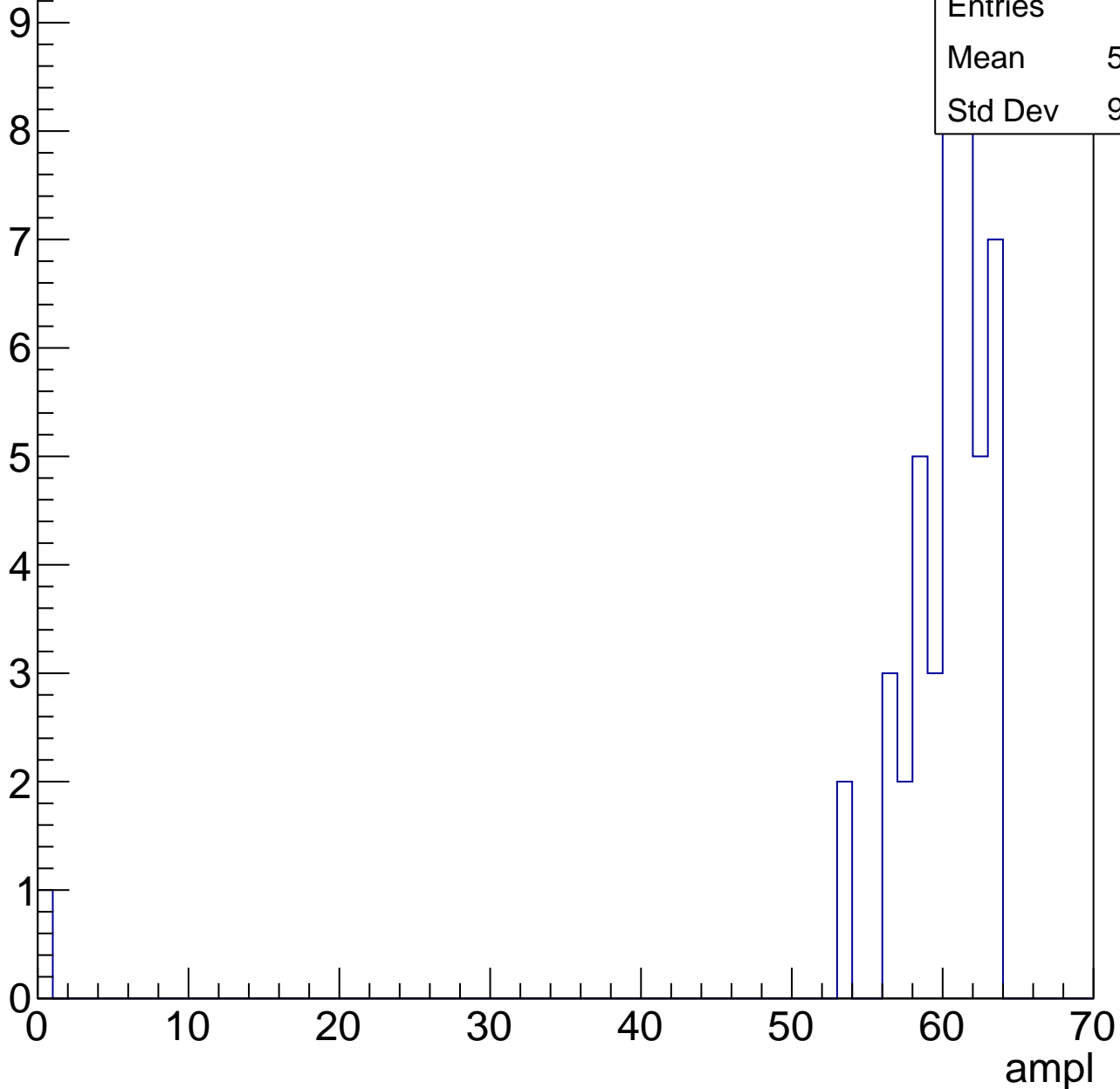


B0L102S, U20-ch117, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

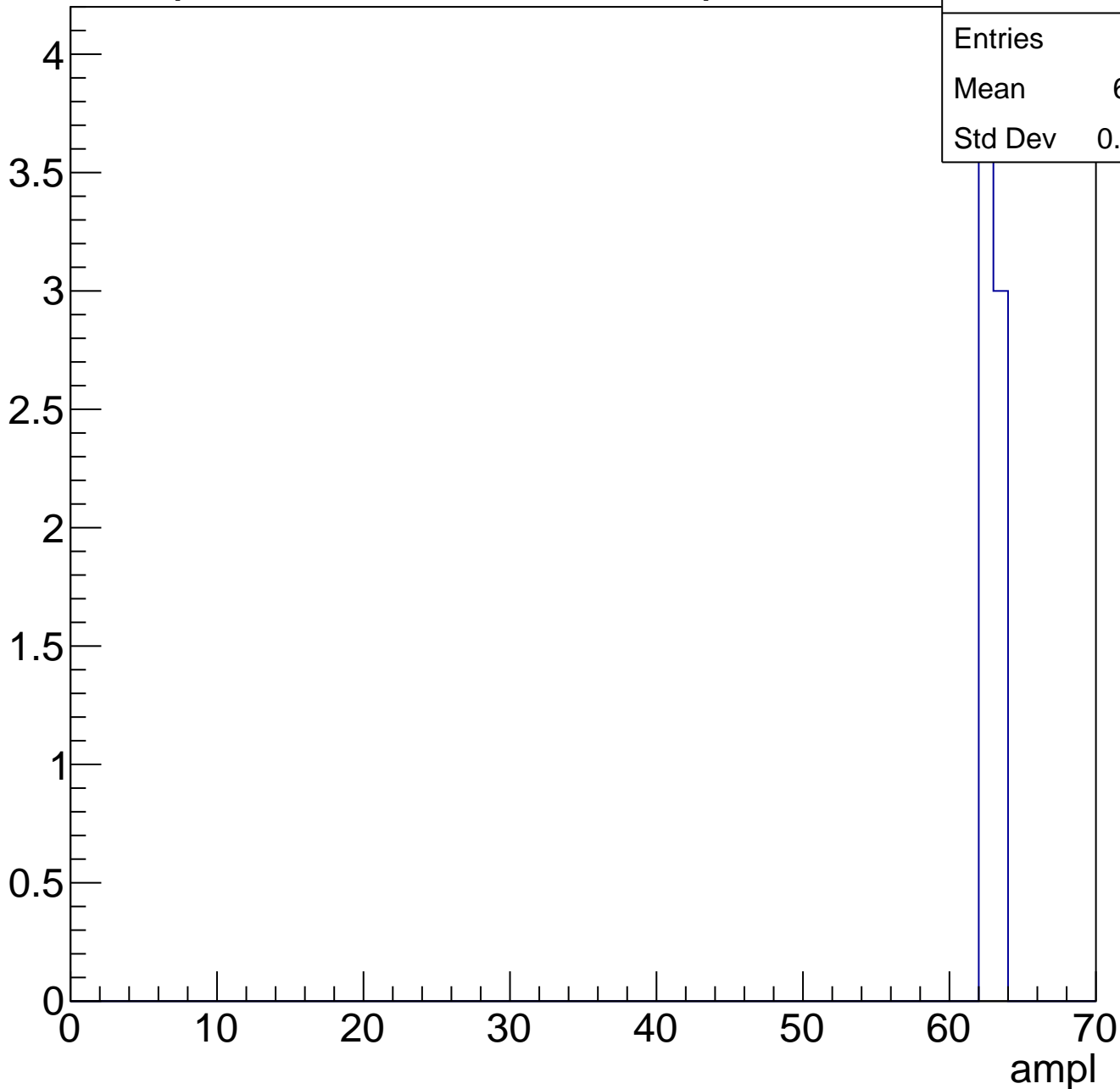
| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.59 |
| Std Dev | 9.074 |



B0L102S, U20-ch117, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

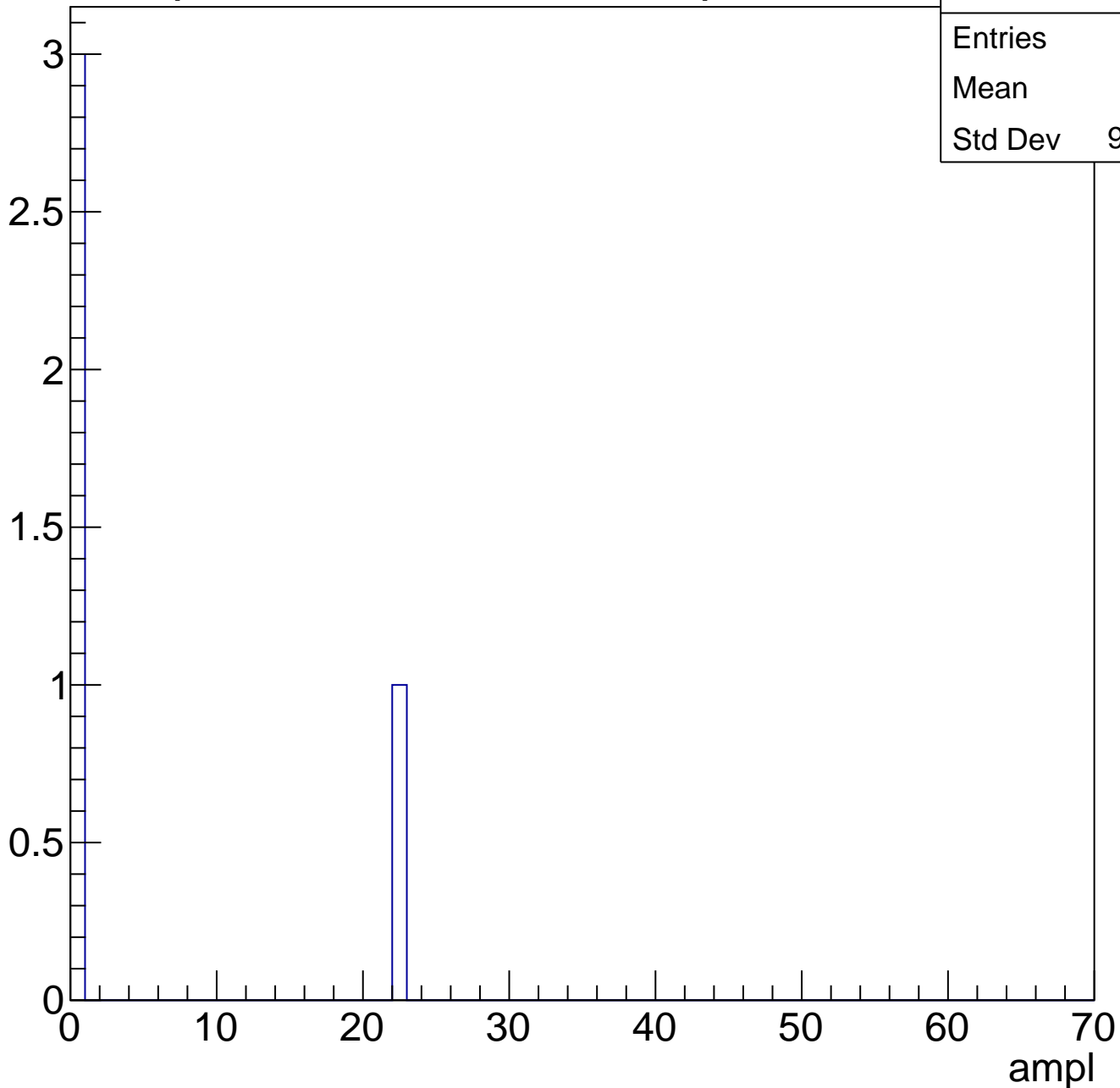


| | |
|---------|--------|
| Entries | 7 |
| Mean | 62.43 |
| Std Dev | 0.4949 |

B0L102S, U20-ch117, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 5.5 |
| Std Dev | 9.526 |

B0L102S, U20-ch118, adc0

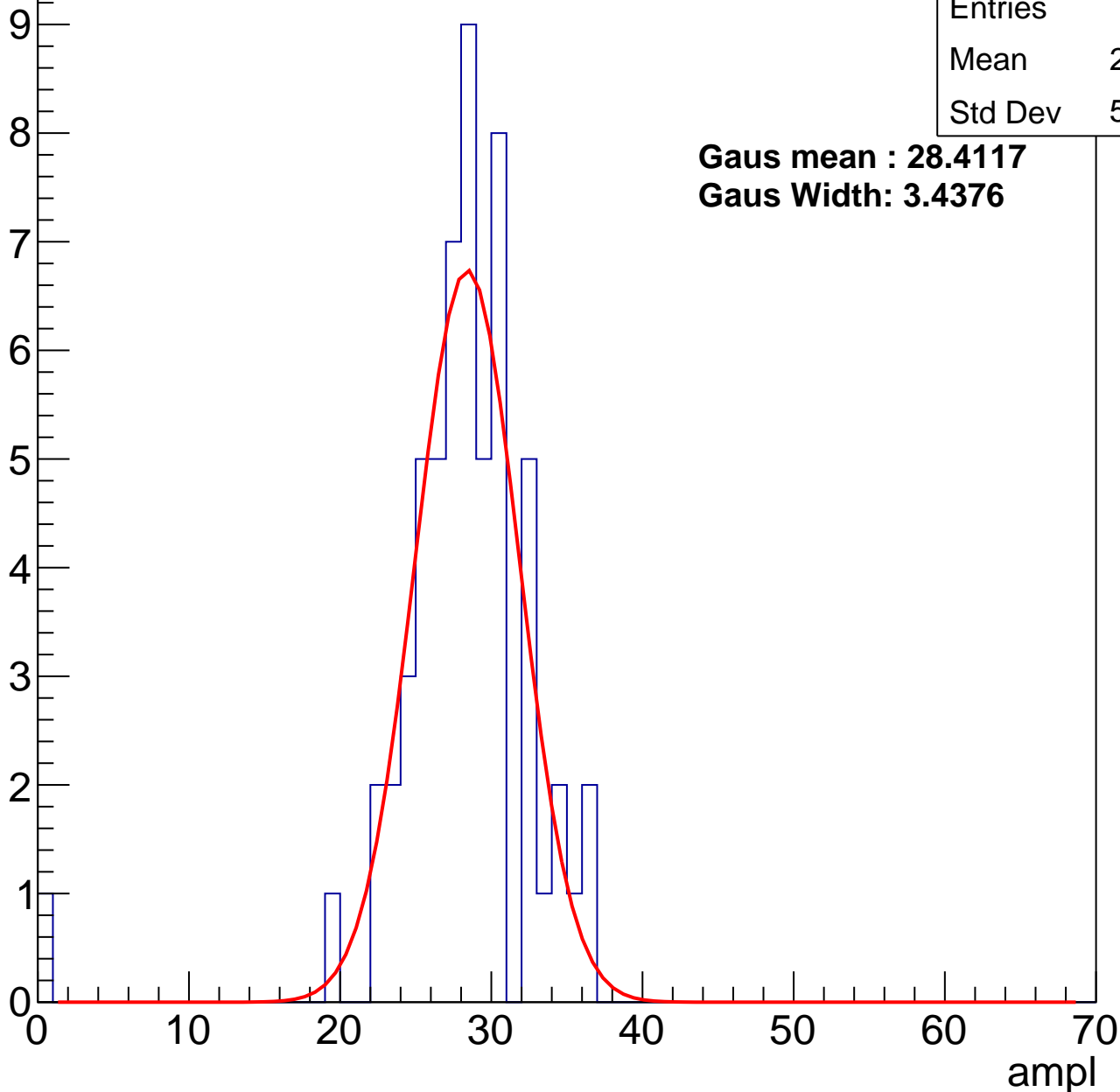
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 27.63 |
| Std Dev | 5.025 |

Gaus mean : 28.4117

Gaus Width: 3.4376



B0L102S, U20-ch118, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 68 |
| Mean | 33.94 |
| Std Dev | 5.445 |

Gaus mean : 35.6170

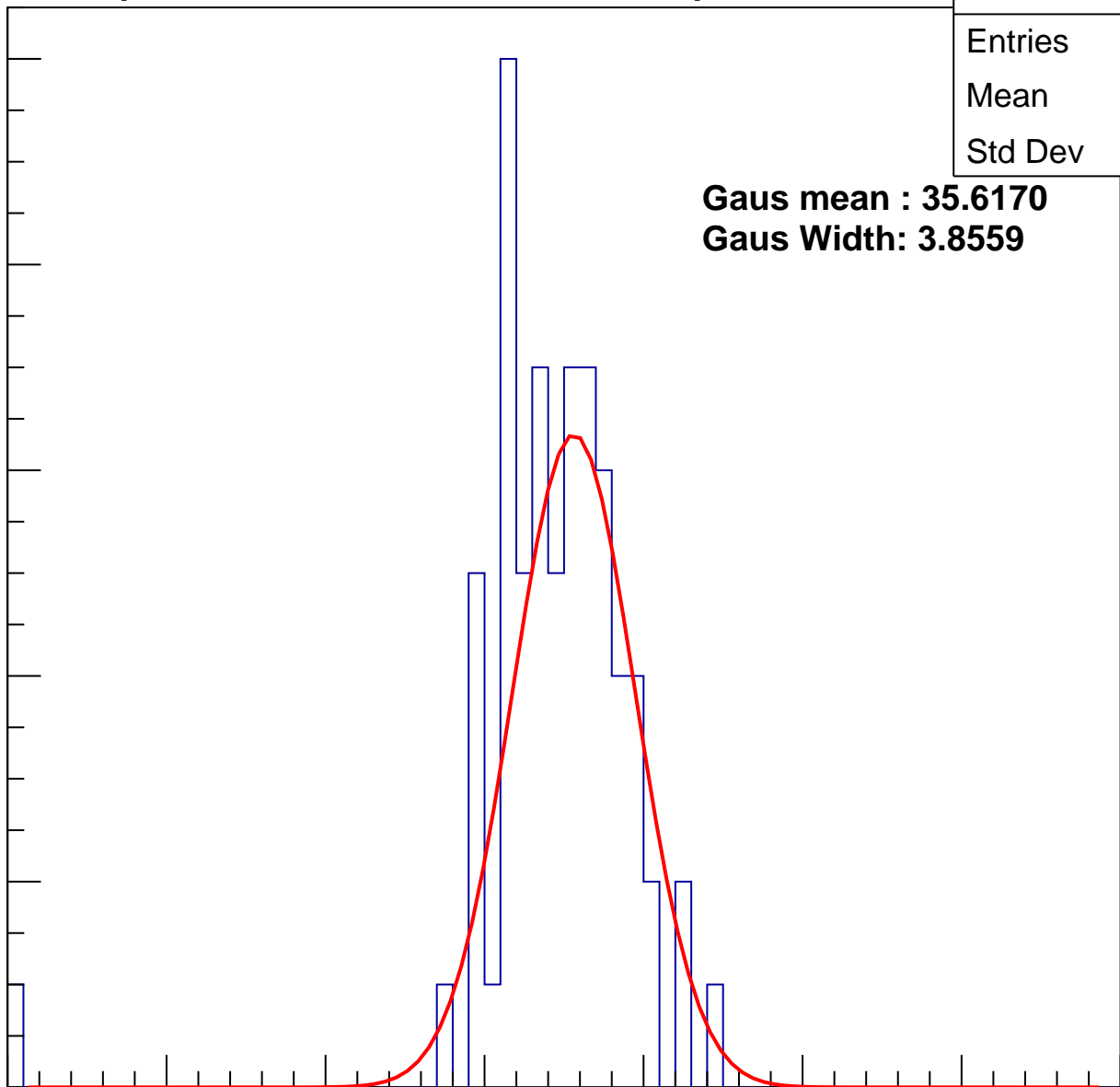
Gaus Width: 3.8559

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

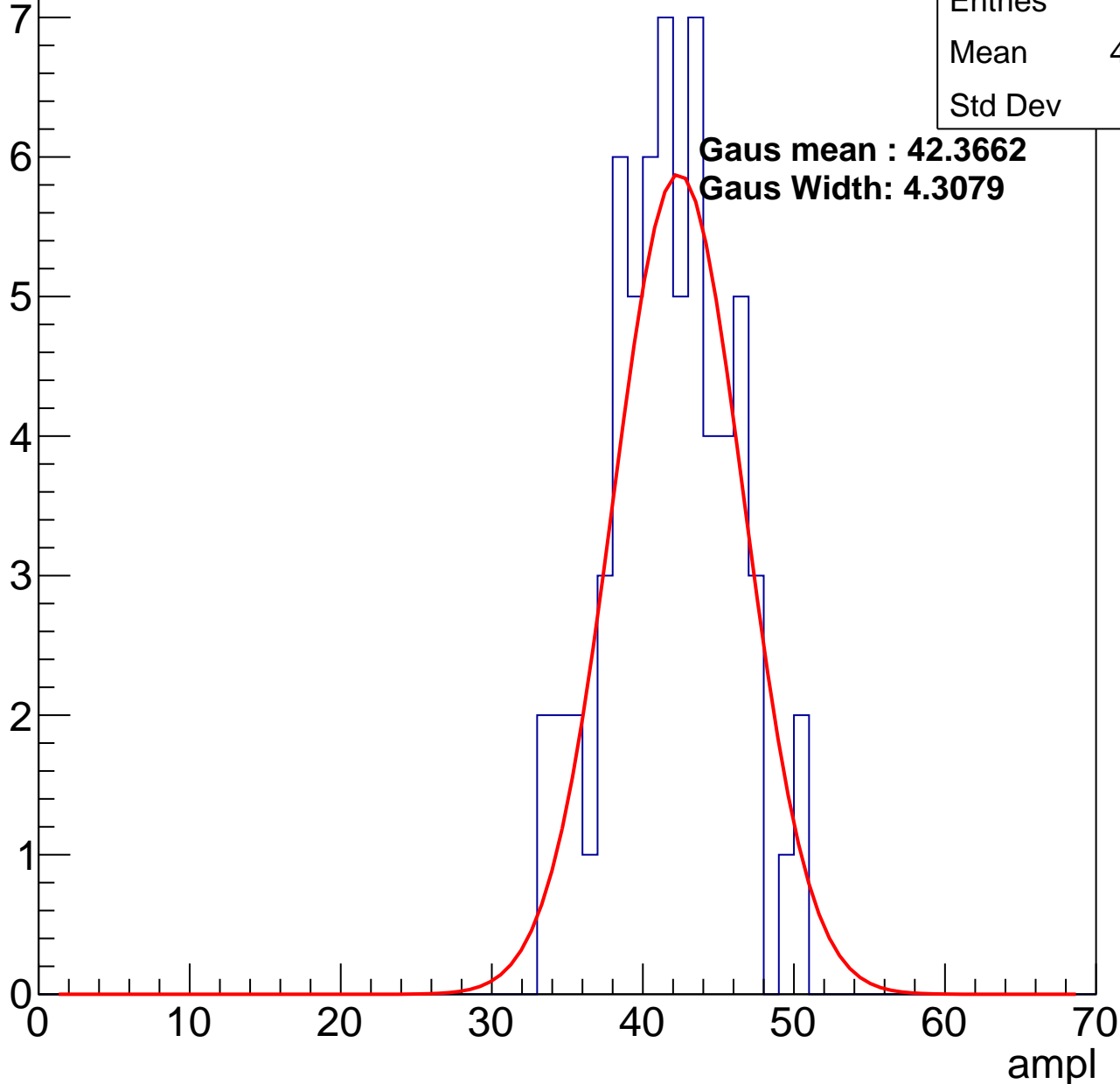


B0L102S, U20-ch118, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 41.35 |
| Std Dev | 3.99 |

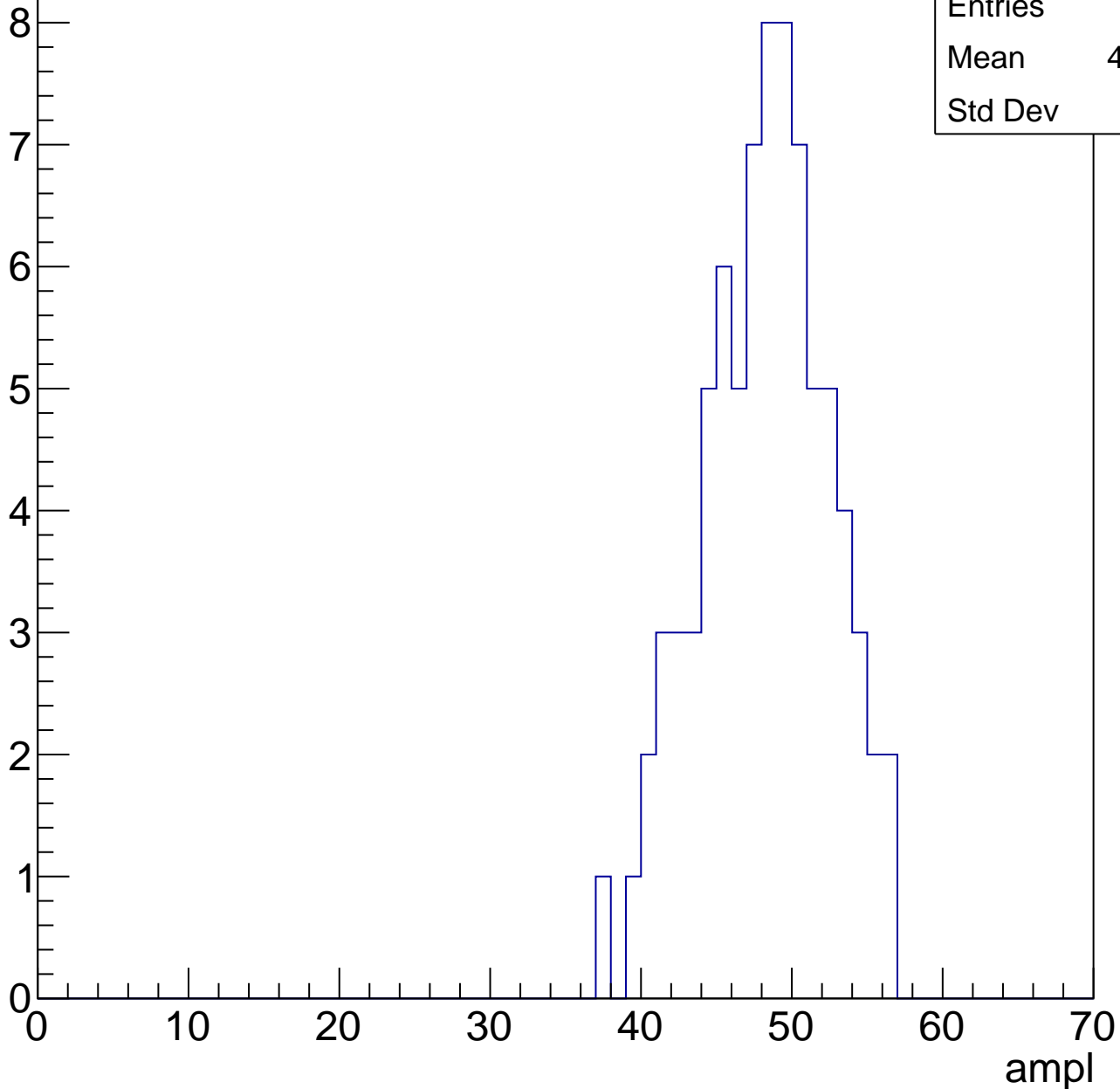


B0L102S, U20-ch118, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 47.75 |
| Std Dev | 4.2 |

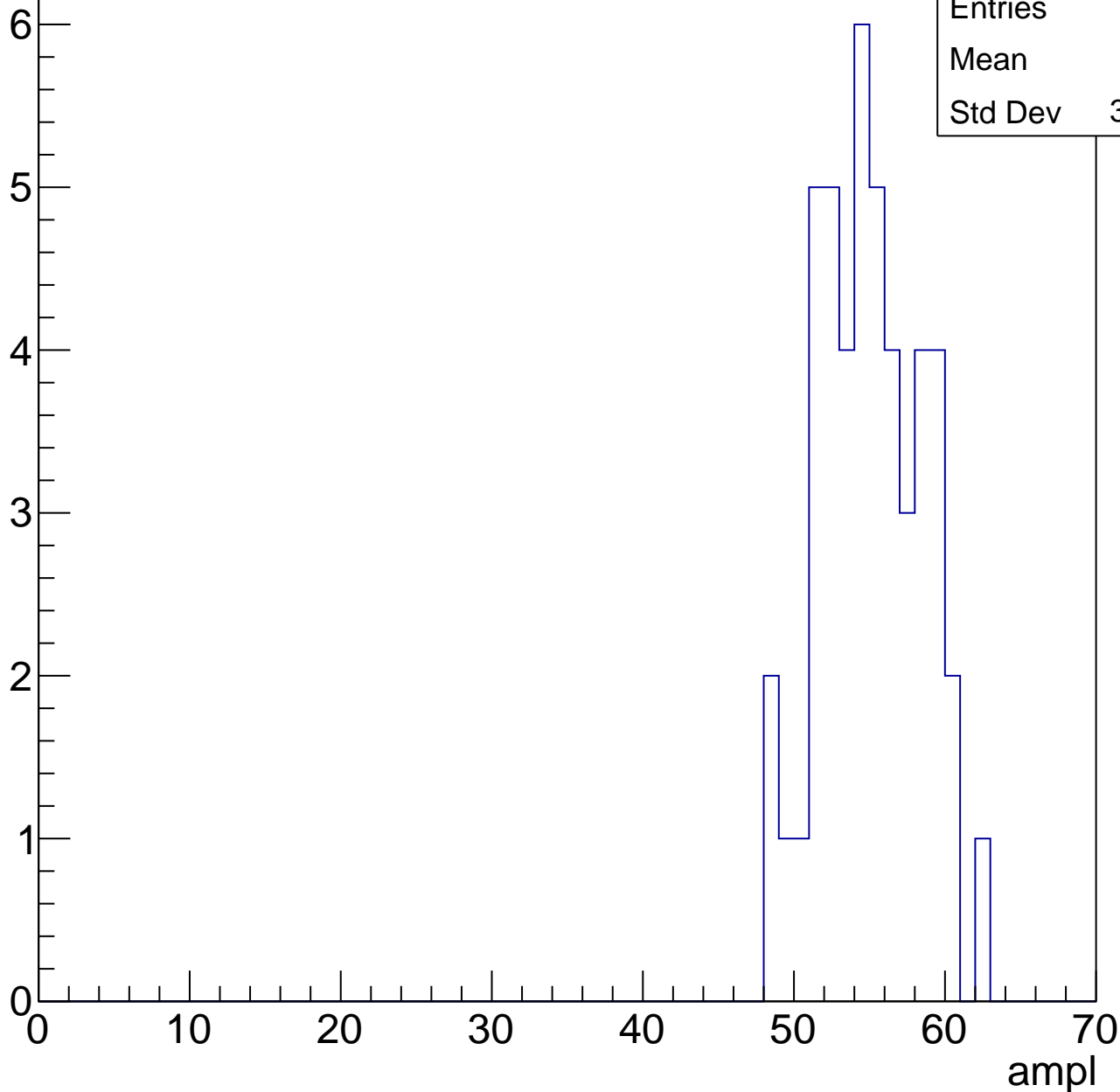


B0L102S, U20-ch118, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 54.6 |
| Std Dev | 3.305 |

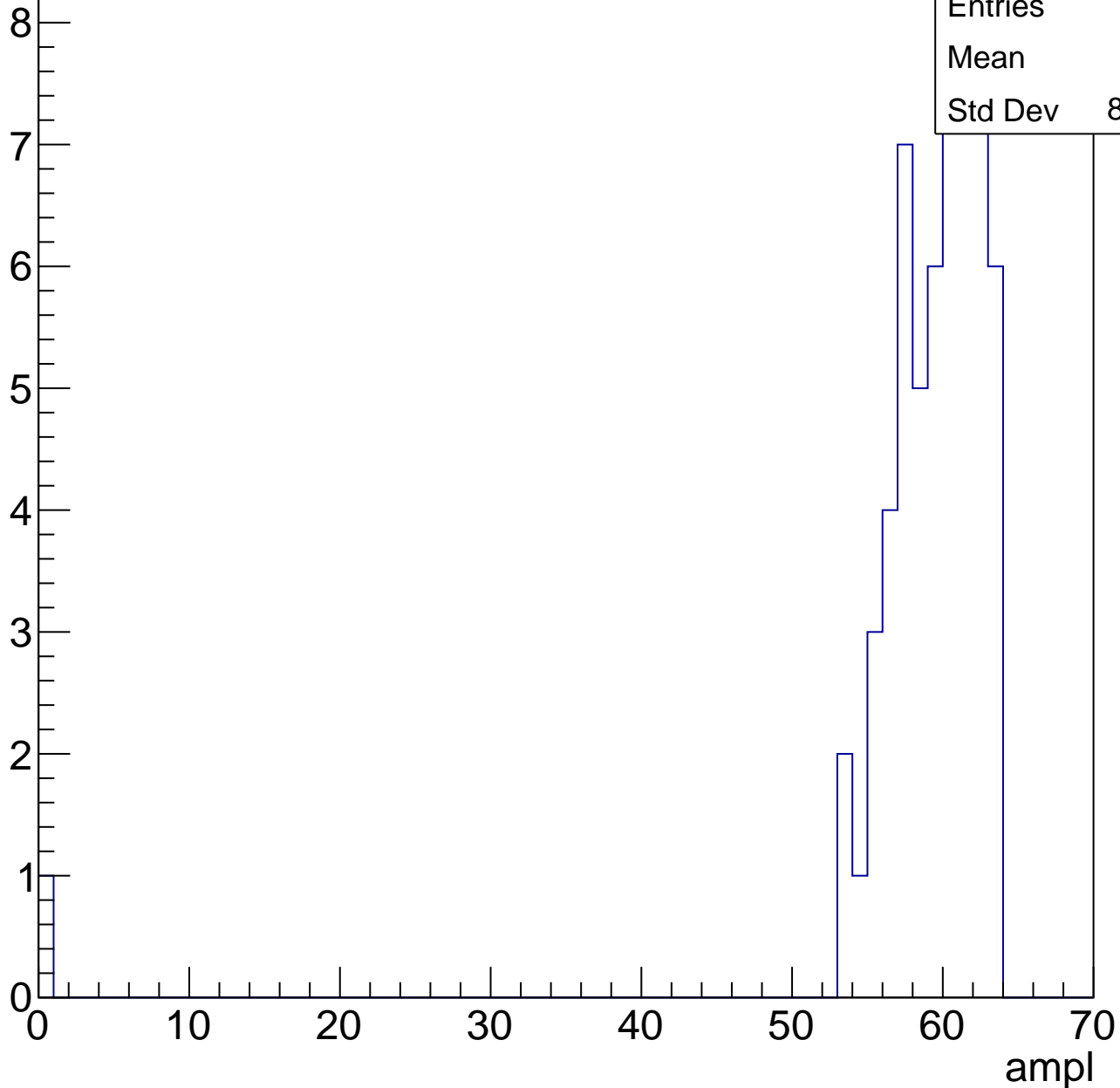


B0L102S, U20-ch118, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

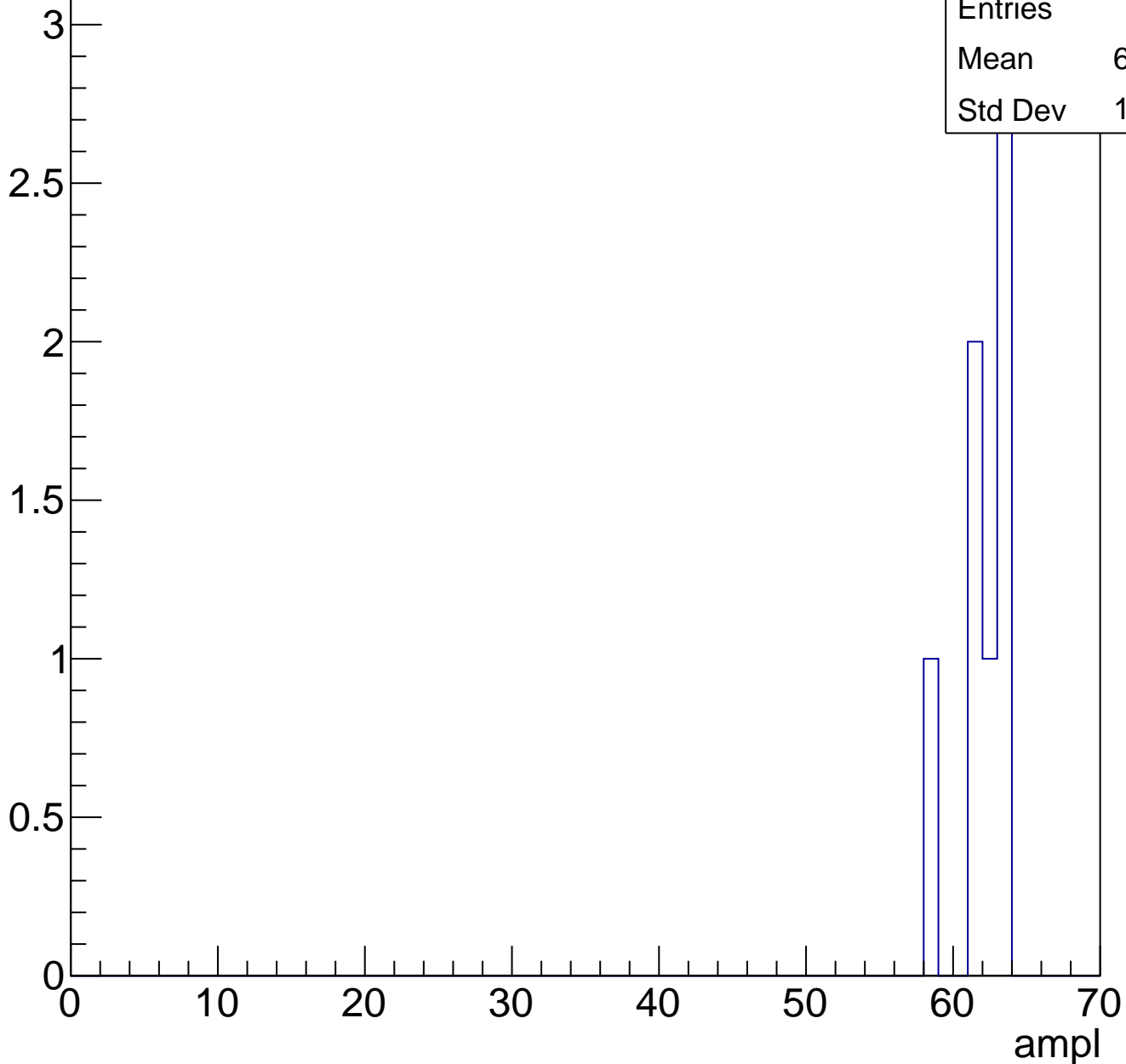
| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.2 |
| Std Dev | 8.094 |



B0L102S, U20-ch118, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch118, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch119, adc0

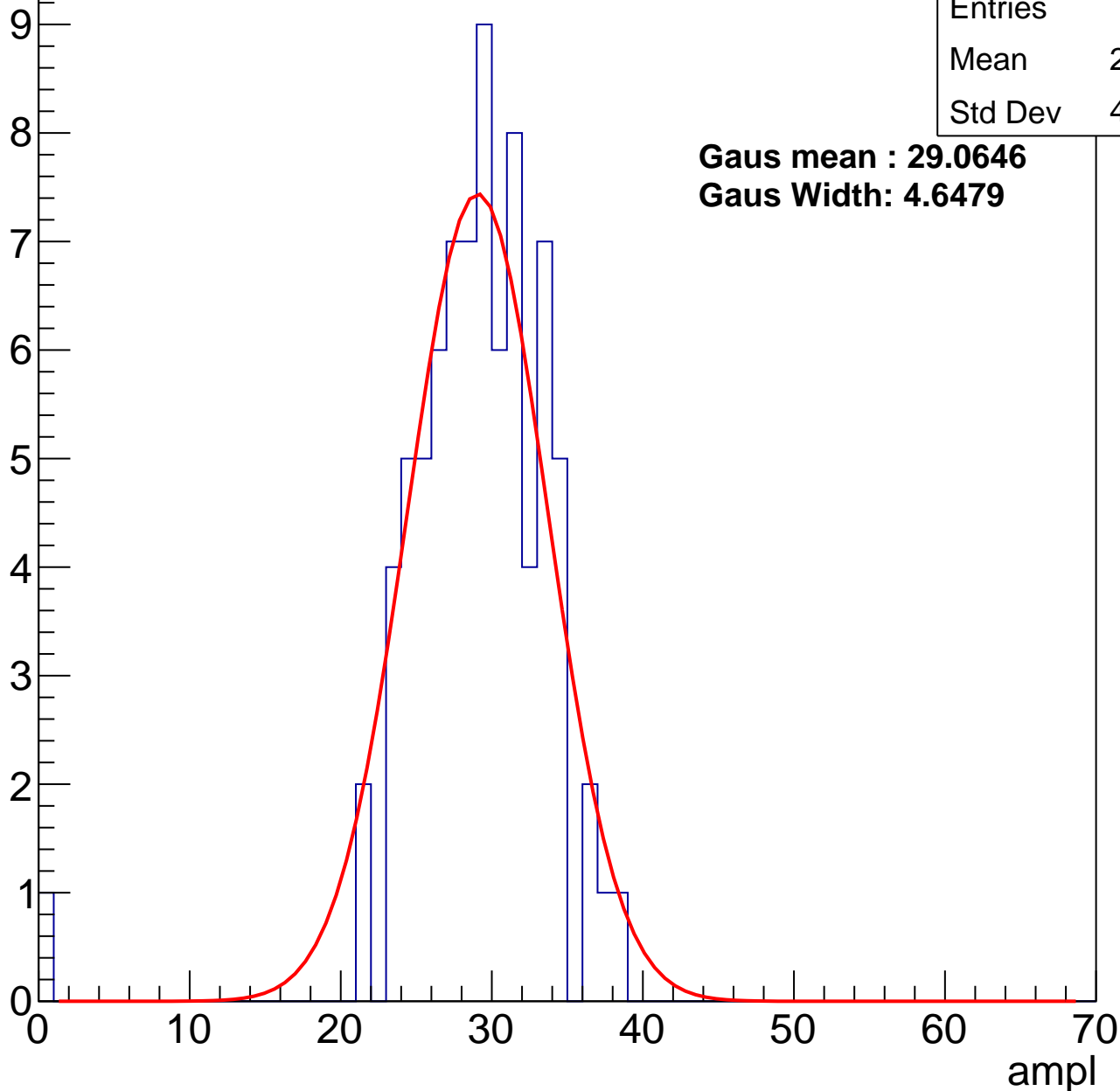
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 28.56 |
| Std Dev | 4.932 |

Gaus mean : 29.0646

Gaus Width: 4.6479



B0L102S, U20-ch119, adc1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.29 |
| Std Dev | 3.359 |

Gaus mean : 36.9289

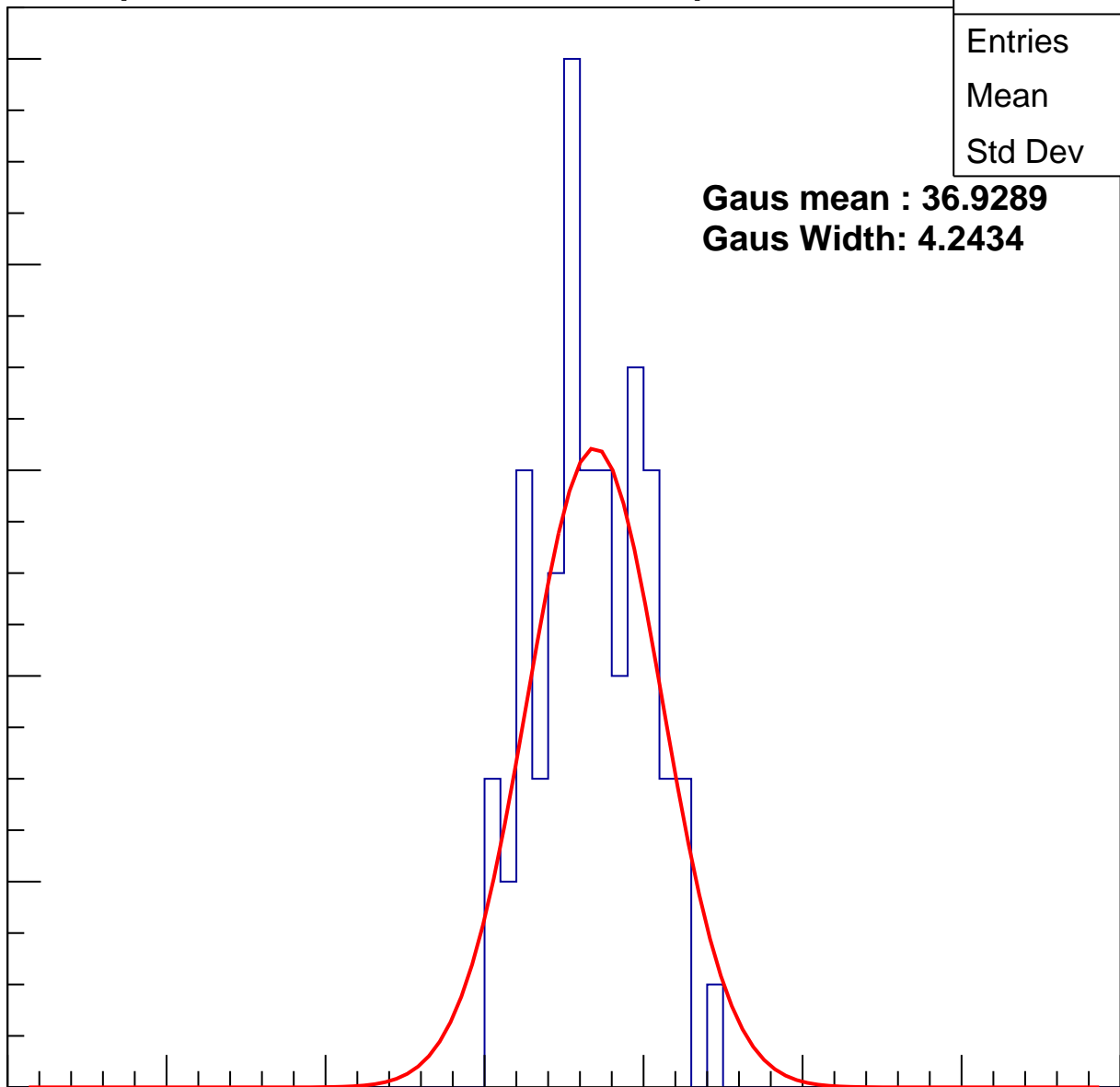
Gaus Width: 4.2434

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch119, adc2

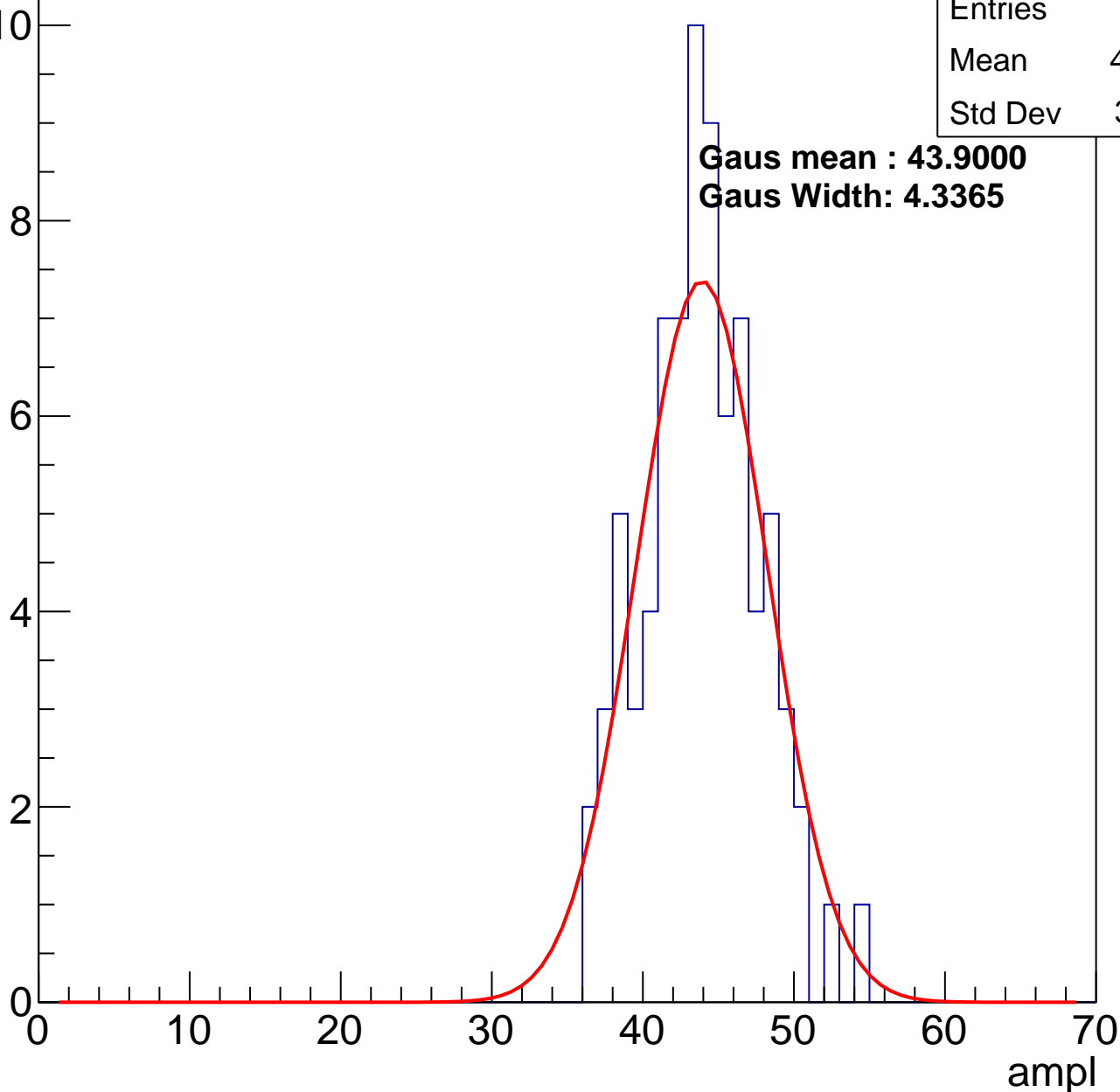
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 43.42 |
| Std Dev | 3.781 |

Gaus mean : 43.9000

Gaus Width: 4.3365

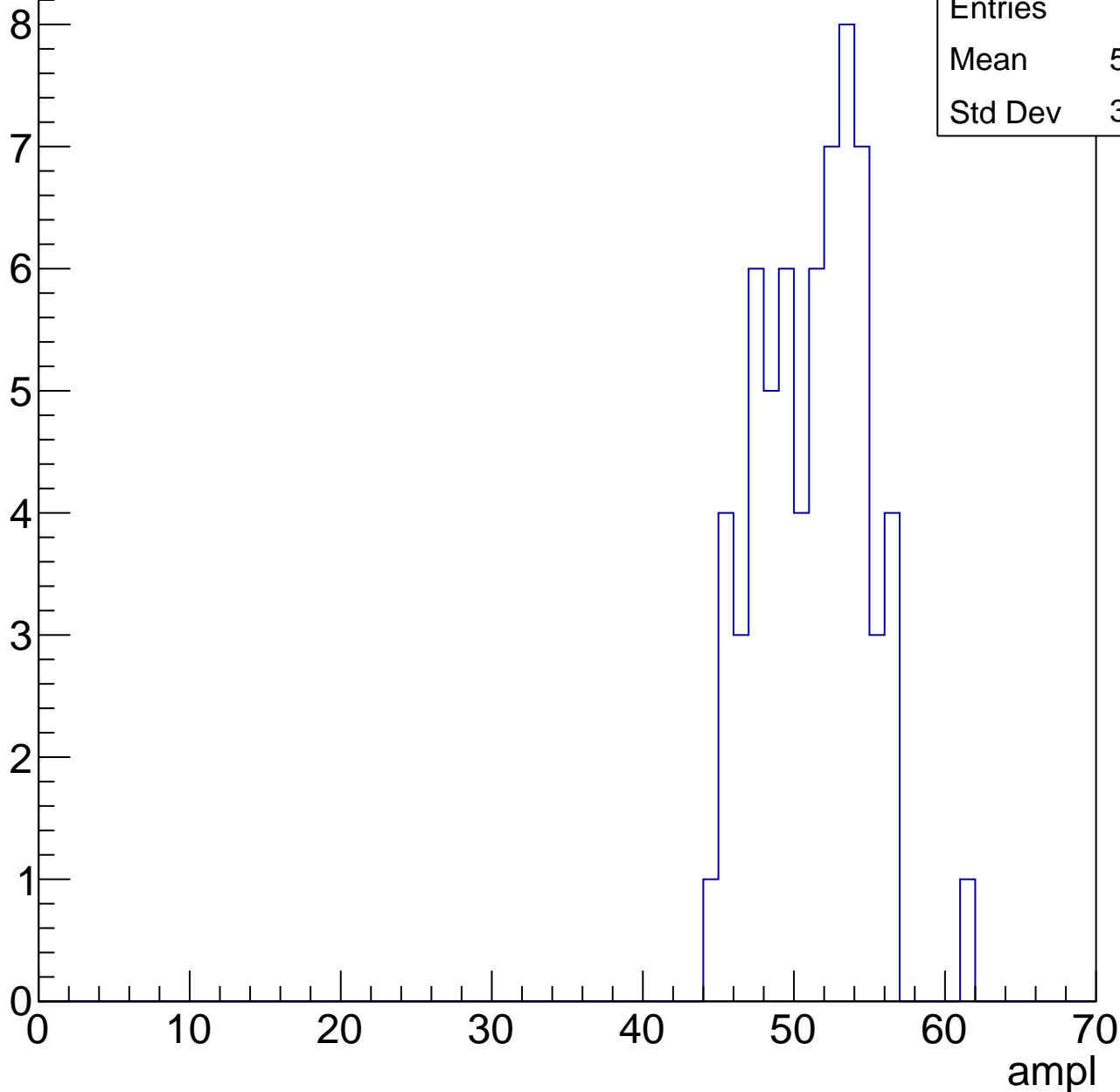


B0L102S, U20-ch119, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.77 |
| Std Dev | 3.472 |

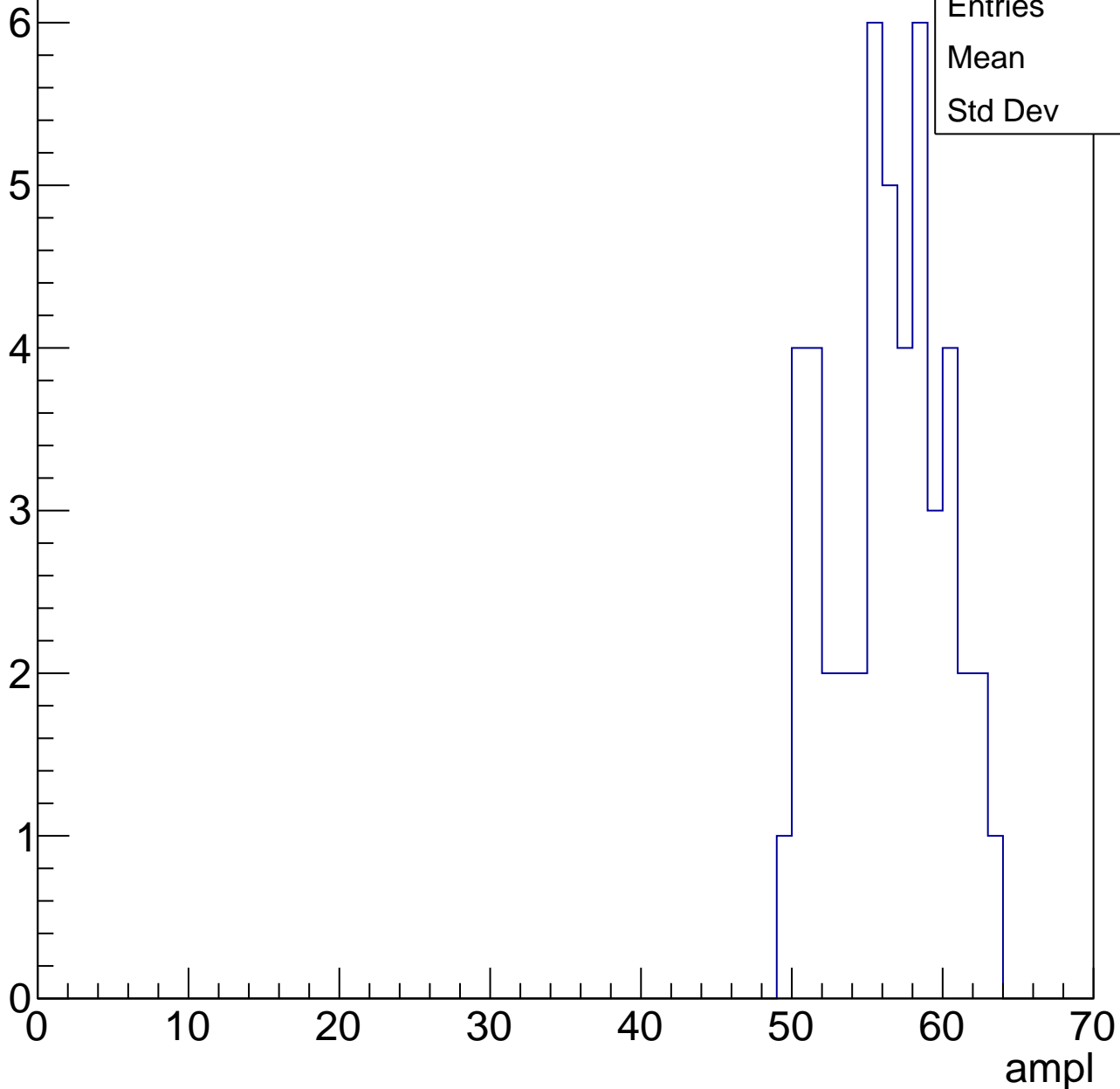


B0L102S, U20-ch119, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|------|
| Entries | 48 |
| Mean | 55.9 |
| Std Dev | 3.67 |

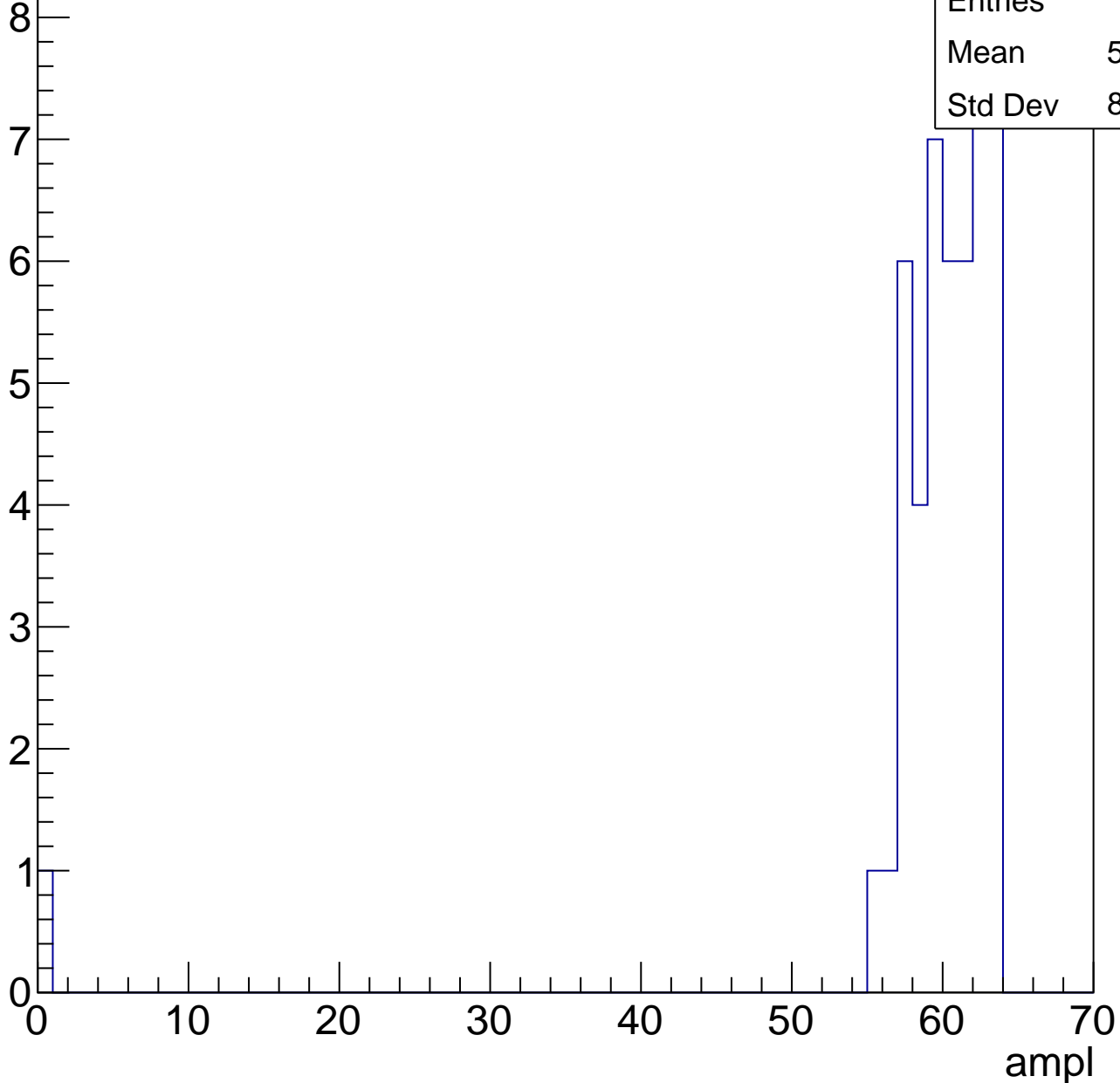


B0L102S, U20-ch119, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.83 |
| Std Dev | 8.854 |



B0L102S, U20-ch119, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch119, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch120, adc0

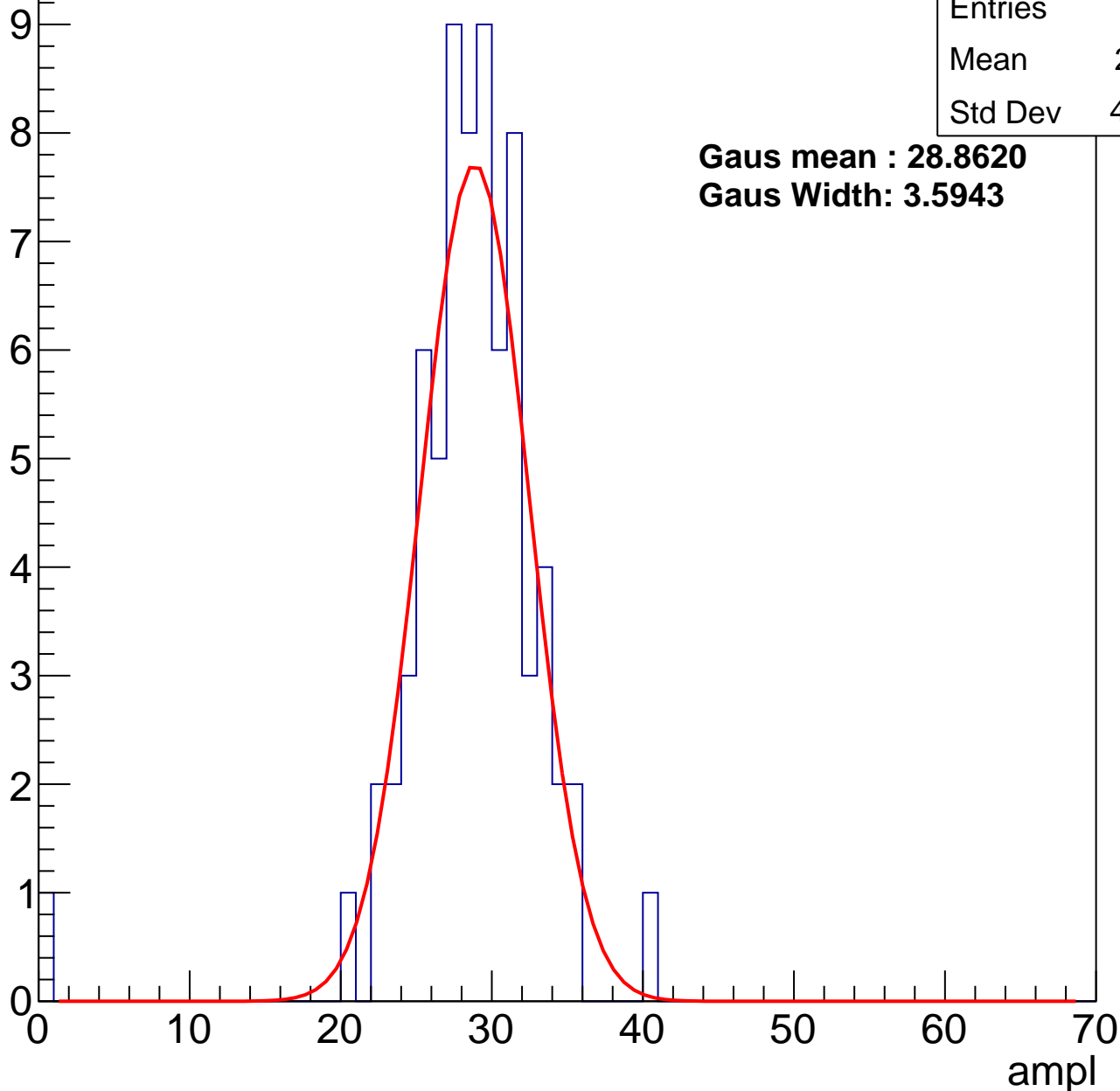
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 28.11 |
| Std Dev | 4.812 |

Gaus mean : 28.8620

Gaus Width: 3.5943



B0L102S, U20-ch120, adc1

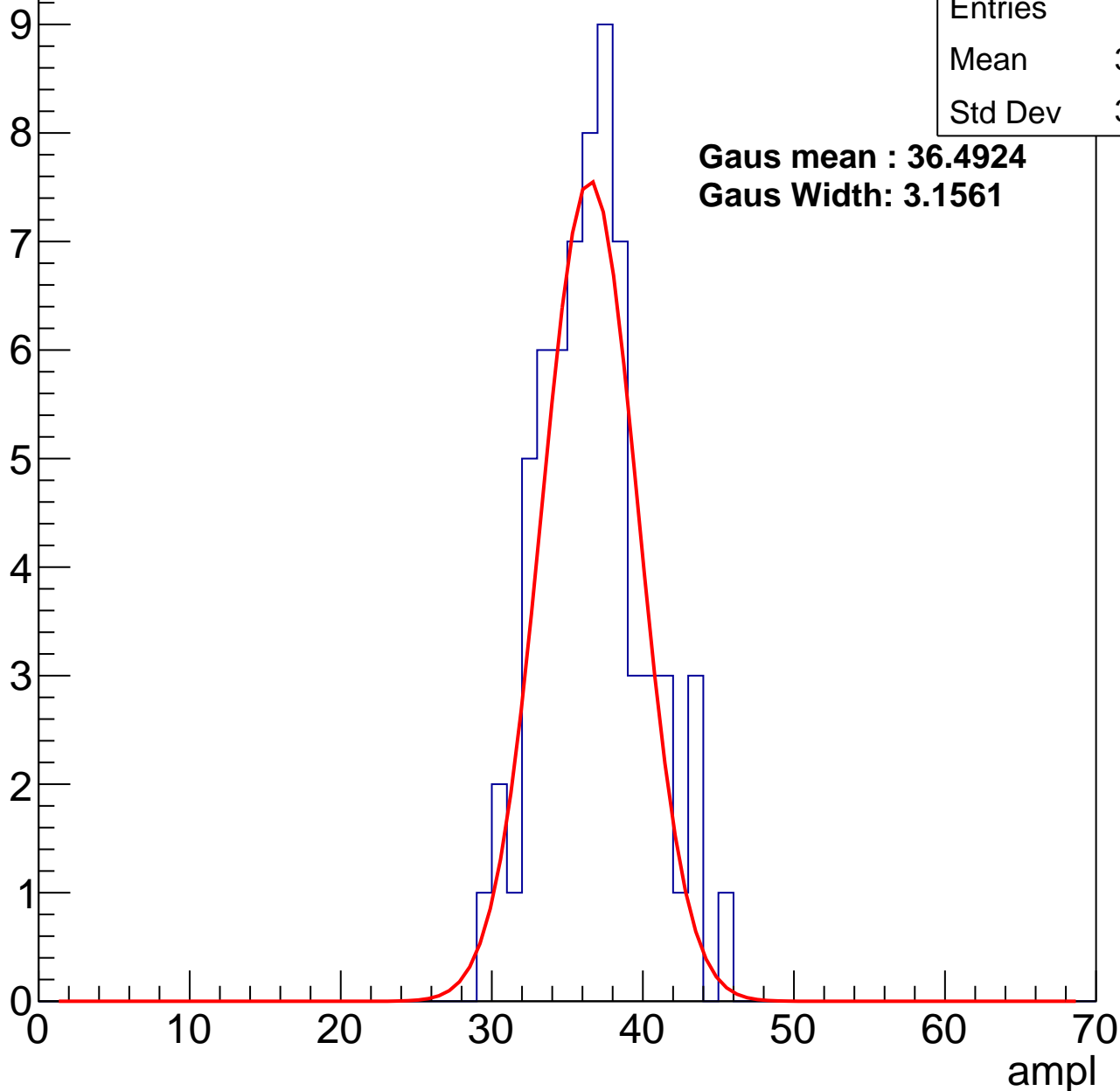
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.21 |
| Std Dev | 3.391 |

Gaus mean : 36.4924

Gaus Width: 3.1561



B0L102S, U20-ch120, adc2

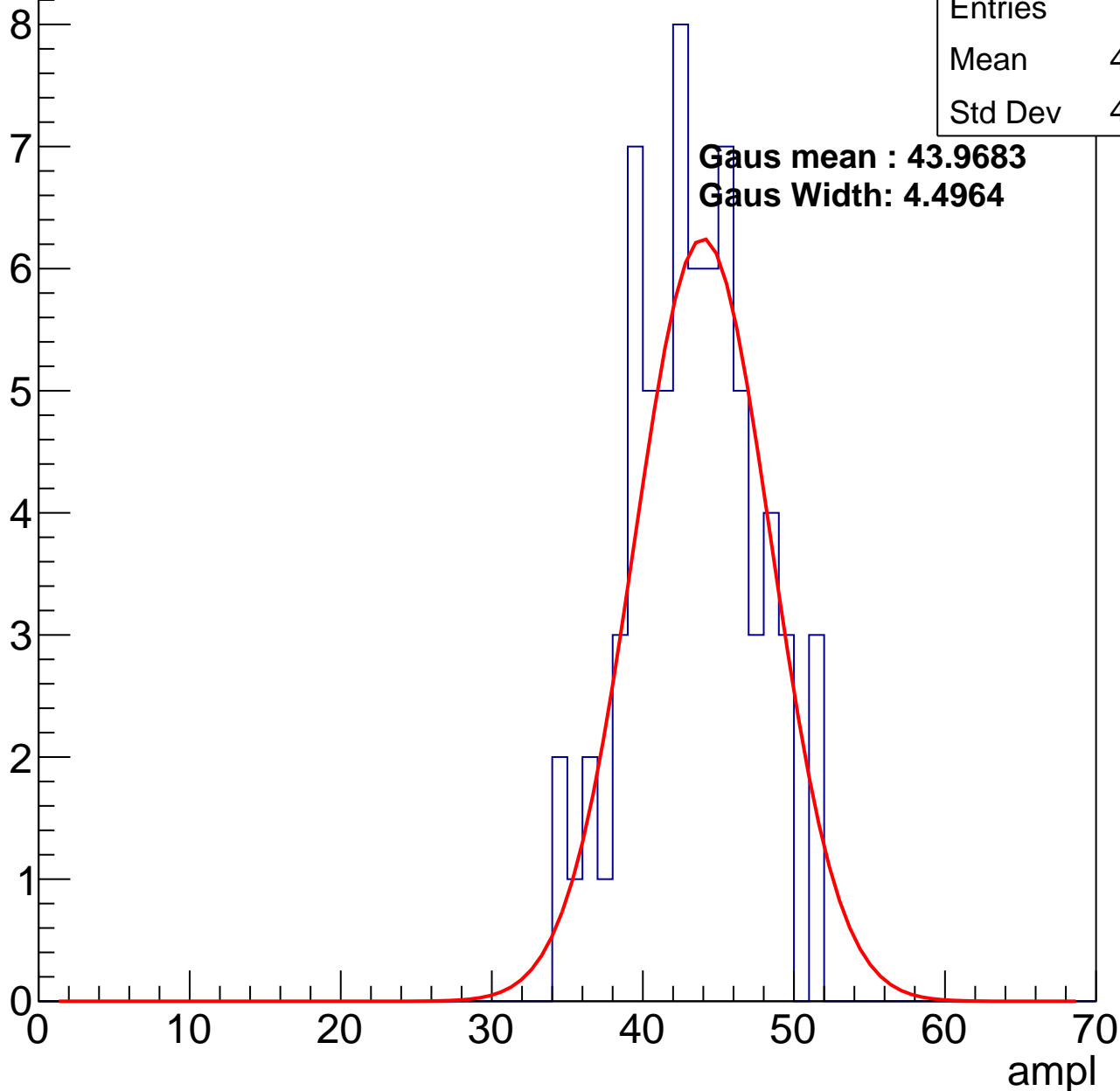
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.82 |
| Std Dev | 4.015 |

Gaus mean : 43.9683

Gaus Width: 4.4964

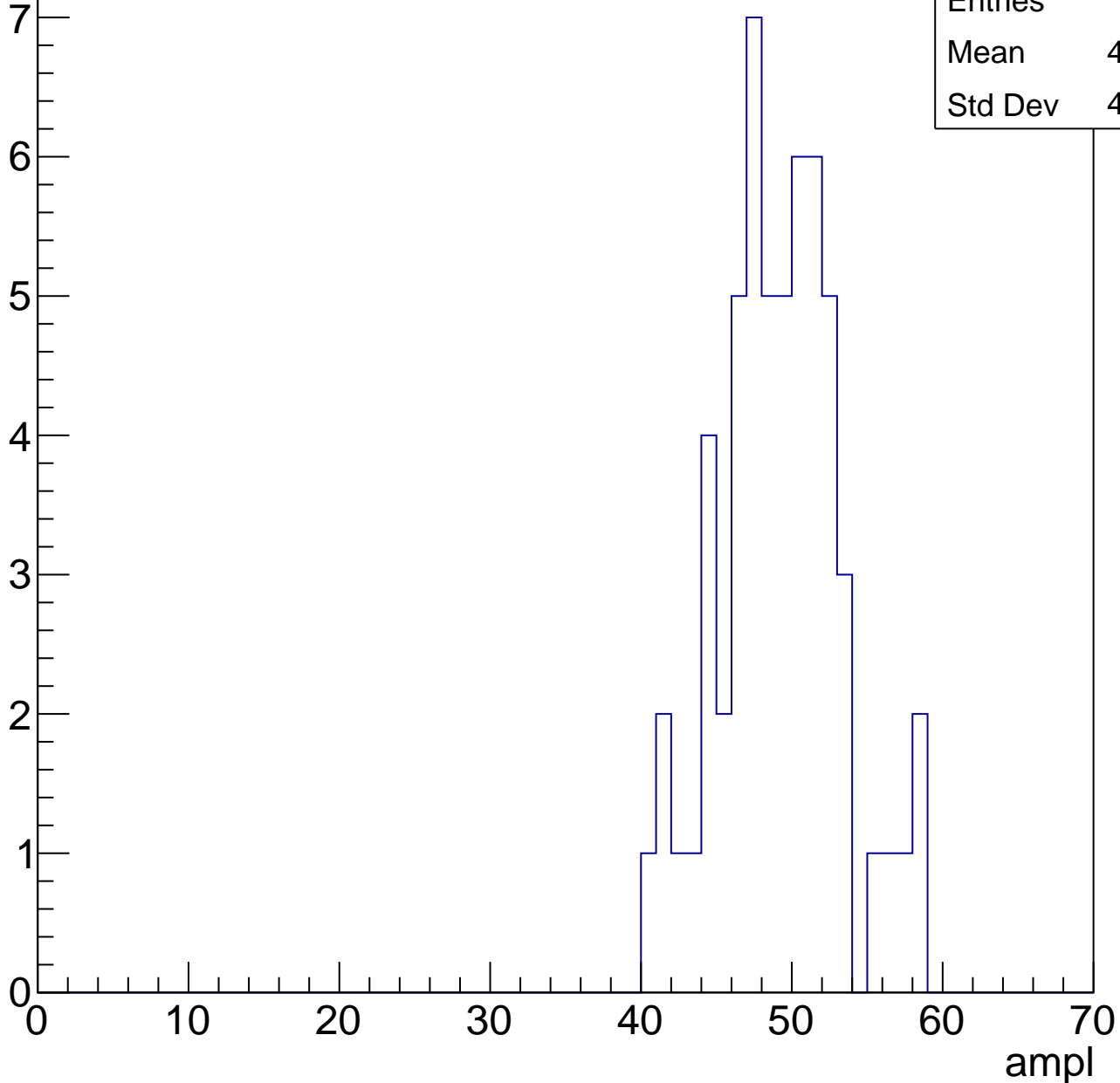


B0L102S, U20-ch120, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 48.72 |
| Std Dev | 4.012 |

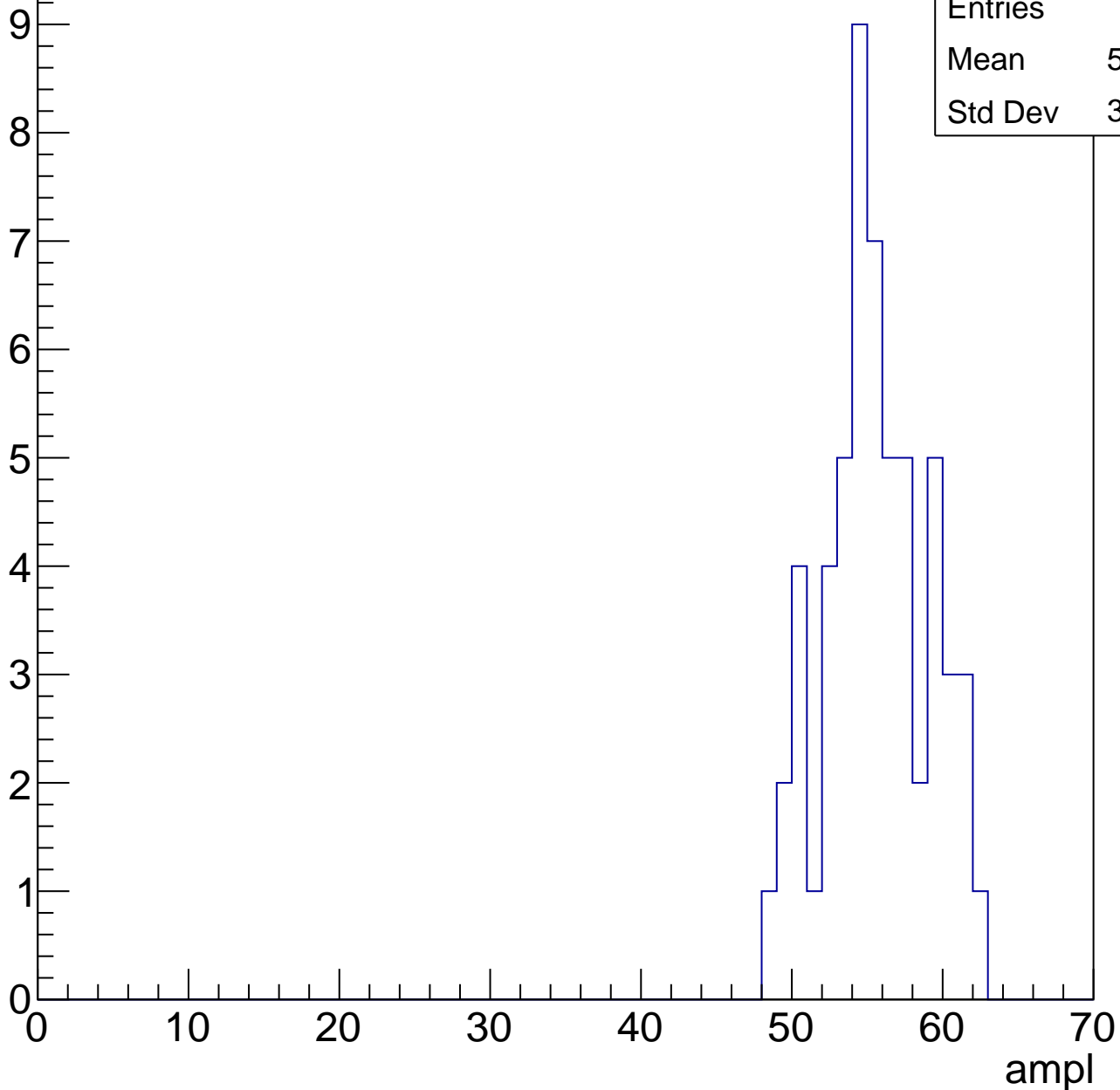


B0L102S, U20-ch120, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.12 |
| Std Dev | 3.393 |

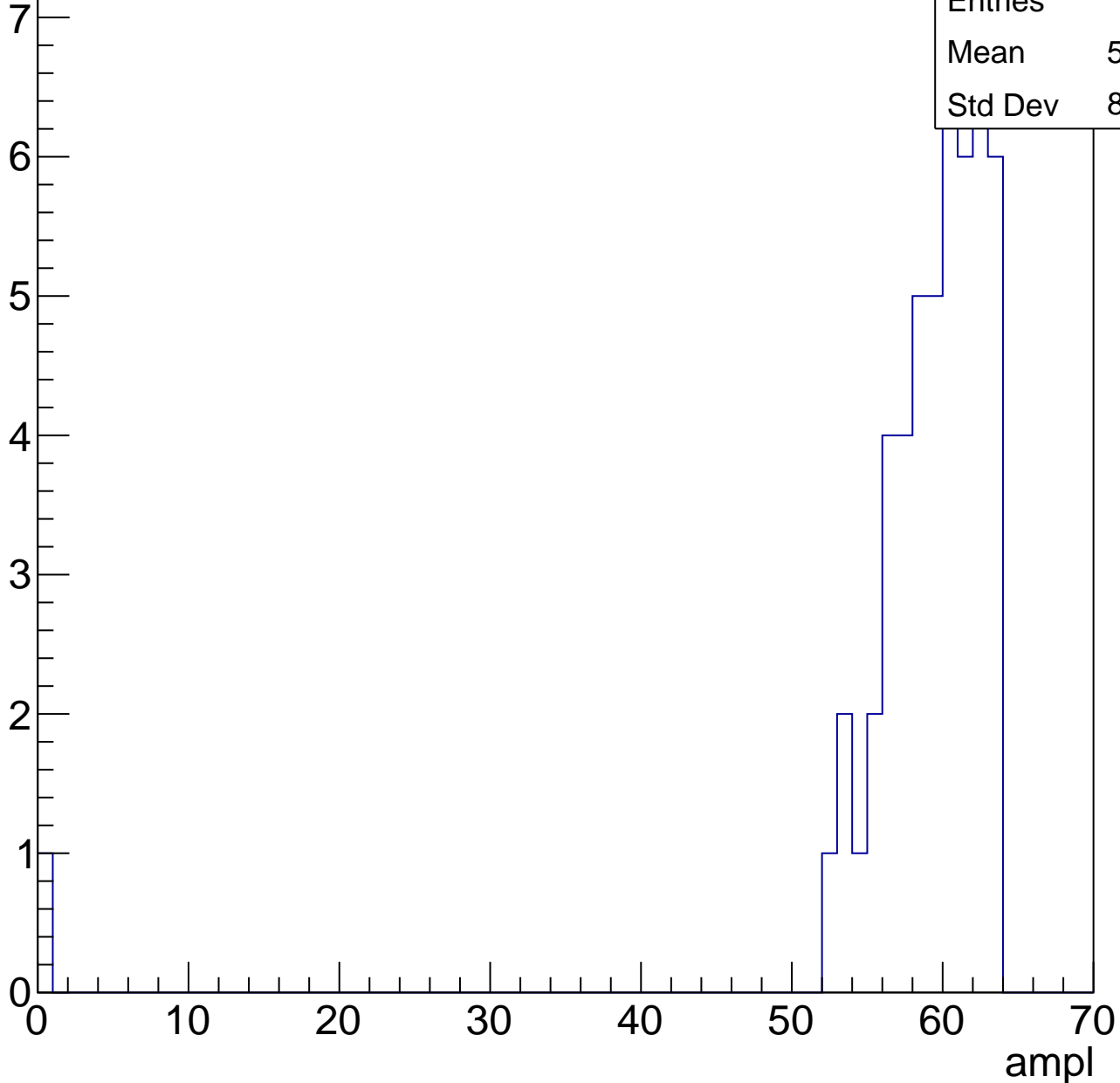


B0L102S, U20-ch120, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

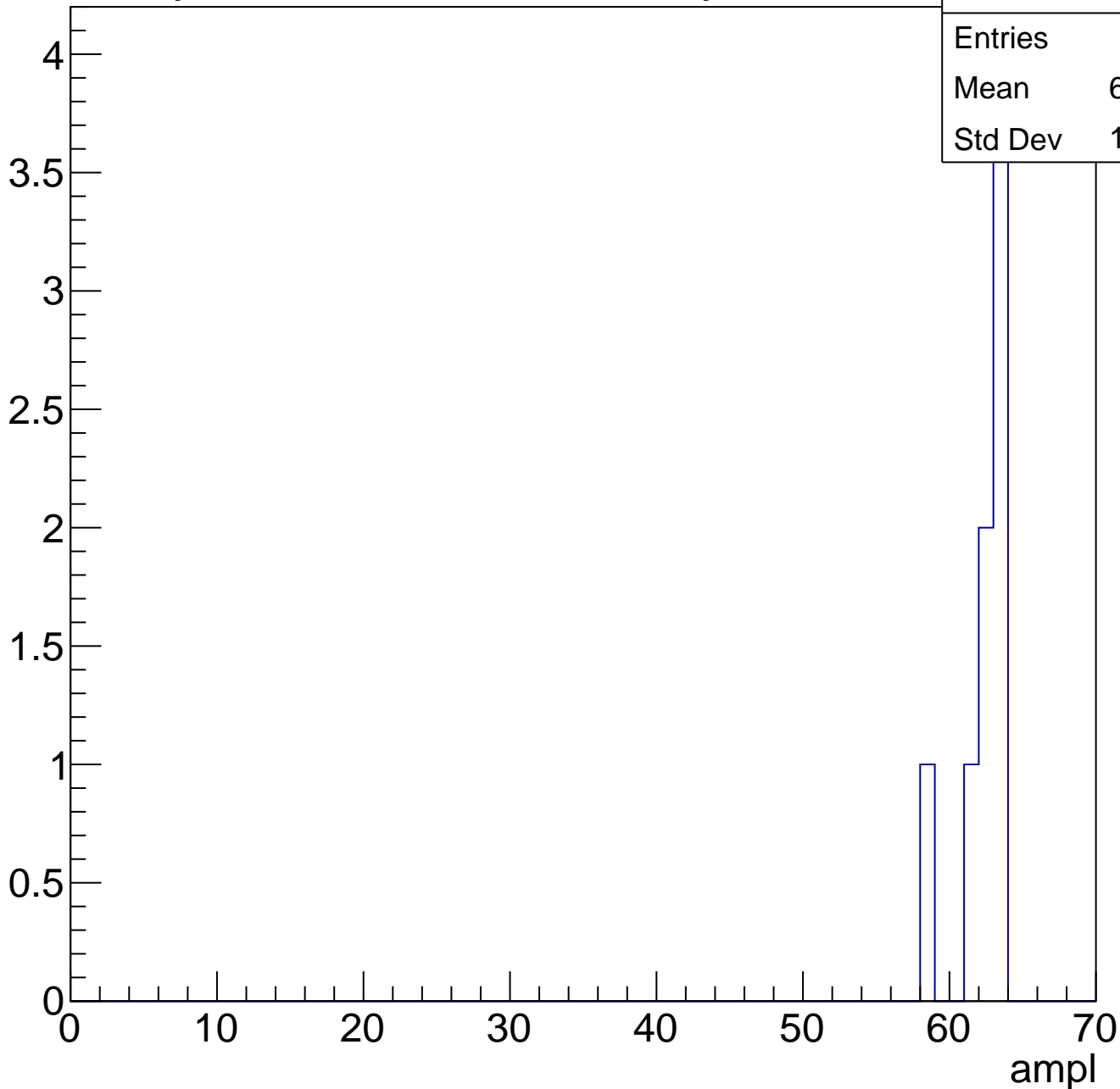
| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.98 |
| Std Dev | 8.692 |



B0L102S, U20-ch120, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch120, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch121, adc0

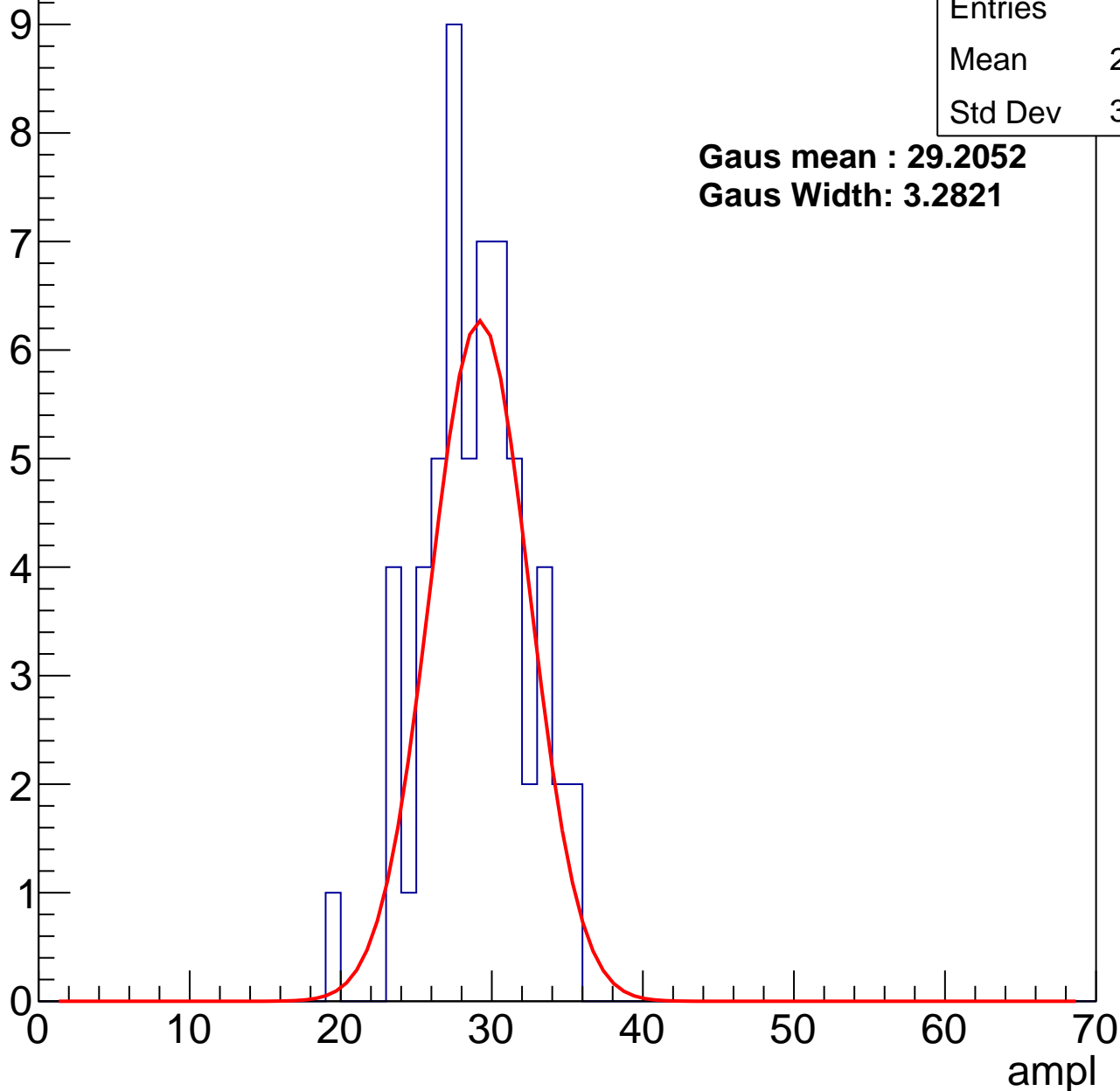
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 28.45 |
| Std Dev | 3.302 |

Gaus mean : 29.2052

Gaus Width: 3.2821



B0L102S, U20-ch121, adc1

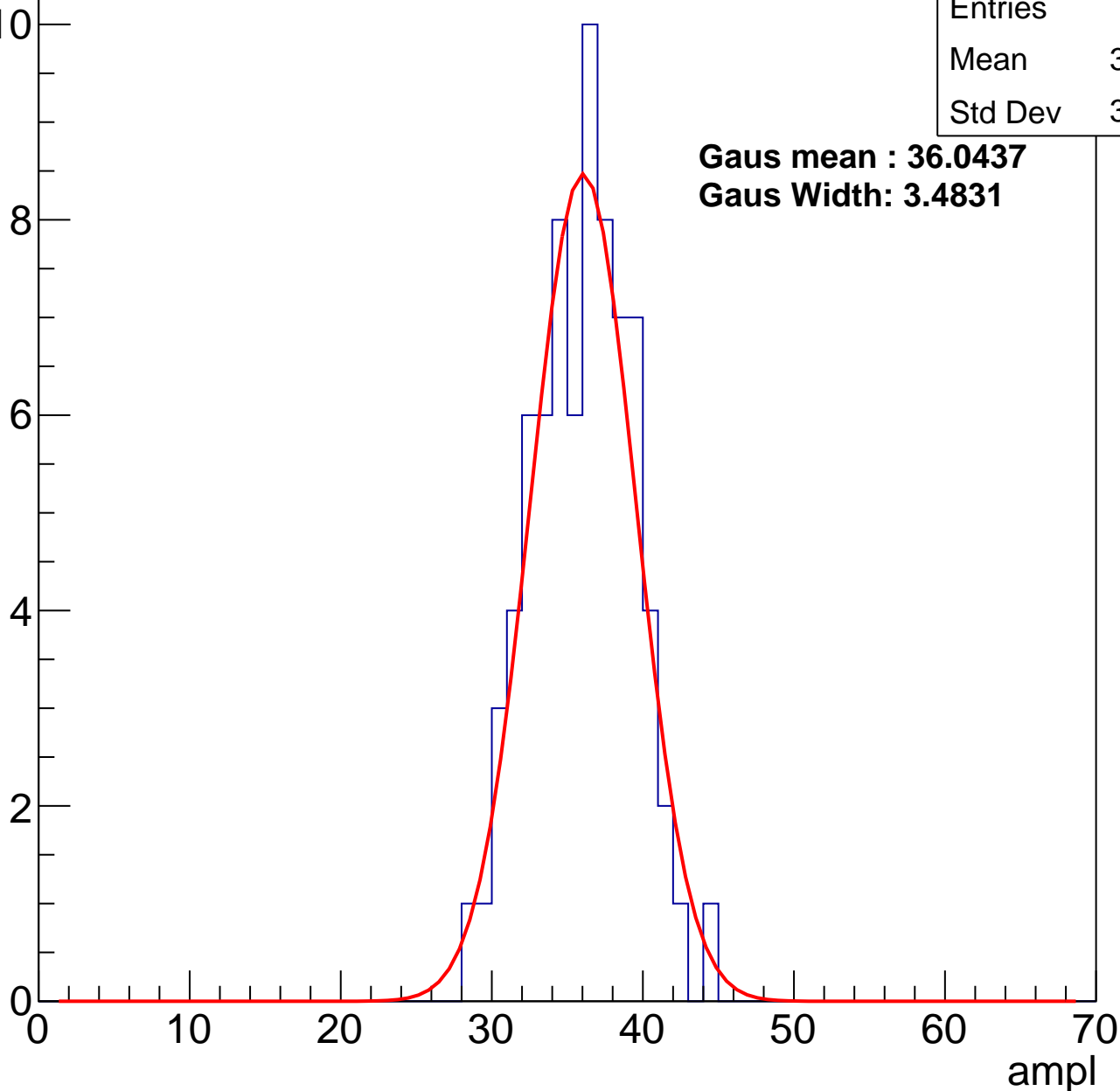
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 35.55 |
| Std Dev | 3.279 |

Gaus mean : 36.0437

Gaus Width: 3.4831



B0L102S, U20-ch121, adc2

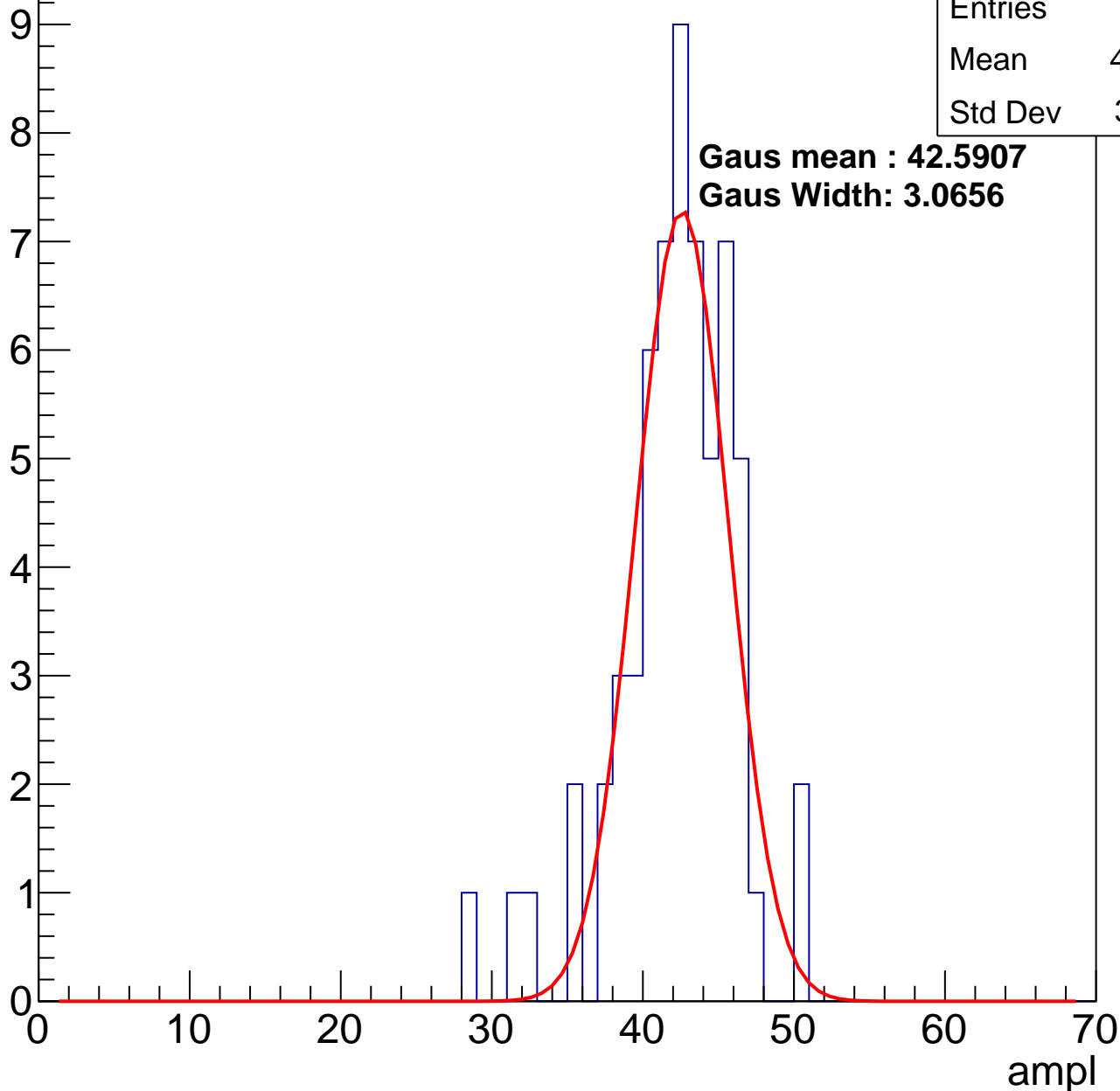
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.68 |
| Std Dev | 3.991 |

Gaus mean : 42.5907

Gaus Width: 3.0656

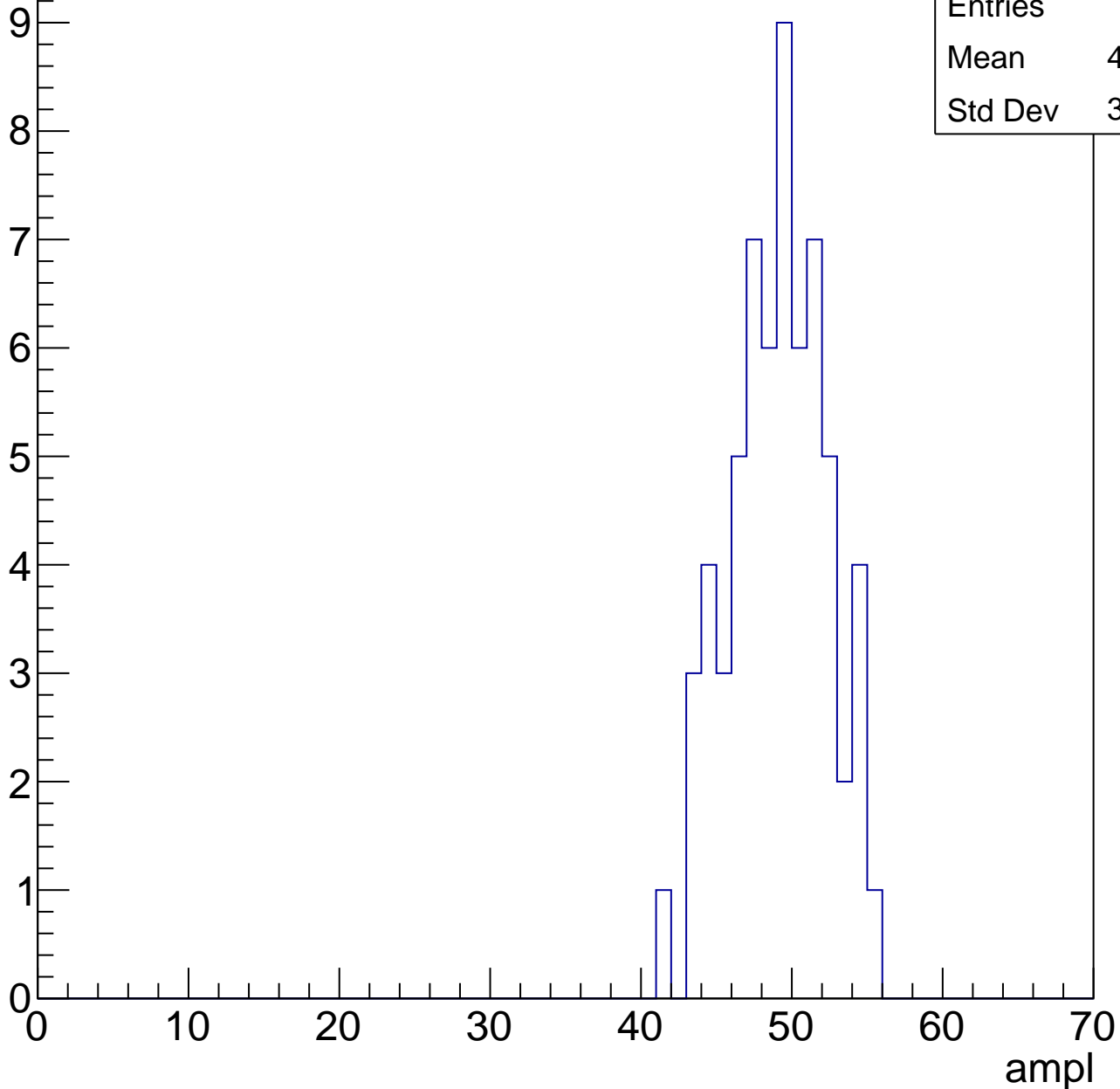


B0L102S, U20-ch121, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.62 |
| Std Dev | 3.179 |

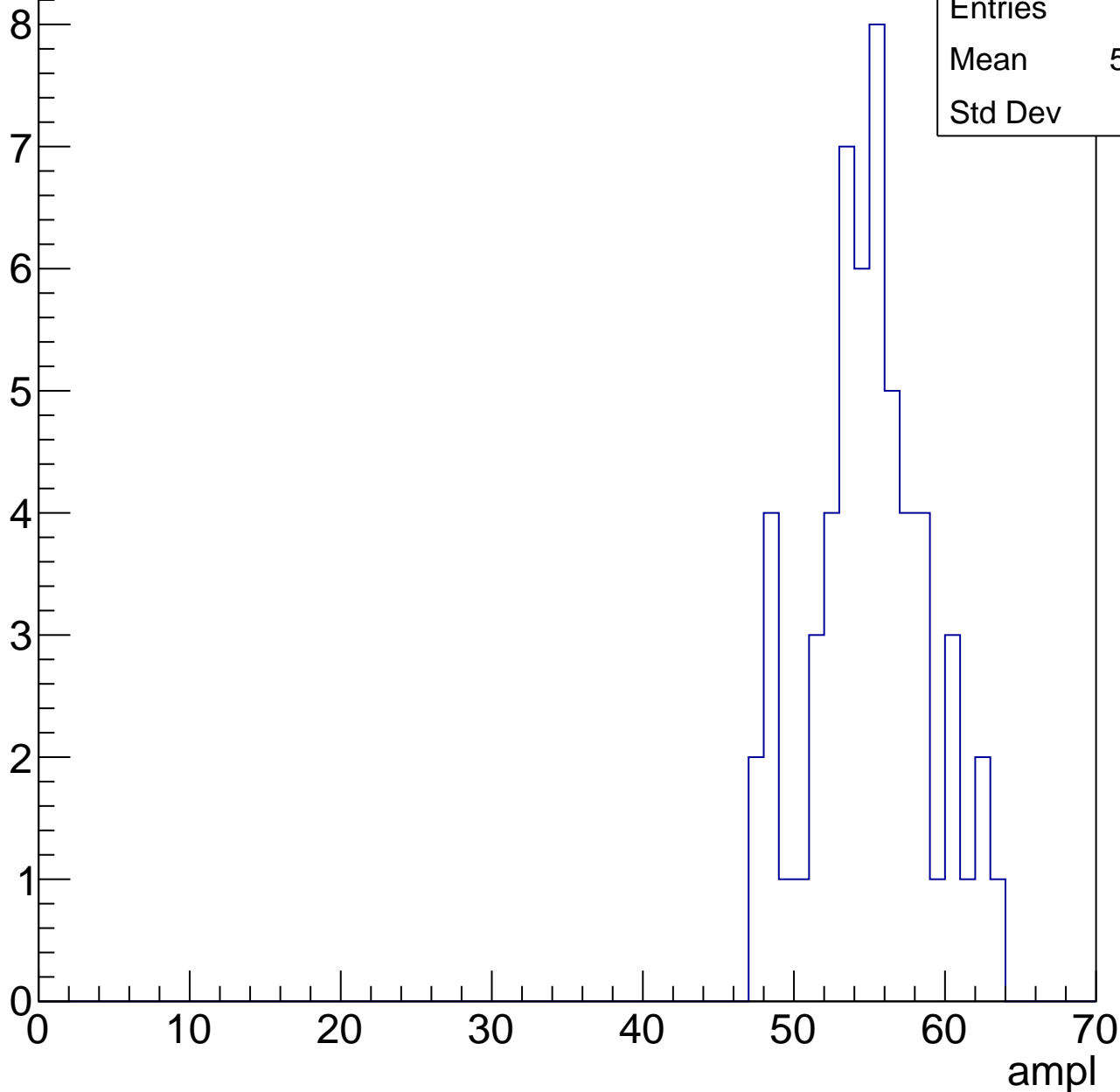


B0L102S, U20-ch121, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.53 |
| Std Dev | 3.83 |

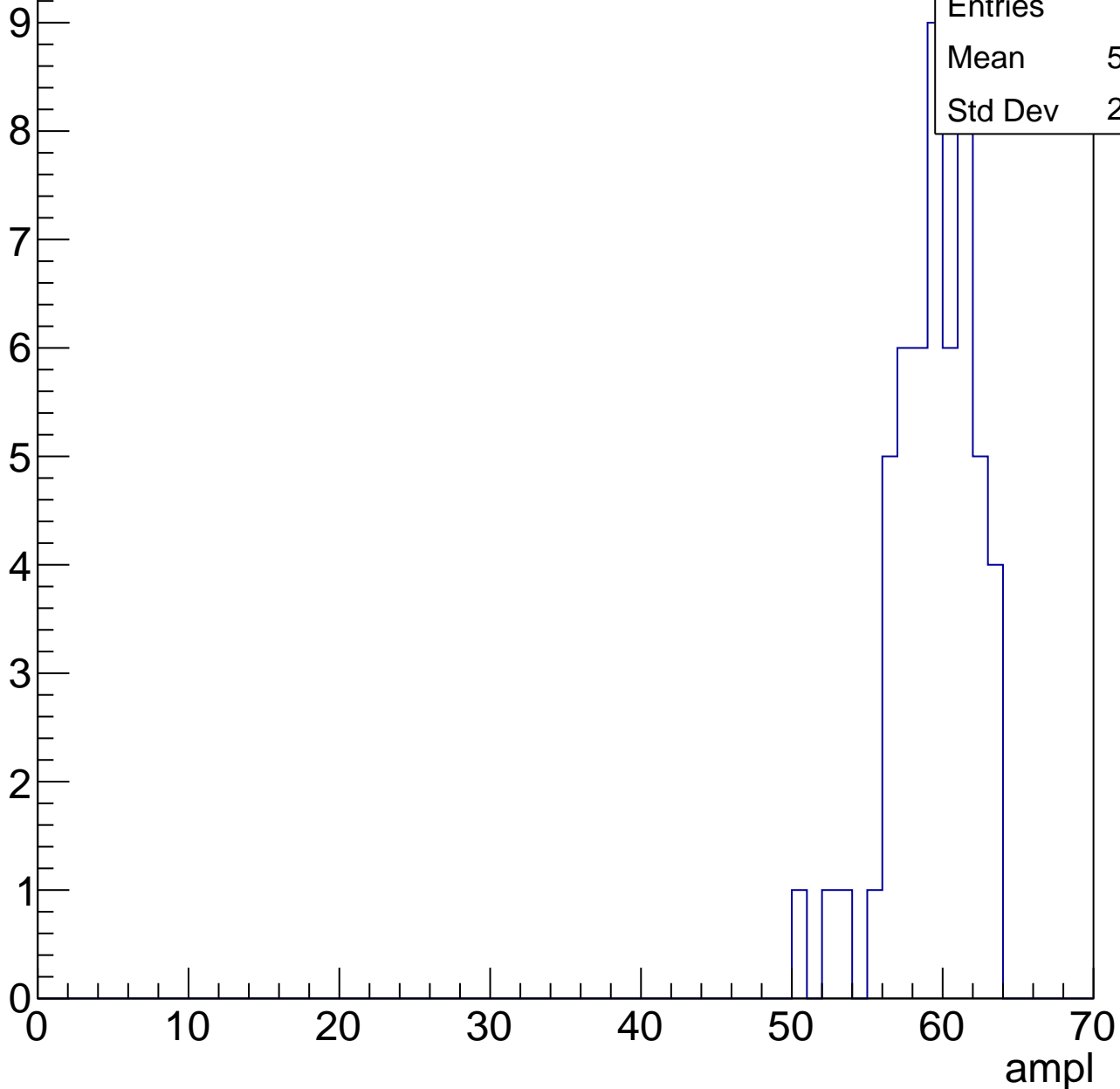


B0L102S, U20-ch121, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.93 |
| Std Dev | 2.748 |

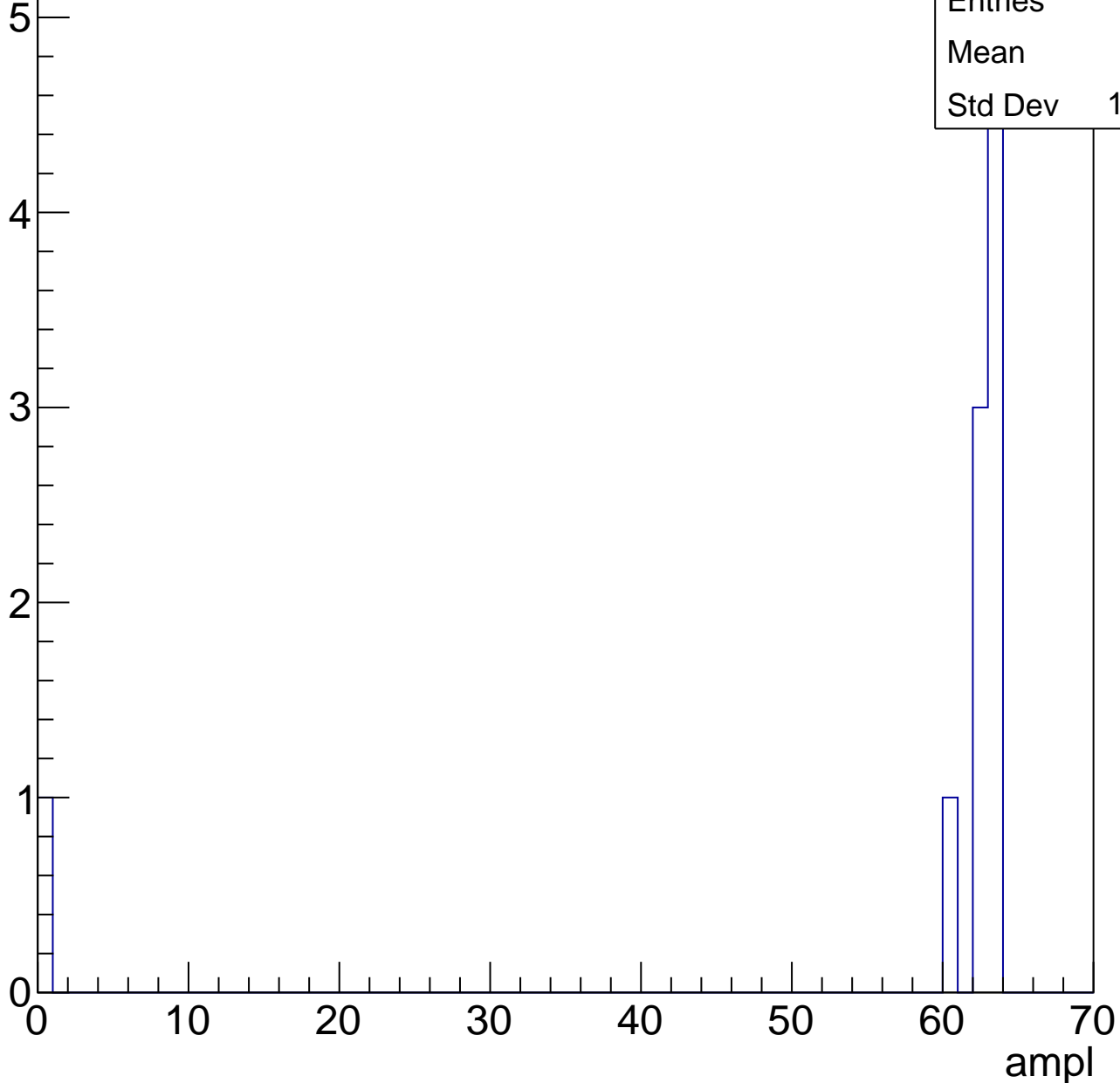


B0L102S, U20-ch121, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 56.1 |
| Std Dev | 18.72 |



B0L102S, U20-ch121, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch122, adc0

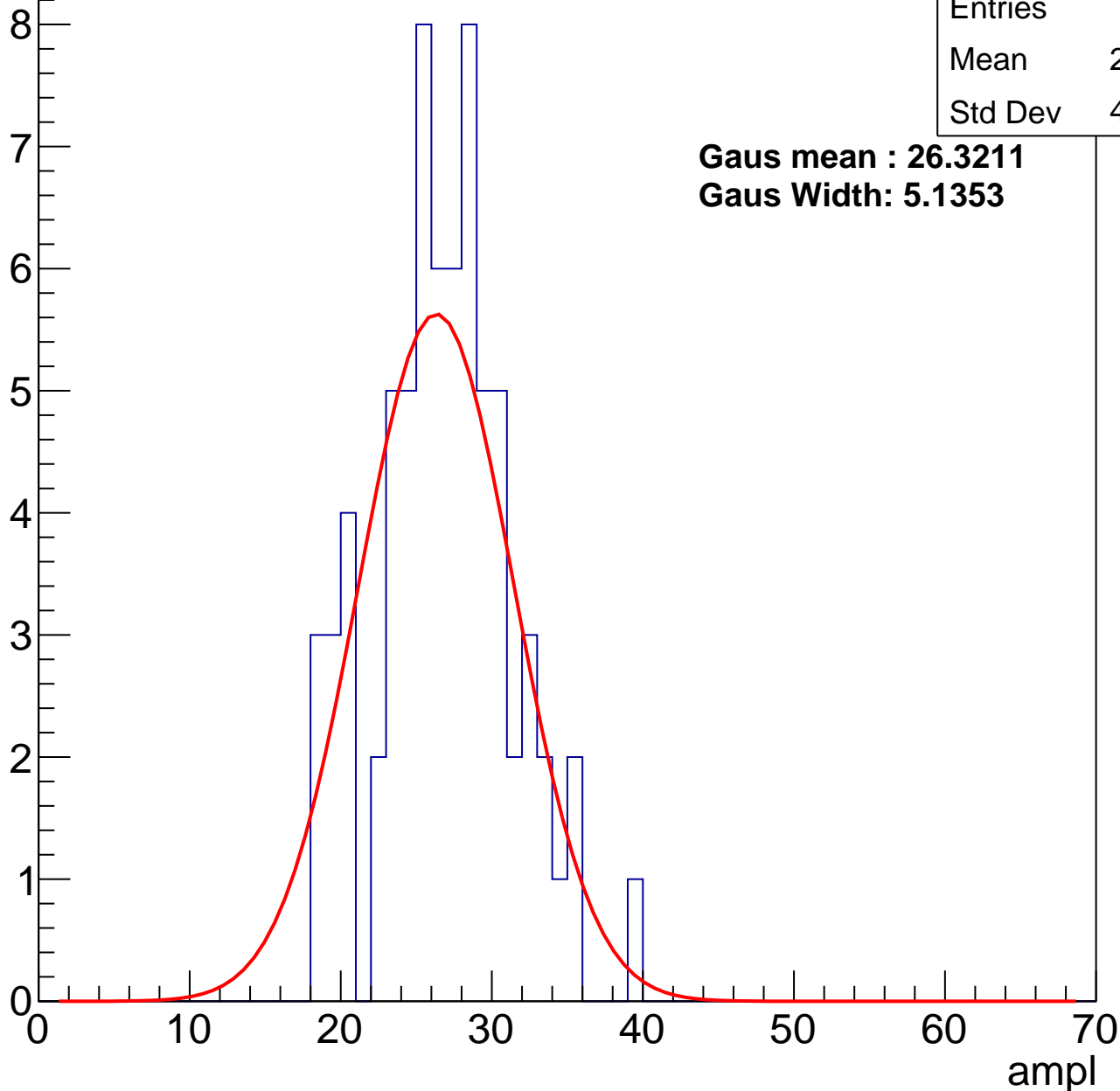
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 26.39 |
| Std Dev | 4.426 |

Gaus mean : 26.3211

Gaus Width: 5.1353



B0L102S, U20-ch122, adc1

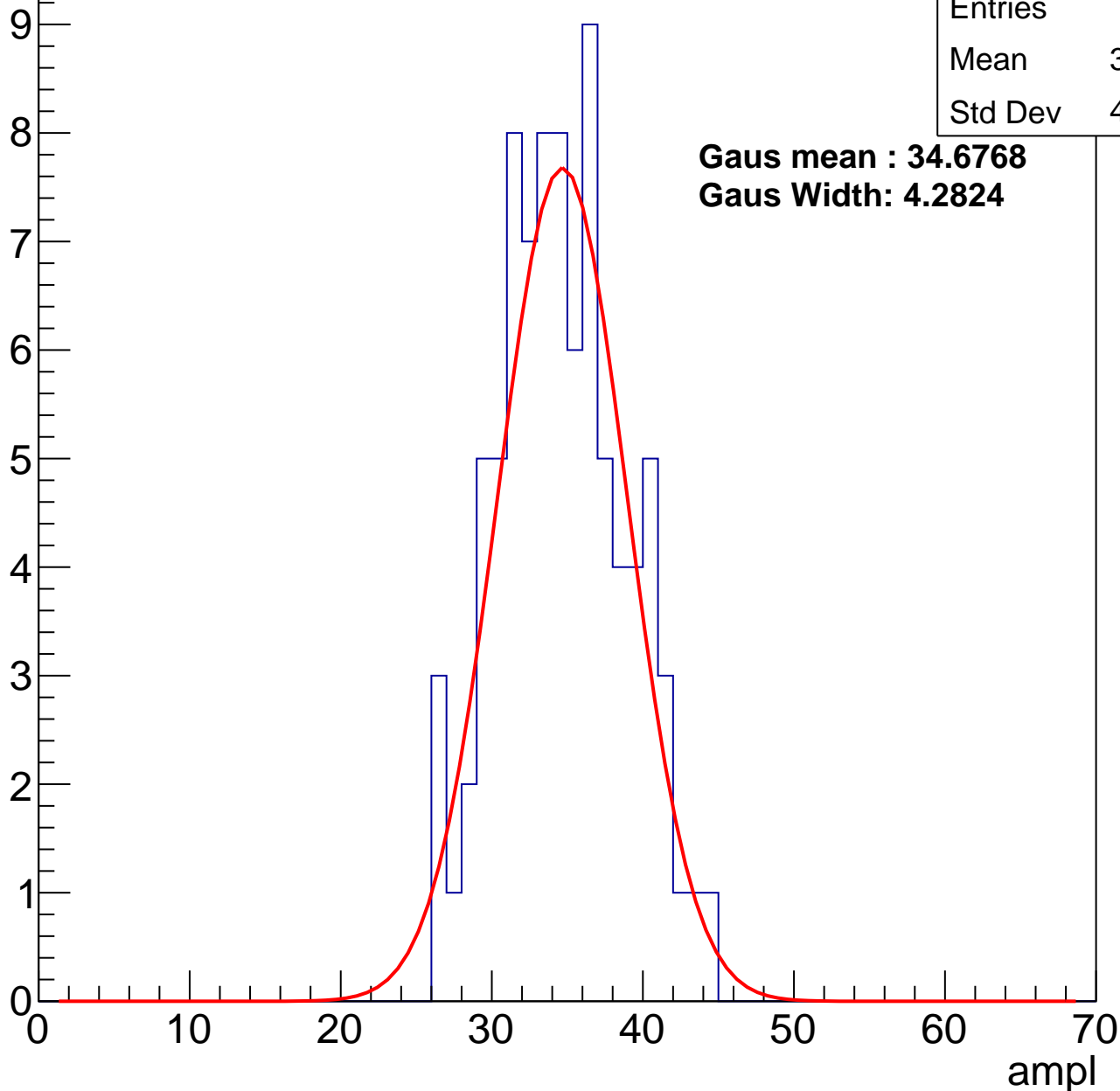
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 34.22 |
| Std Dev | 4.099 |

Gaus mean : 34.6768

Gaus Width: 4.2824



B0L102S, U20-ch122, adc2

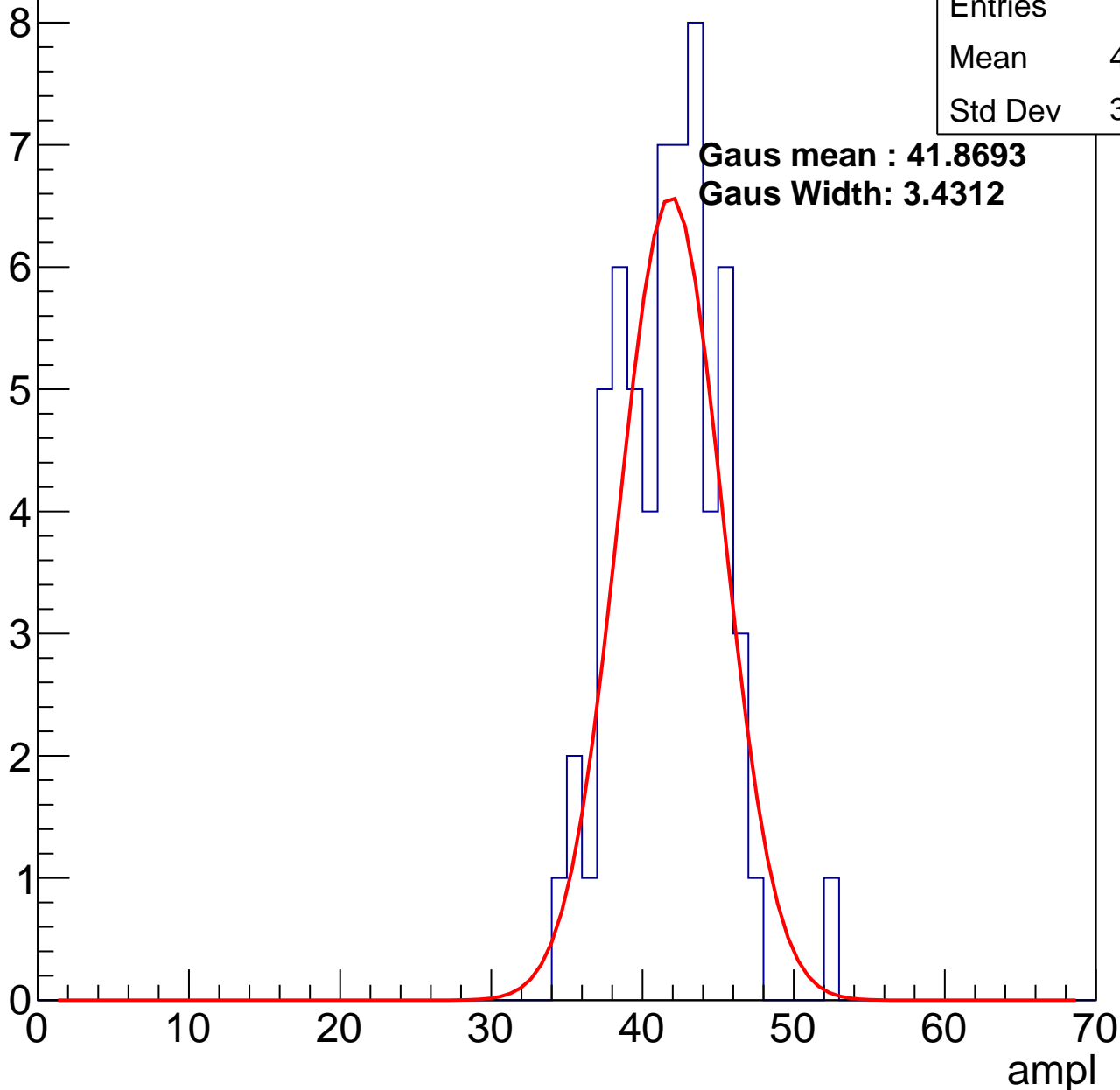
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.25 |
| Std Dev | 3.396 |

Gaus mean : 41.8693

Gaus Width: 3.4312

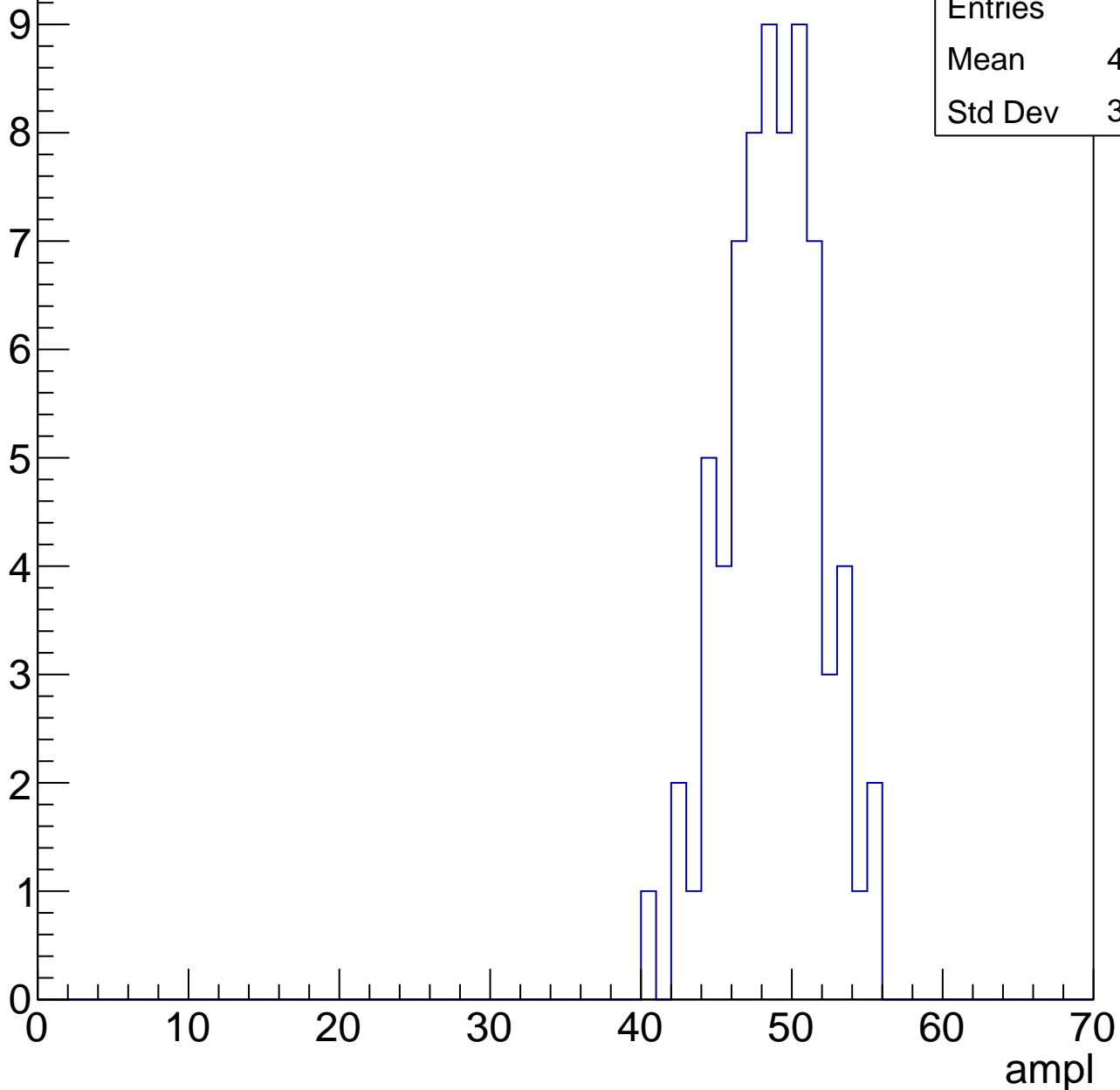


B0L102S, U20-ch122, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 48.28 |
| Std Dev | 3.132 |

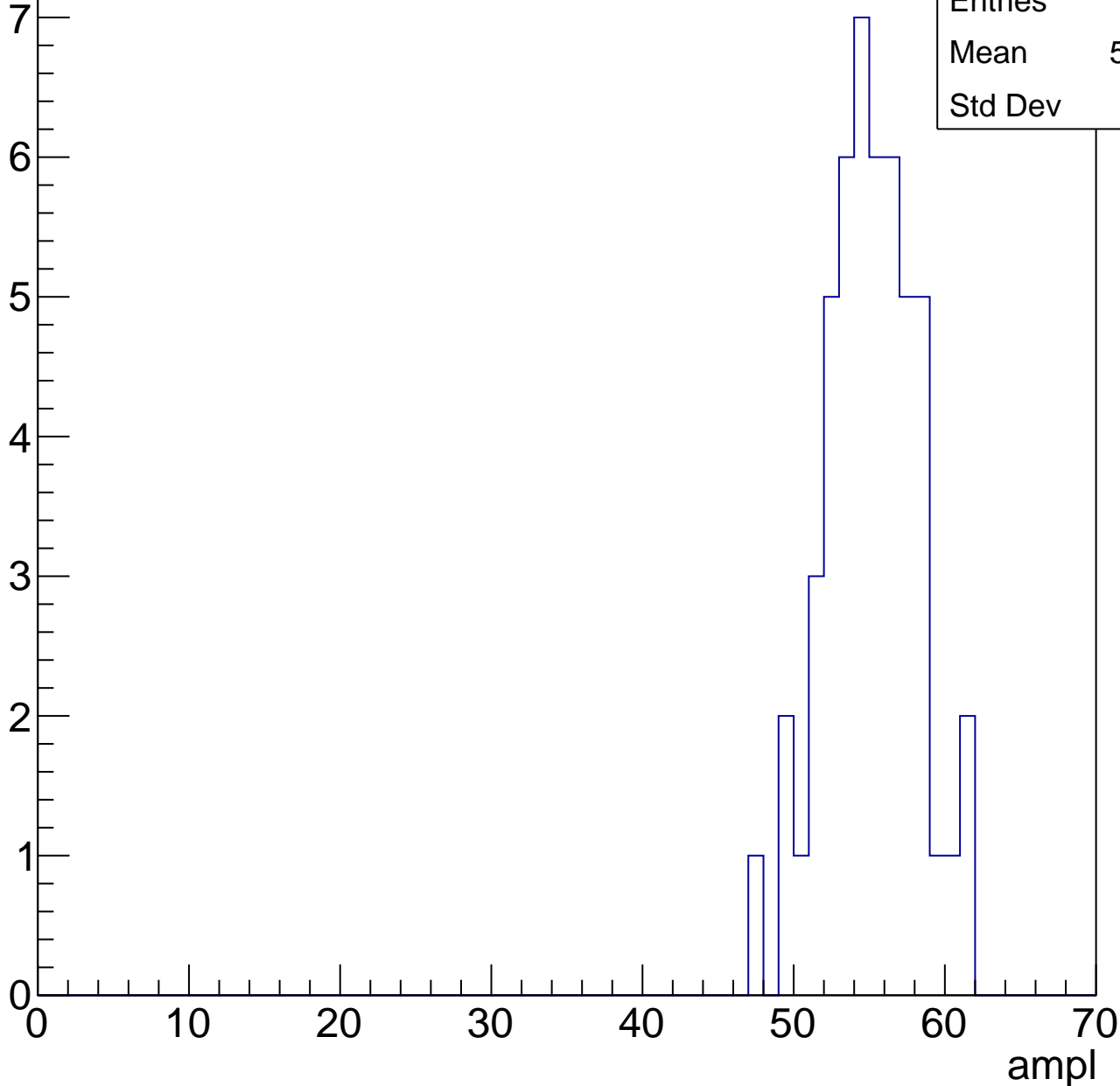


B0L102S, U20-ch122, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.63 |
| Std Dev | 3.01 |

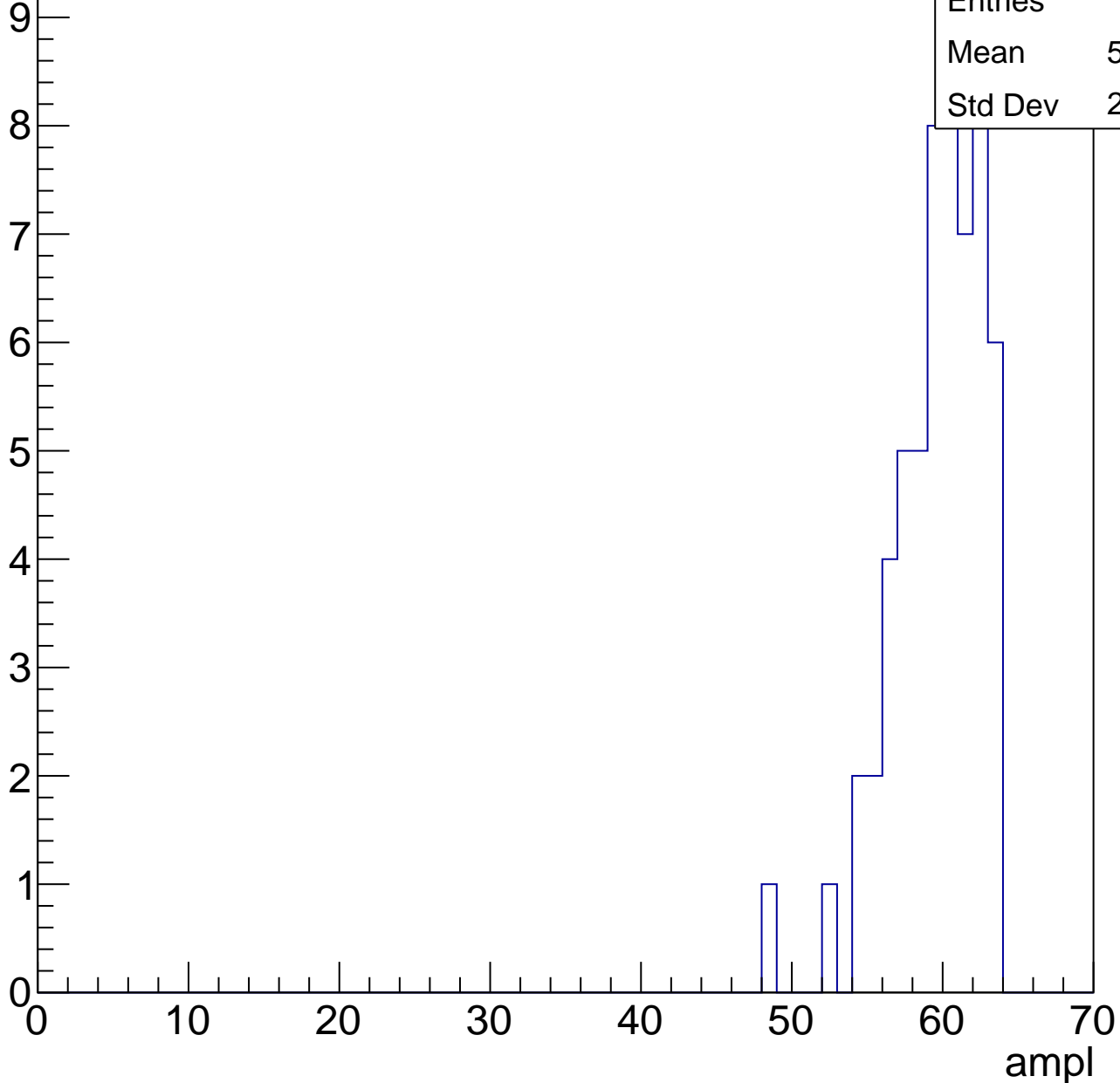


B0L102S, U20-ch122, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

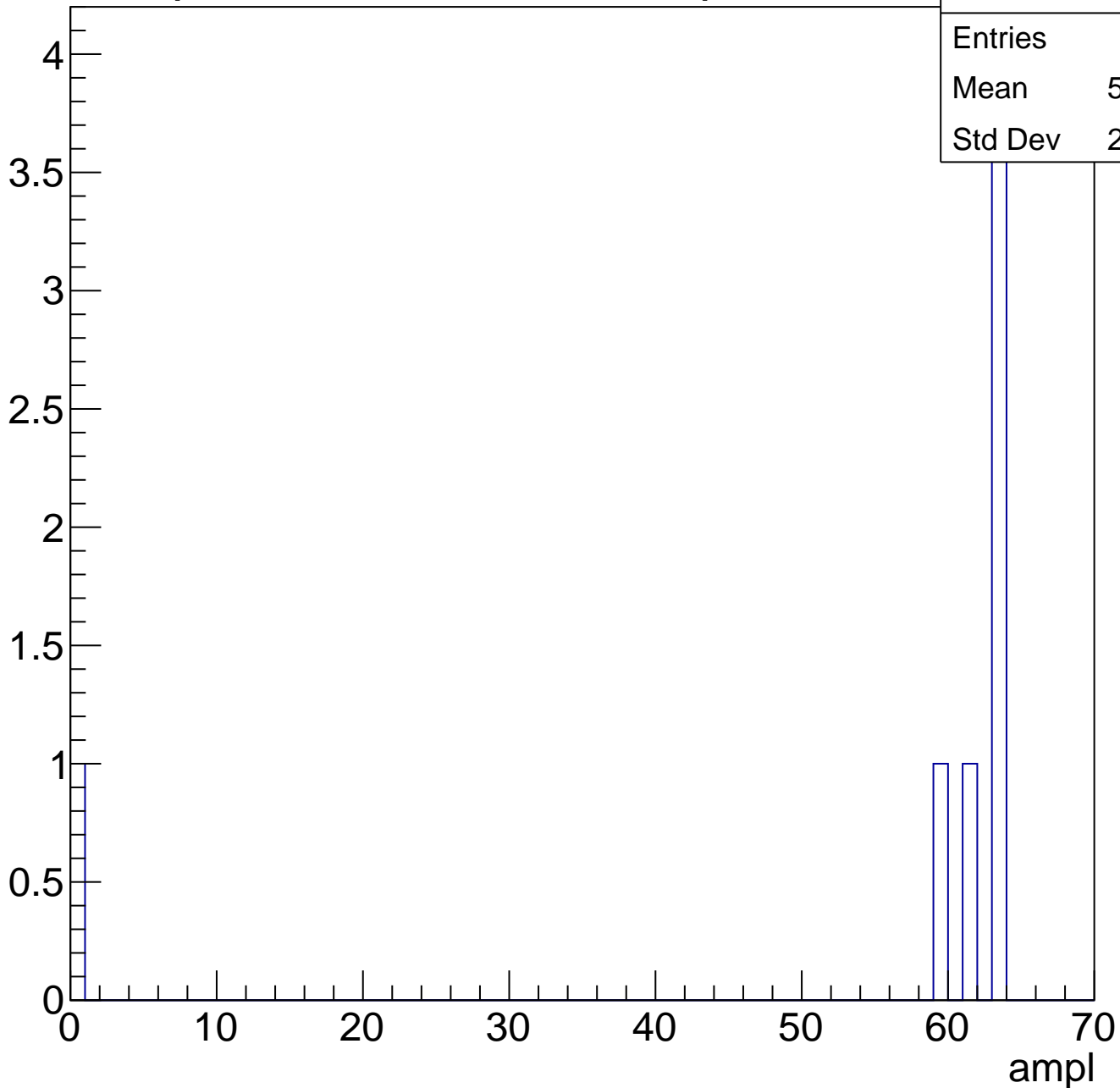
| | |
|---------|-------|
| Entries | 59 |
| Mean | 59.19 |
| Std Dev | 2.983 |



B0L102S, U20-ch122, adc6

calib_packv5_042523_0143.root, FC#12, port B1

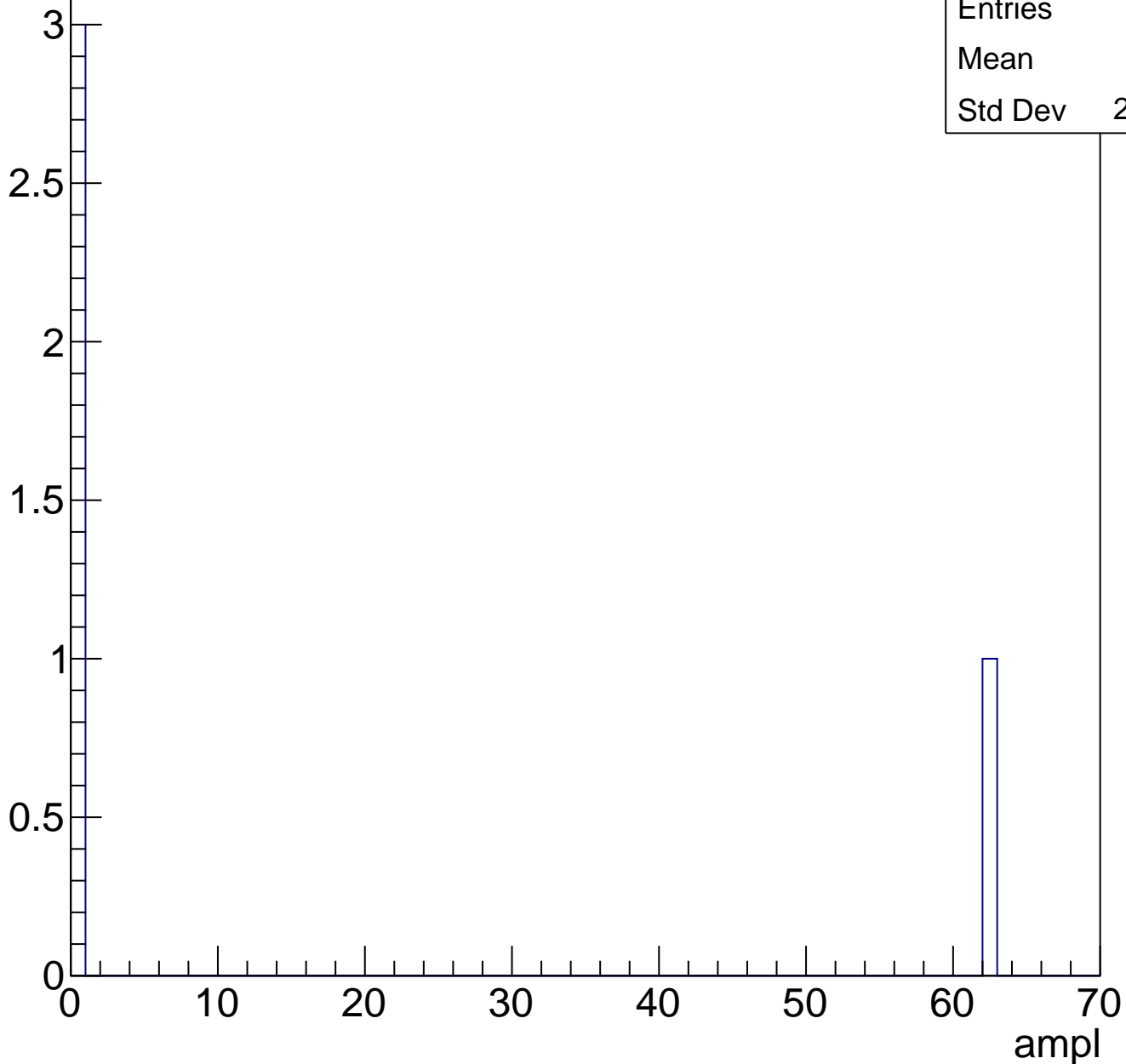
Entry



B0L102S, U20-ch122, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch123, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 62 |
| Mean | 29.69 |
| Std Dev | 4.992 |

Gaus mean : 30.5766

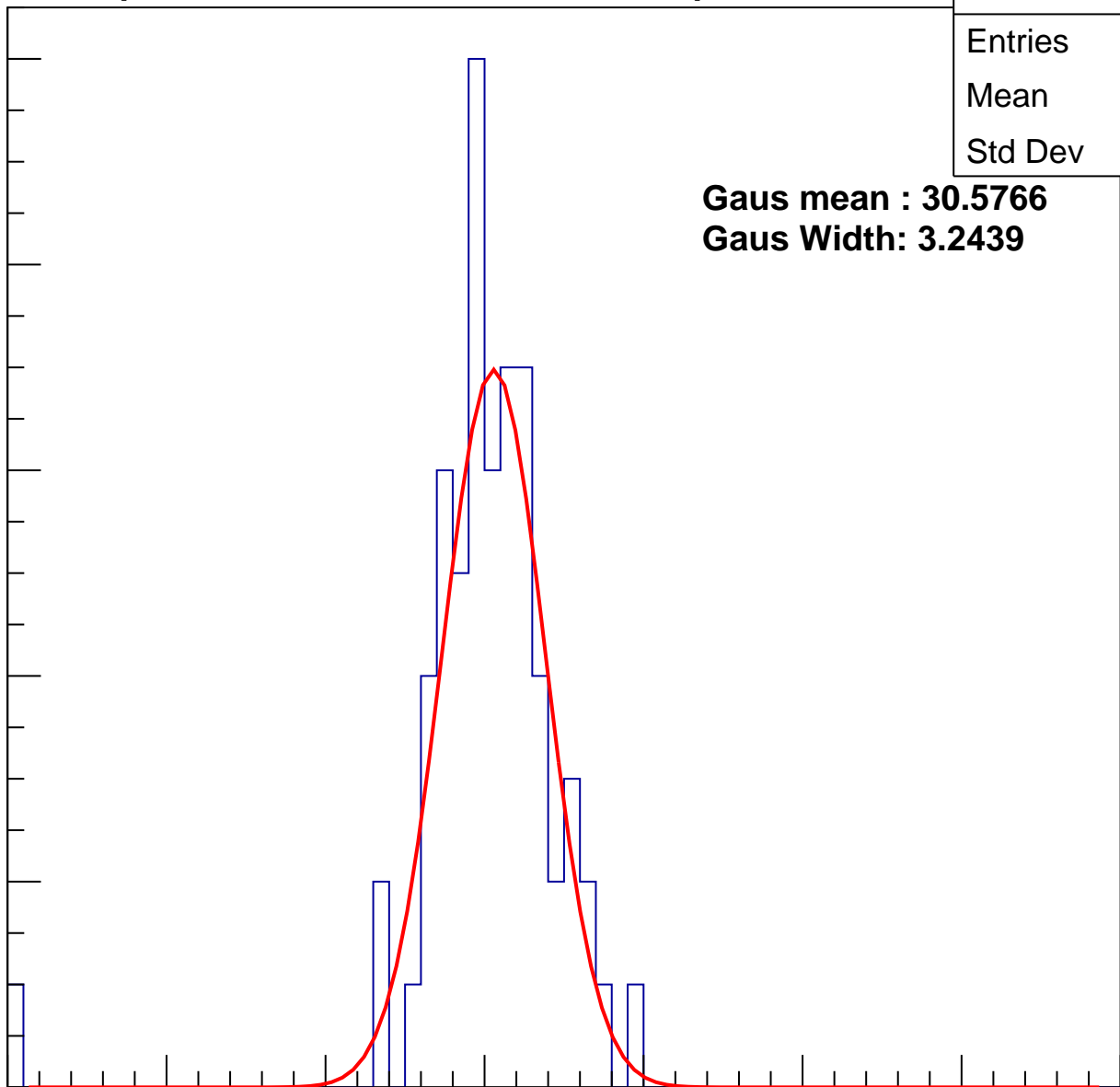
Gaus Width: 3.2439

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch123, adc1

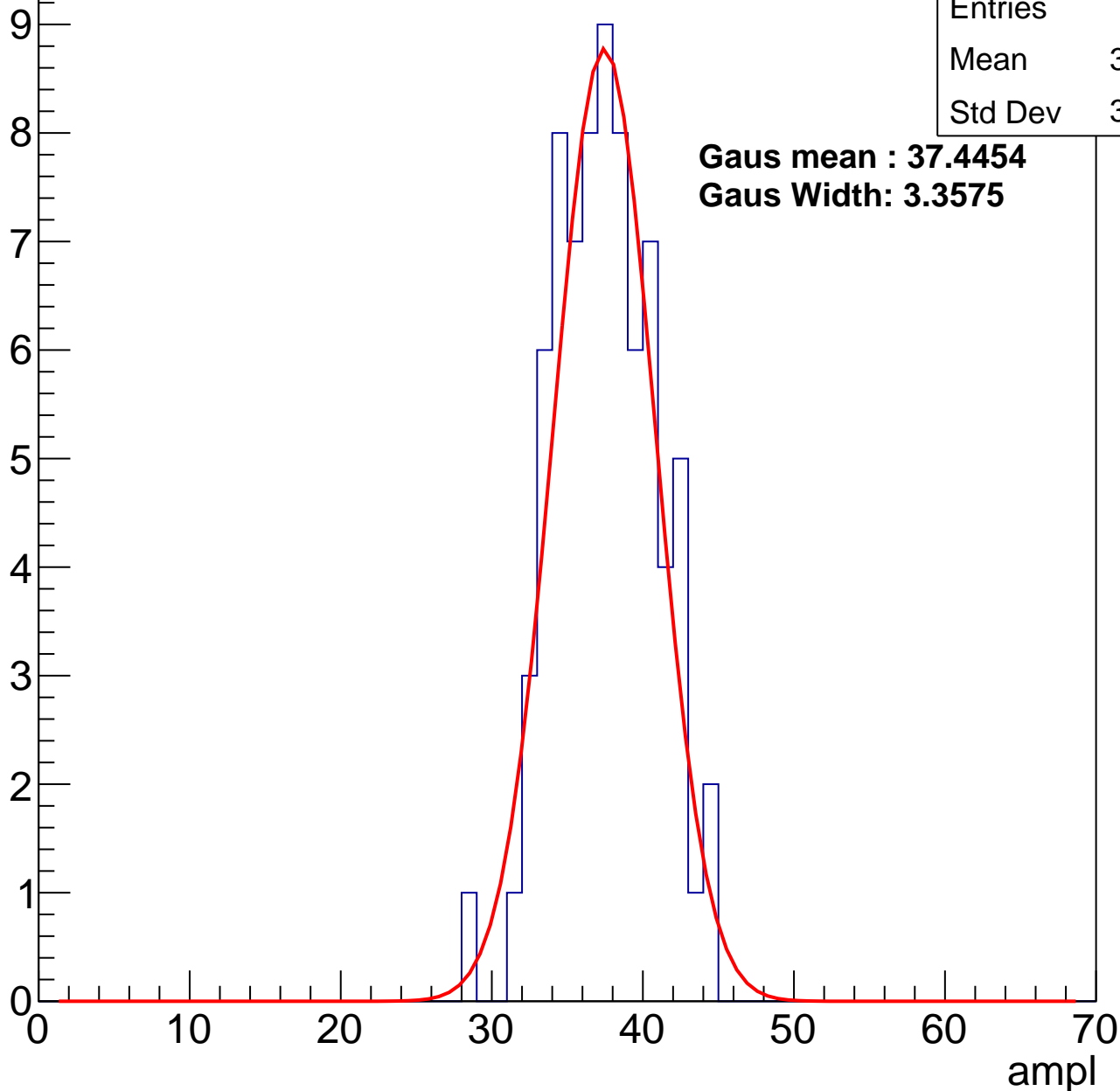
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 37.03 |
| Std Dev | 3.273 |

Gaus mean : 37.4454

Gaus Width: 3.3575



B0L102S, U20-ch123, adc2

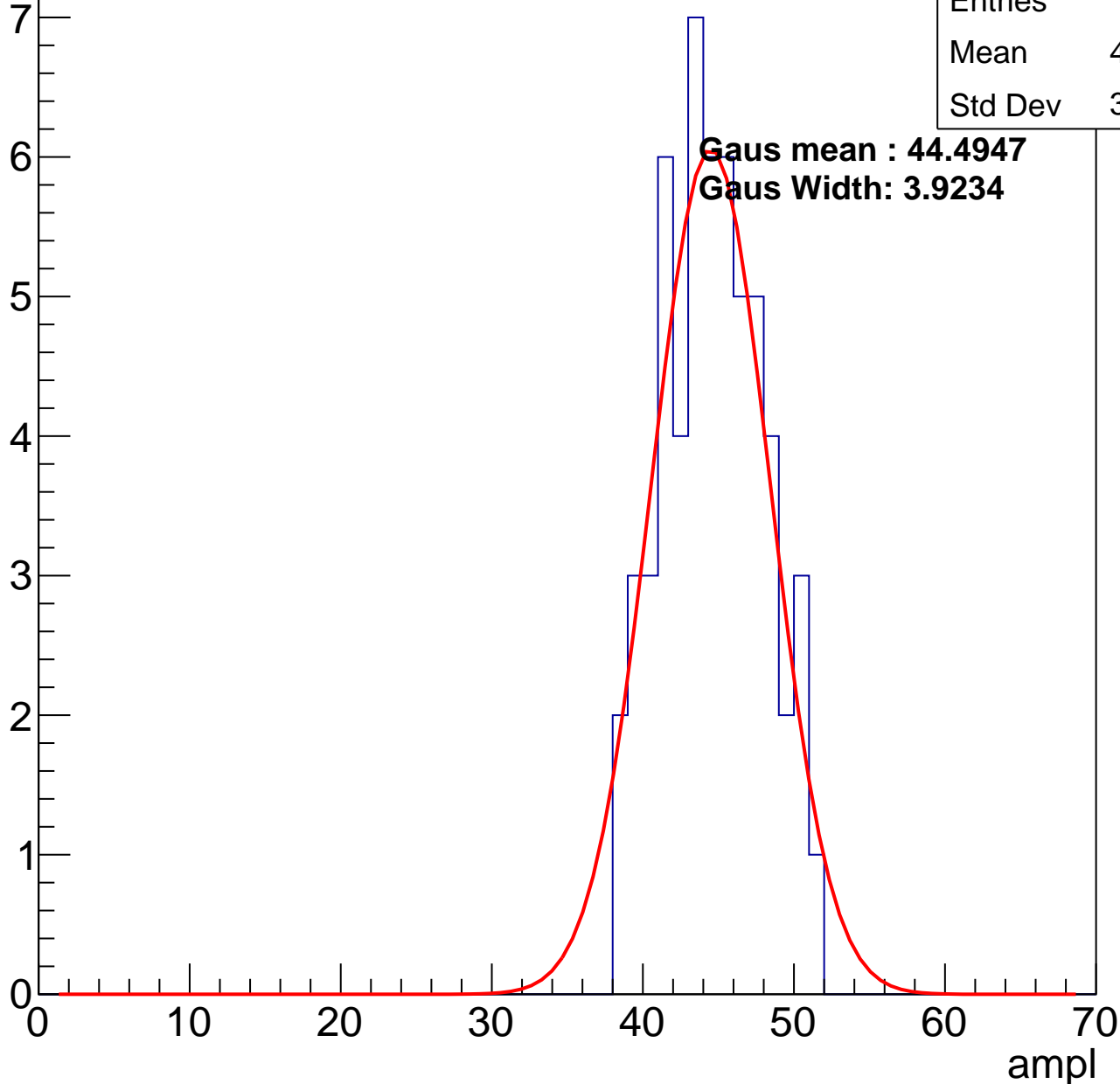
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 44.18 |
| Std Dev | 3.277 |

Gaus mean : 44.4947

Gaus Width: 3.9234

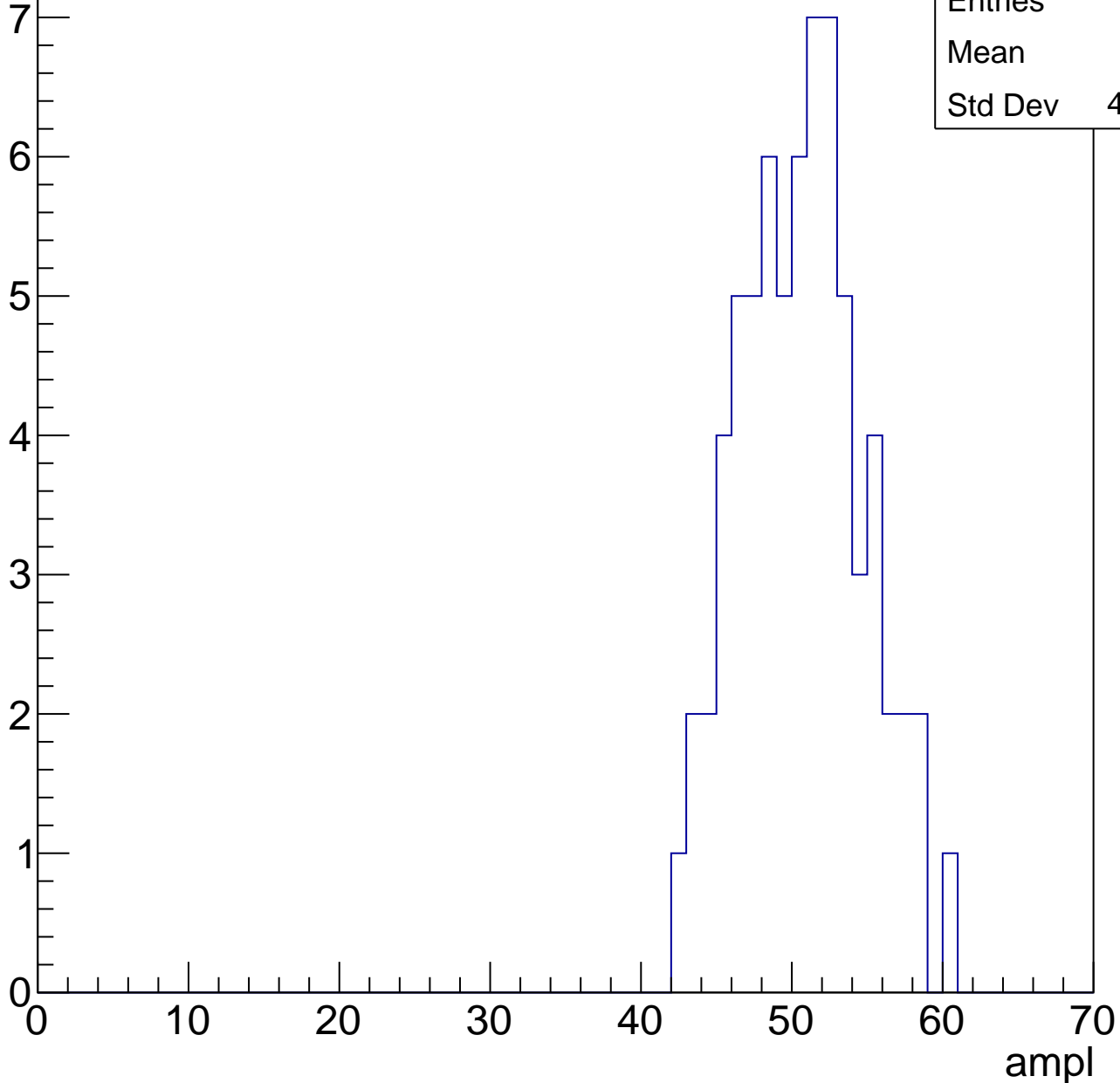


B0L102S, U20-ch123, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.2 |
| Std Dev | 4.013 |

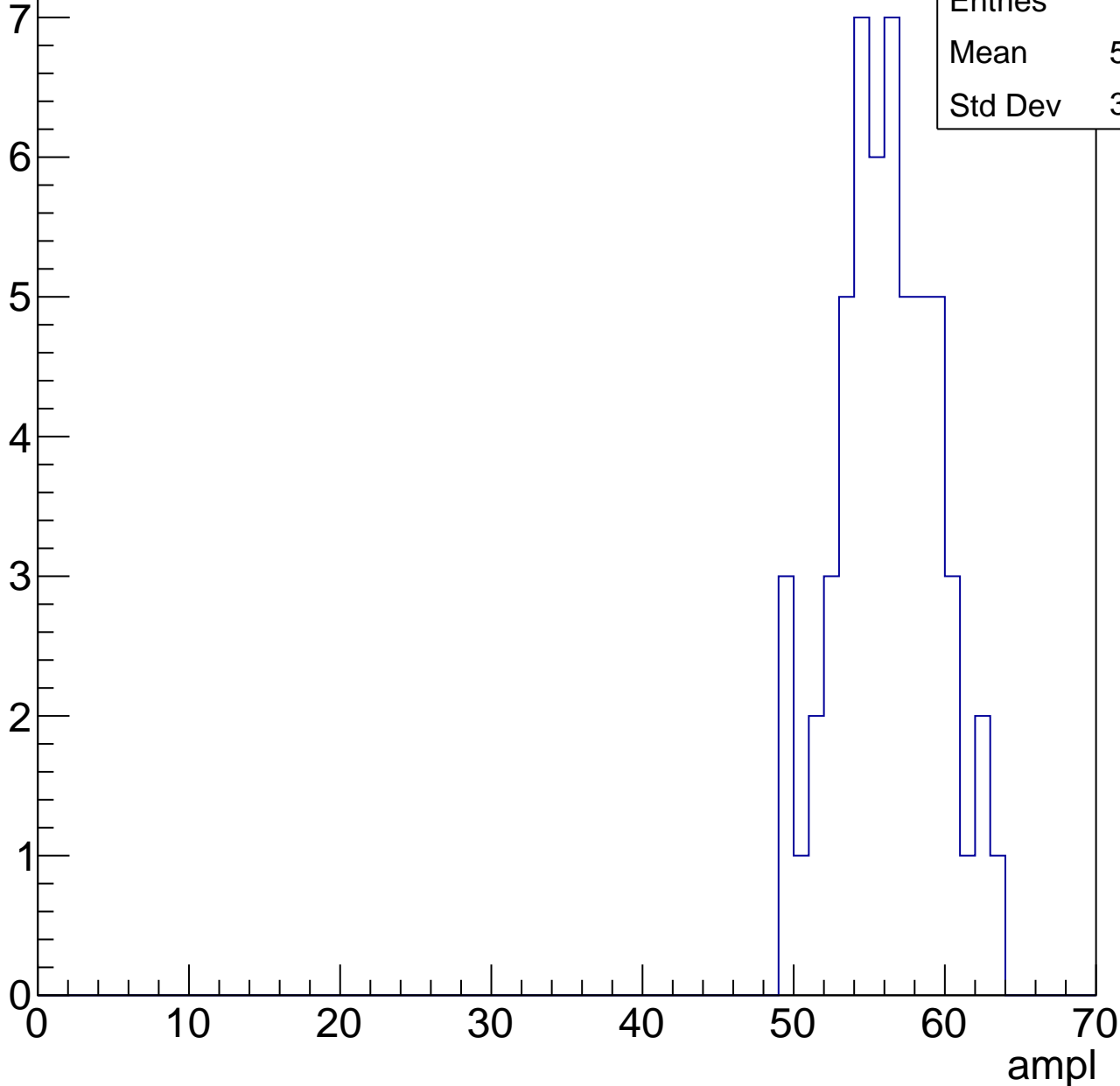


B0L102S, U20-ch123, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

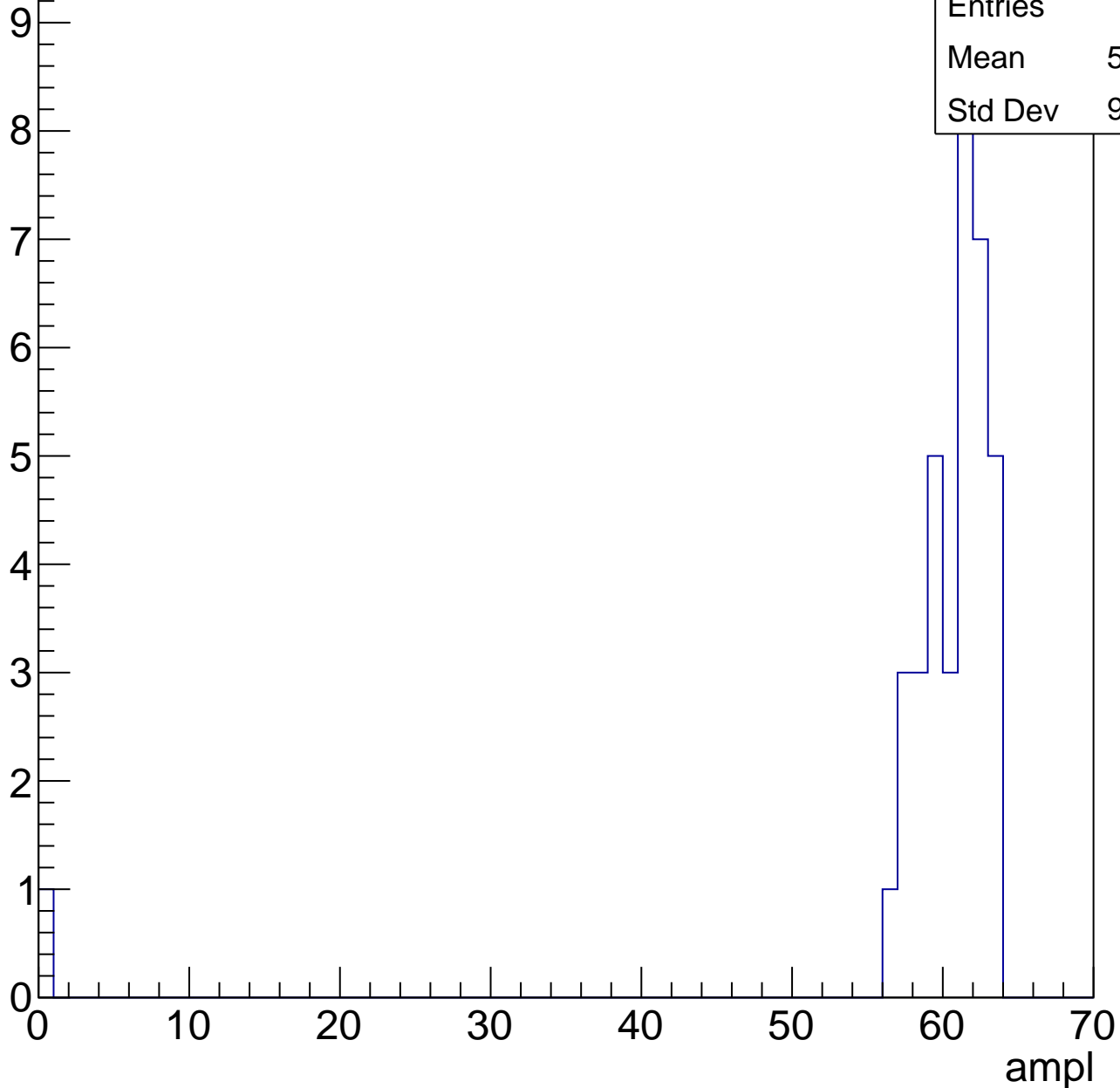
| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.68 |
| Std Dev | 3.323 |



B0L102S, U20-ch123, adc5

calib_packv5_042523_0143.root, FC#12, port B1

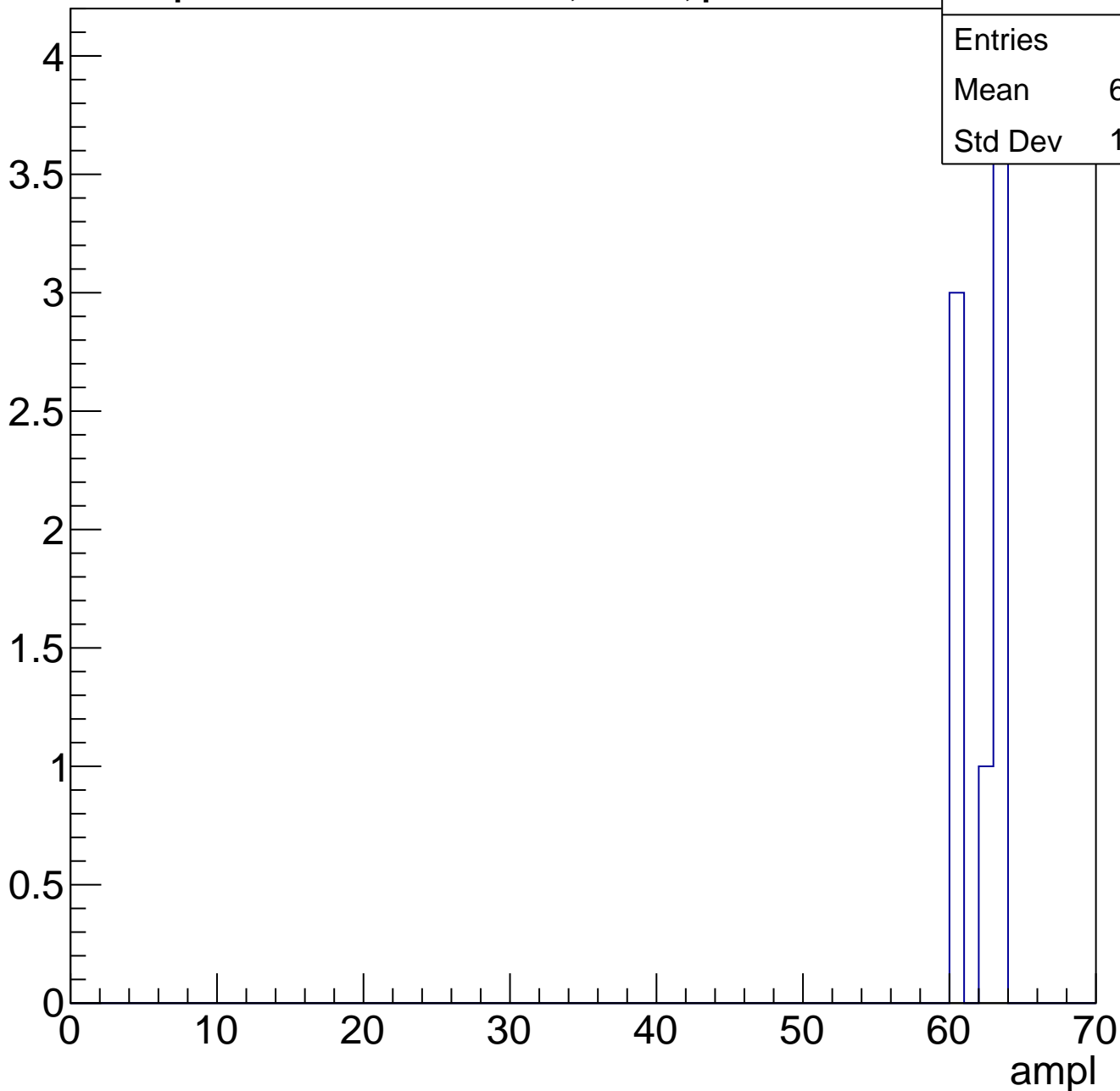
Entry



B0L102S, U20-ch123, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch123, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch124, adc0

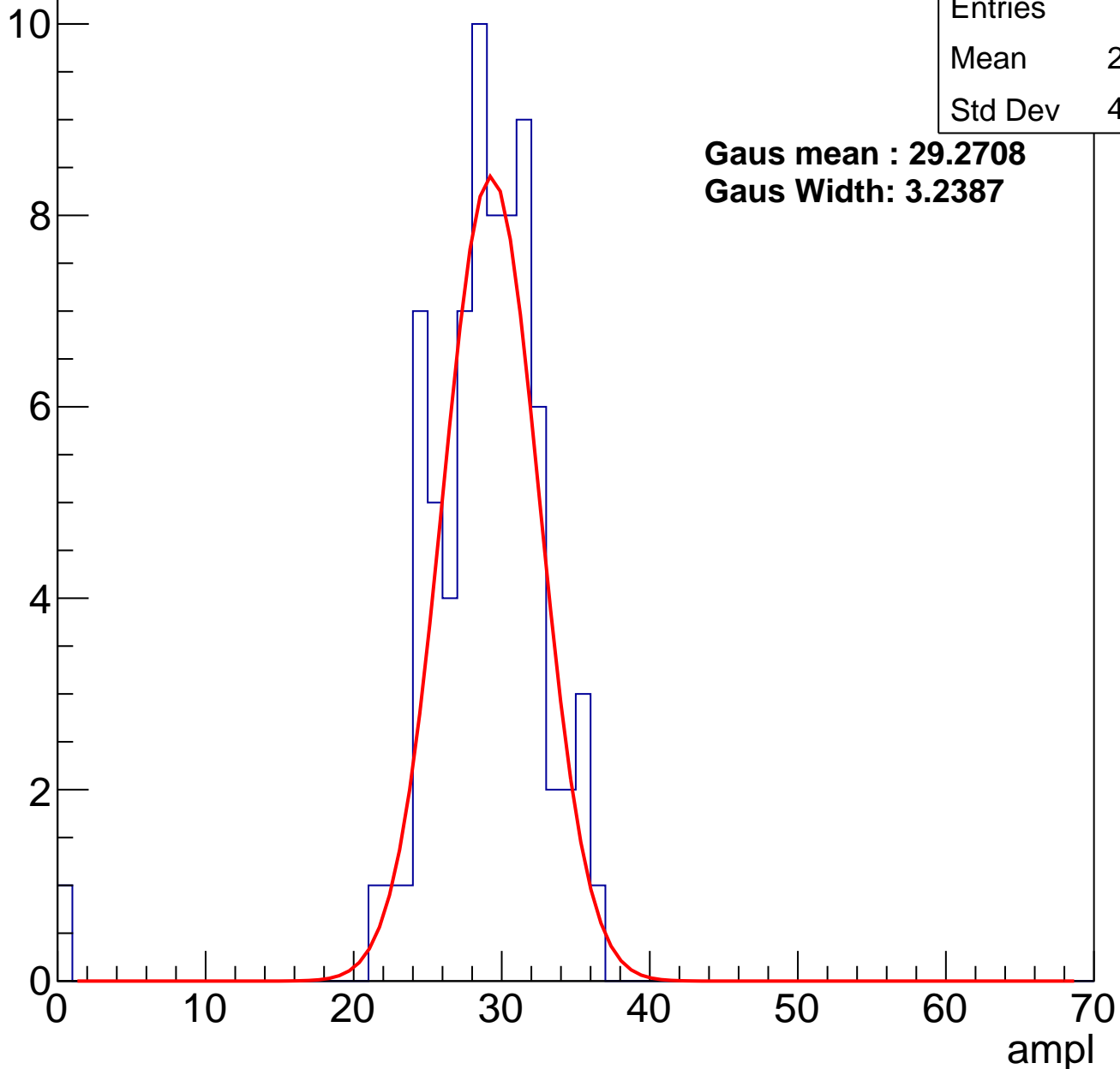
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 76 |
| Mean | 28.29 |
| Std Dev | 4.614 |

Gaus mean : 29.2708

Gaus Width: 3.2387

Entry



B0L102S, U20-ch124, adc1

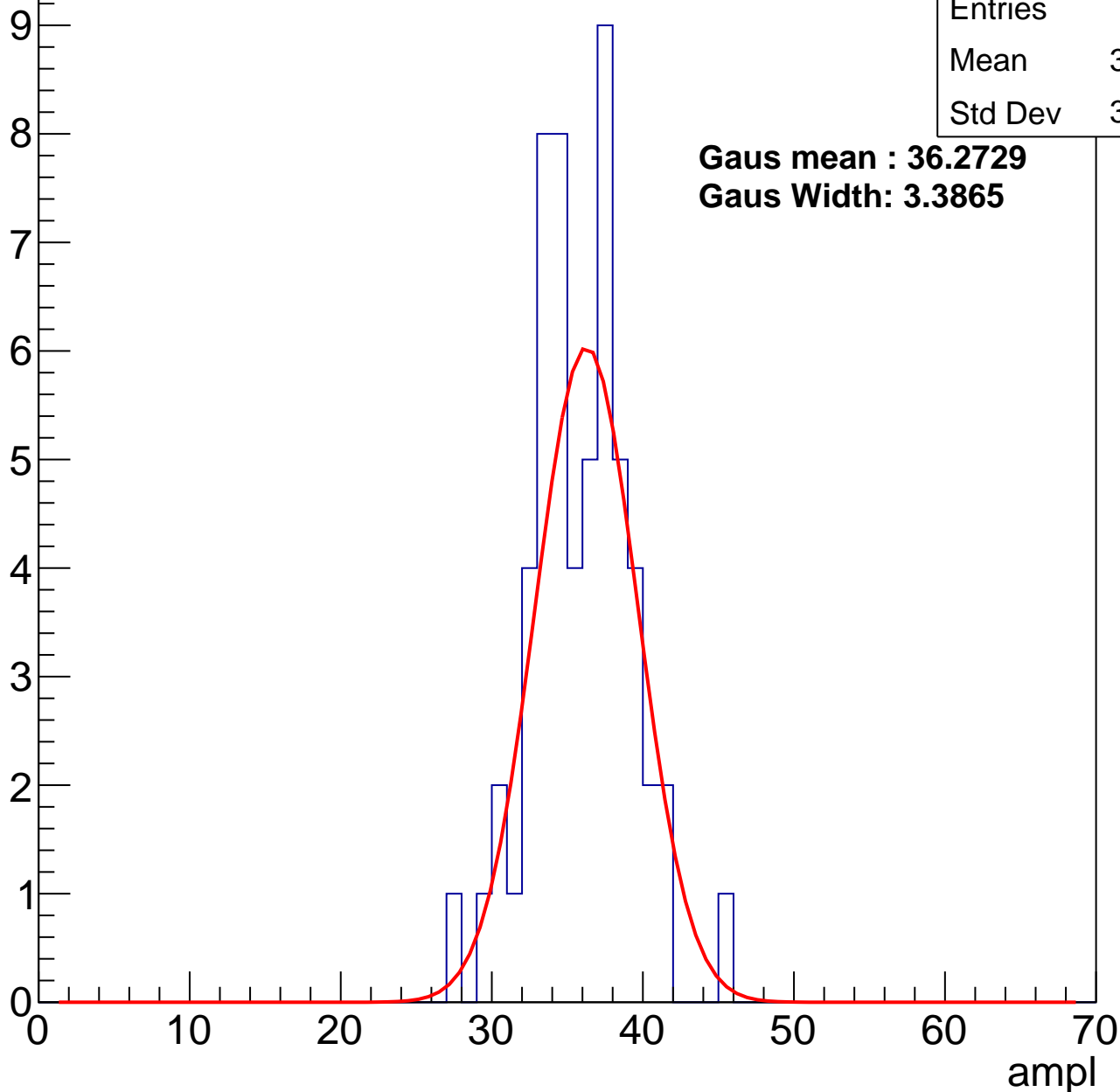
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 35.39 |
| Std Dev | 3.259 |

Gaus mean : 36.2729

Gaus Width: 3.3865



B0L102S, U20-ch124, adc2

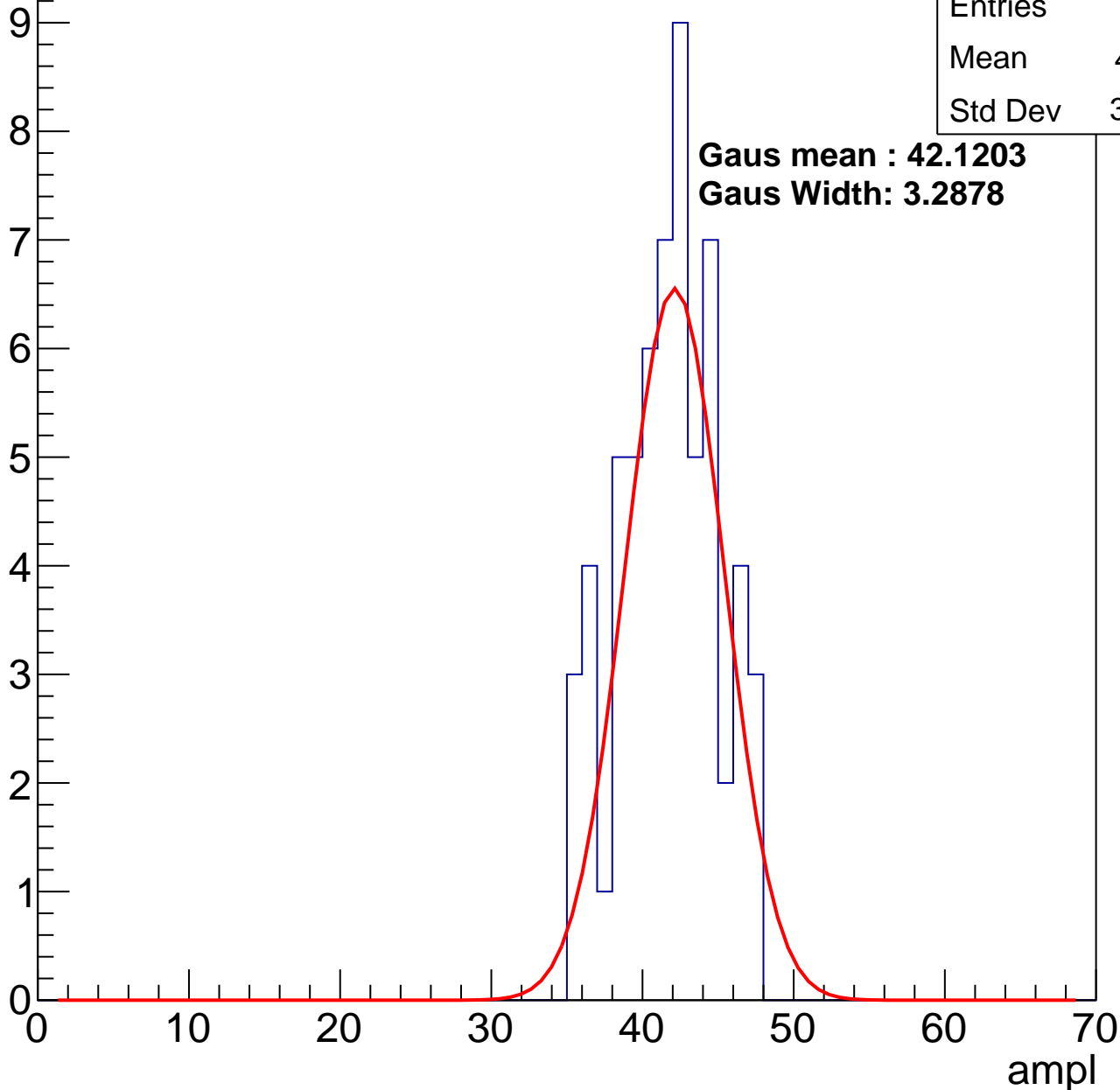
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.21 |
| Std Dev | 3.199 |

Gaus mean : 42.1203

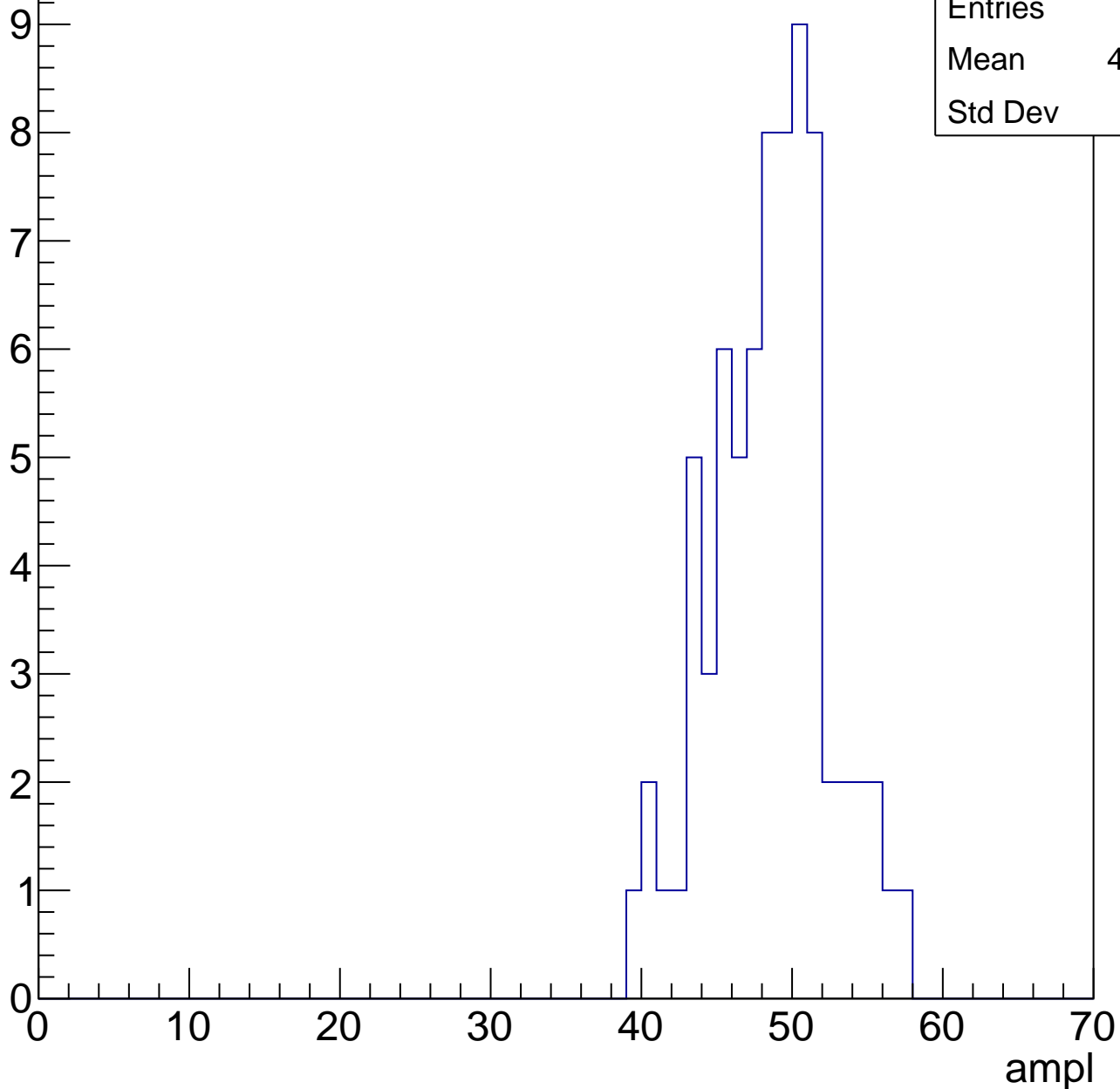
Gaus Width: 3.2878



B0L102S, U20-ch124, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



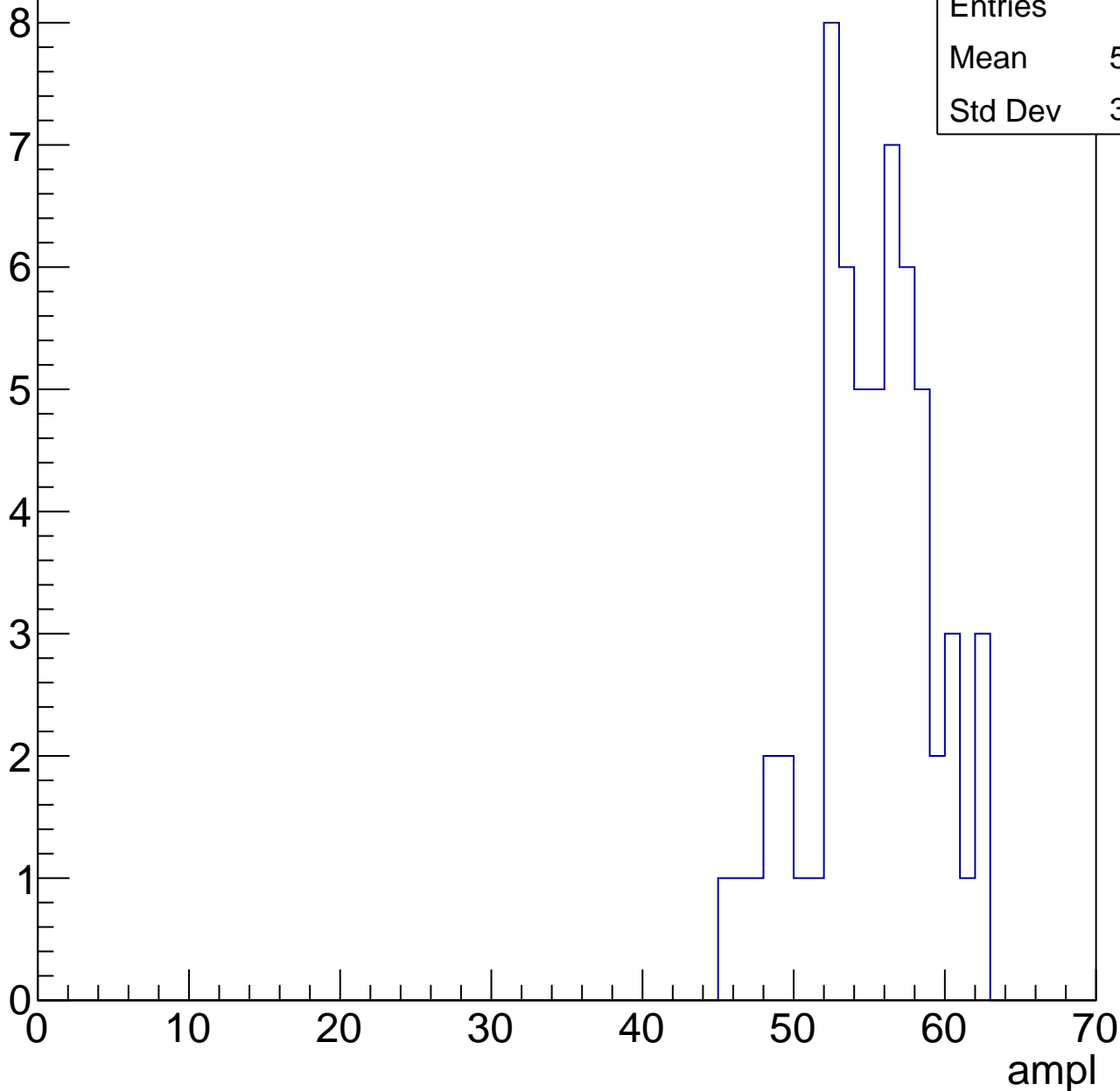
| | |
|---------|-------|
| Entries | 73 |
| Mean | 48.03 |
| Std Dev | 3.81 |

B0L102S, U20-ch124, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

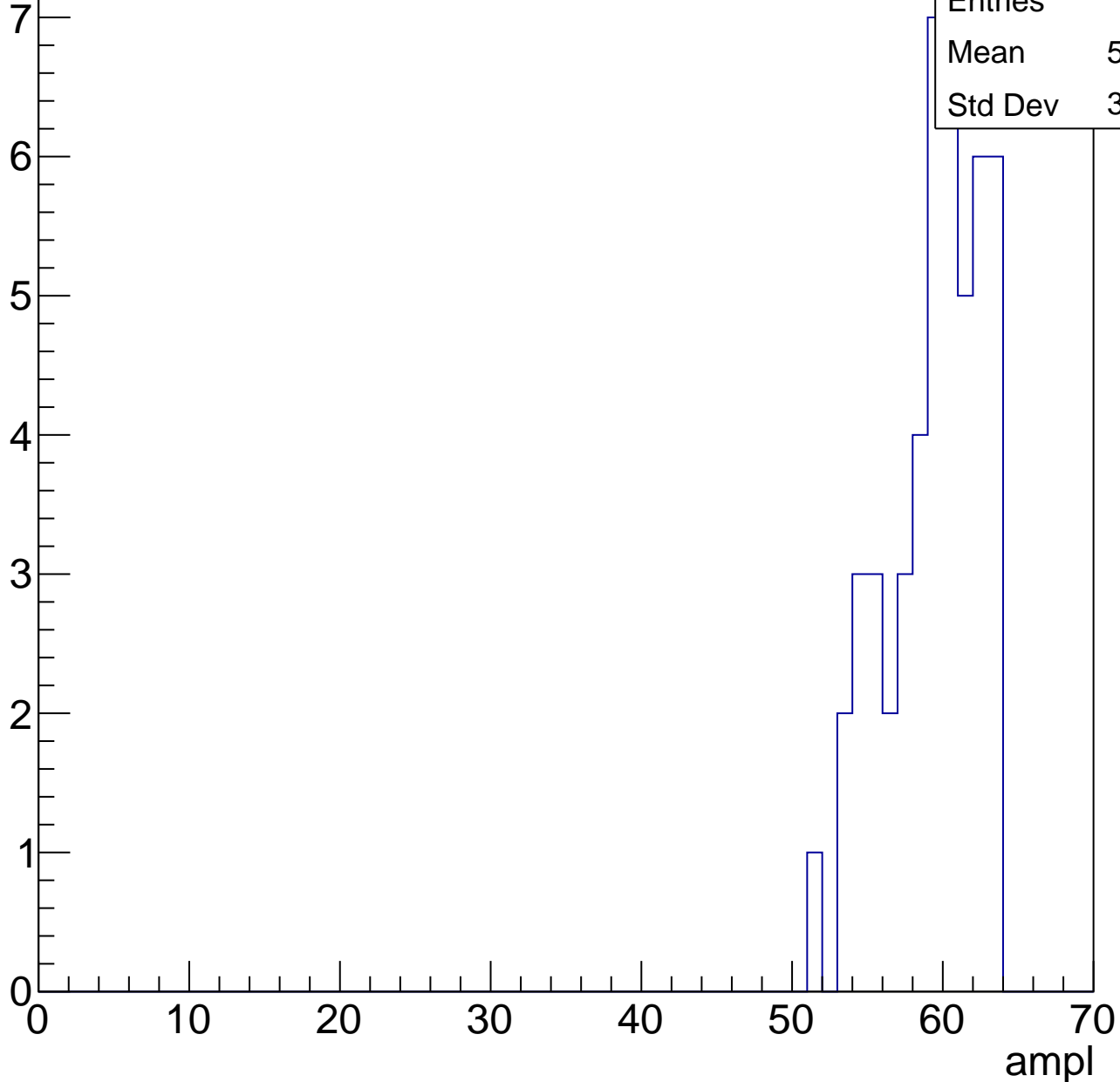
| | |
|---------|-------|
| Entries | 60 |
| Mean | 54.68 |
| Std Dev | 3.909 |



B0L102S, U20-ch124, adc5

calib_packv5_042523_0143.root, FC#12, port B1

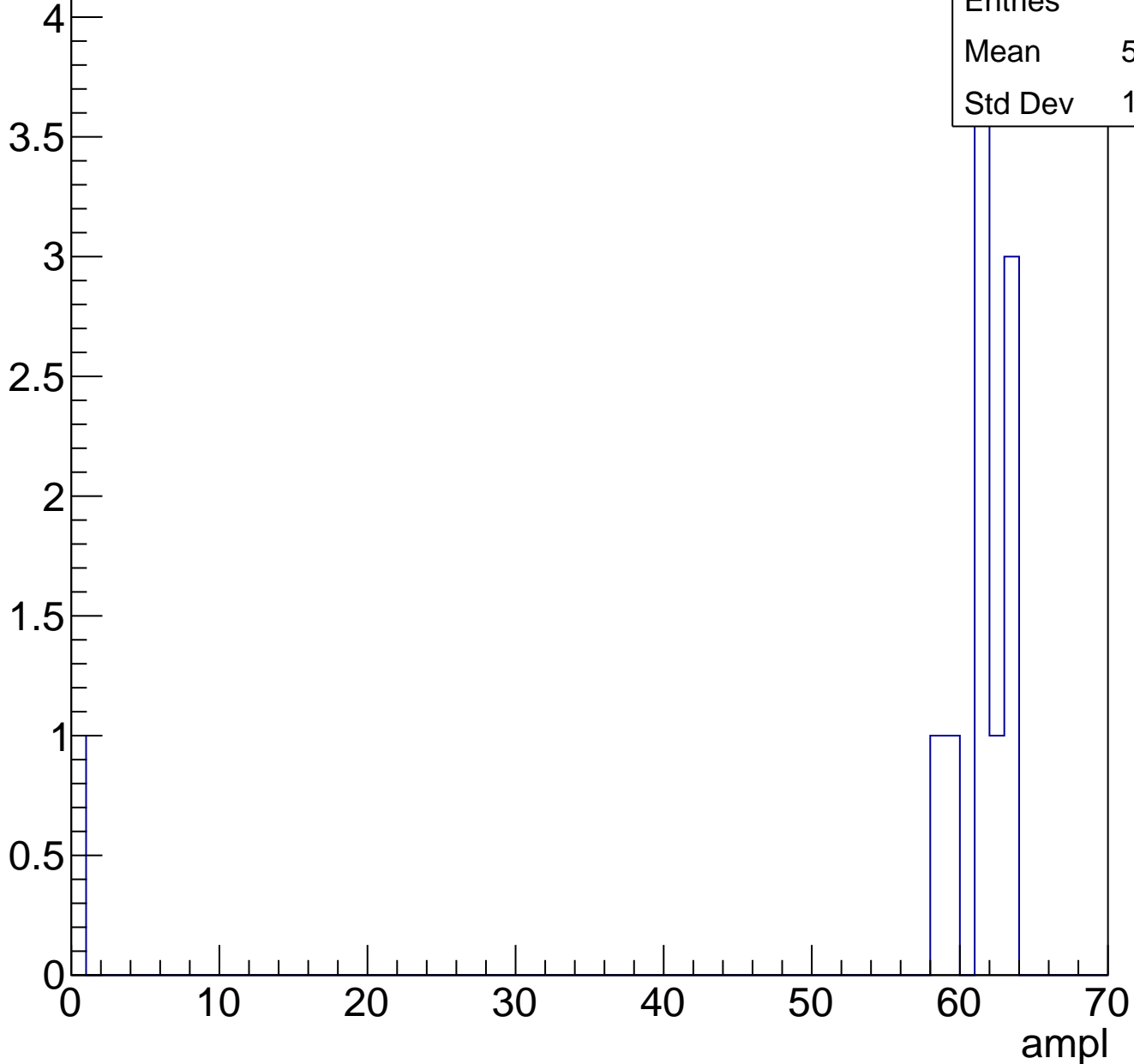
Entry



B0L102S, U20-ch124, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch124, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B0L102S, U20-ch125, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 29.01 |
| Std Dev | 5.245 |

Gaus mean : 29.5339

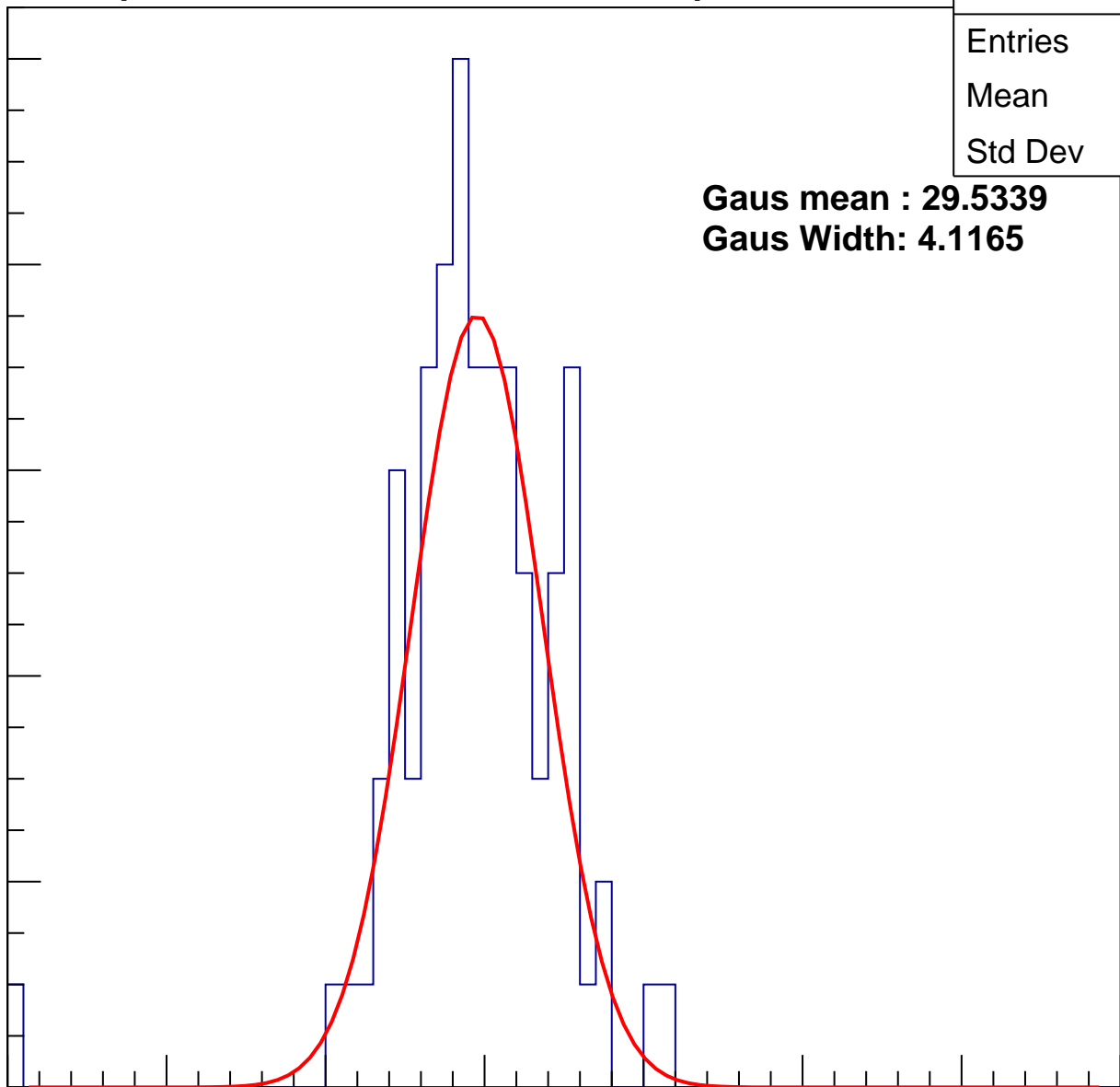
Gaus Width: 4.1165

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch125, adc1

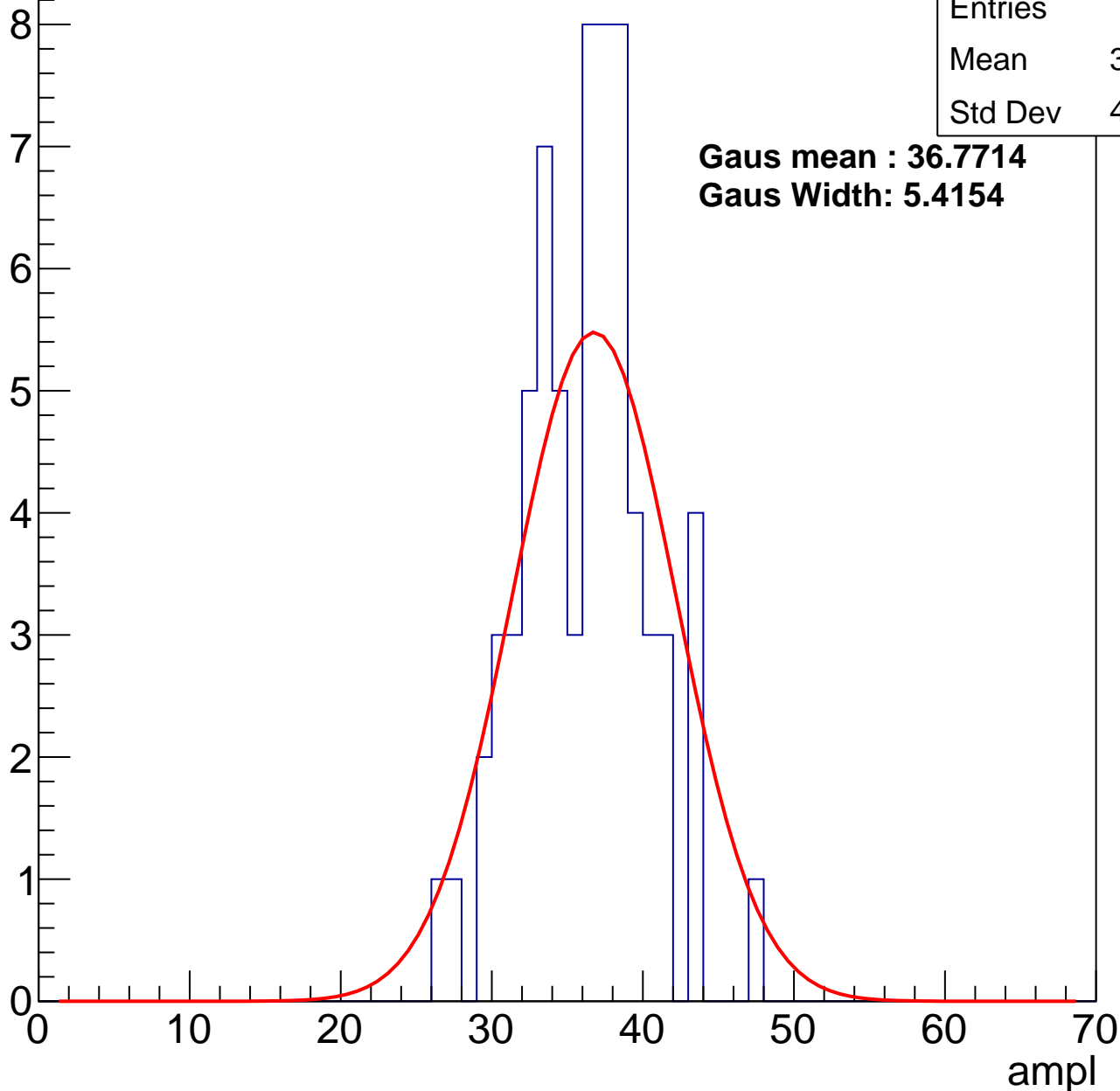
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.74 |
| Std Dev | 4.063 |

Gaus mean : 36.7714

Gaus Width: 5.4154



B0L102S, U20-ch125, adc2

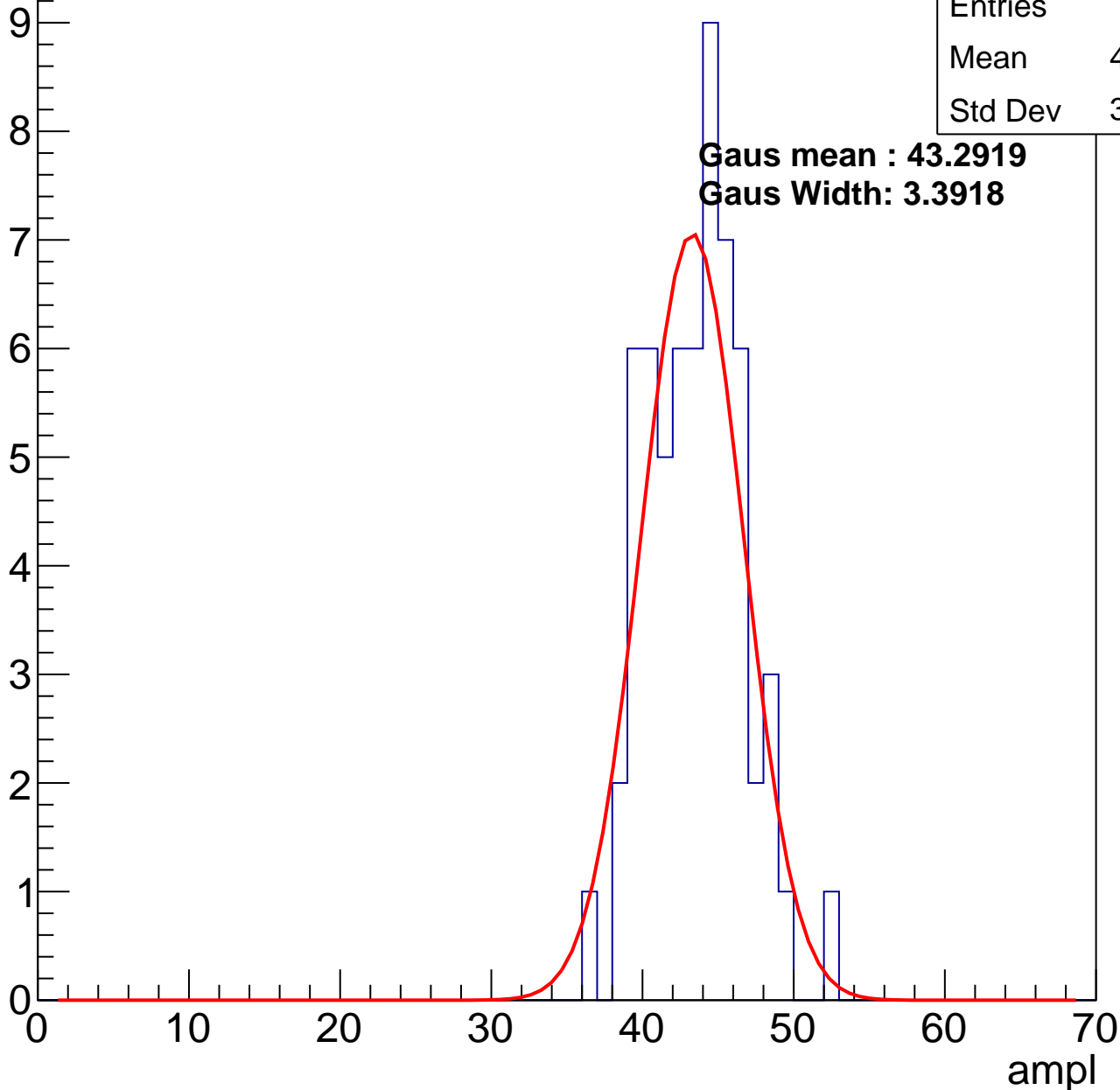
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 43.07 |
| Std Dev | 3.109 |

Gaus mean : 43.2919

Gaus Width: 3.3918

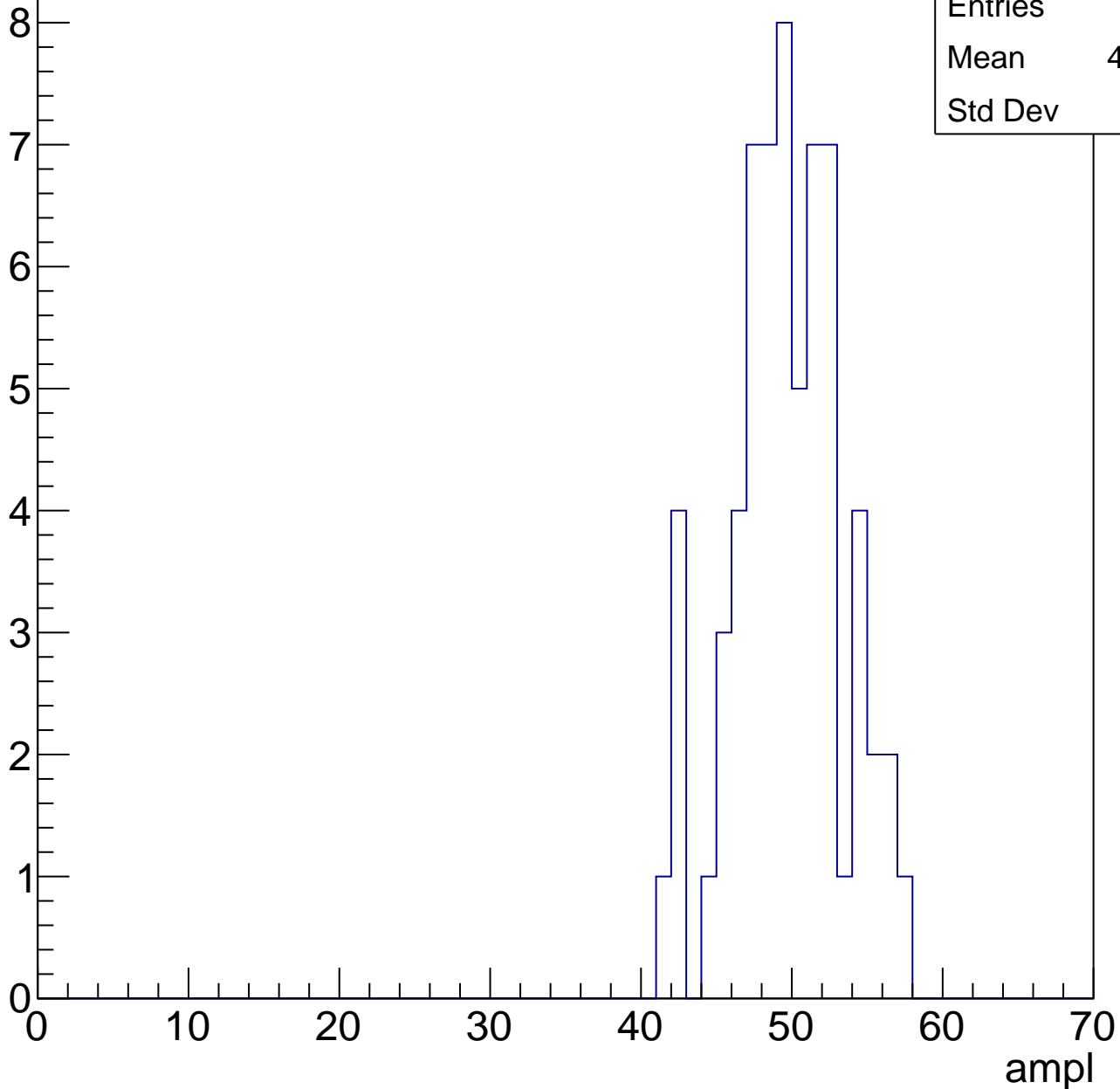


B0L102S, U20-ch125, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.19 |
| Std Dev | 3.64 |

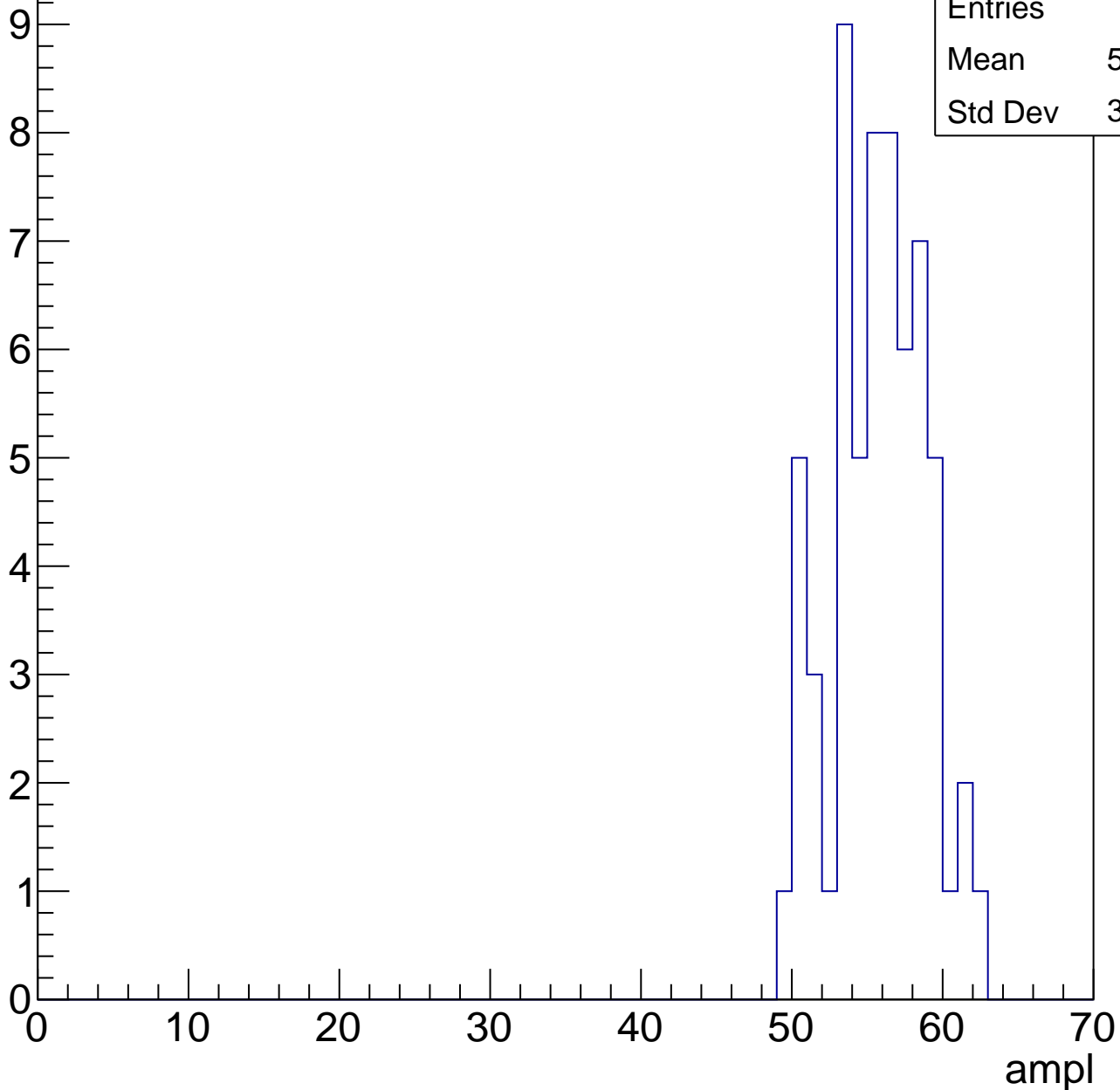


B0L102S, U20-ch125, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 55.26 |
| Std Dev | 3.048 |

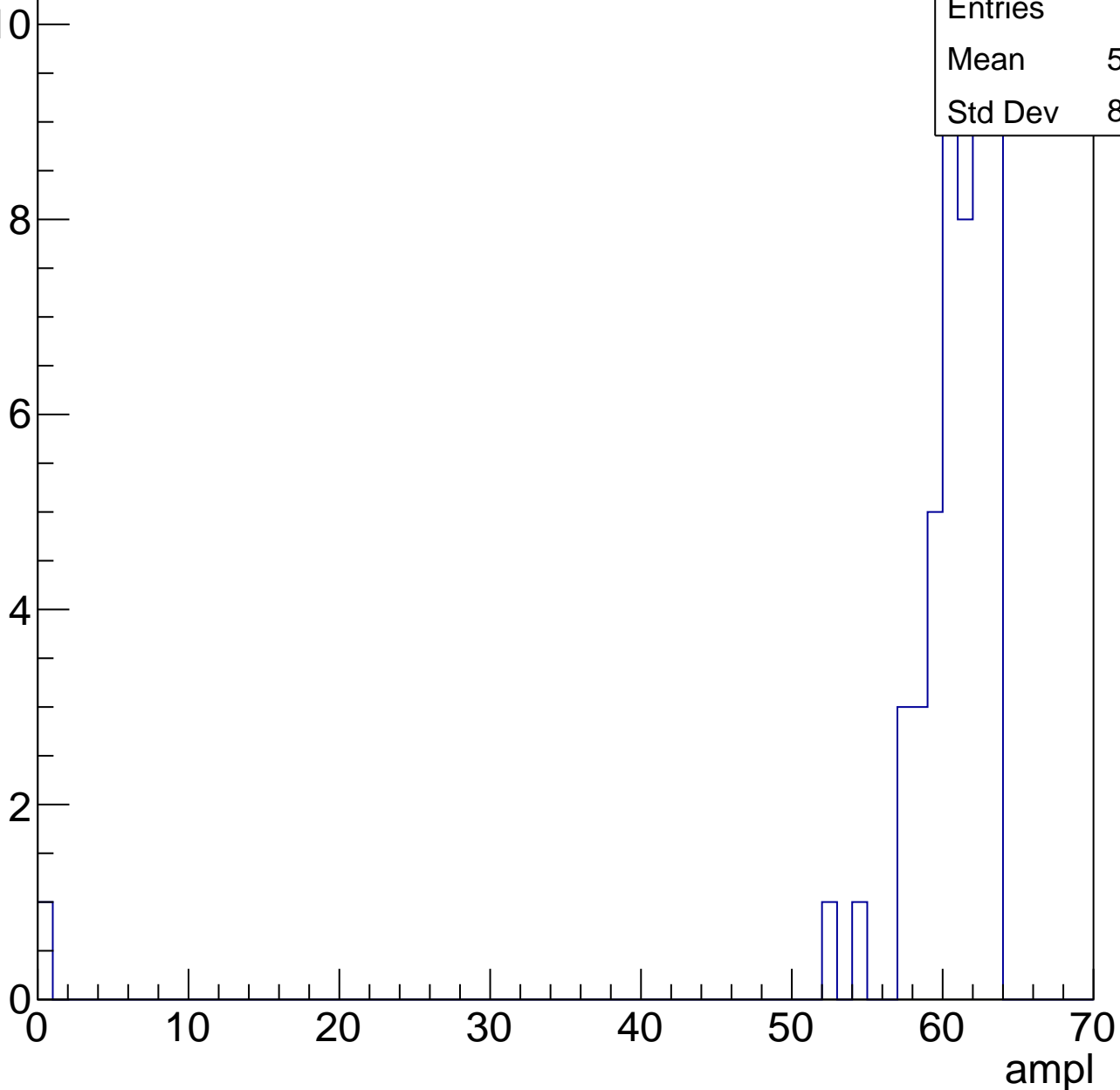


B0L102S, U20-ch125, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 59.24 |
| Std Dev | 8.774 |



B0L102S, U20-ch125, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch125, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch126, adc0

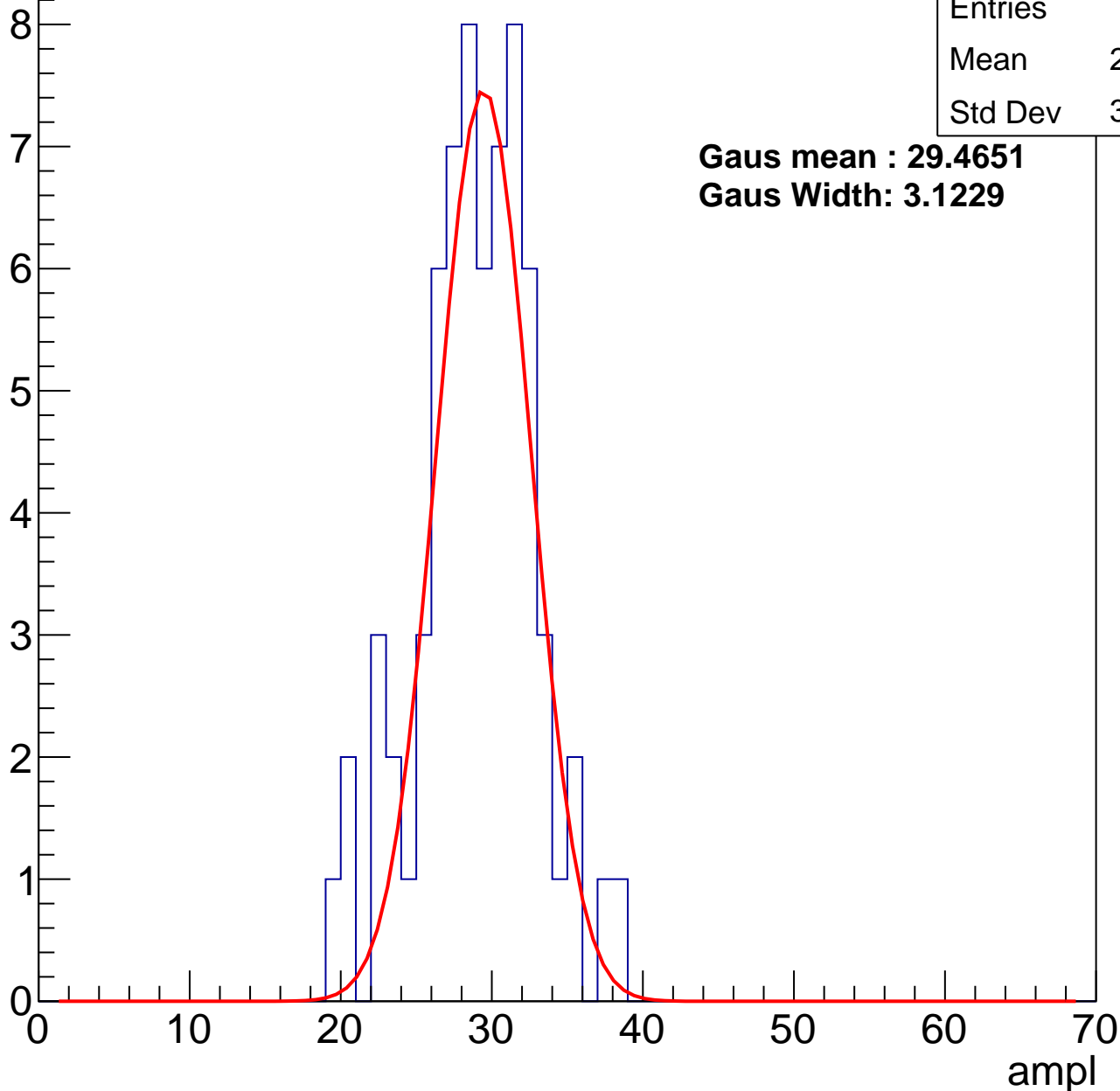
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.54 |
| Std Dev | 3.859 |

Gaus mean : 29.4651

Gaus Width: 3.1229



B0L102S, U20-ch126, adc1

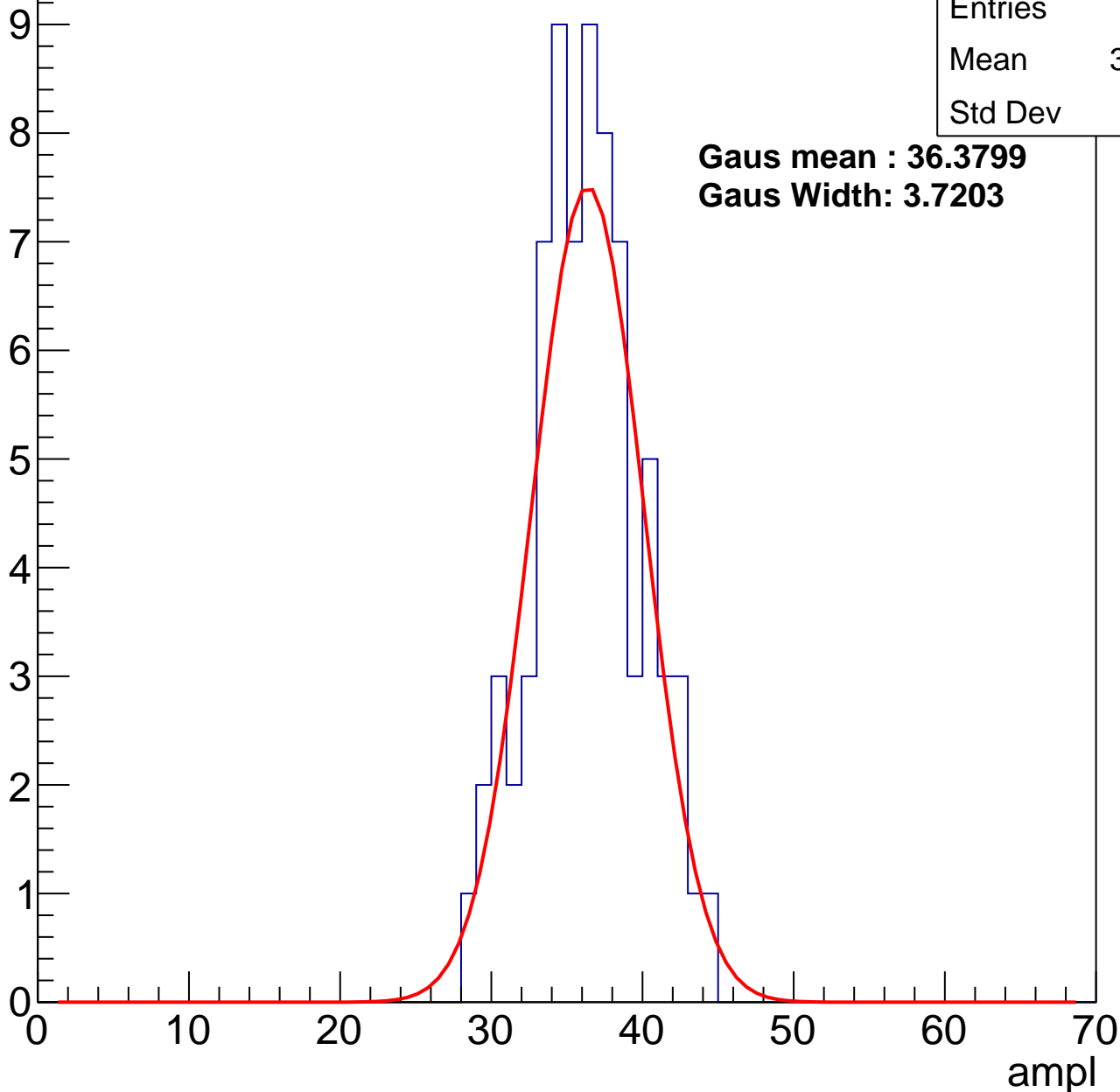
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.88 |
| Std Dev | 3.51 |

Gaus mean : 36.3799

Gaus Width: 3.7203

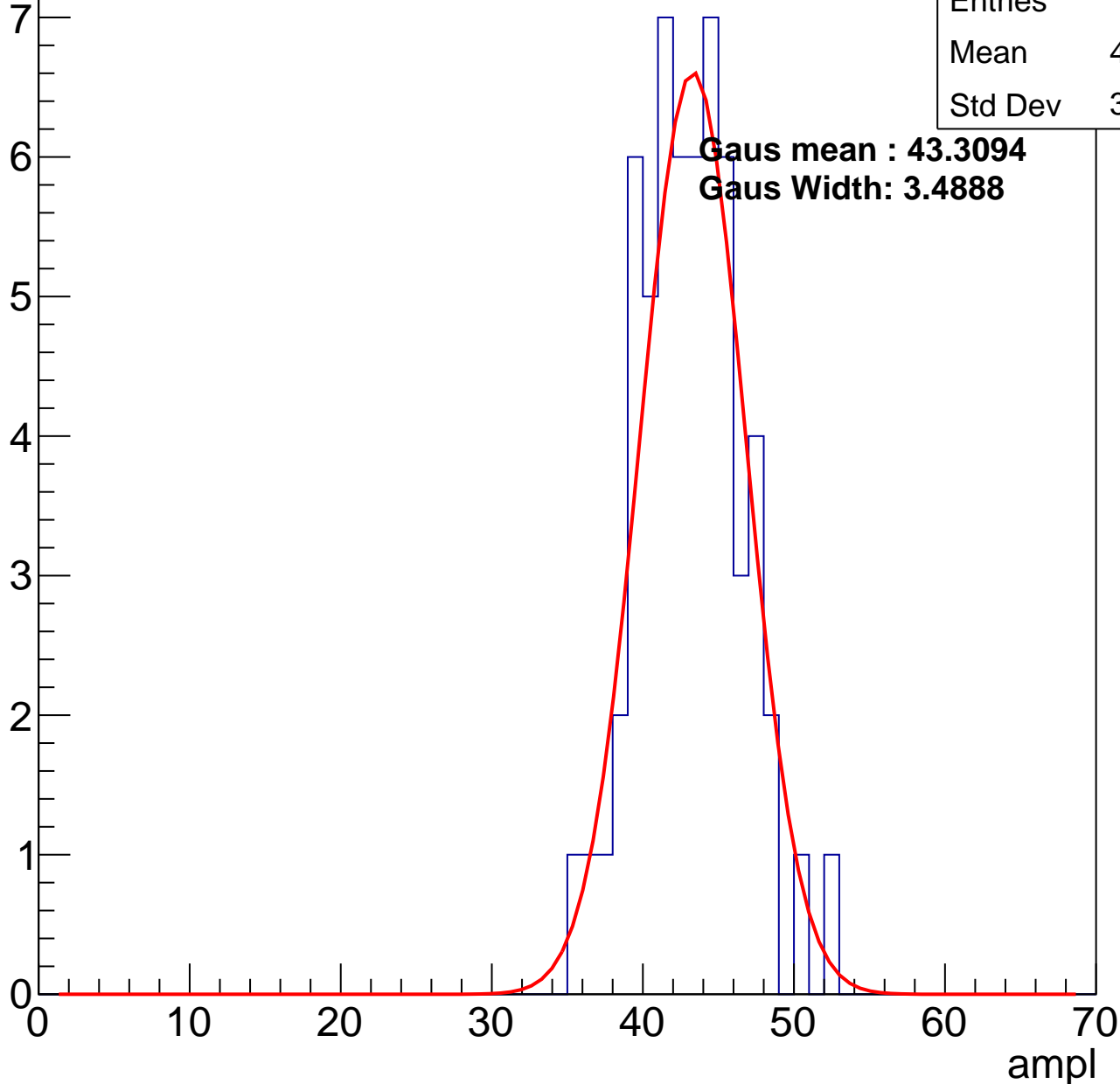


B0L102S, U20-ch126, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.66 |
| Std Dev | 3.368 |

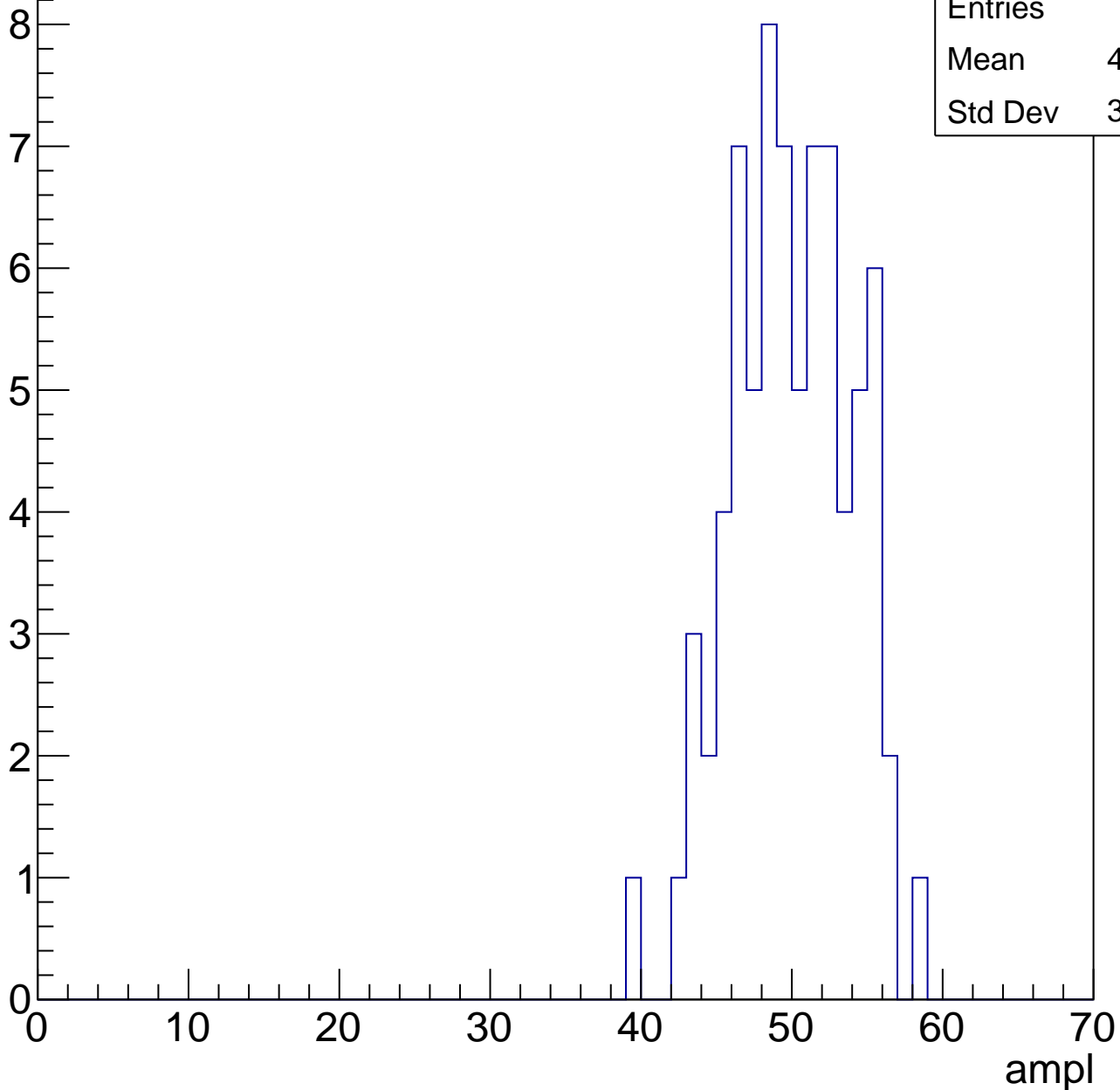


B0L102S, U20-ch126, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.53 |
| Std Dev | 3.866 |

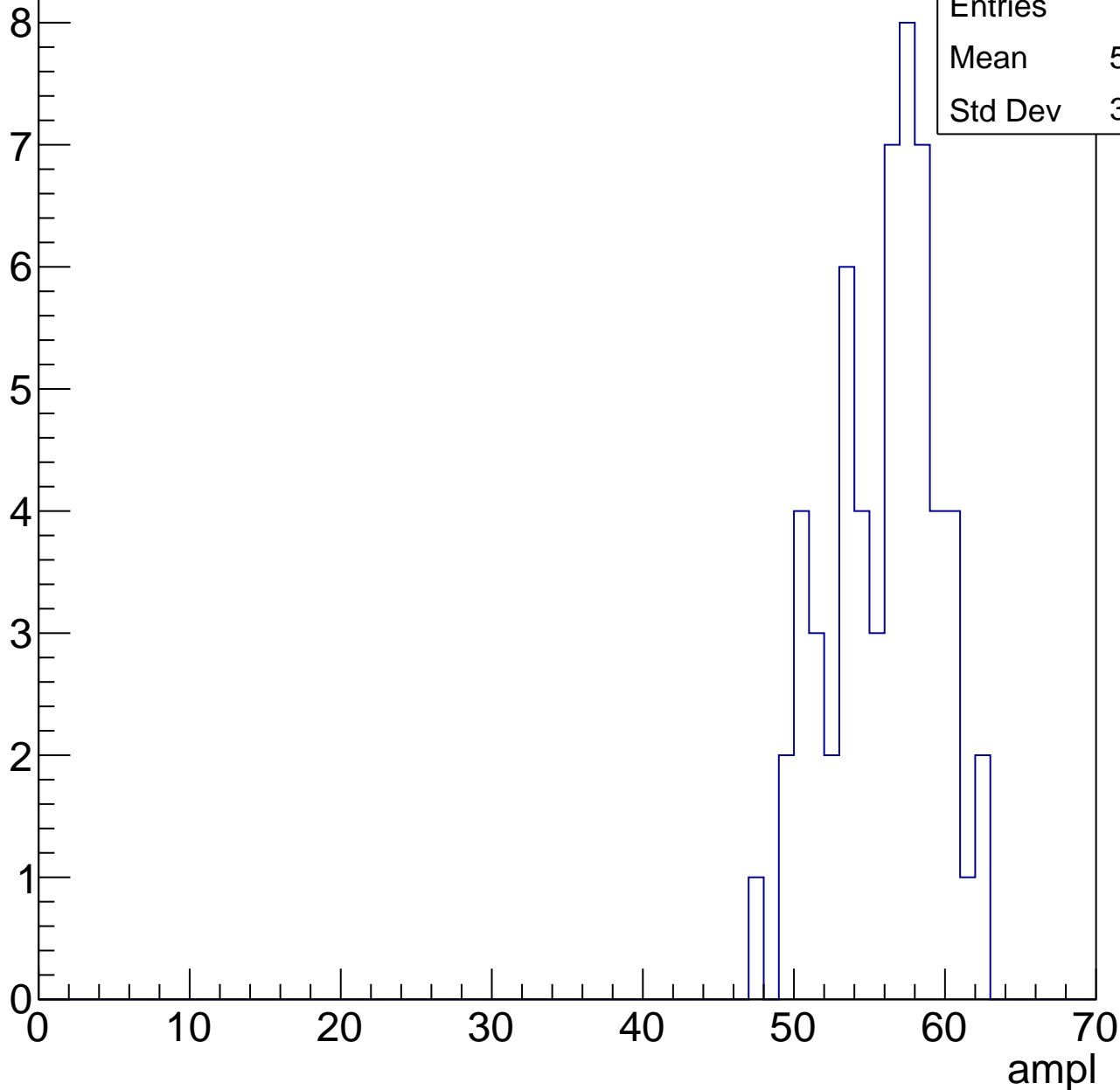


B0L102S, U20-ch126, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.45 |
| Std Dev | 3.519 |

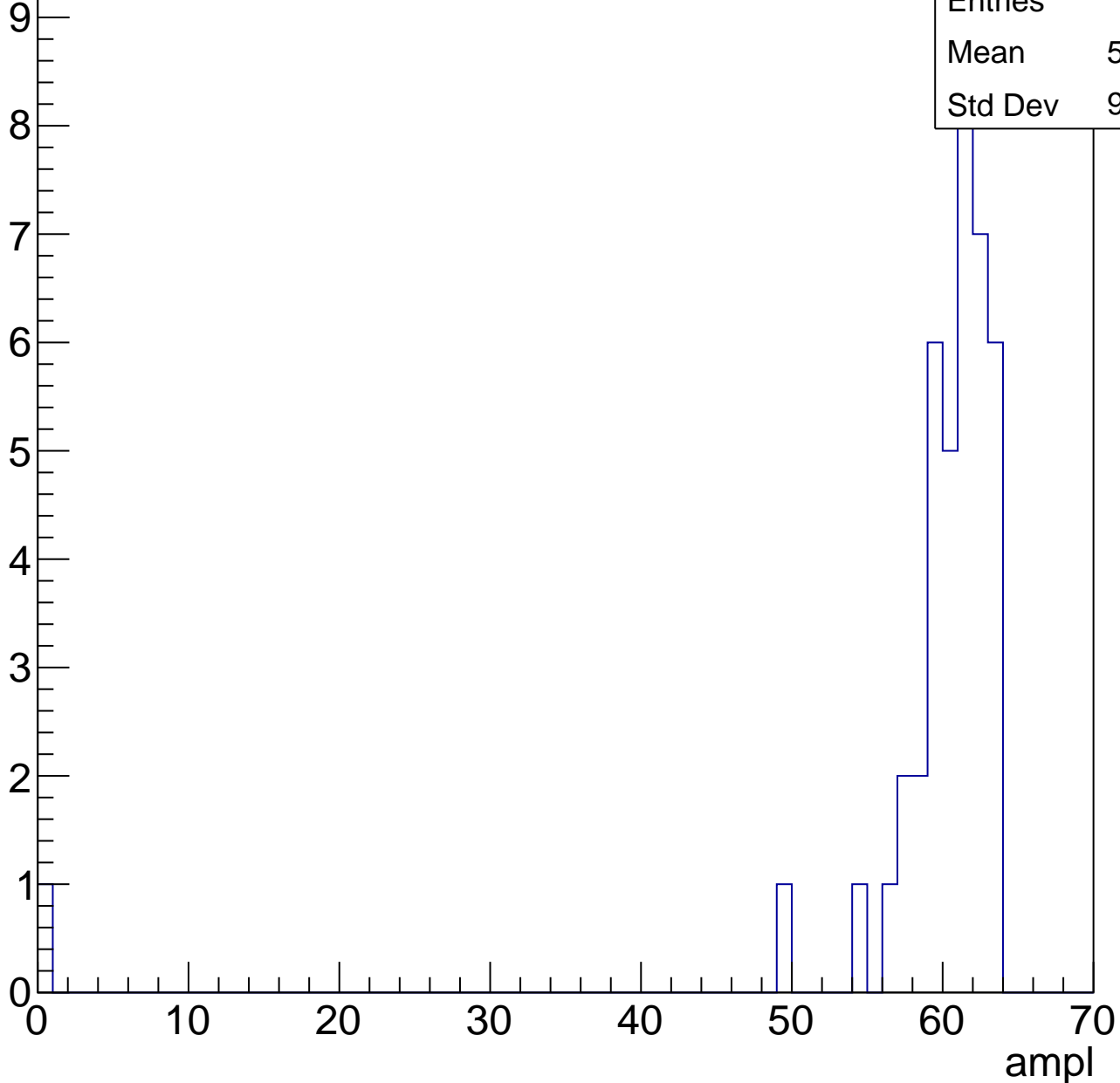


B0L102S, U20-ch126, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

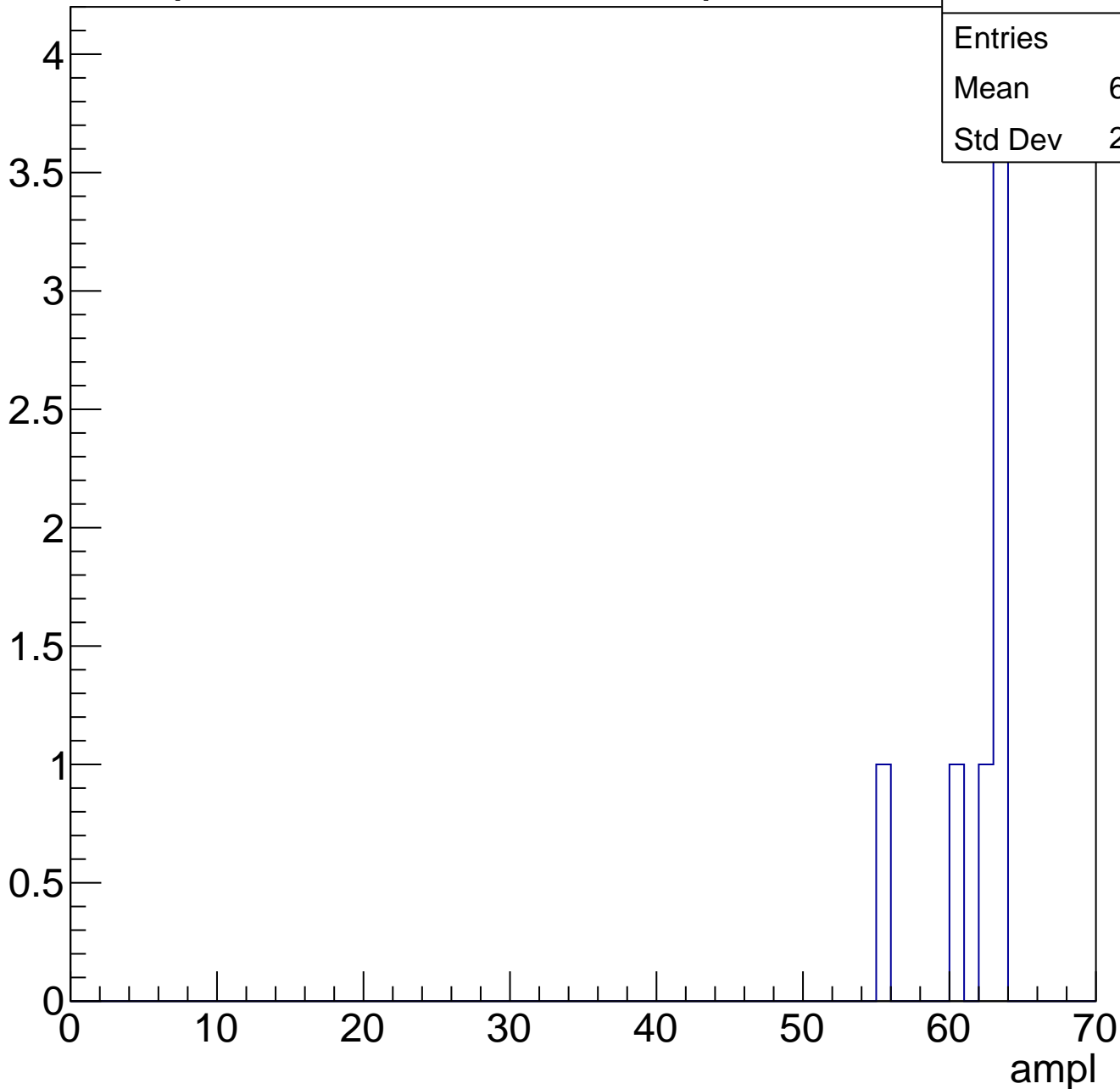
| | |
|---------|-------|
| Entries | 41 |
| Mean | 58.63 |
| Std Dev | 9.652 |



B0L102S, U20-ch126, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U20-ch126, adc7

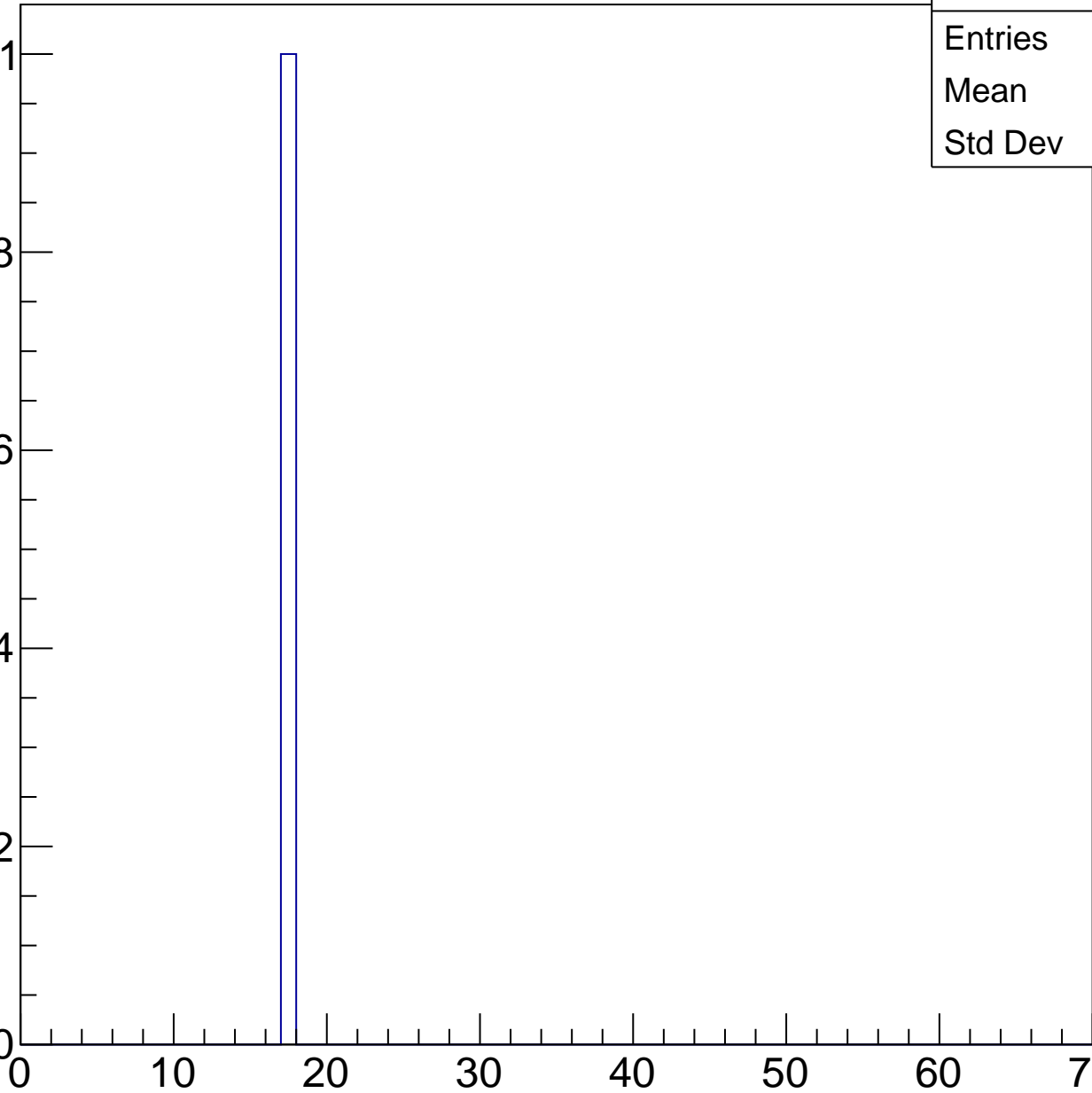
calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 17 |
| Std Dev | 0 |

ampl



B0L102S, U20-ch127, adc0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 85 |
| Mean | 26.84 |
| Std Dev | 3.678 |

Gaus mean : 28.1787

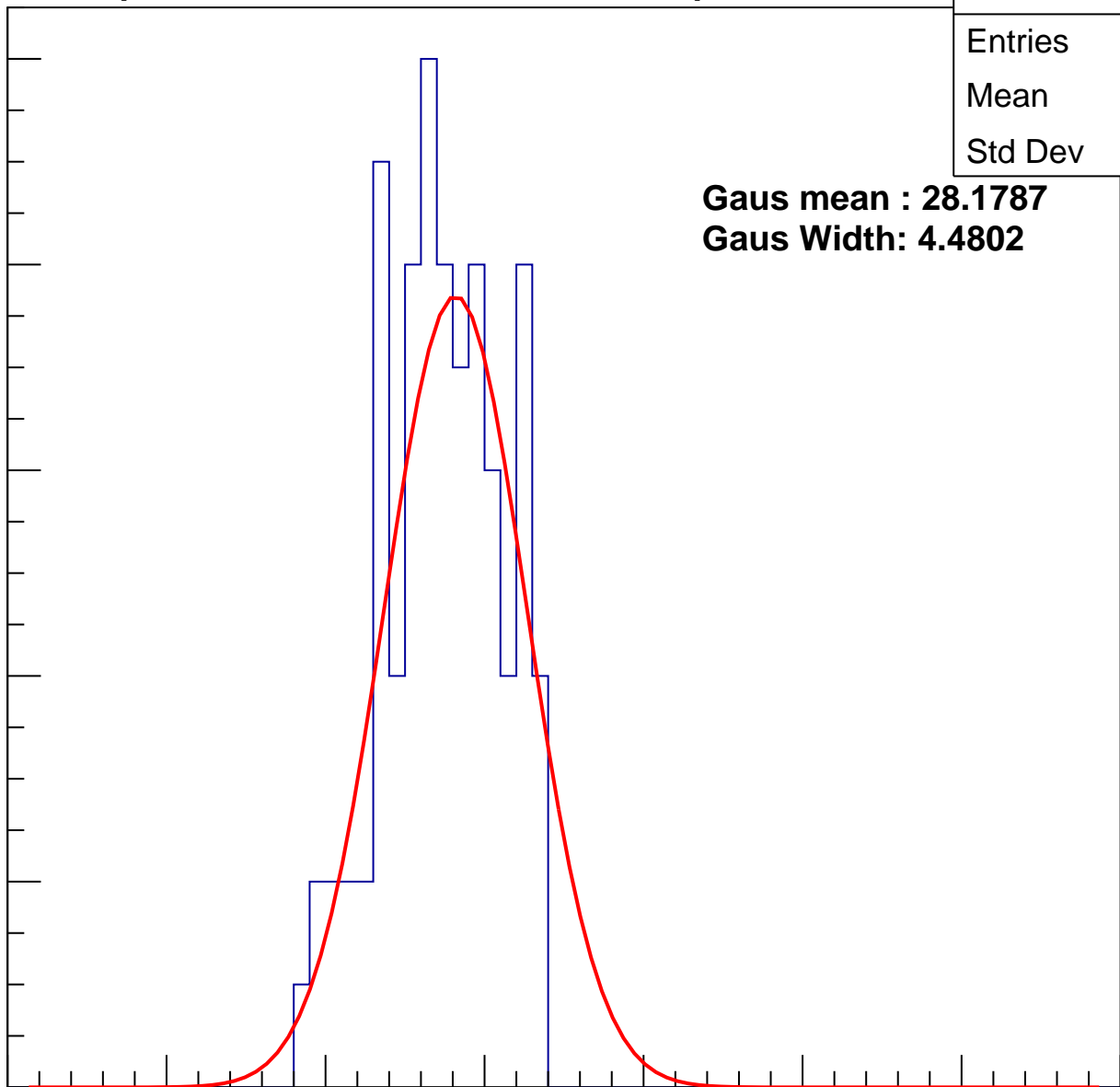
Gaus Width: 4.4802

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U20-ch127, adc1

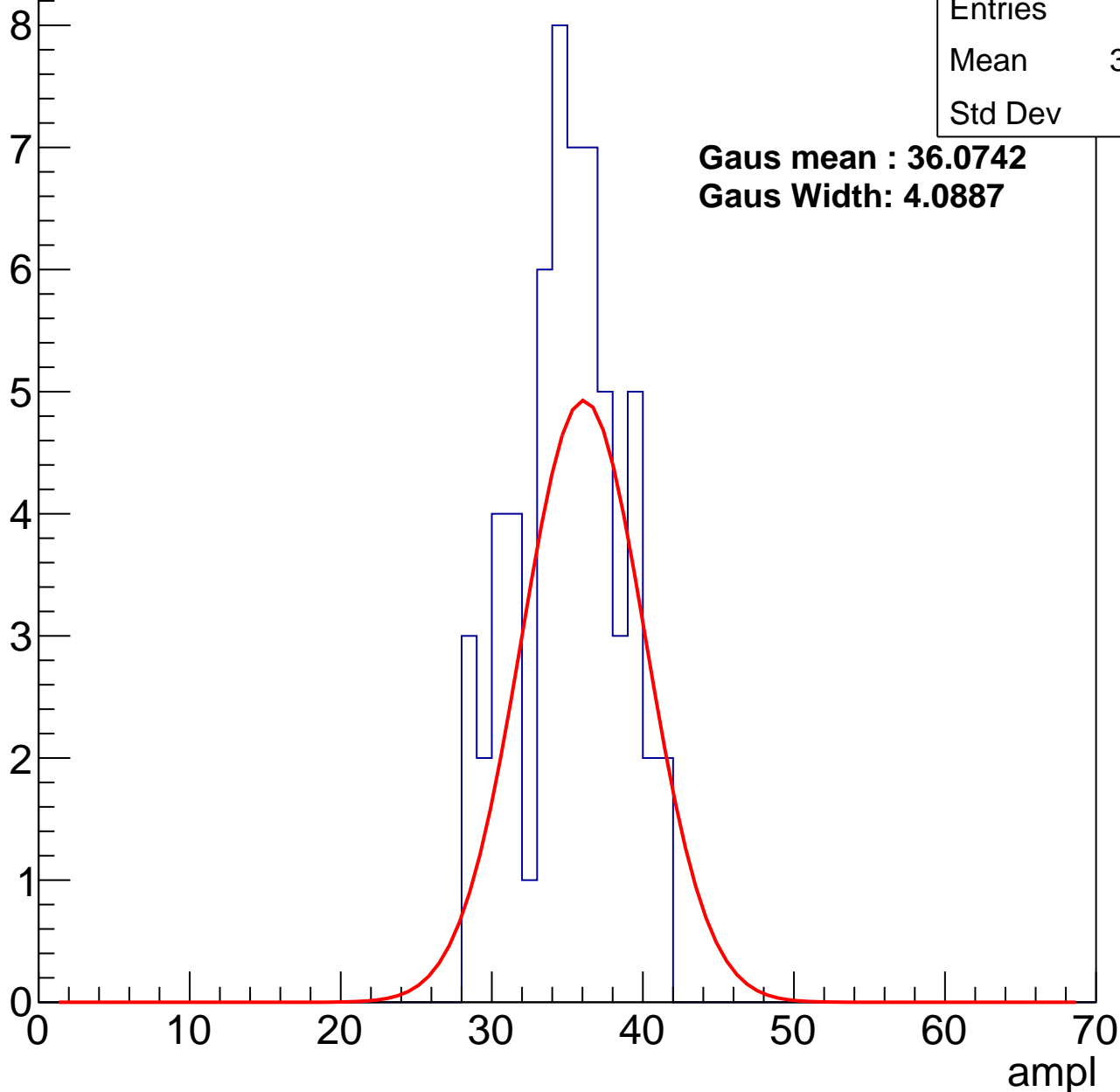
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 34.59 |
| Std Dev | 3.37 |

Gaus mean : 36.0742

Gaus Width: 4.0887



B0L102S, U20-ch127, adc2

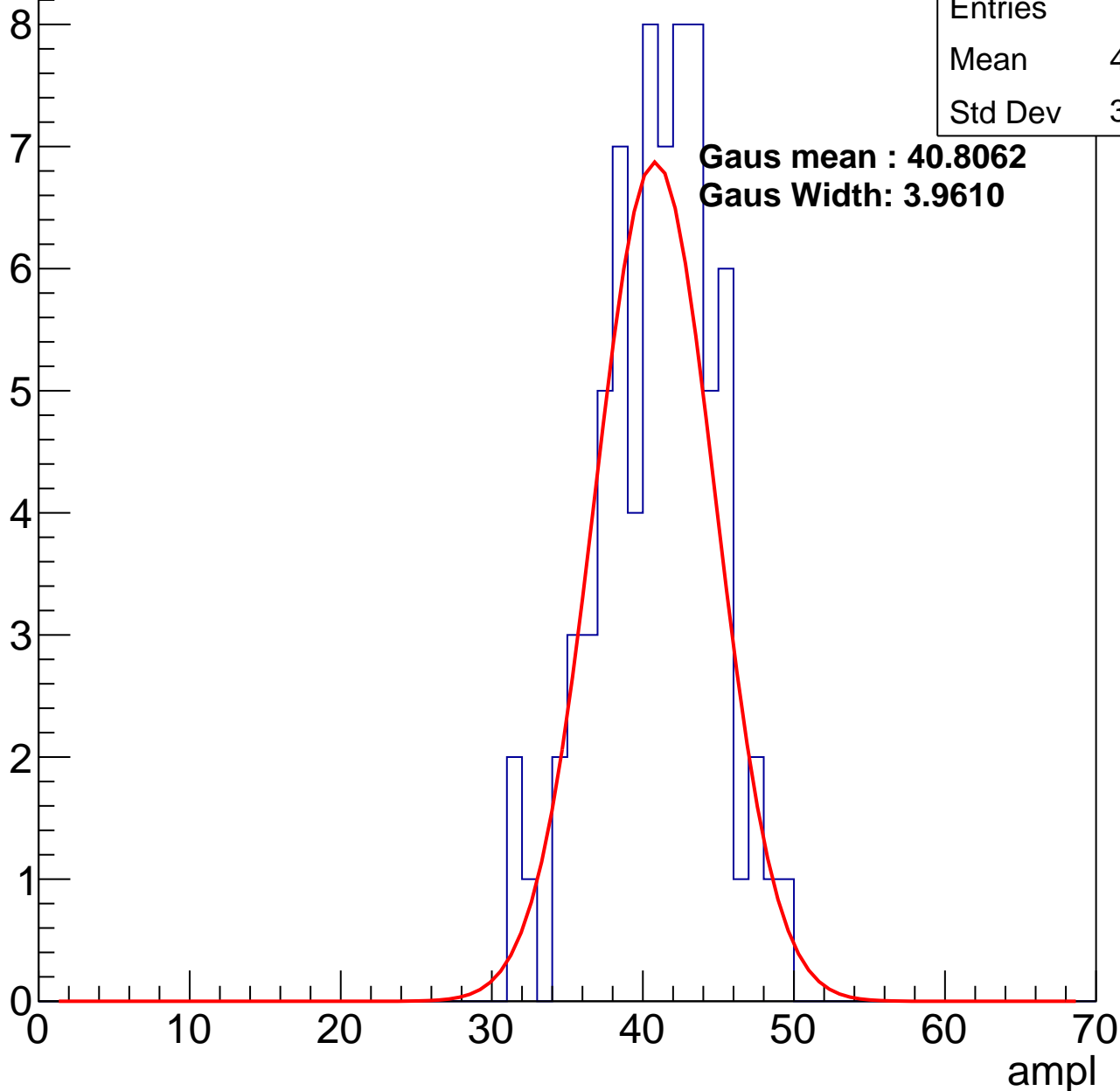
calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 40.49 |
| Std Dev | 3.856 |

Gaus mean : 40.8062

Gaus Width: 3.9610

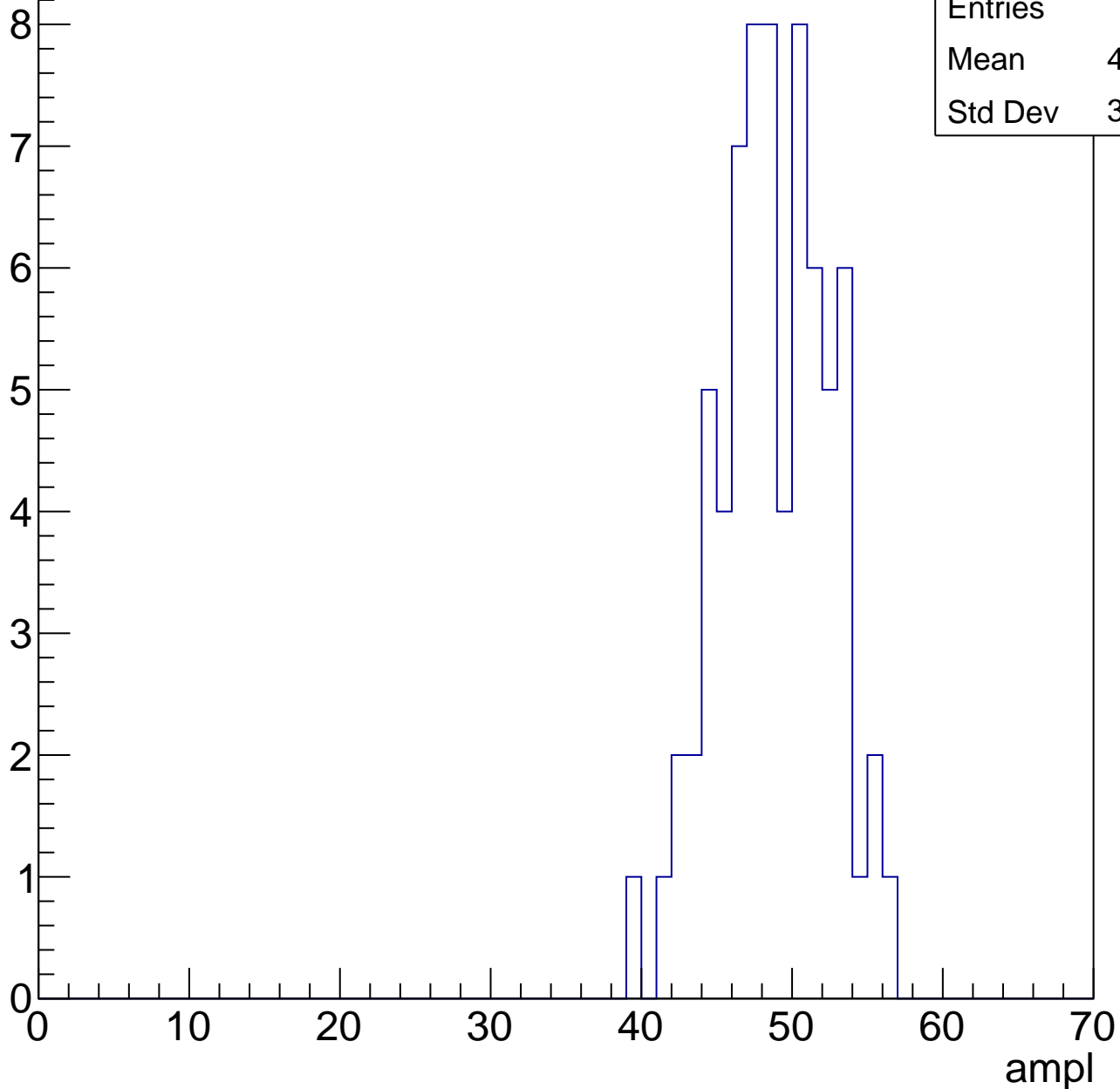


B0L102S, U20-ch127, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 48.34 |
| Std Dev | 3.588 |

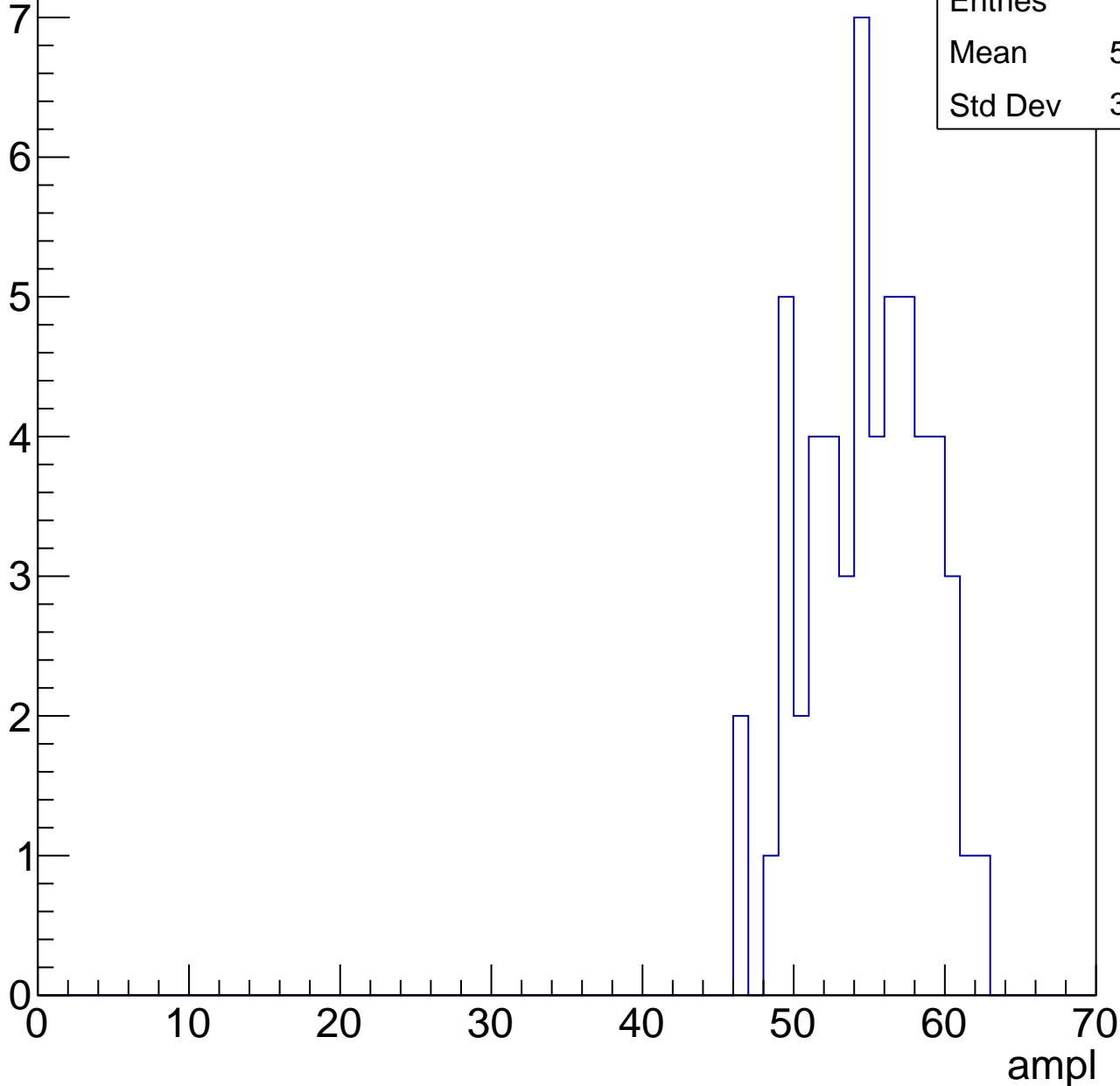


B0L102S, U20-ch127, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.36 |
| Std Dev | 3.868 |

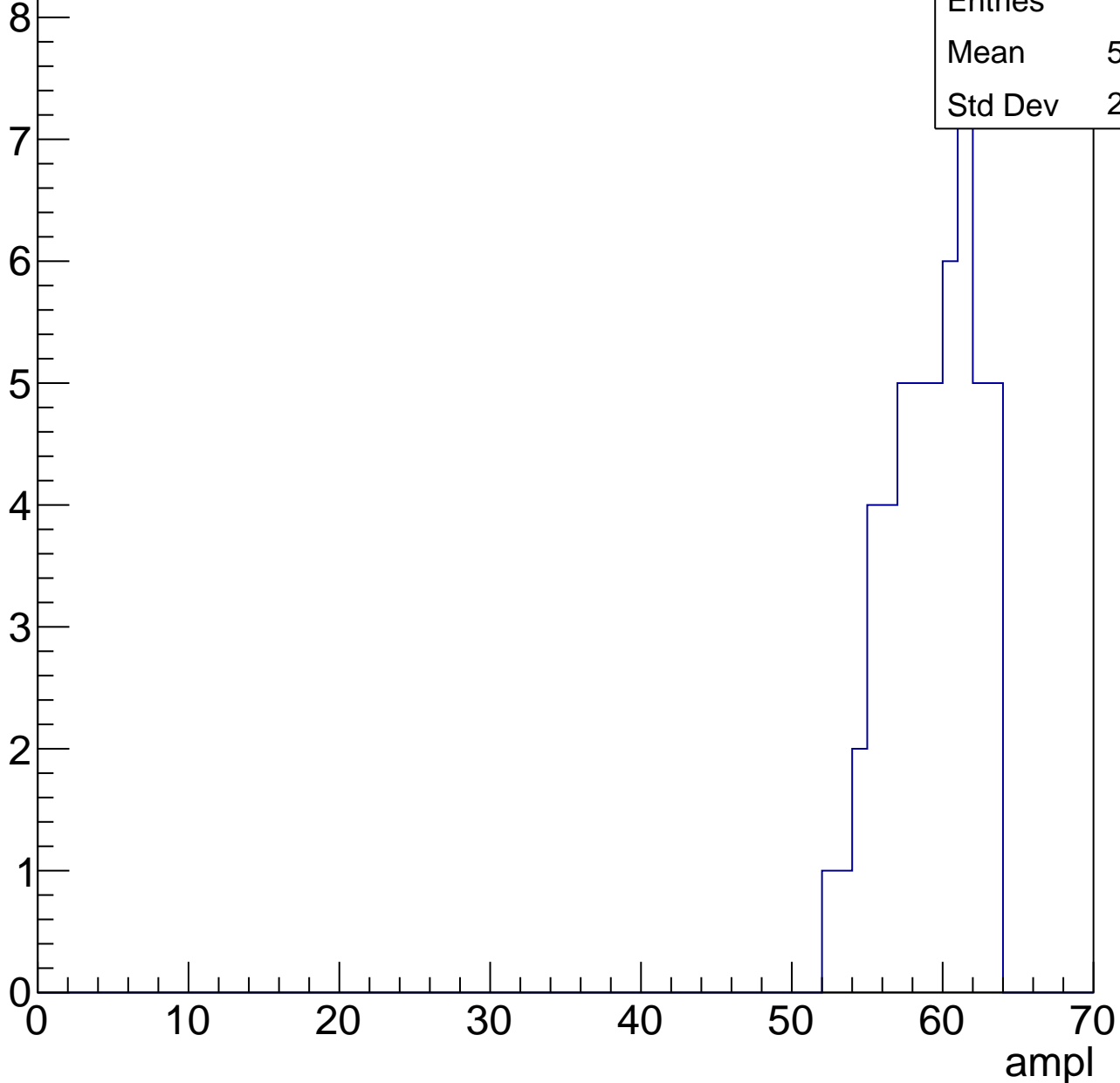


B0L102S, U20-ch127, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.82 |
| Std Dev | 2.875 |

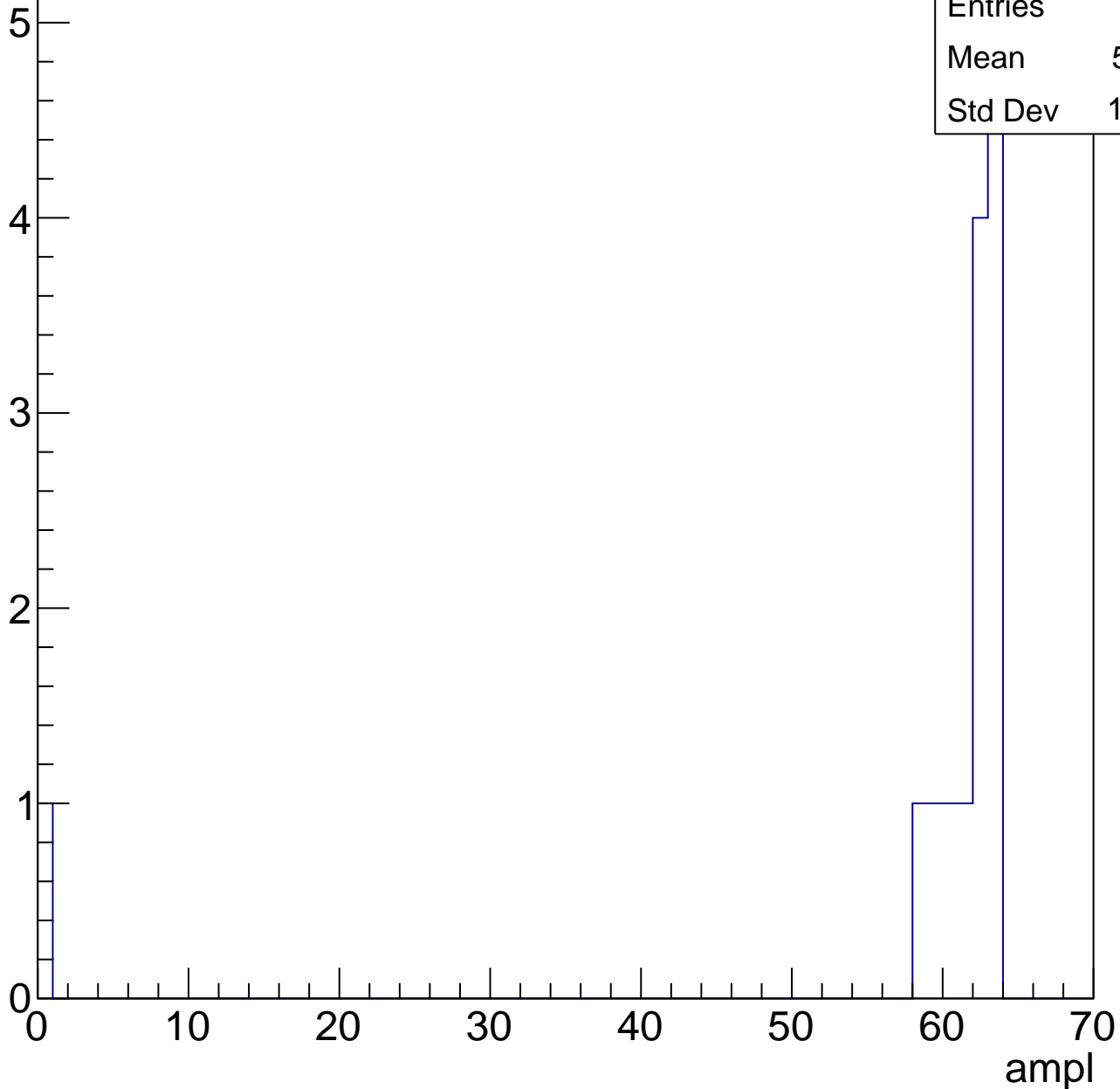


B0L102S, U20-ch127, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.21 |
| Std Dev | 15.94 |



B0L102S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

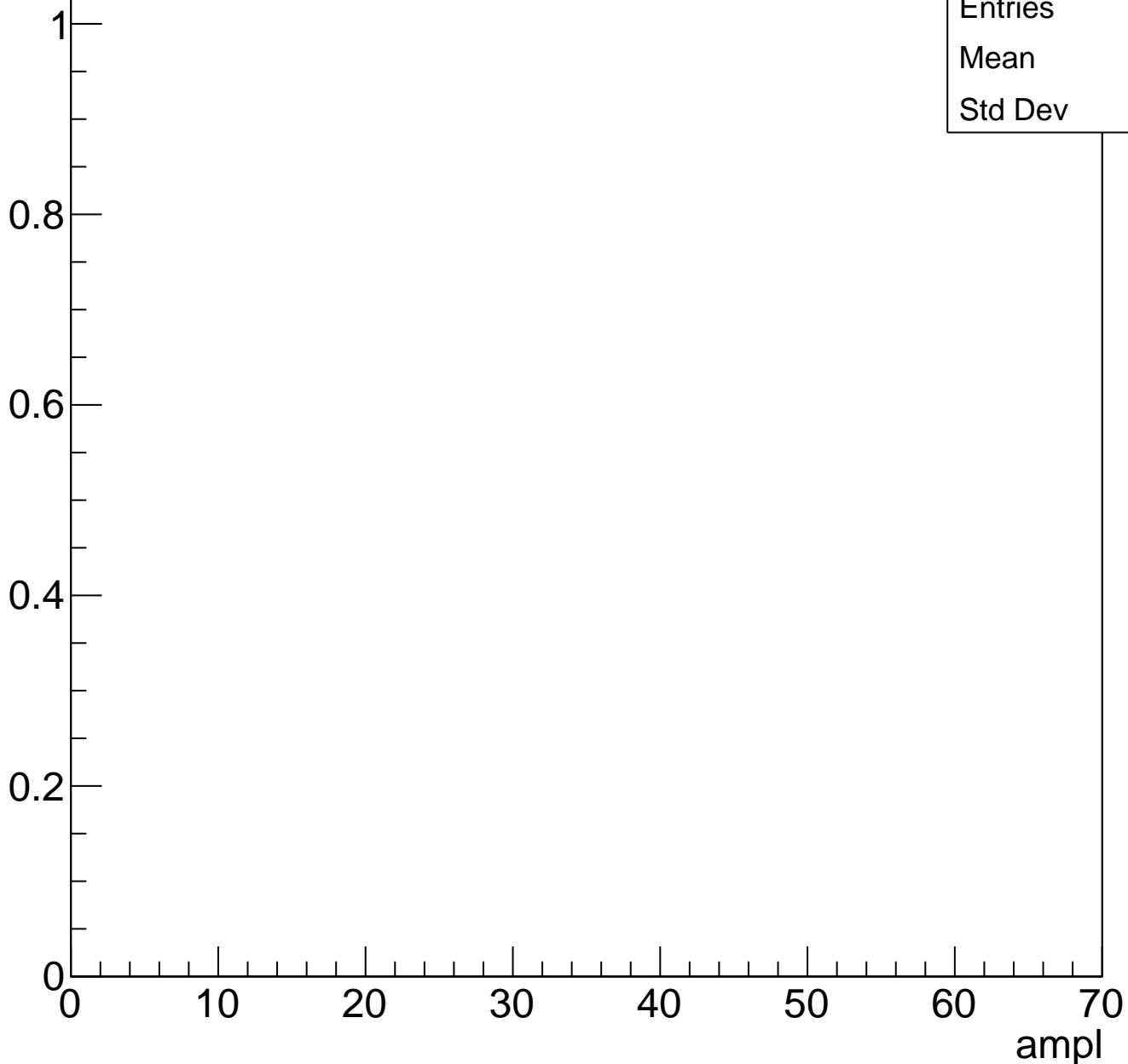


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L102S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |