

B0L102S, U14-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.7
Std Dev	12.5

Turn on : 23.6090

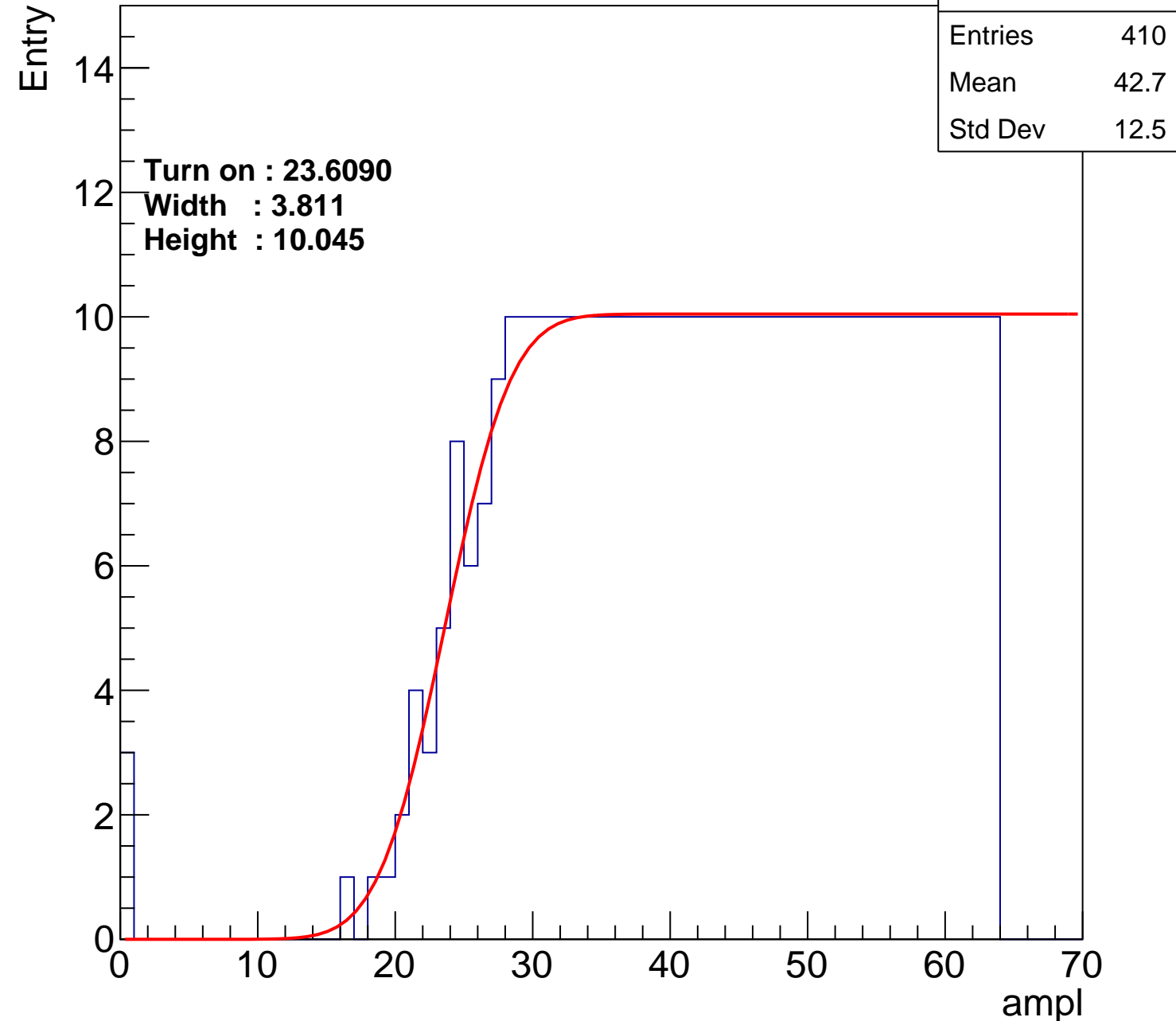
Width : 3.811

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.99
Std Dev	11.86

Turn on : 26.8427

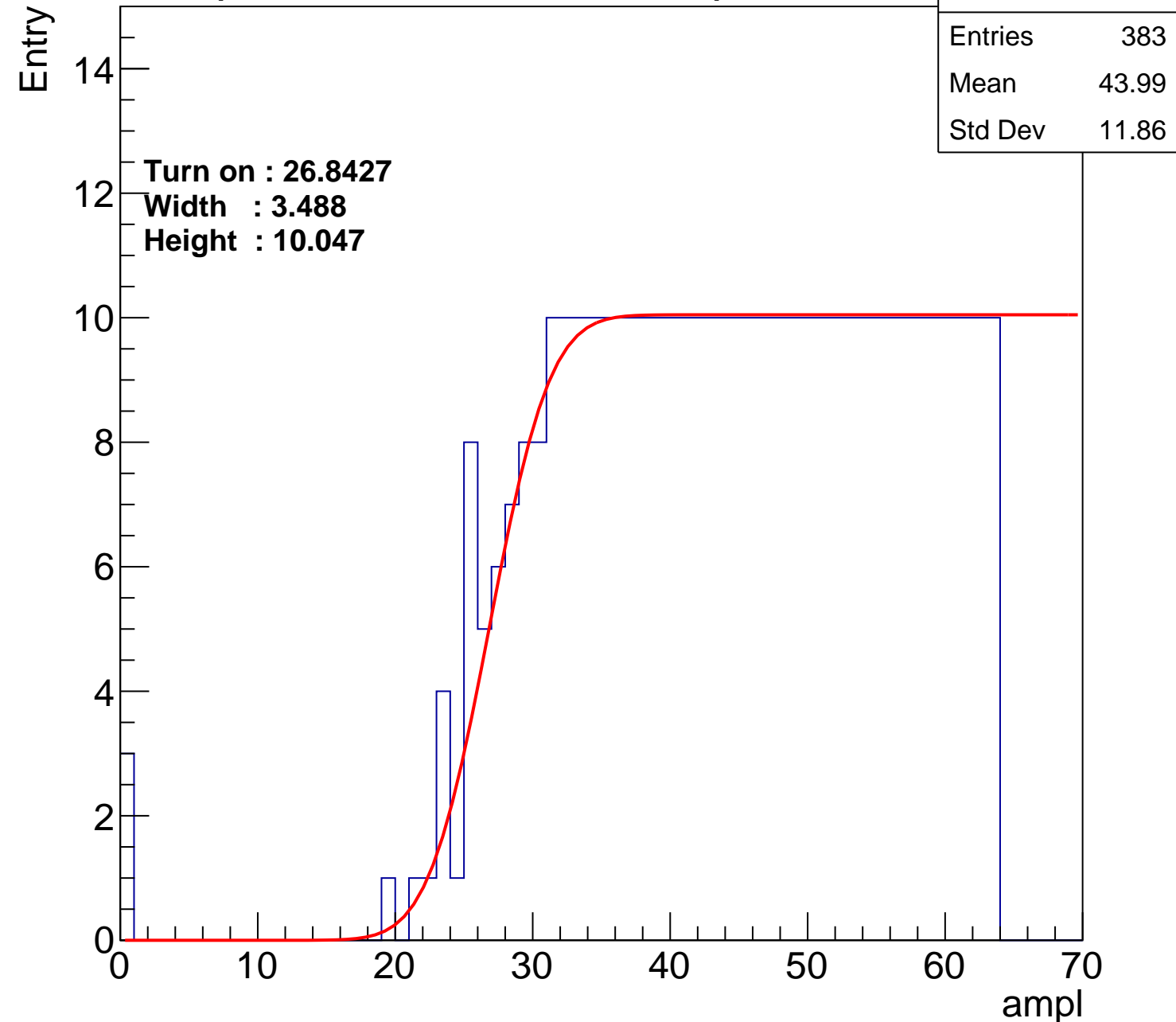
Width : 3.488

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.27
Std Dev	12.38

Turn on : 24.8347

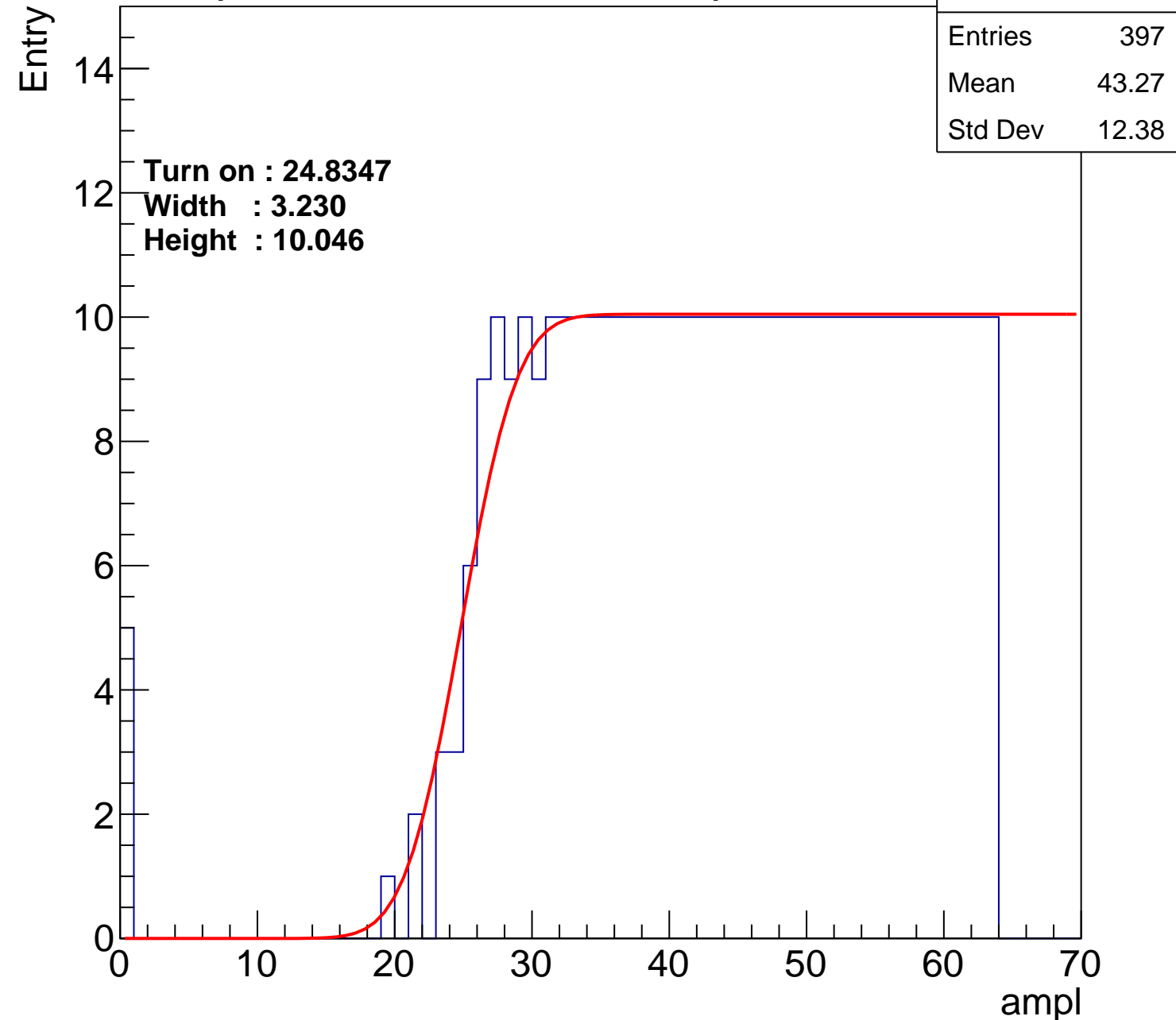
Width : 3.230

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch3

calib_packv5_042523_0143.root, FC#12, port B1

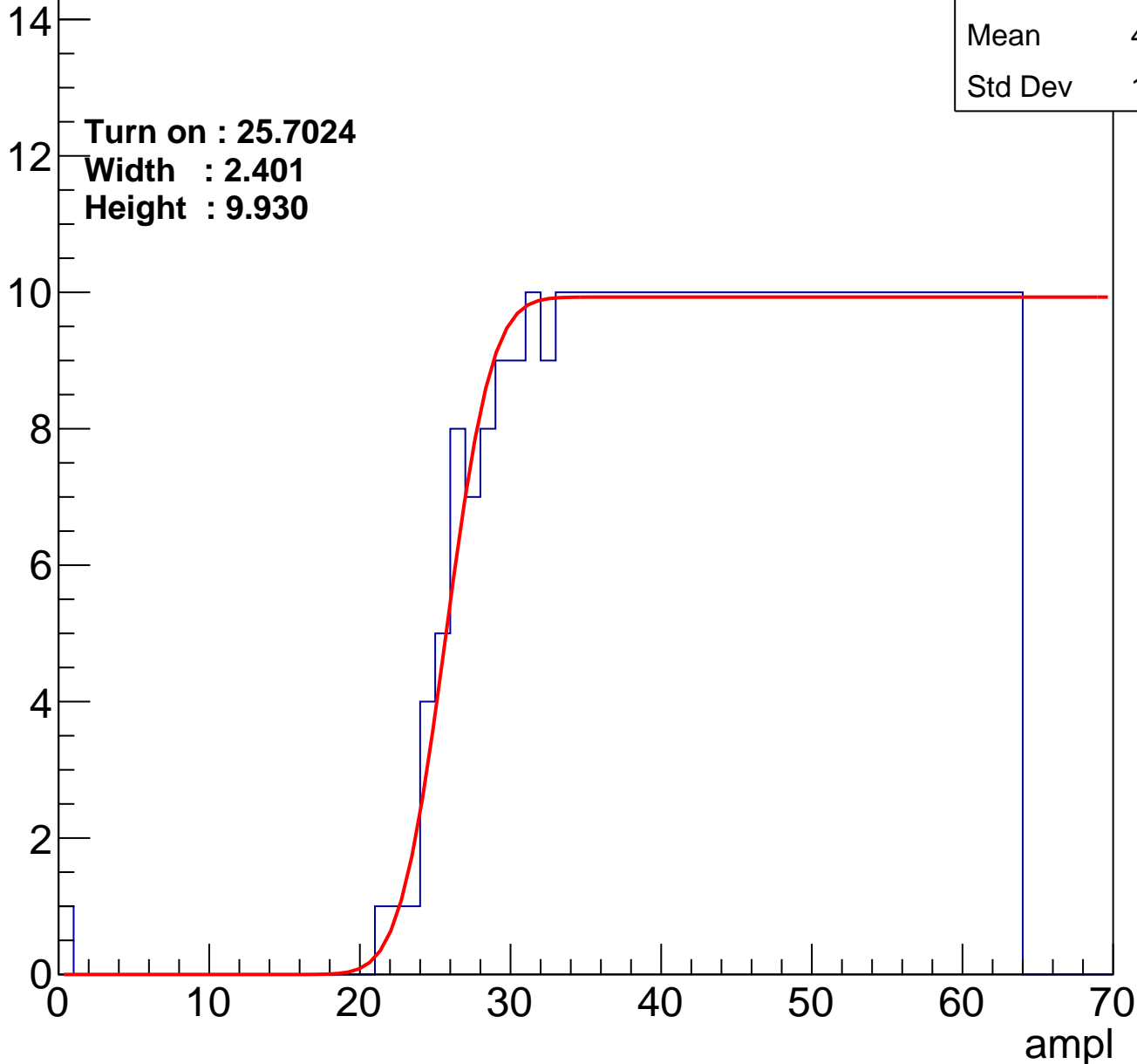
Entries	383
Mean	44.17
Std Dev	11.42

Turn on : 25.7024

Width : 2.401

Height : 9.930

Entry



B0L102S, U14-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.32
Std Dev	11.65

Turn on : 26.7272

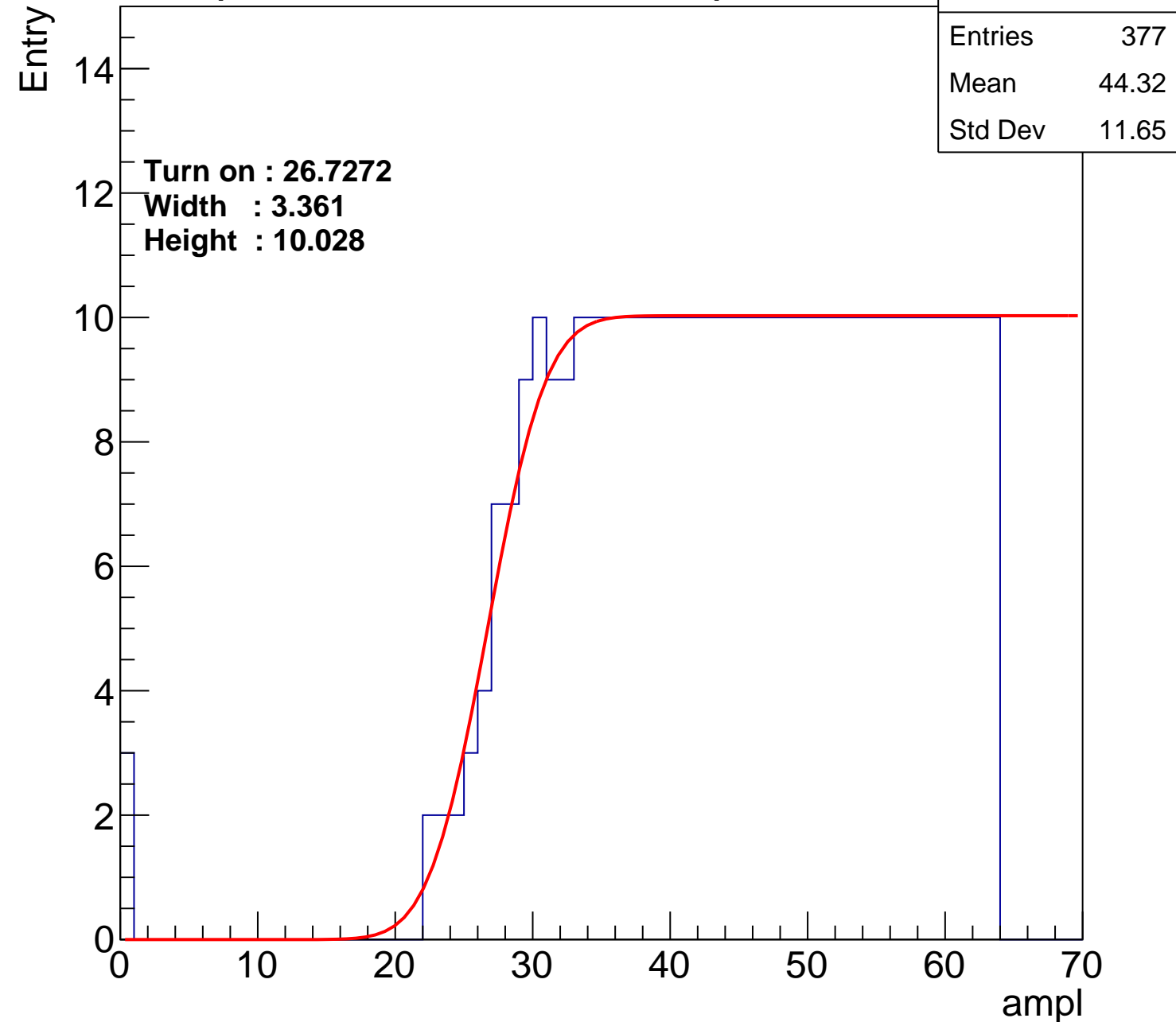
Width : 3.361

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44
Std Dev	11.97

Turn on : 26.4705

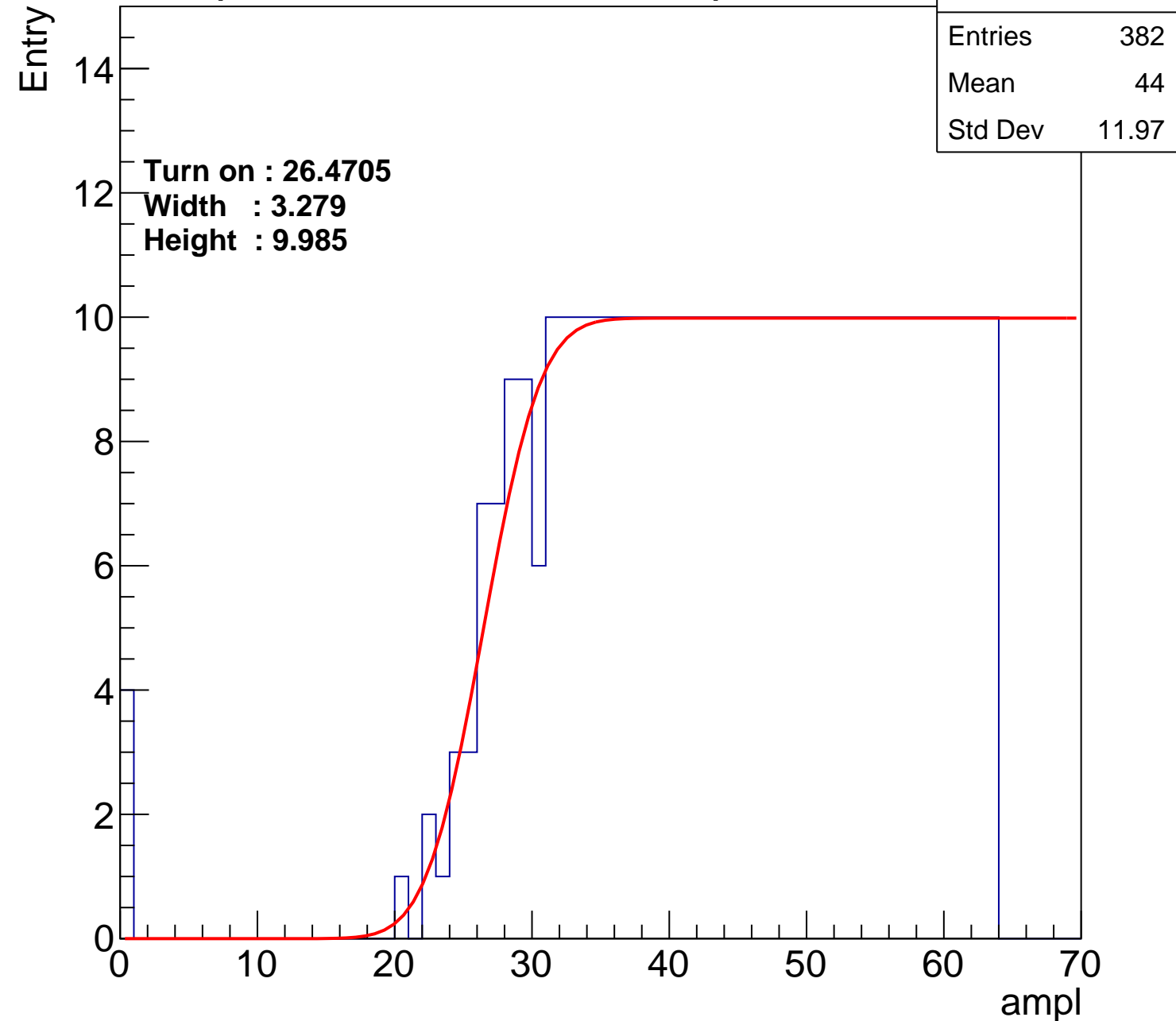
Width : 3.279

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.6
Std Dev	11.48

Turn on : 27.3570

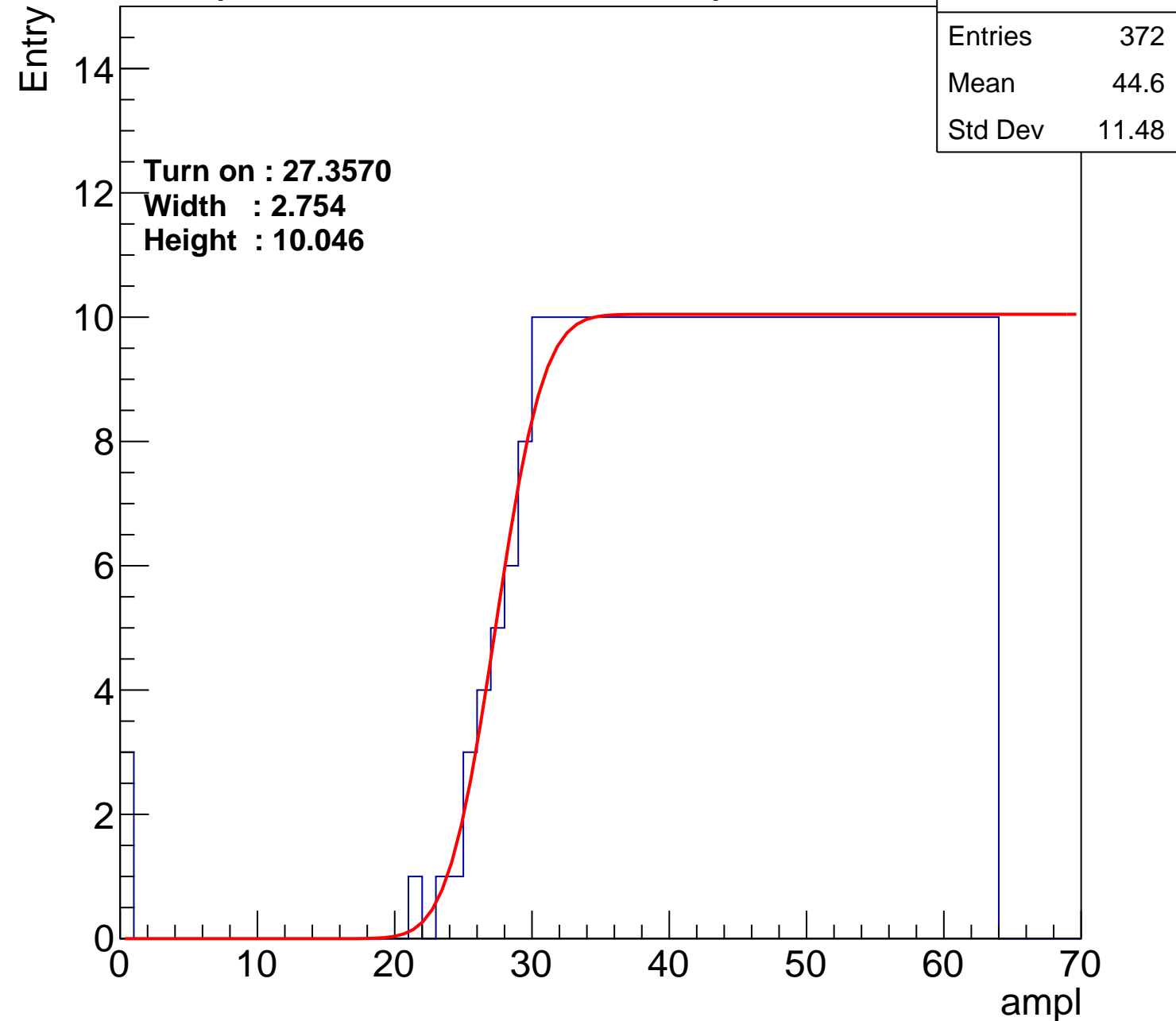
Width : 2.754

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 25.2045

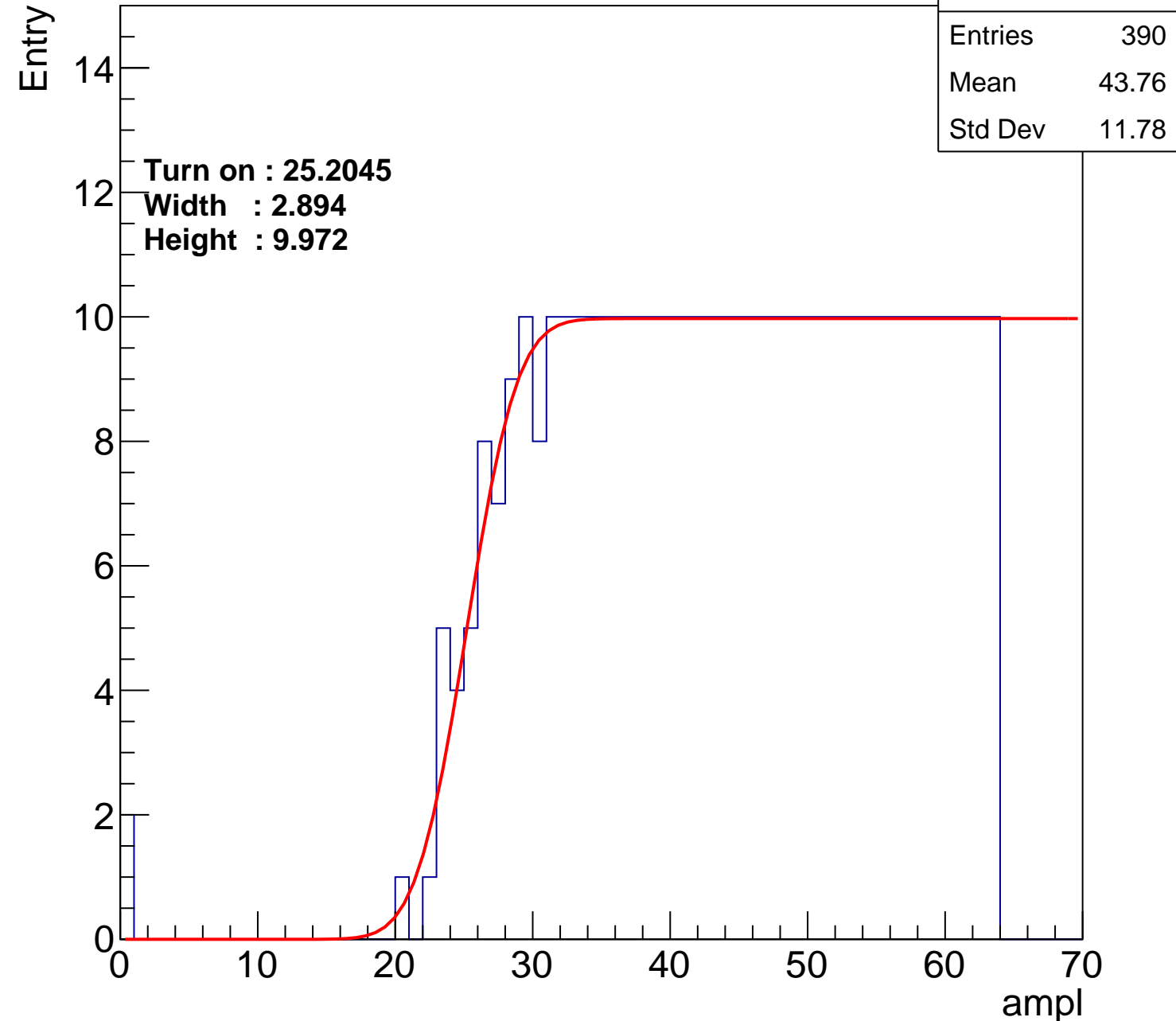
Width : 2.894

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch8

calib_packv5_042523_0143.root, FC#12, port B1

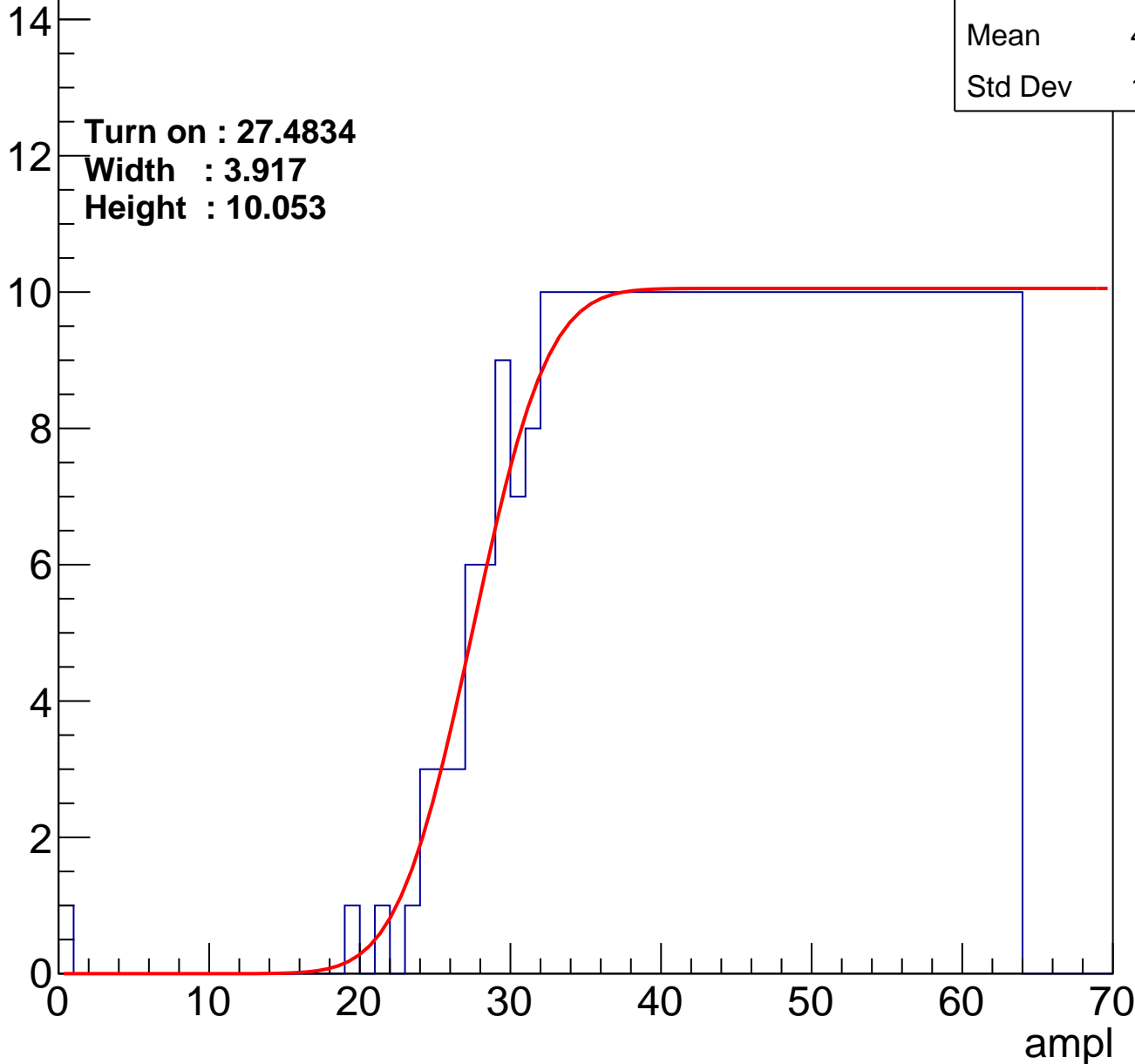
Entries	369
Mean	44.82
Std Dev	11.14

Turn on : 27.4834

Width : 3.917

Height : 10.053

Entry



B0L102S, U14-ch9

calib_packv5_042523_0143.root, FC#12, port B1

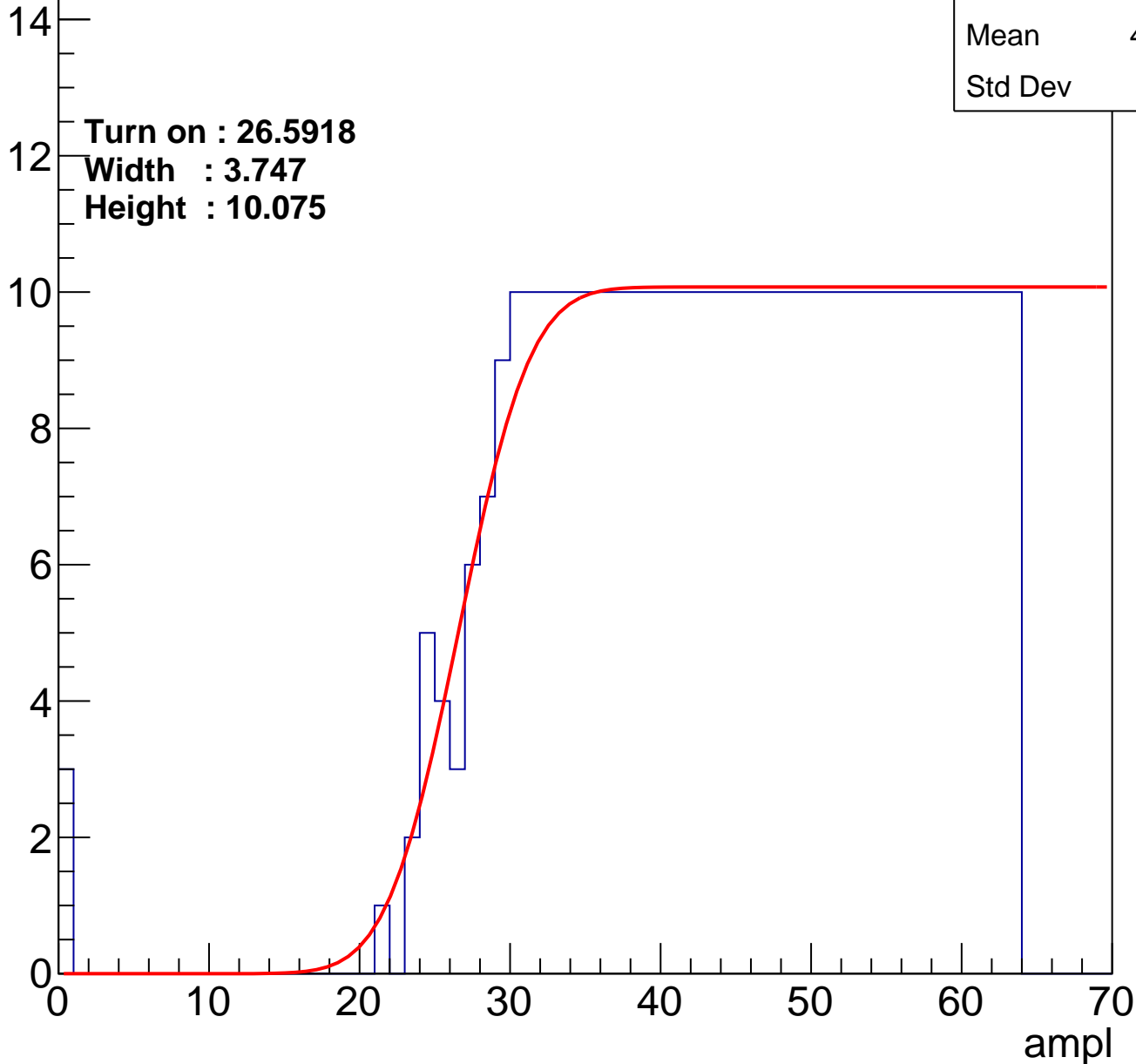
Entry

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.5918

Width : 3.747

Height : 10.075



B0L102S, U14-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.89
Std Dev	11.78

Turn on : 25.6581

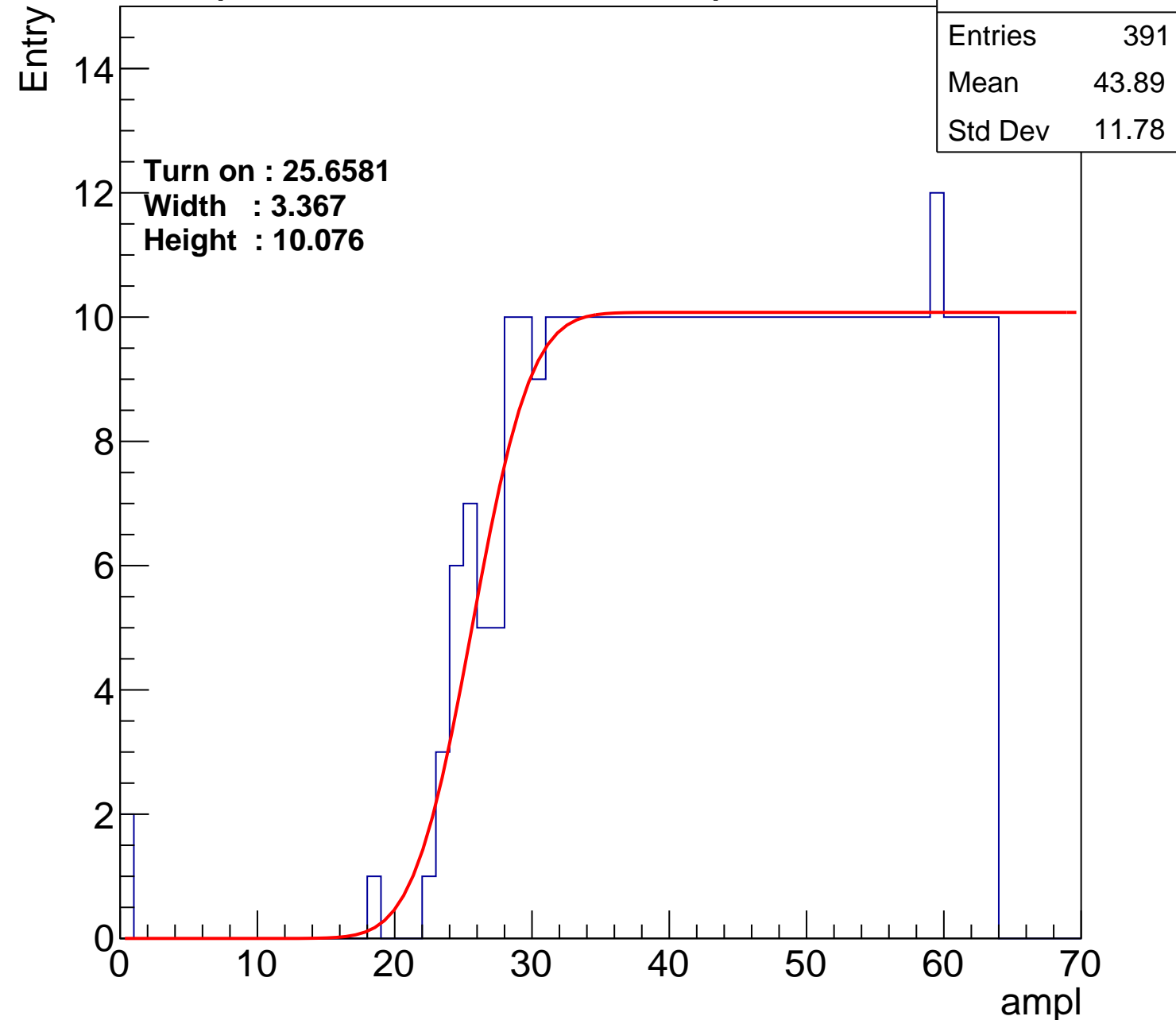
Width : 3.367

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entry

14

12

10

8

6

4

2

0

Turn on : 26.5446

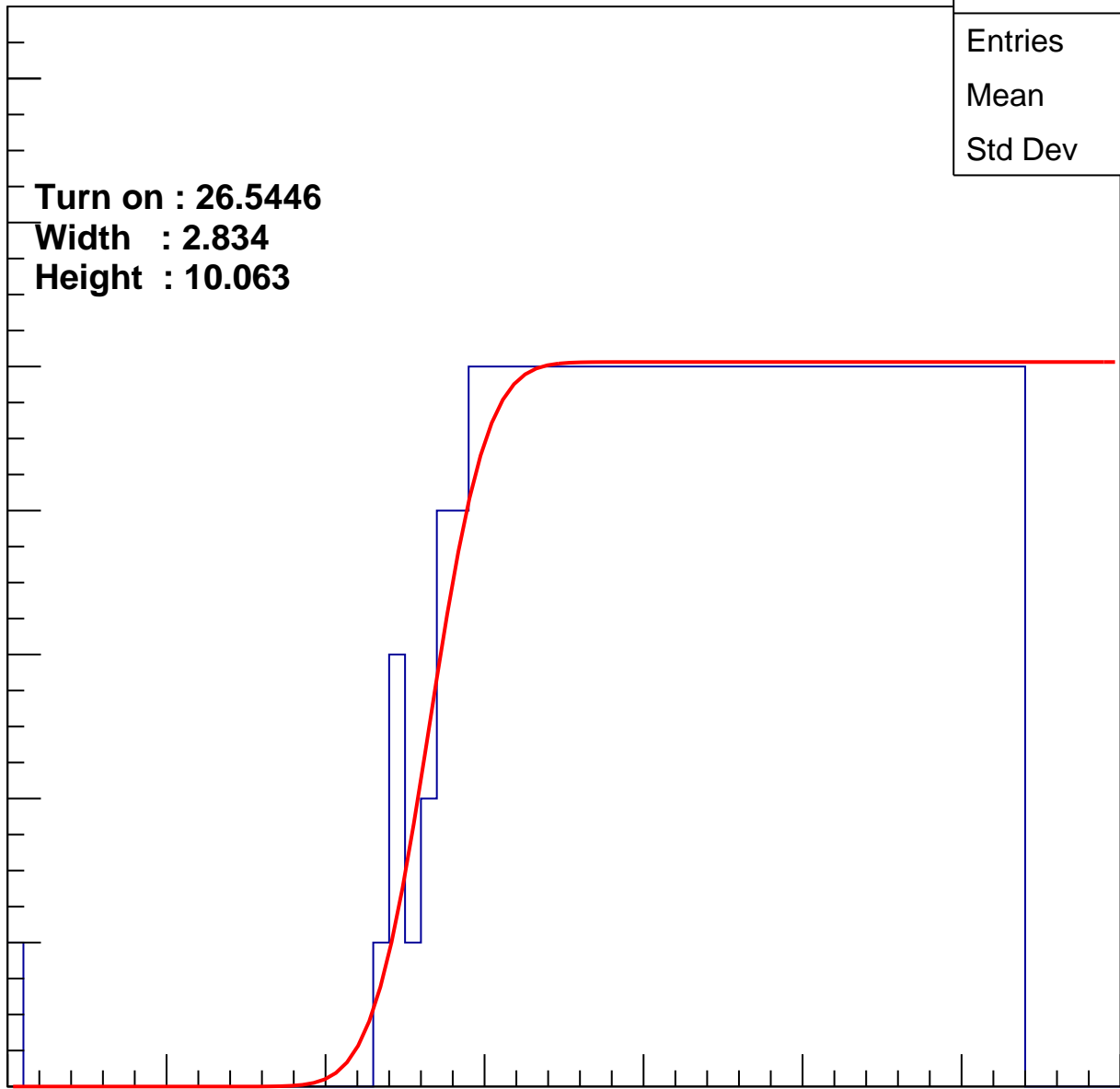
Width : 2.834

Height : 10.063

Entries	382
Mean	44.2
Std Dev	11.51

ampl

0 10 20 30 40 50 60 70



B0L102S, U14-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.9120

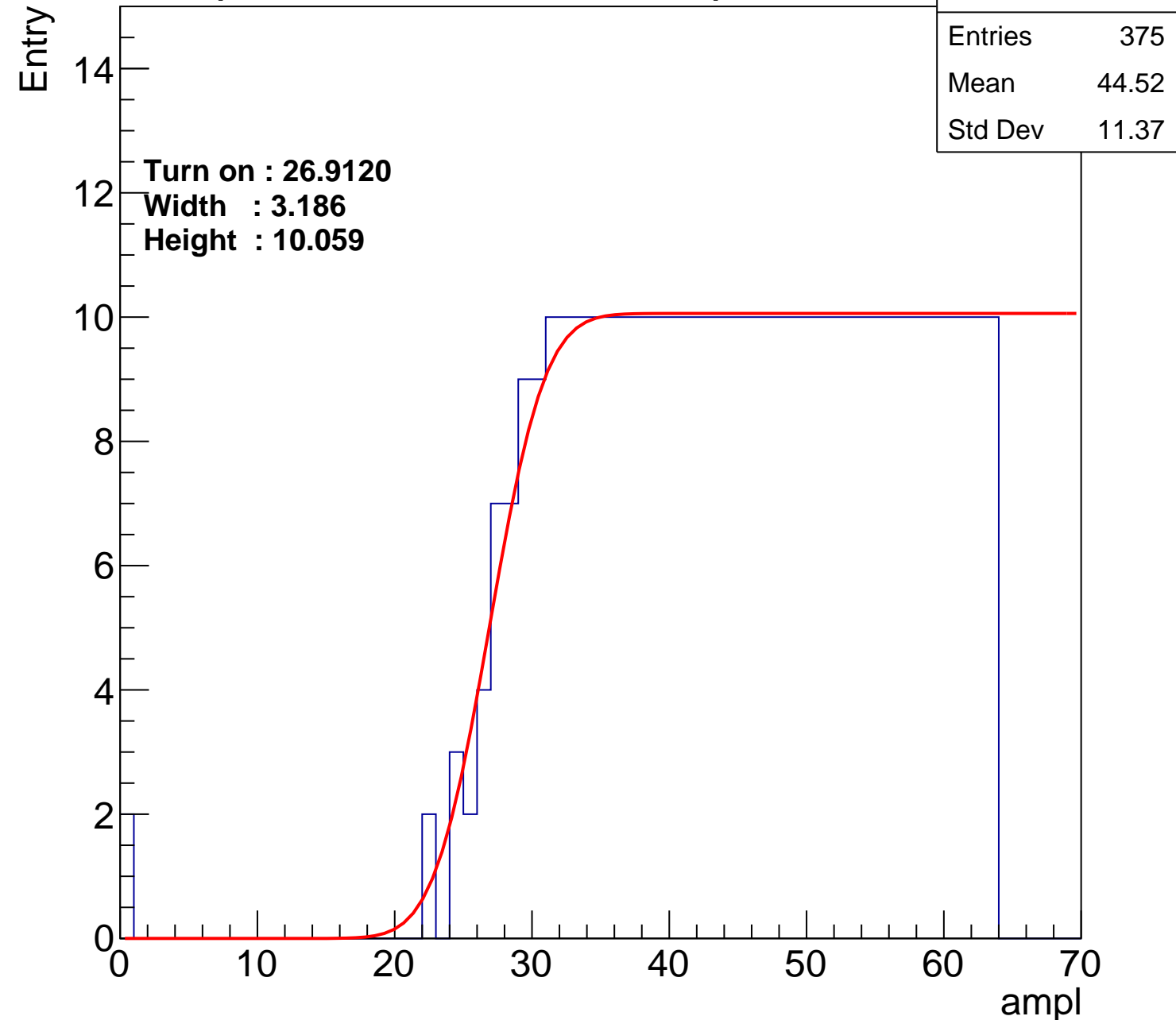
Width : 3.186

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 26.0790

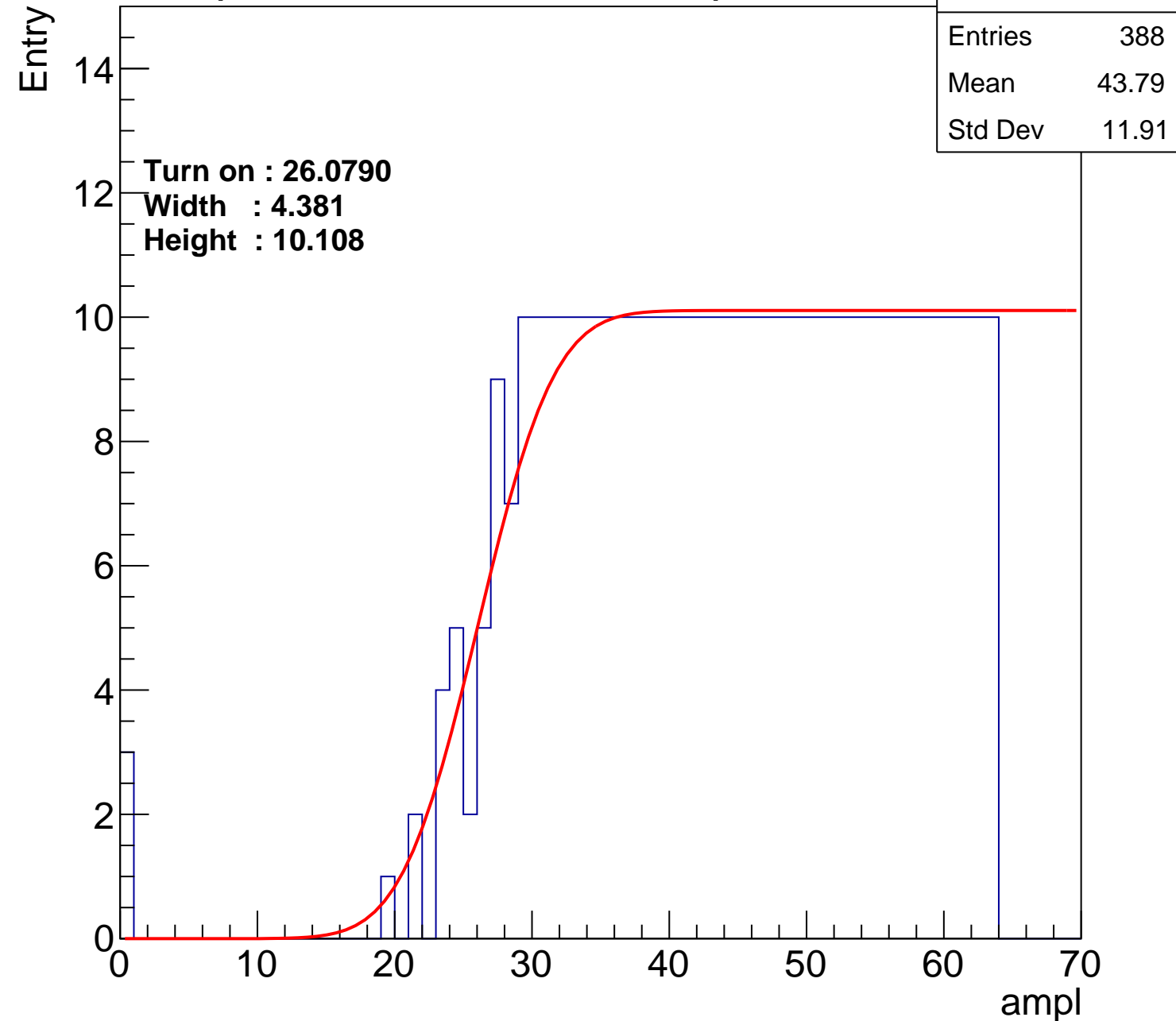
Width : 4.381

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch14

calib_packv5_042523_0143.root, FC#12, port B1

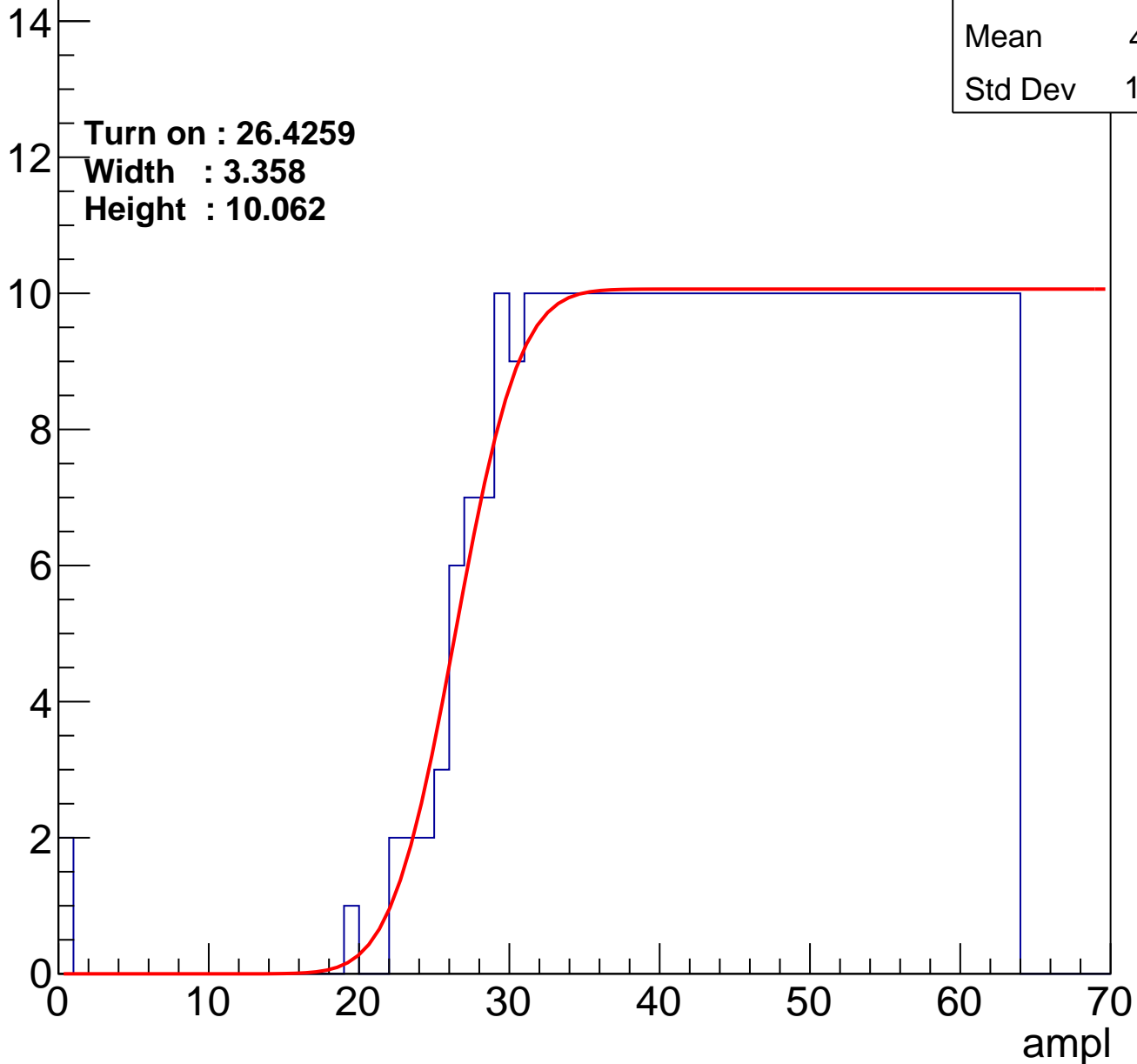
Entries	381
Mean	44.21
Std Dev	11.56

Turn on : 26.4259

Width : 3.358

Height : 10.062

Entry



B0L102S, U14-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.36
Std Dev	11.59

Turn on : 26.8069

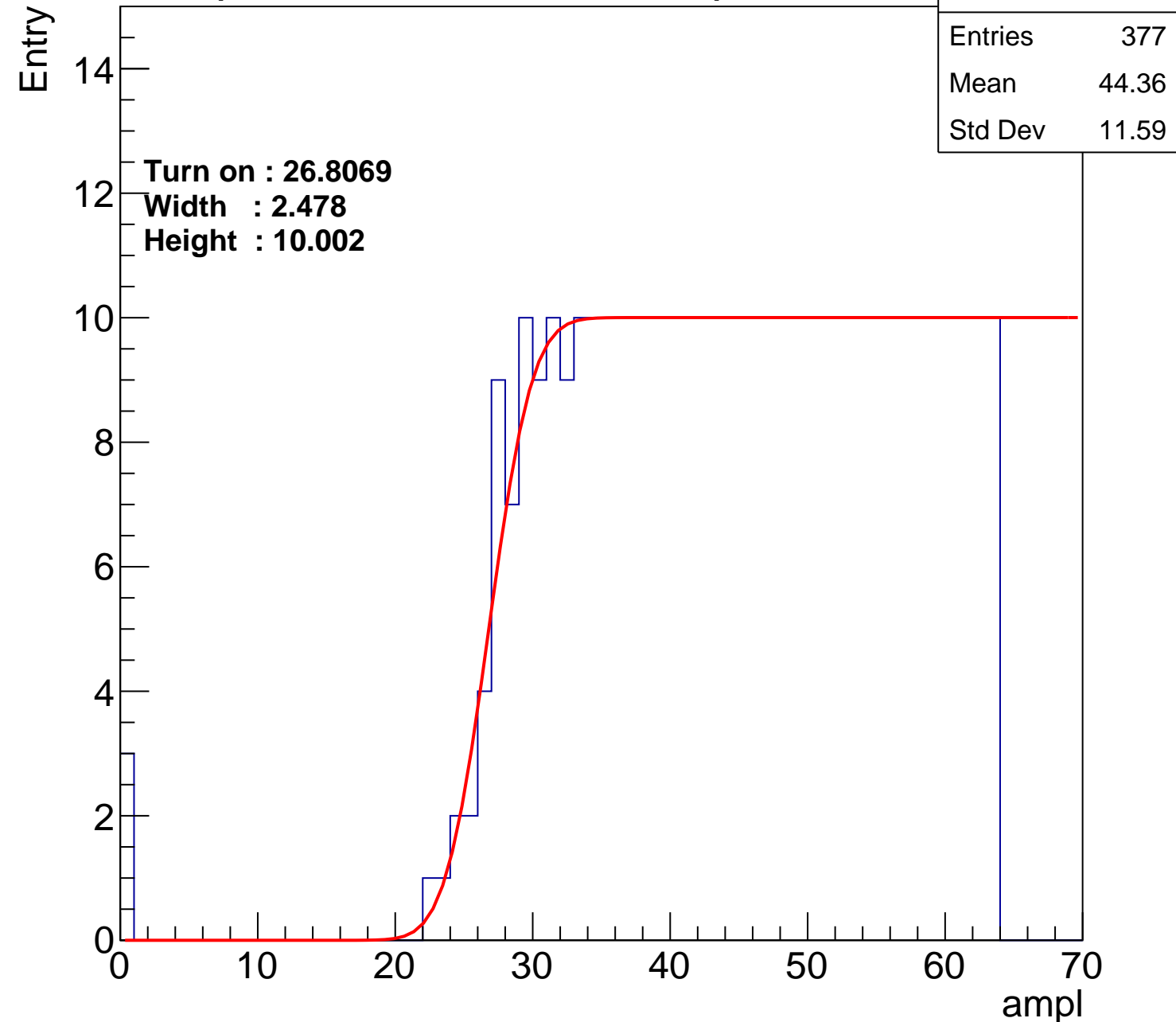
Width : 2.478

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.2778
Width : 2.666
Height : 10.025



B0L102S, U14-ch17

calib_packv5_042523_0143.root, FC#12, port B1

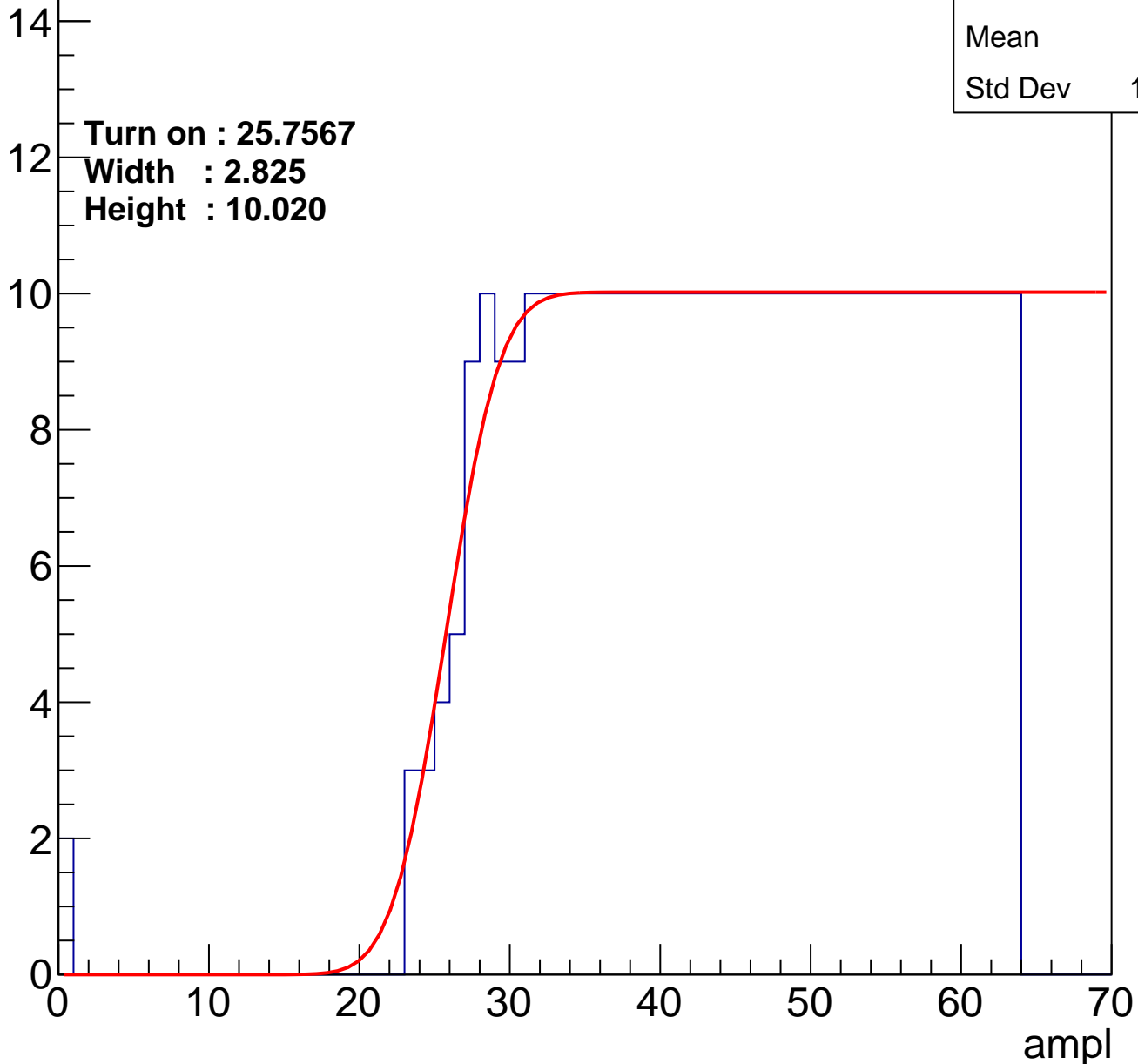
Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 25.7567

Width : 2.825

Height : 10.020

Entry



B0L102S, U14-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 27.0361

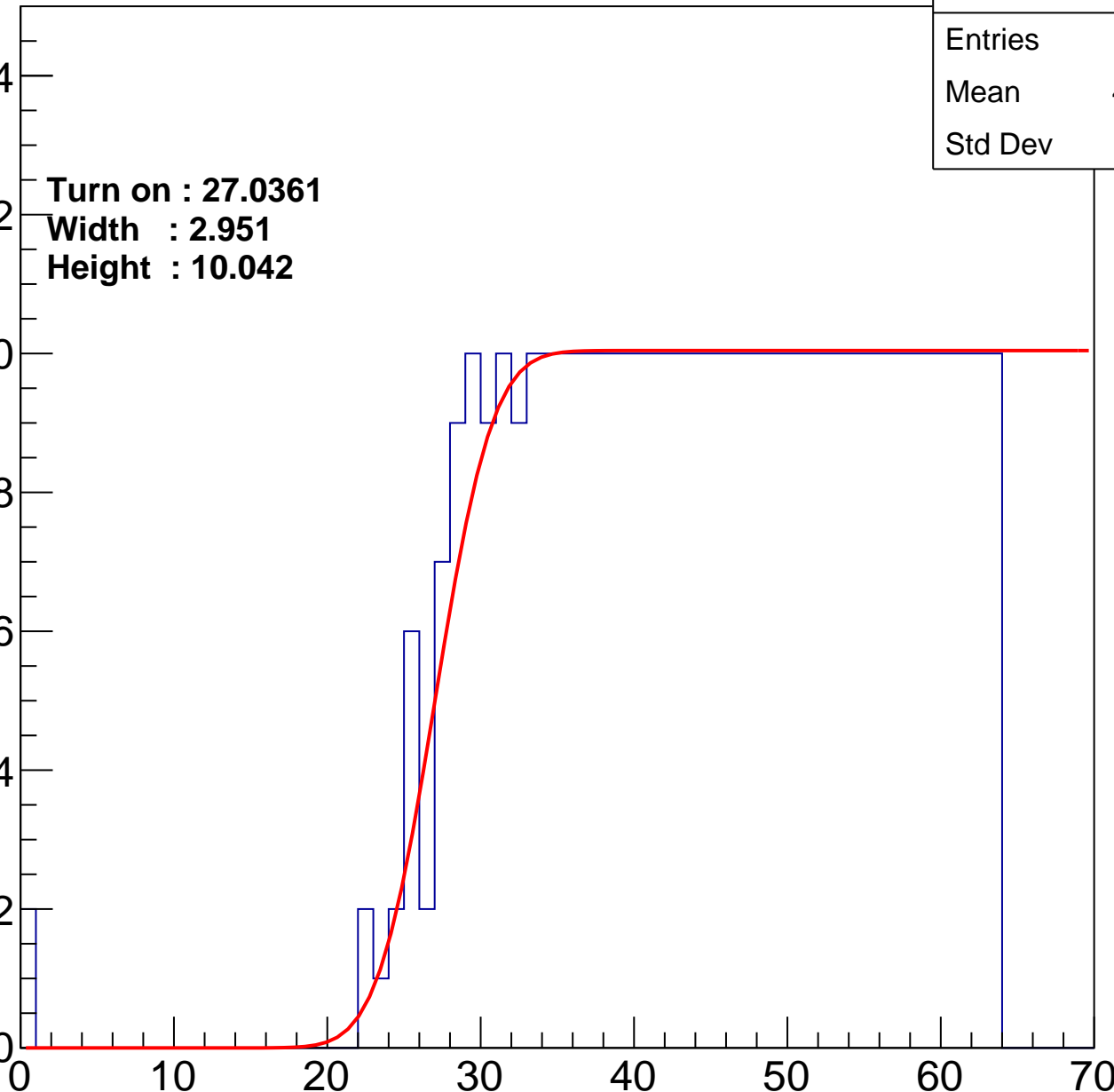
Width : 2.951

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.5437

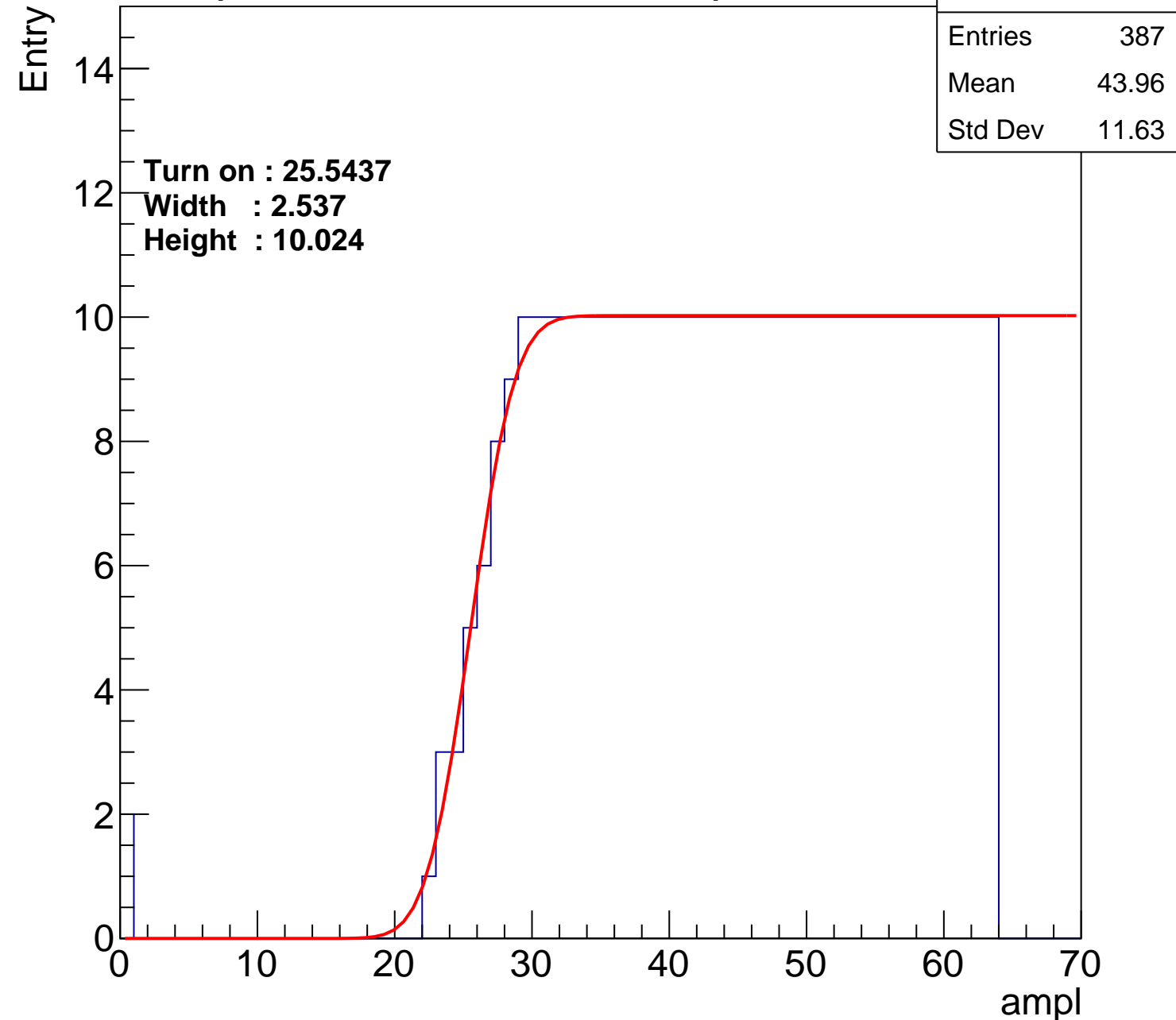
Width : 2.537

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.18
Std Dev	12.42

Turn on : 24.3941

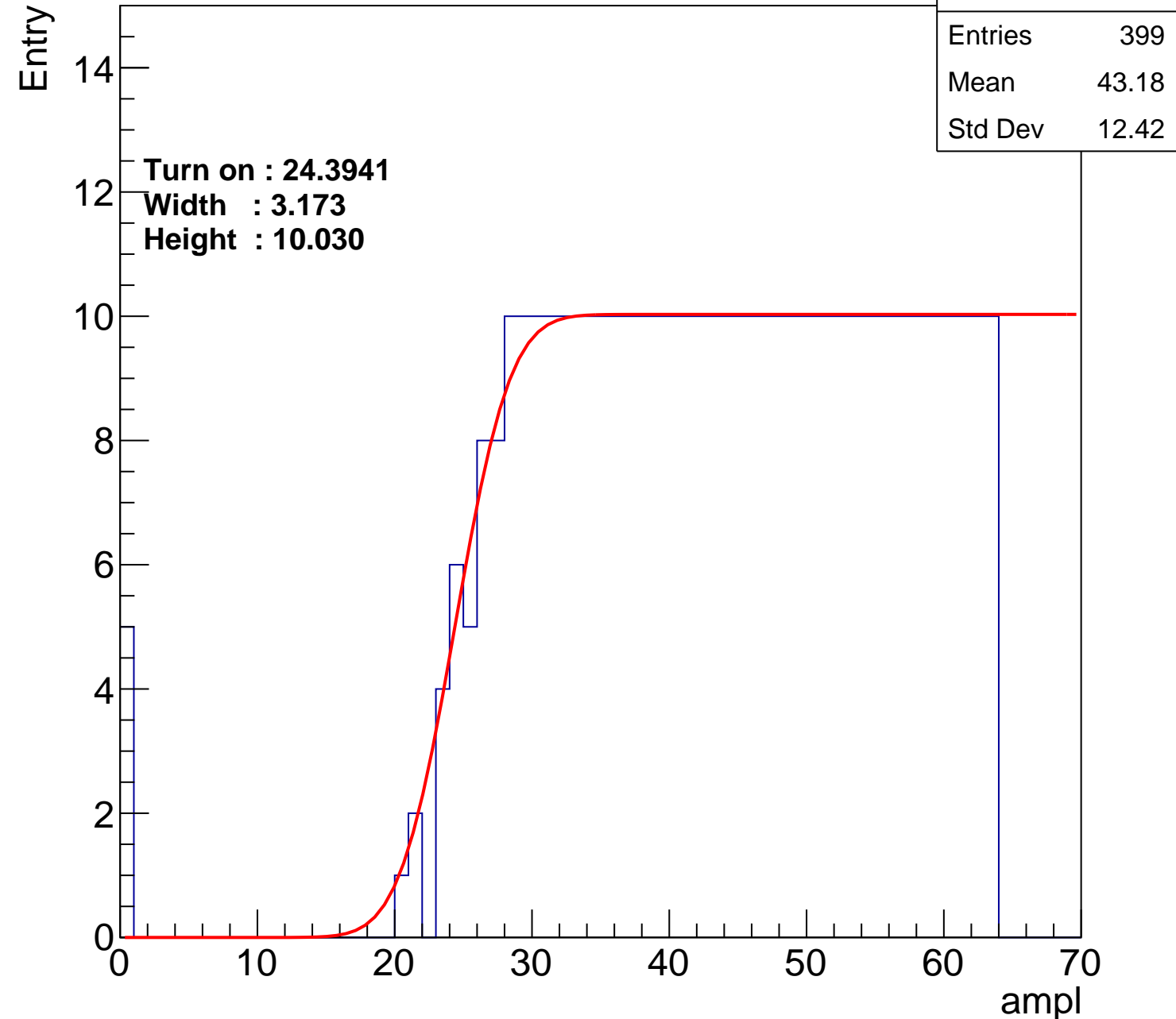
Width : 3.173

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.61
Std Dev	11.83

Turn on : 28.7898

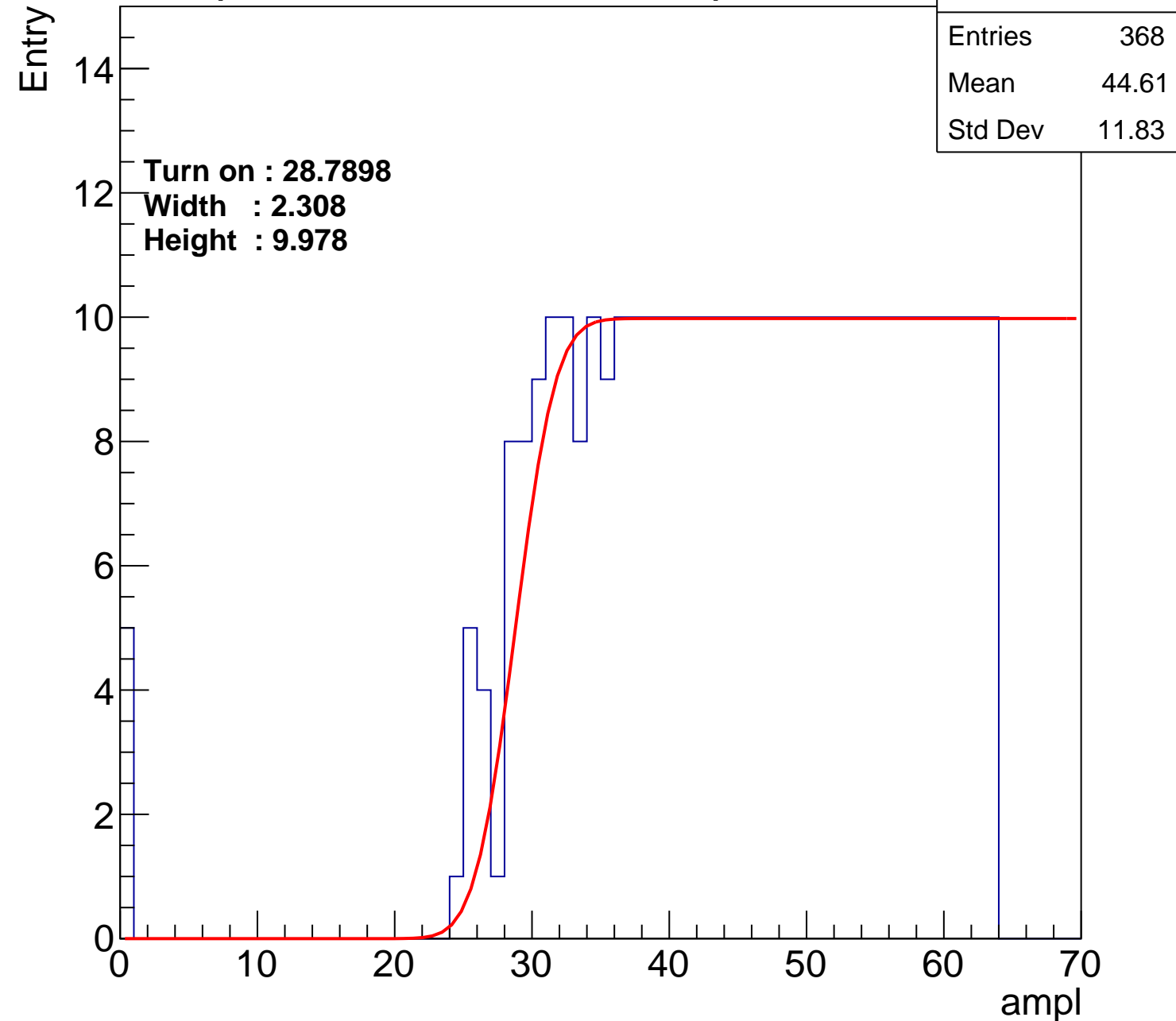
Width : 2.308

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 26.2027

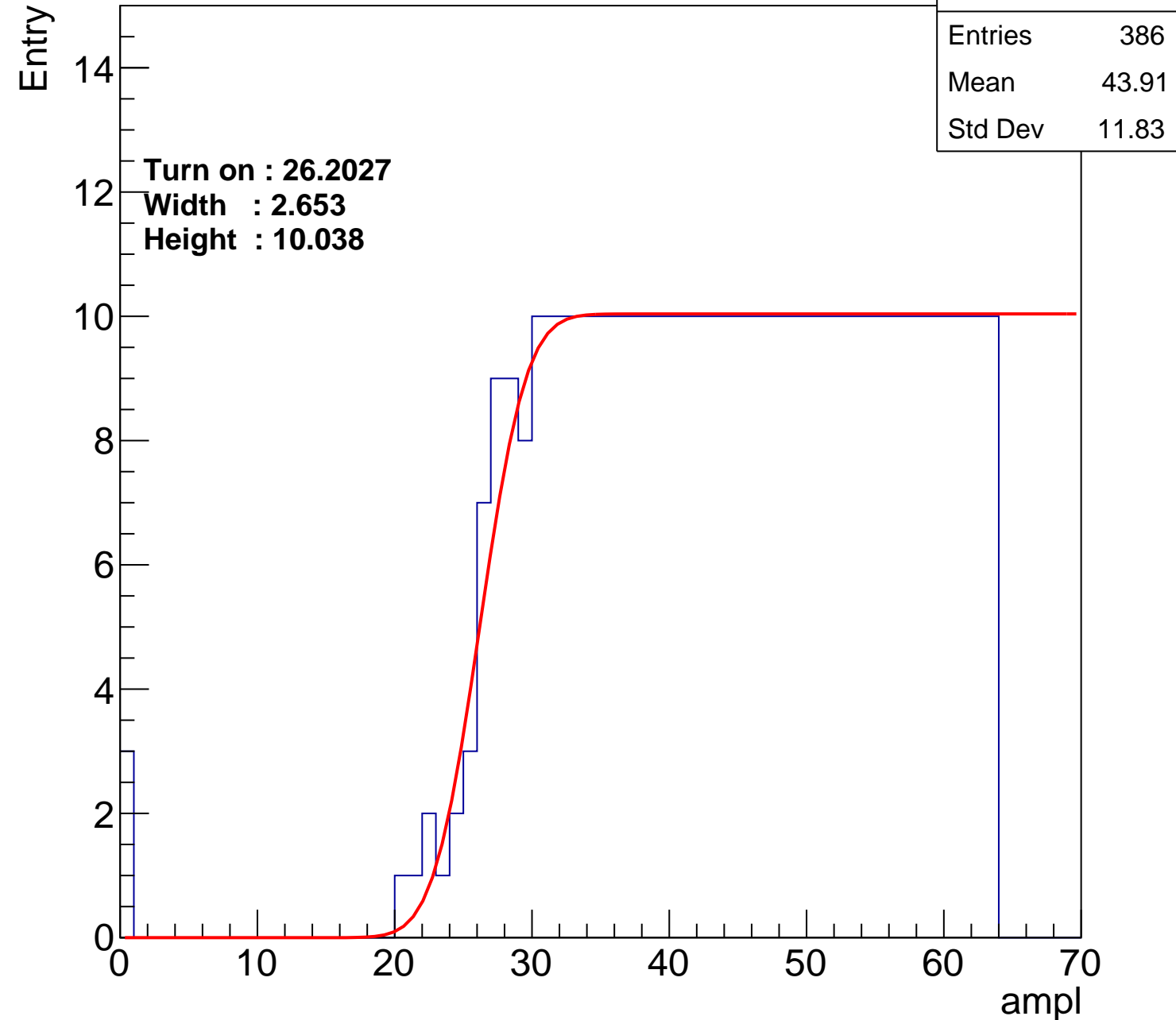
Width : 2.653

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.1736

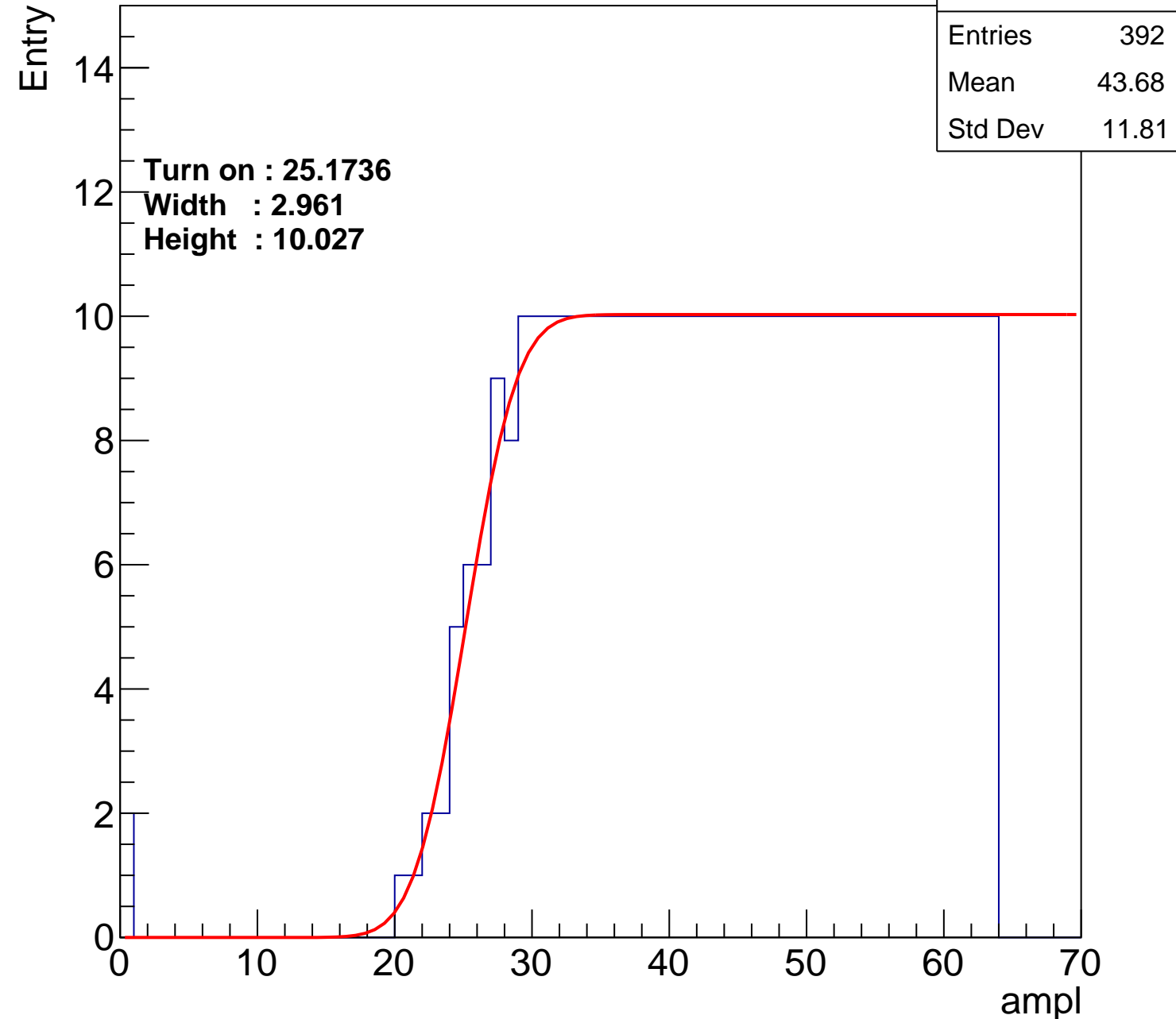
Width : 2.961

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 26.6033

Width : 3.419

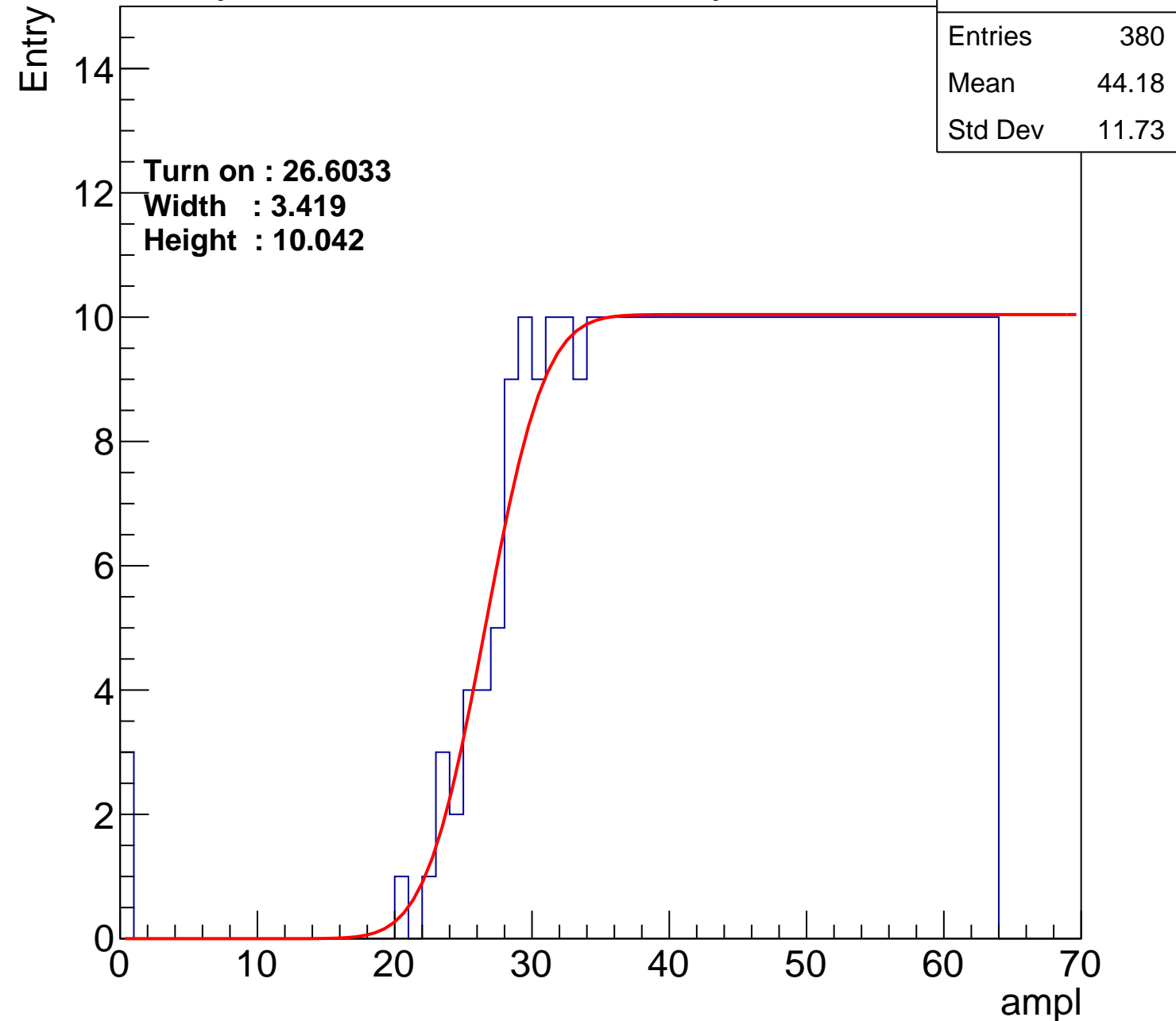
Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U14-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.88
Std Dev	12.12

Turn on : 26.0151

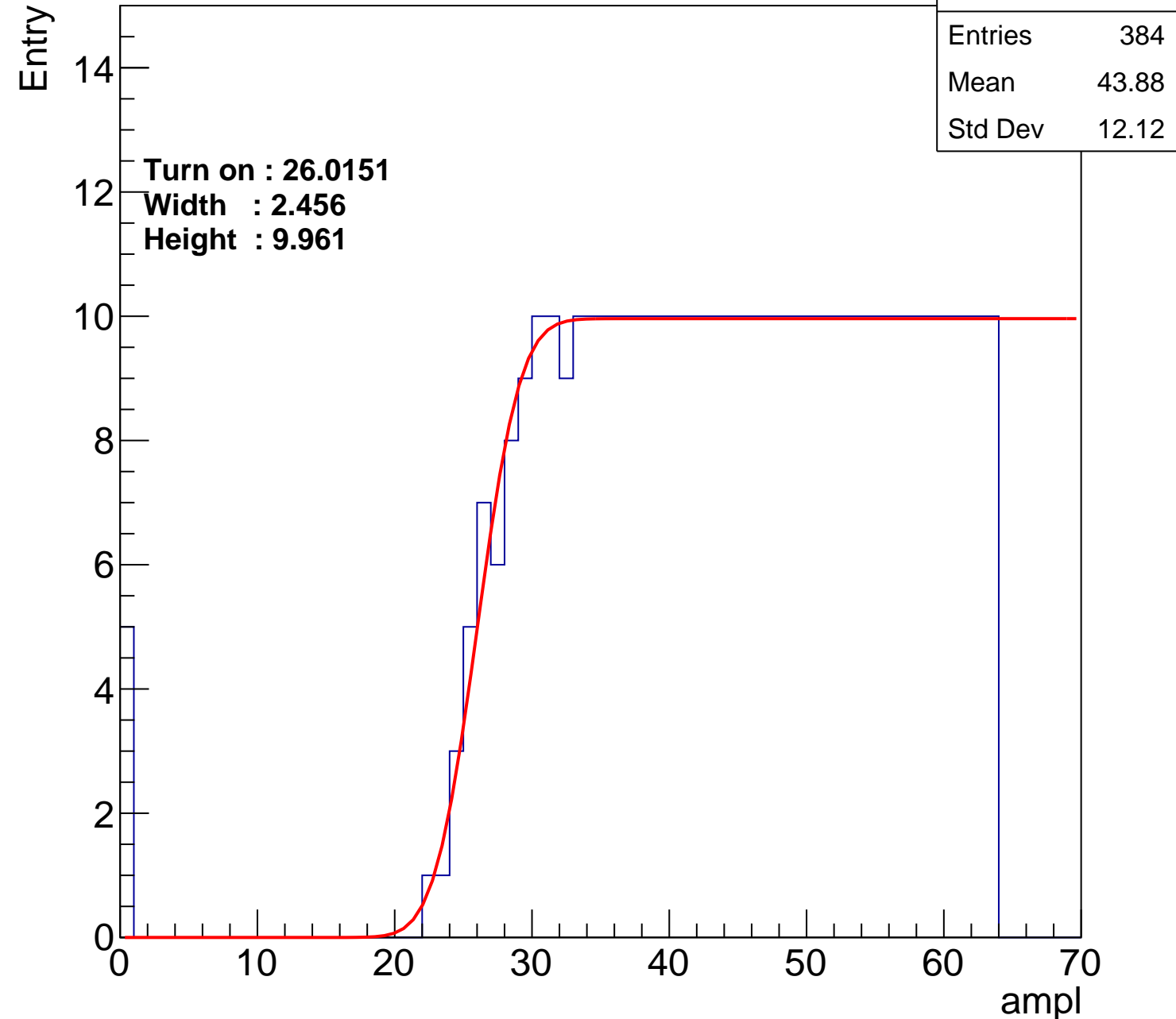
Width : 2.456

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.53
Std Dev	11.78

Turn on : 25.2400

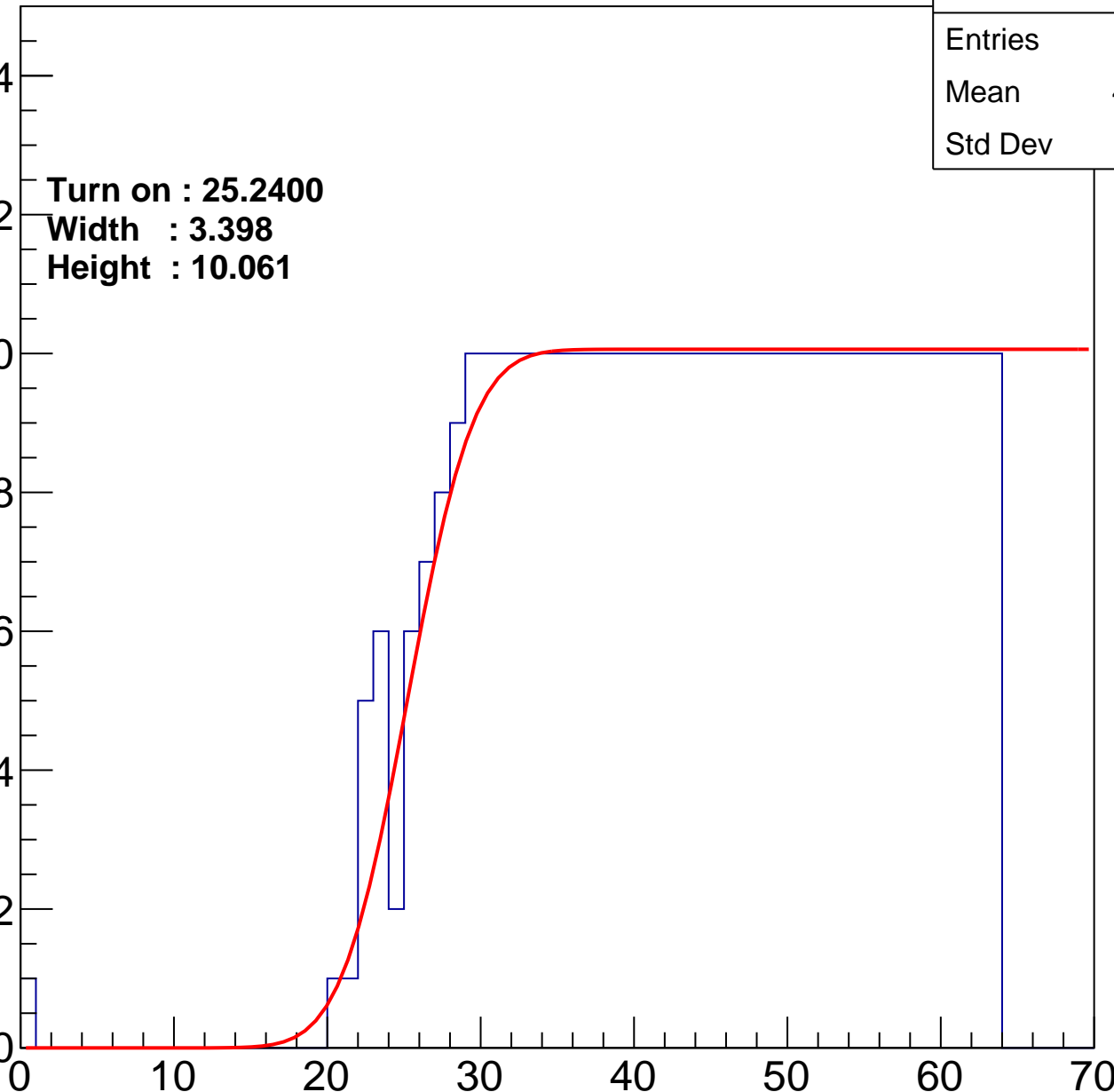
Width : 3.398

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 26.8417

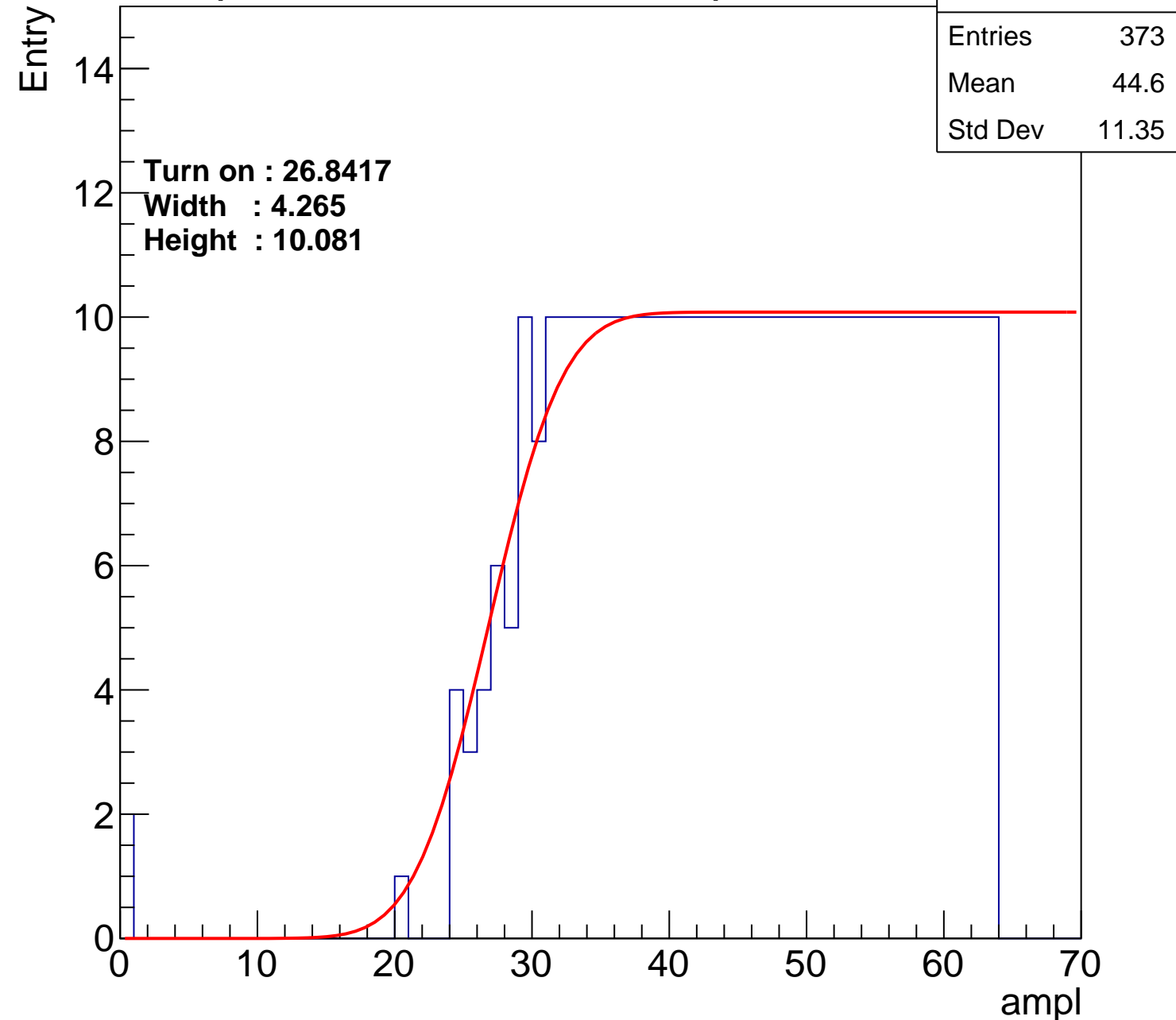
Width : 4.265

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch28

calib_packv5_042523_0143.root, FC#12, port B1

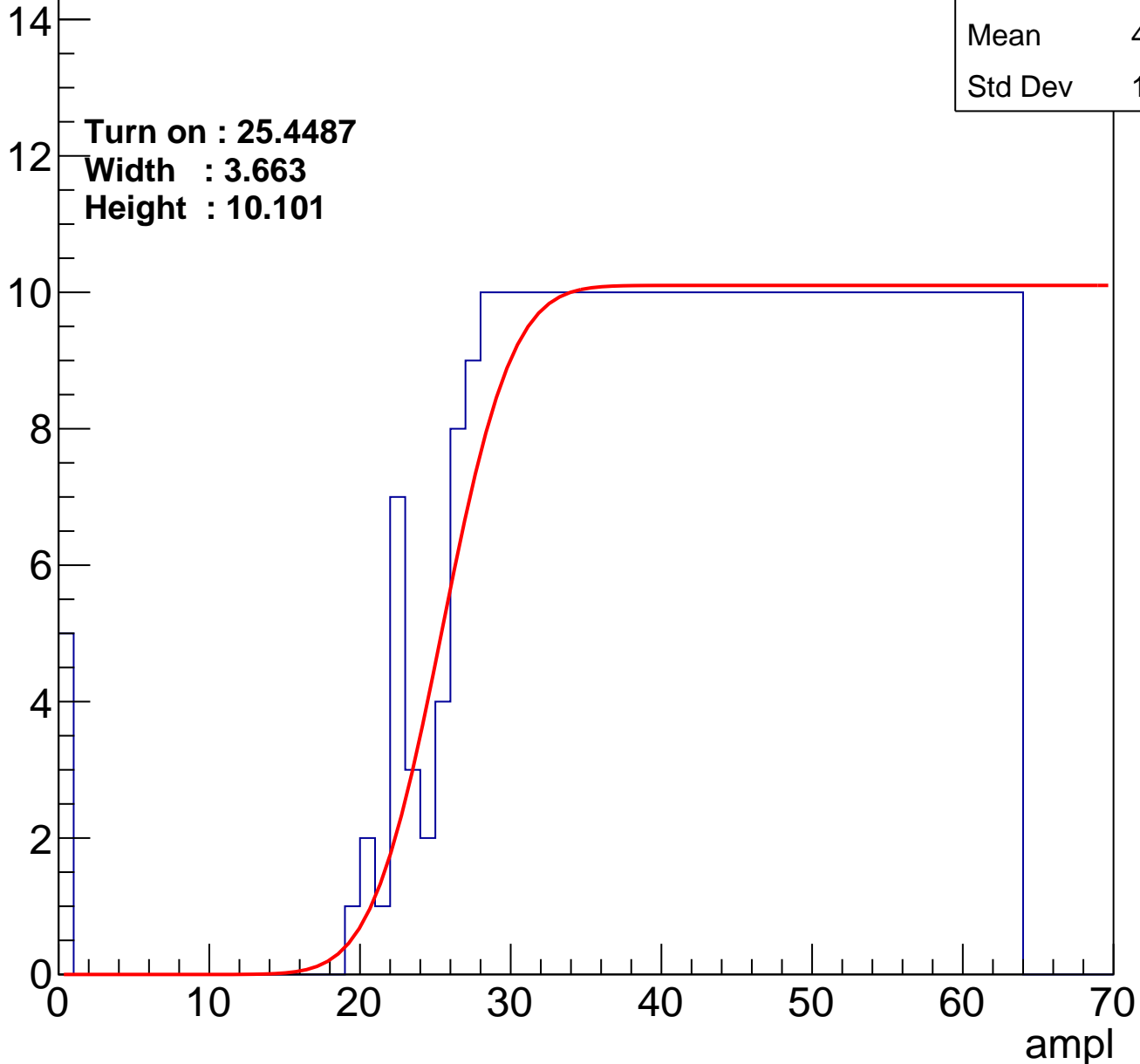
Entries	402
Mean	42.99
Std Dev	12.56

Turn on : 25.4487

Width : 3.663

Height : 10.101

Entry



B0L102S, U14-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.4723

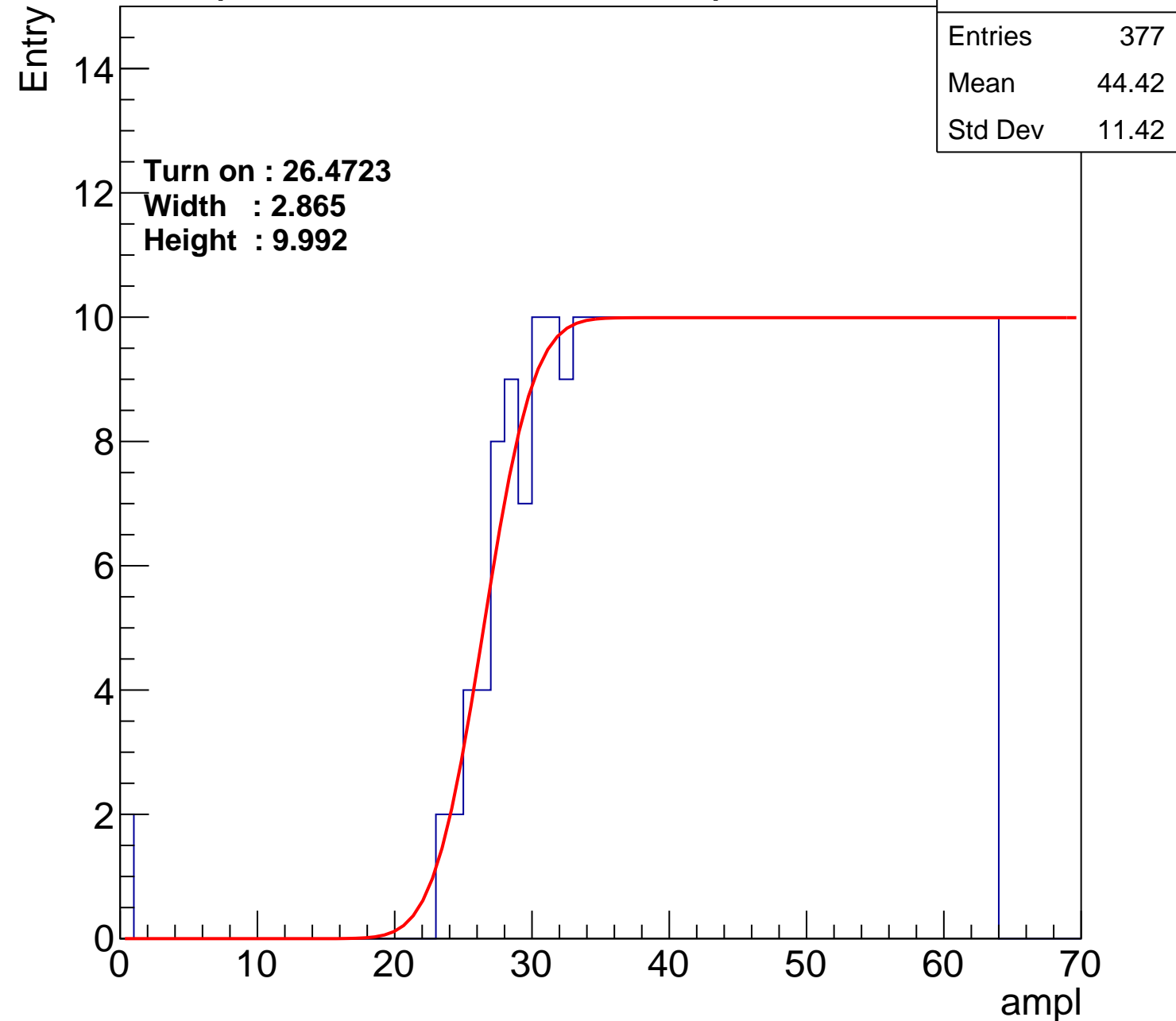
Width : 2.865

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.8
Std Dev	12.79

Turn on : 24.4044

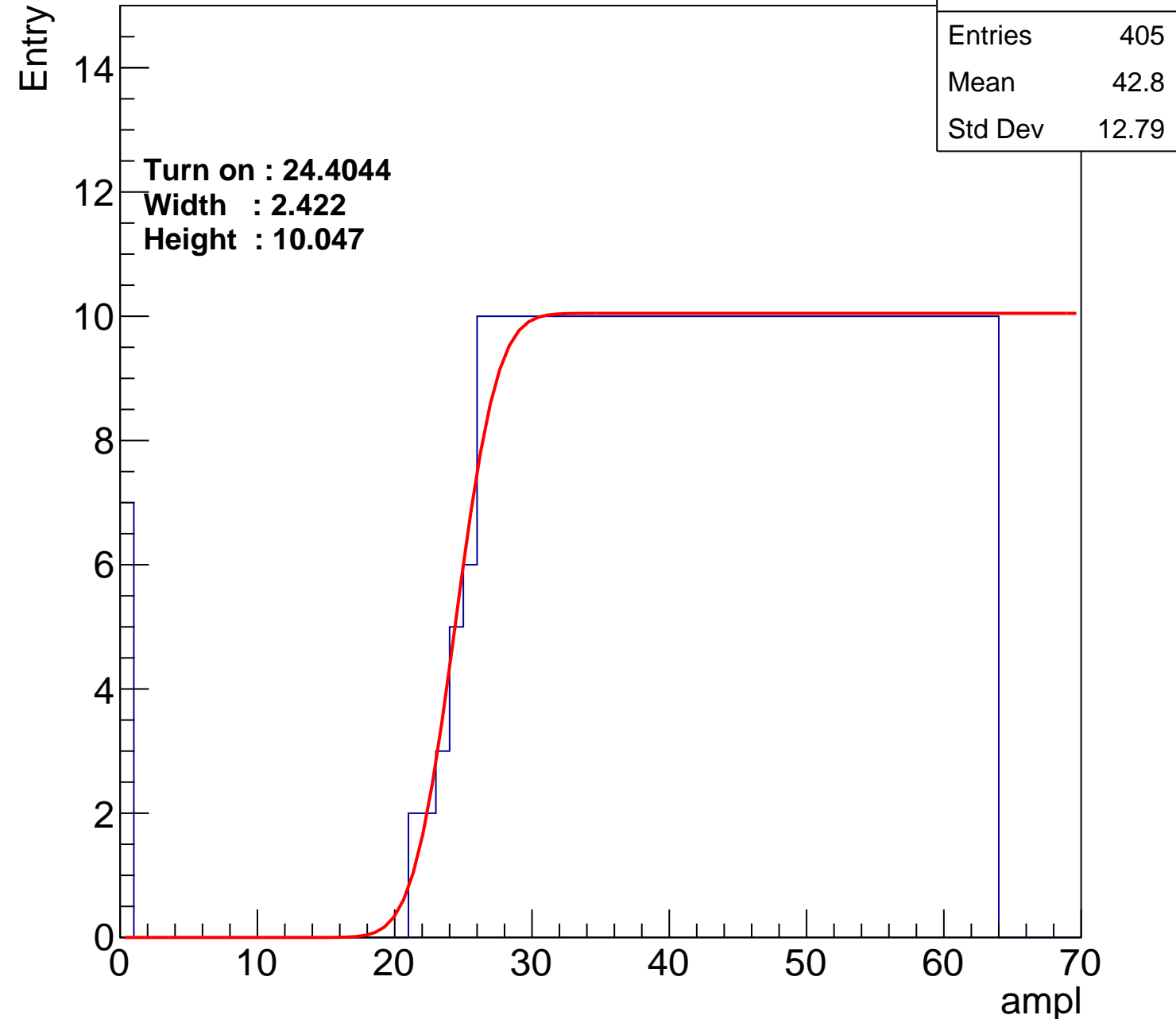
Width : 2.422

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.35
Std Dev	12.12

Turn on : 24.4867

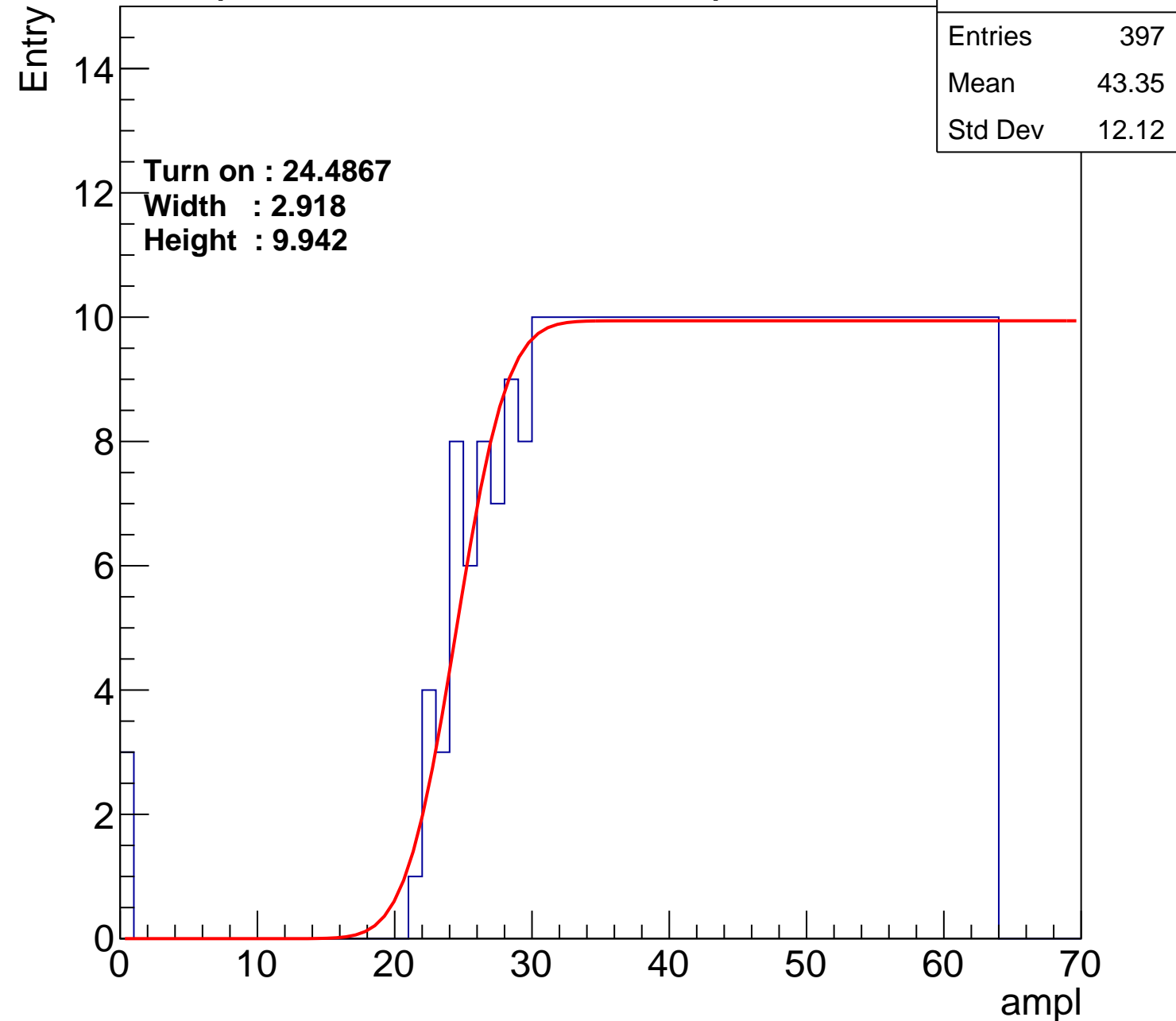
Width : 2.918

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.36
Std Dev	12.62

Turn on : 26.0084

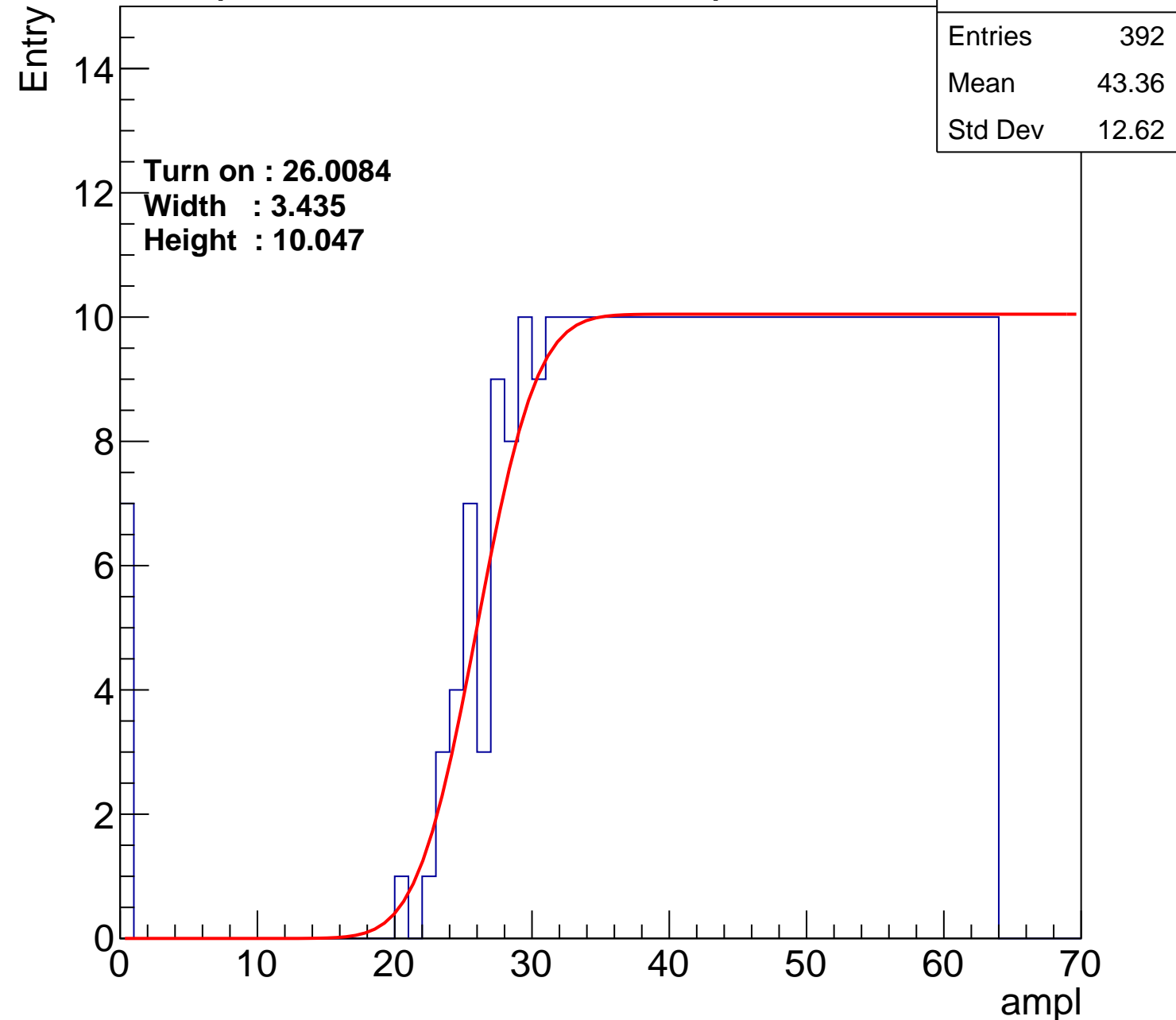
Width : 3.435

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.77
Std Dev	11.3

Turn on : 27.7270

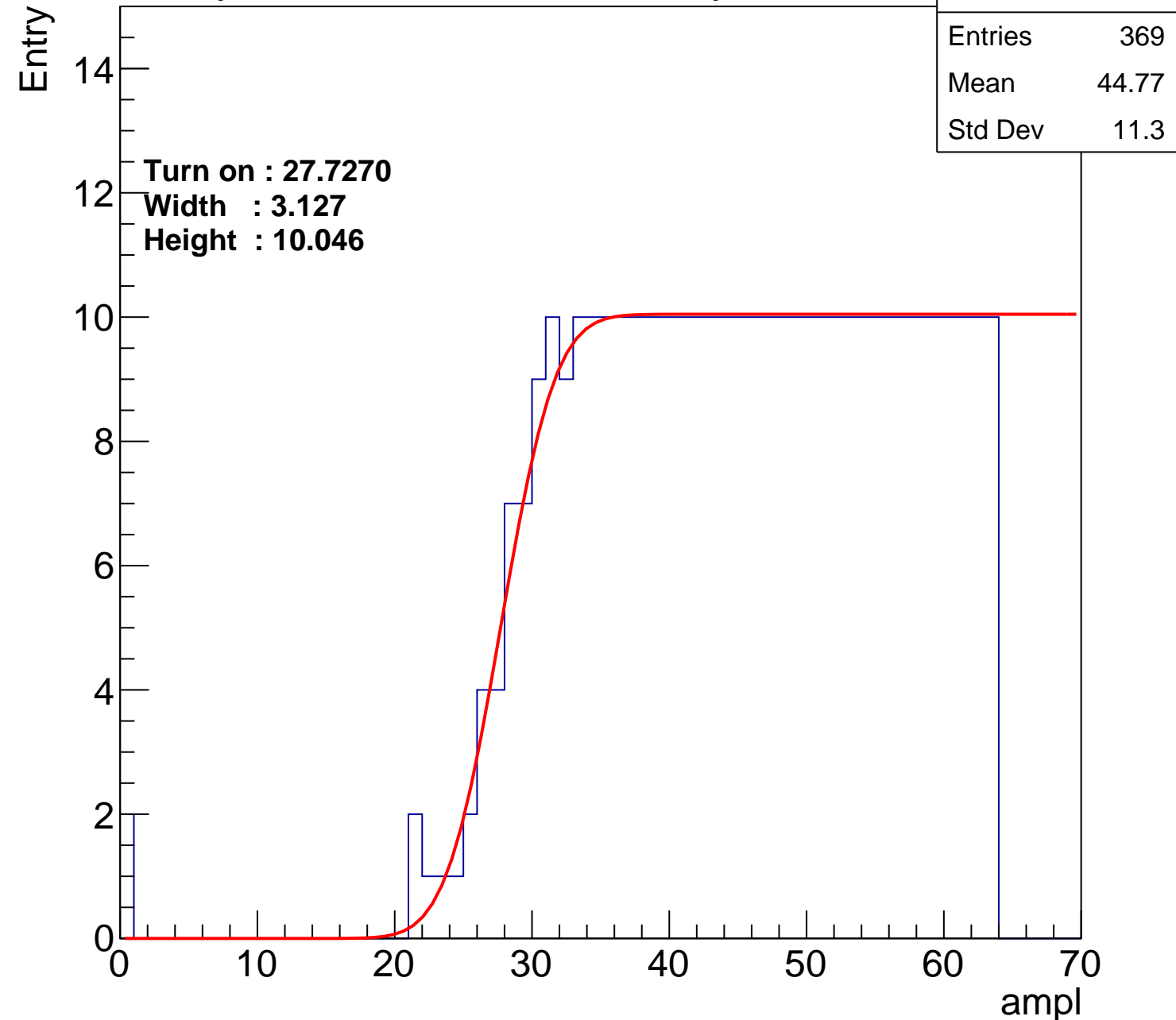
Width : 3.127

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.49
Std Dev	12.29

Turn on : 25.4456

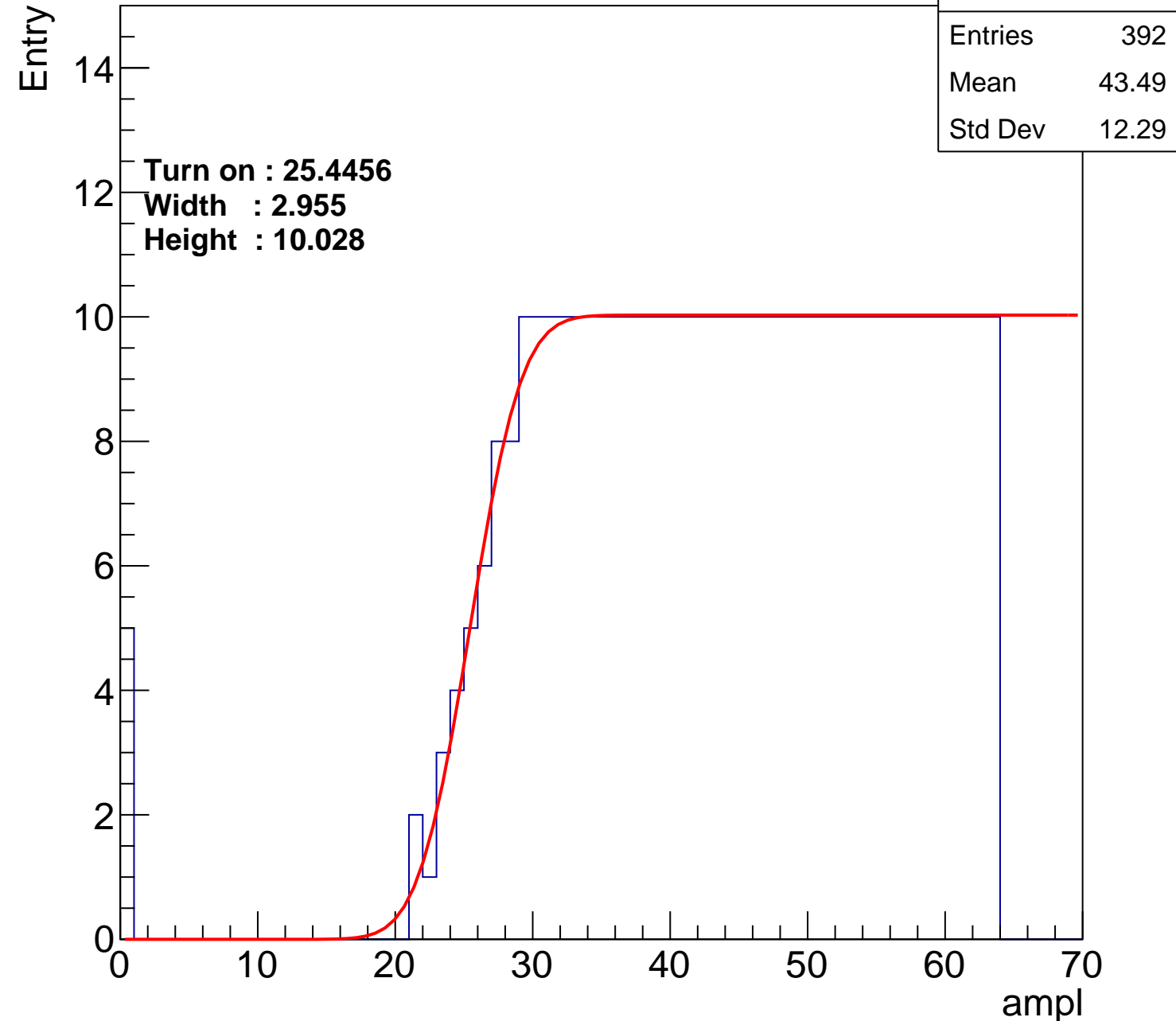
Width : 2.955

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch35

calib_packv5_042523_0143.root, FC#12, port B1

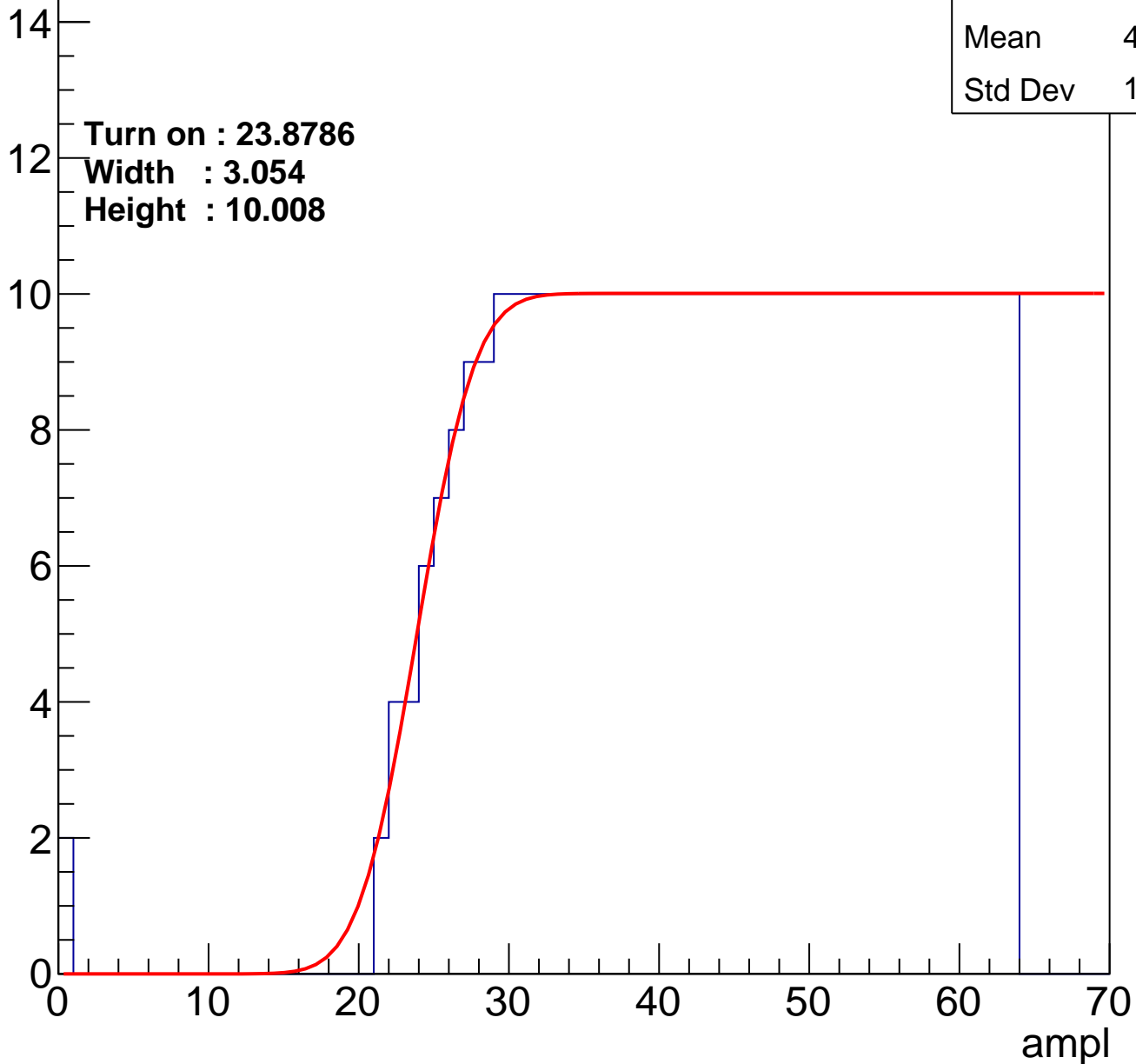
Entries	401
Mean	43.25
Std Dev	12.02

Turn on : 23.8786

Width : 3.054

Height : 10.008

Entry



B0L102S, U14-ch36

calib_packv5_042523_0143.root, FC#12, port B1

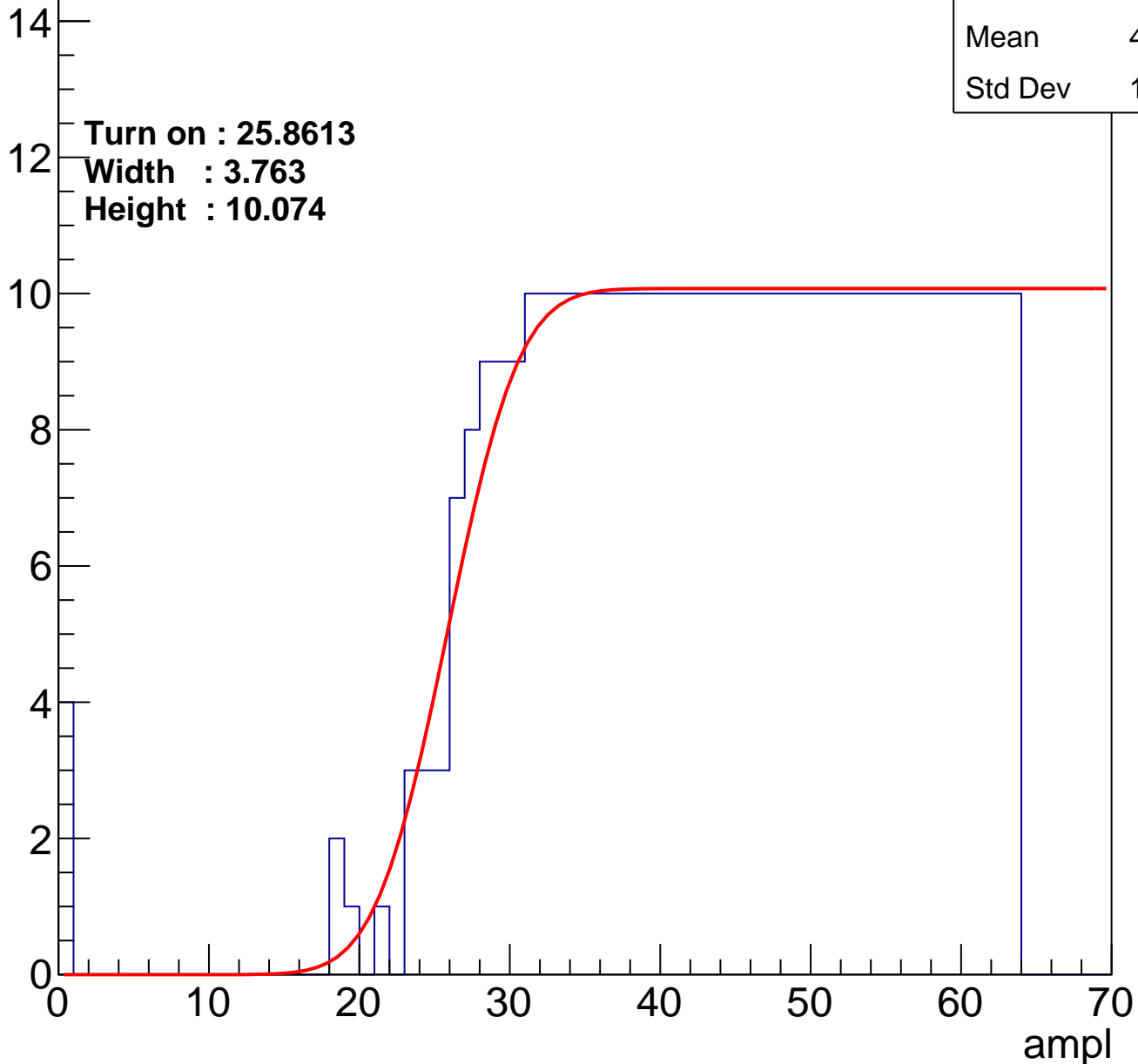
Entries	389
Mean	43.66
Std Dev	12.14

Turn on : 25.8613

Width : 3.763

Height : 10.074

Entry



B0L102S, U14-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.2
Std Dev	12.28

Turn on : 24.5997

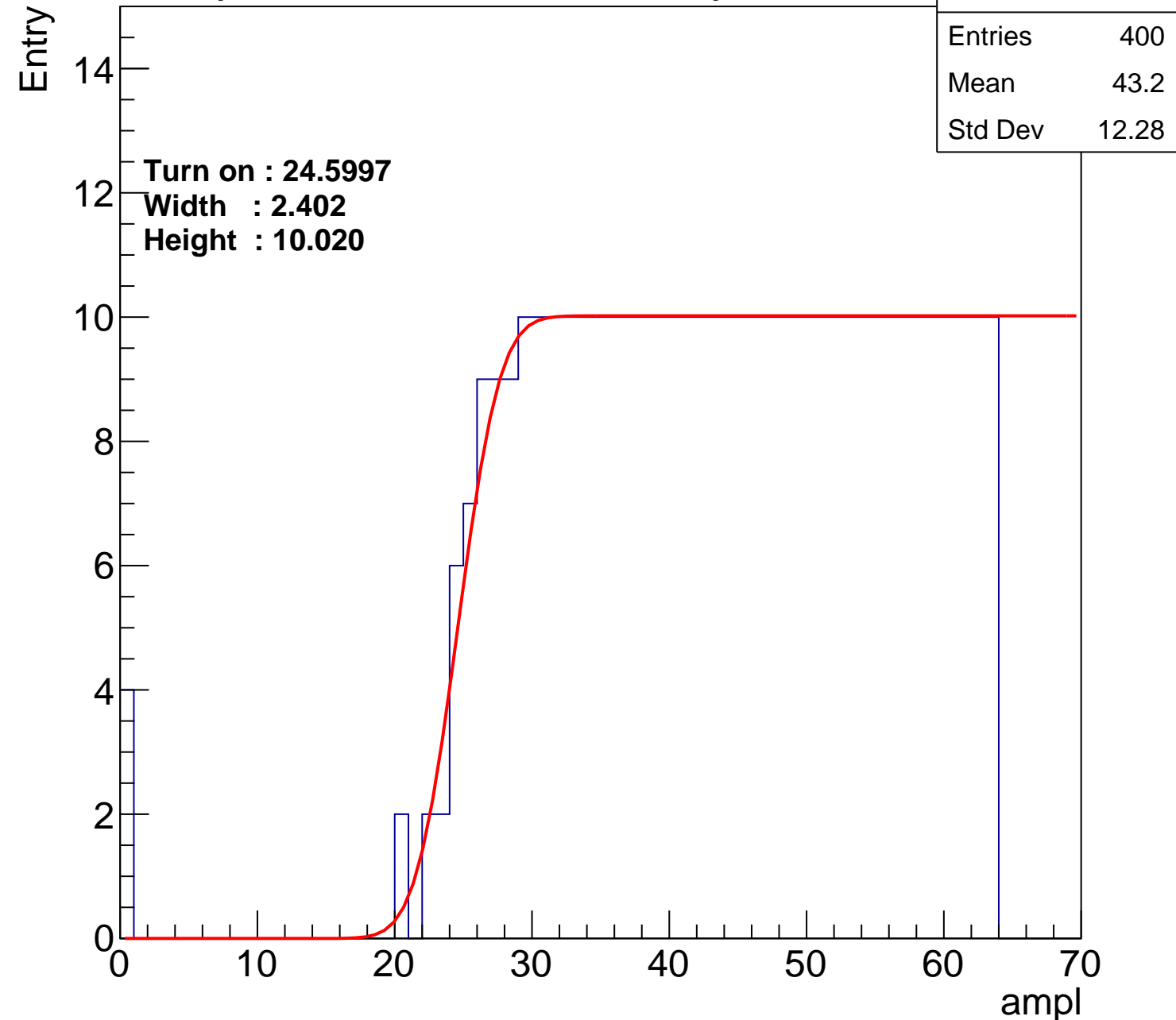
Width : 2.402

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.18
Std Dev	11.47

Turn on : 26.0032

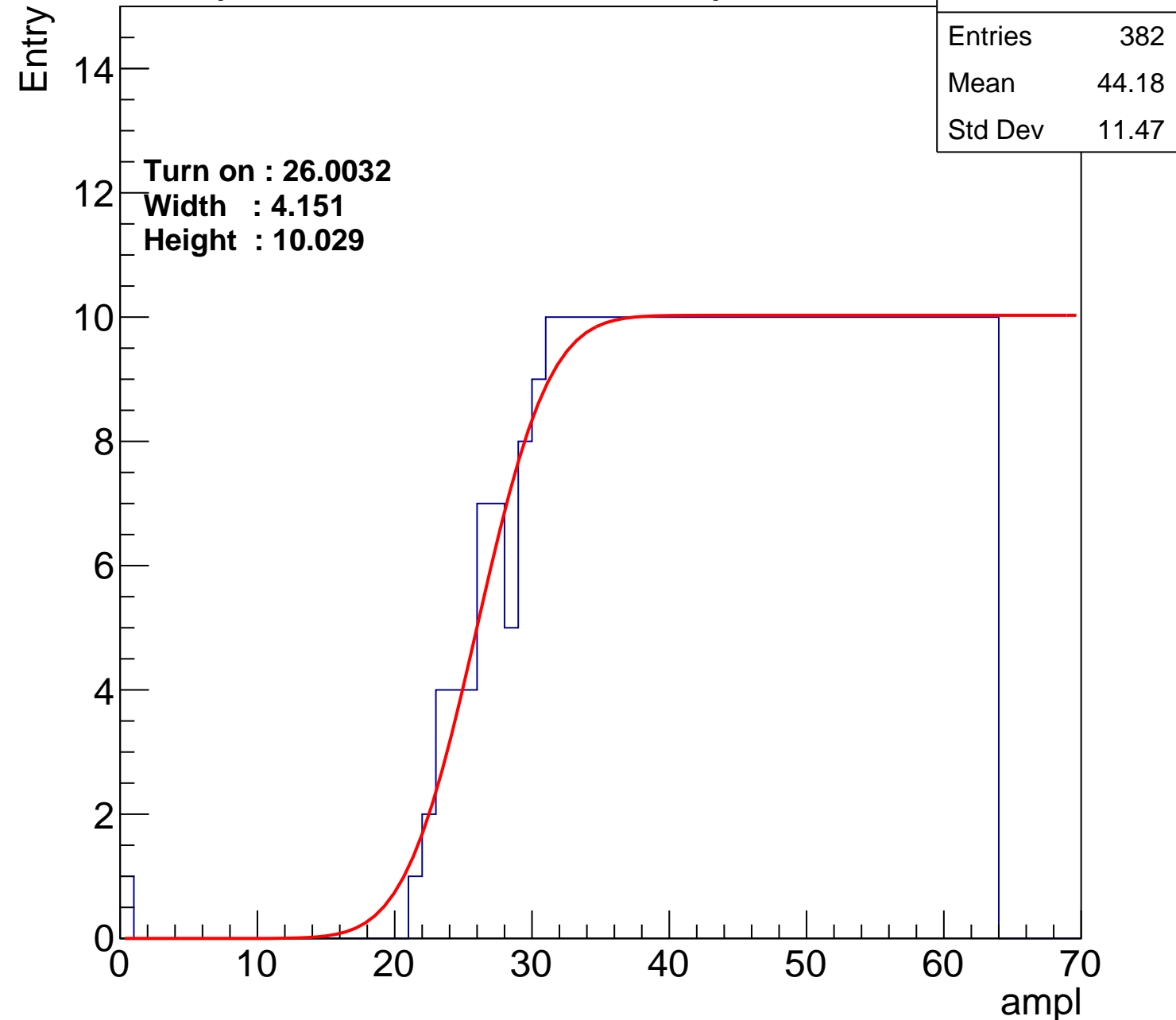
Width : 4.151

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.8989

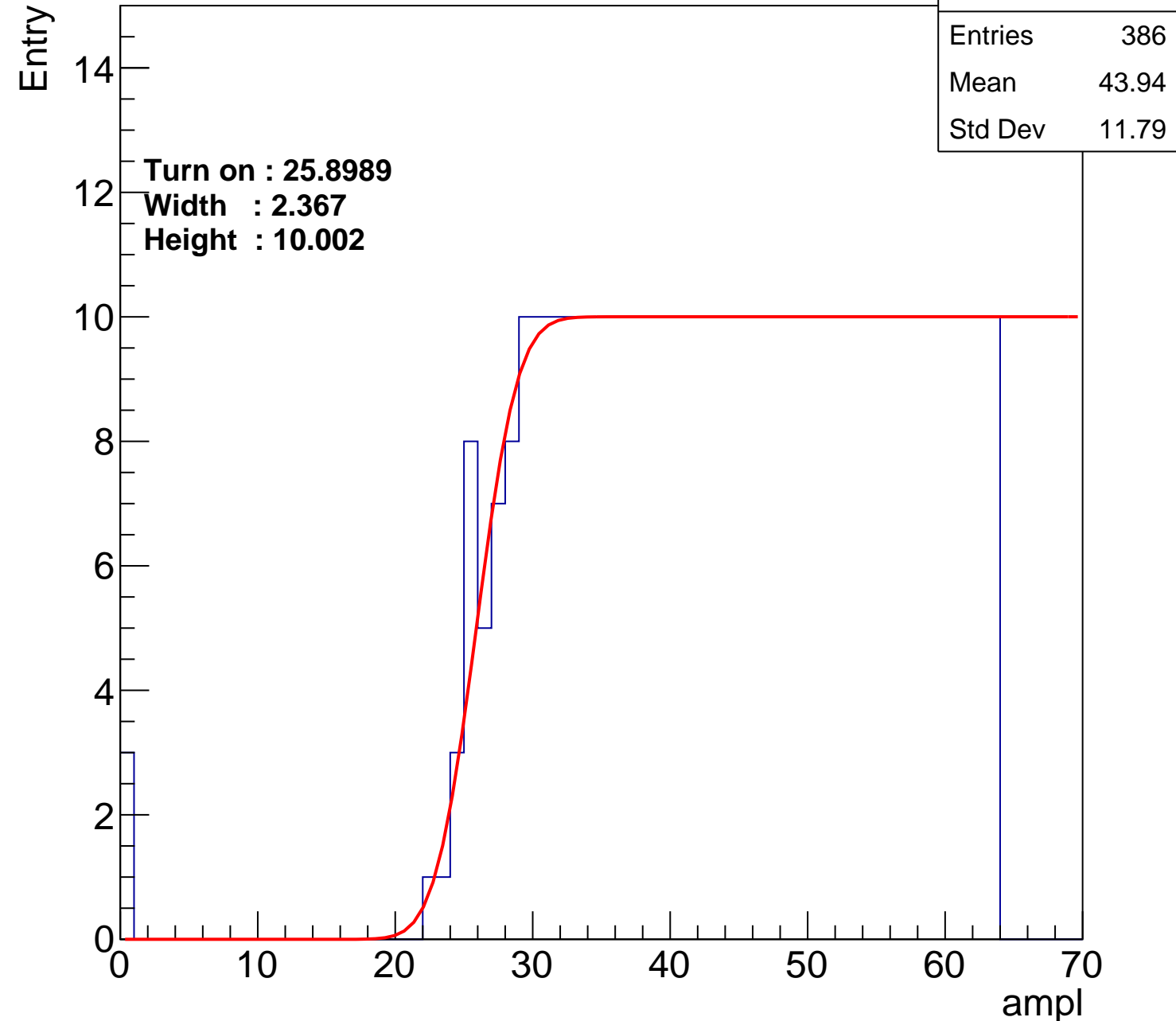
Width : 2.367

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.69
Std Dev	12.1

Turn on : 26.1153

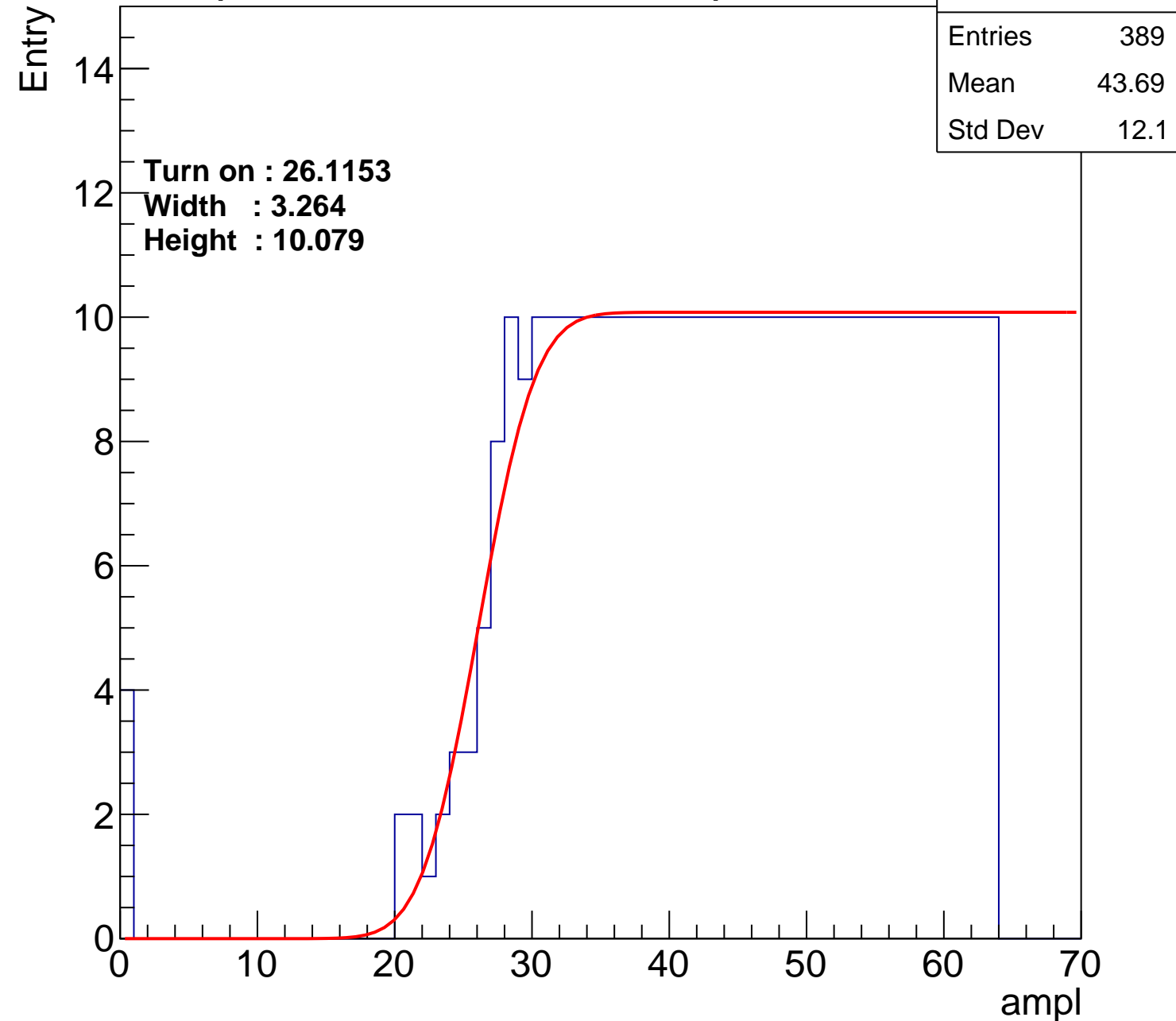
Width : 3.264

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.56
Std Dev	11.5

Turn on : 27.2603

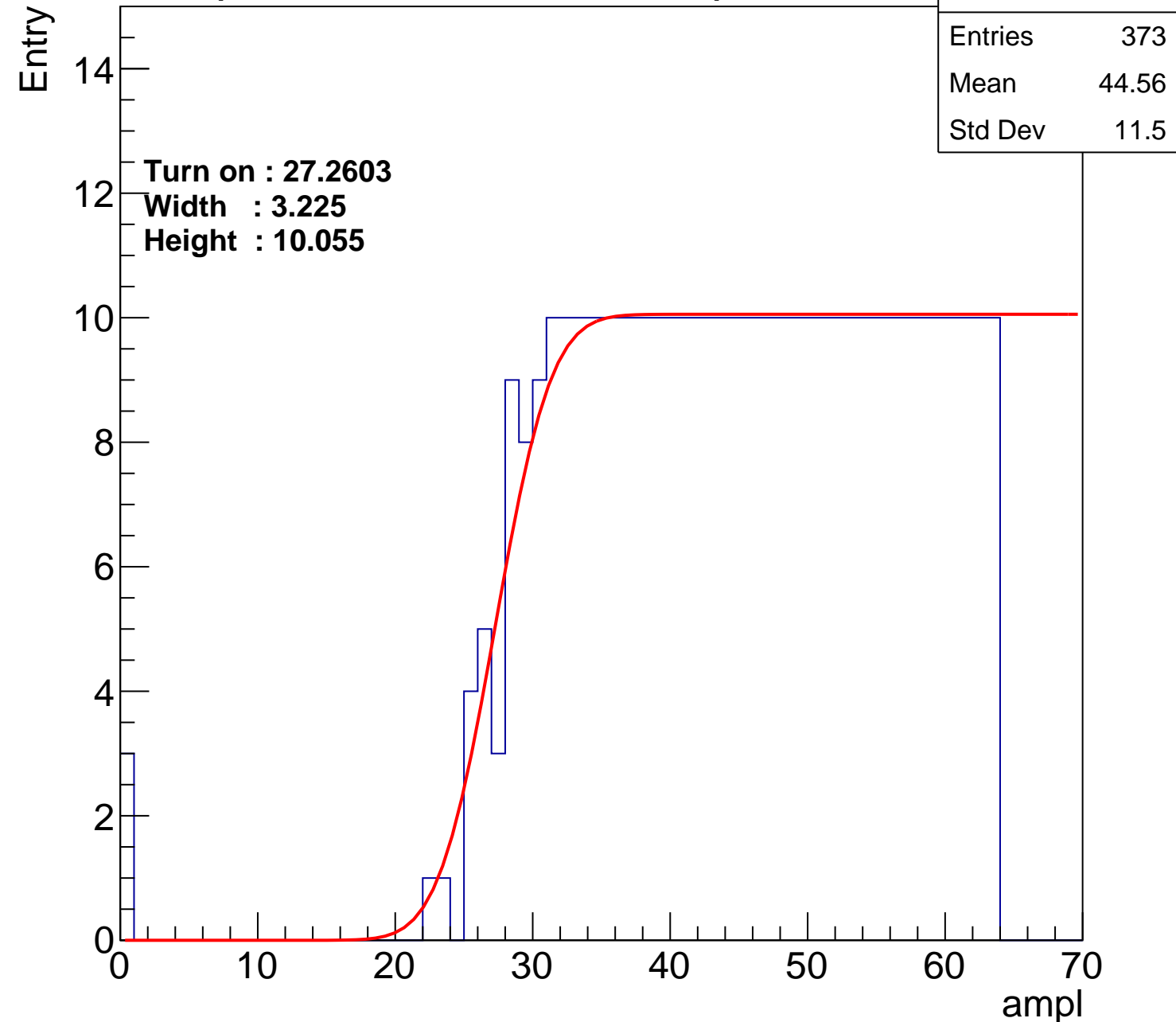
Width : 3.225

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.43
Std Dev	11.83

Turn on : 24.1905

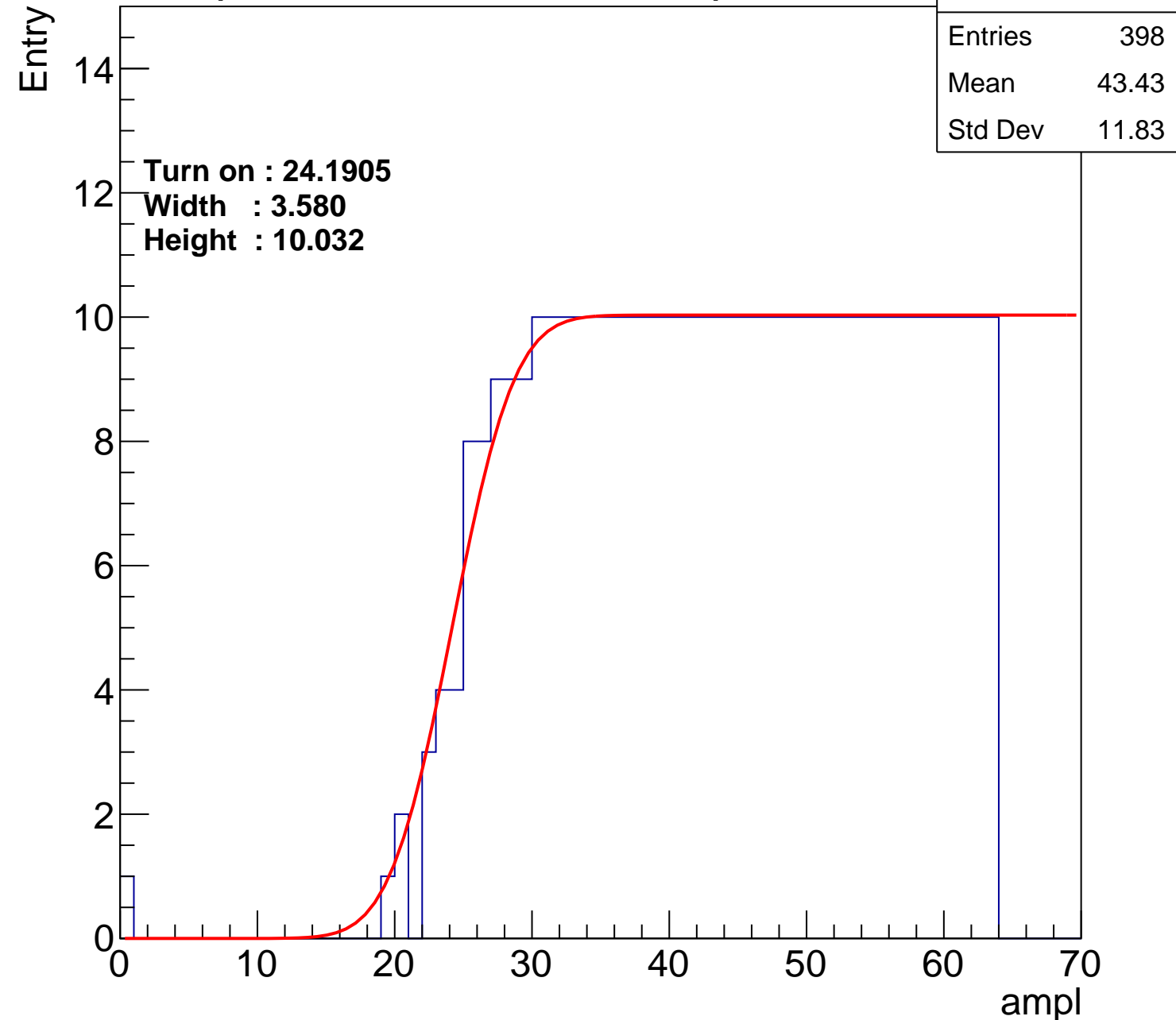
Width : 3.580

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch43

calib_packv5_042523_0143.root, FC#12, port B1

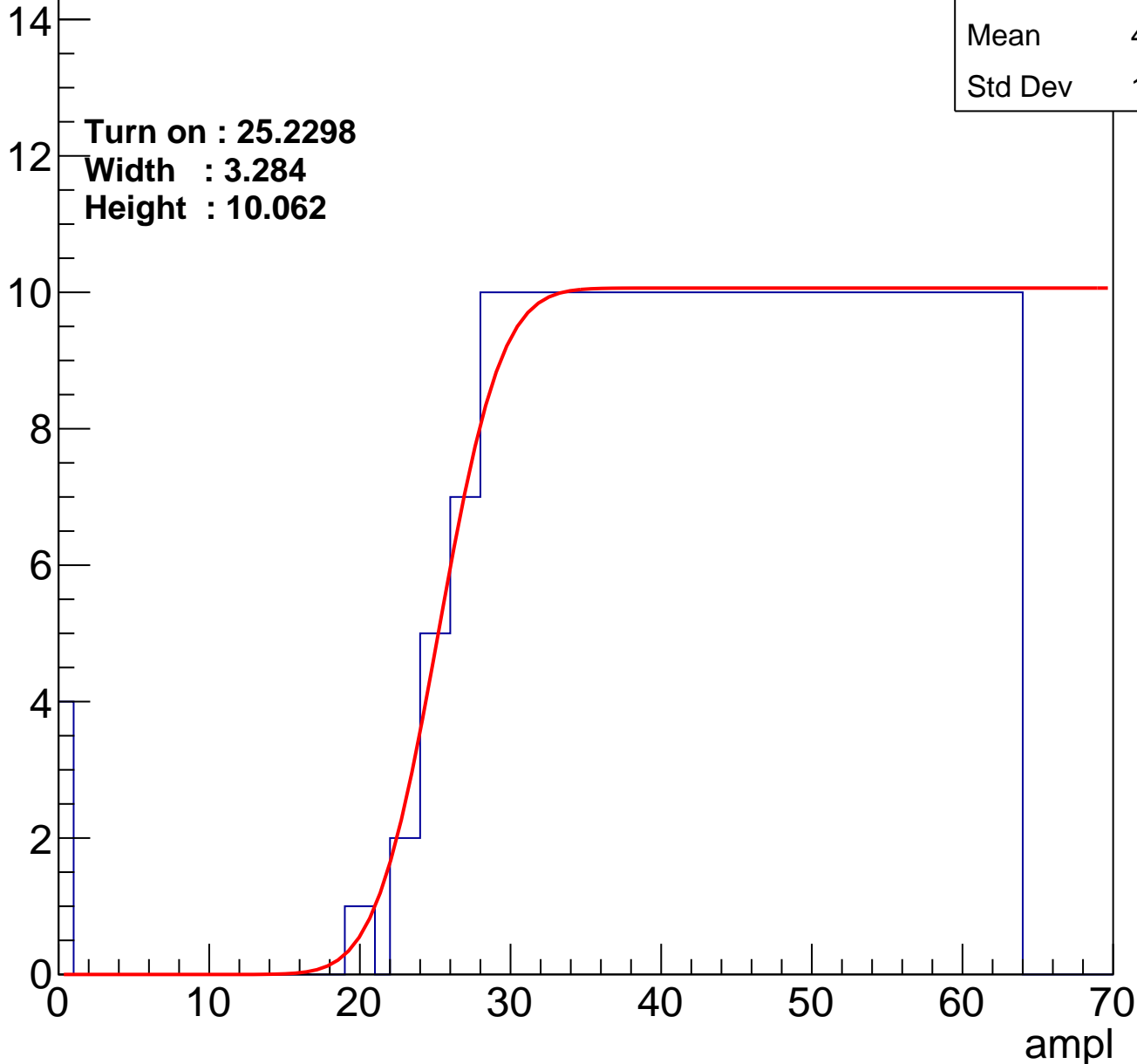
Entries	394
Mean	43.46
Std Dev	12.18

Turn on : 25.2298

Width : 3.284

Height : 10.062

Entry



B0L102S, U14-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.53
Std Dev	12.06

Turn on : 25.7563

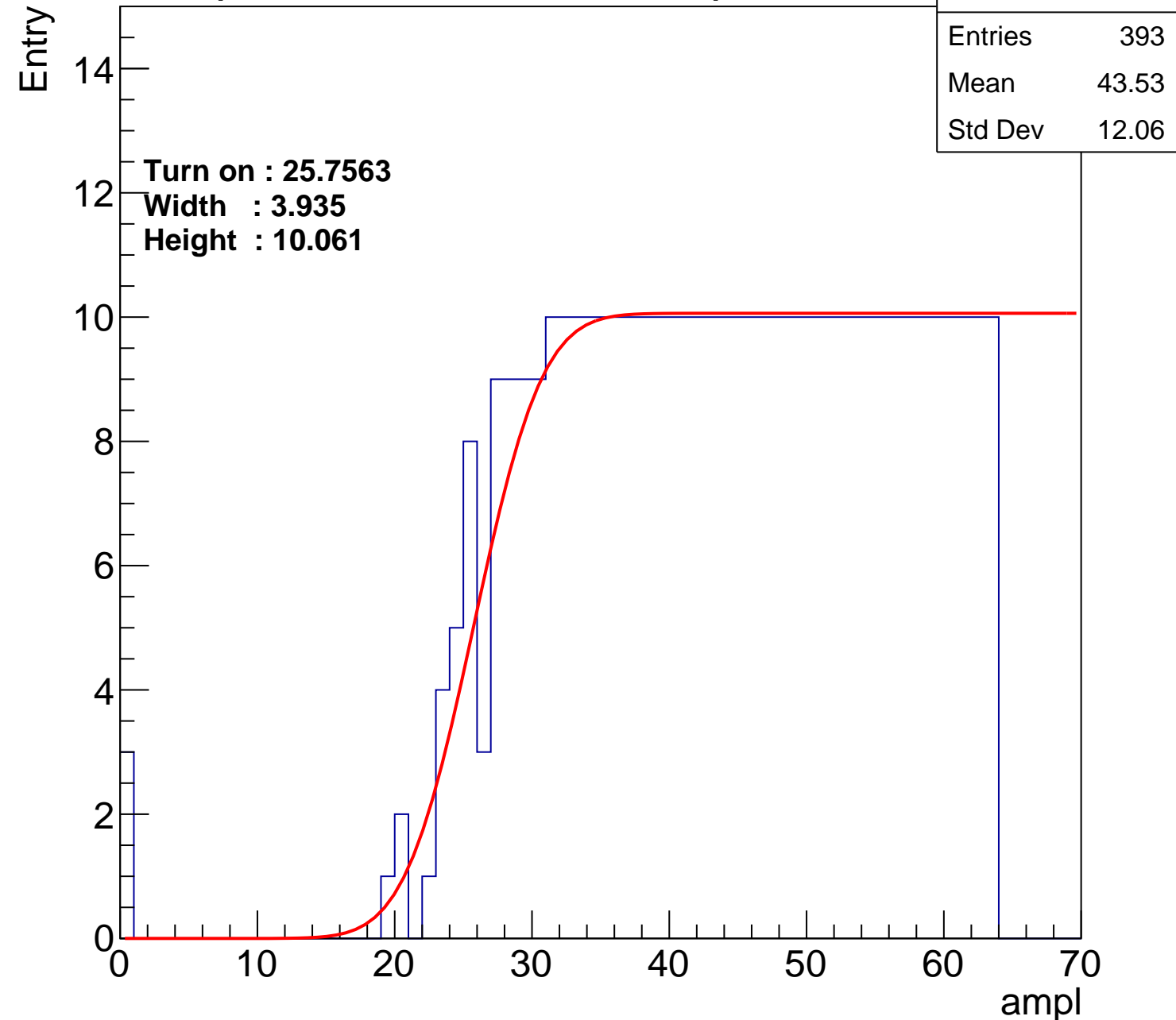
Width : 3.935

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.74
Std Dev	12.3

Turn on : 26.7556

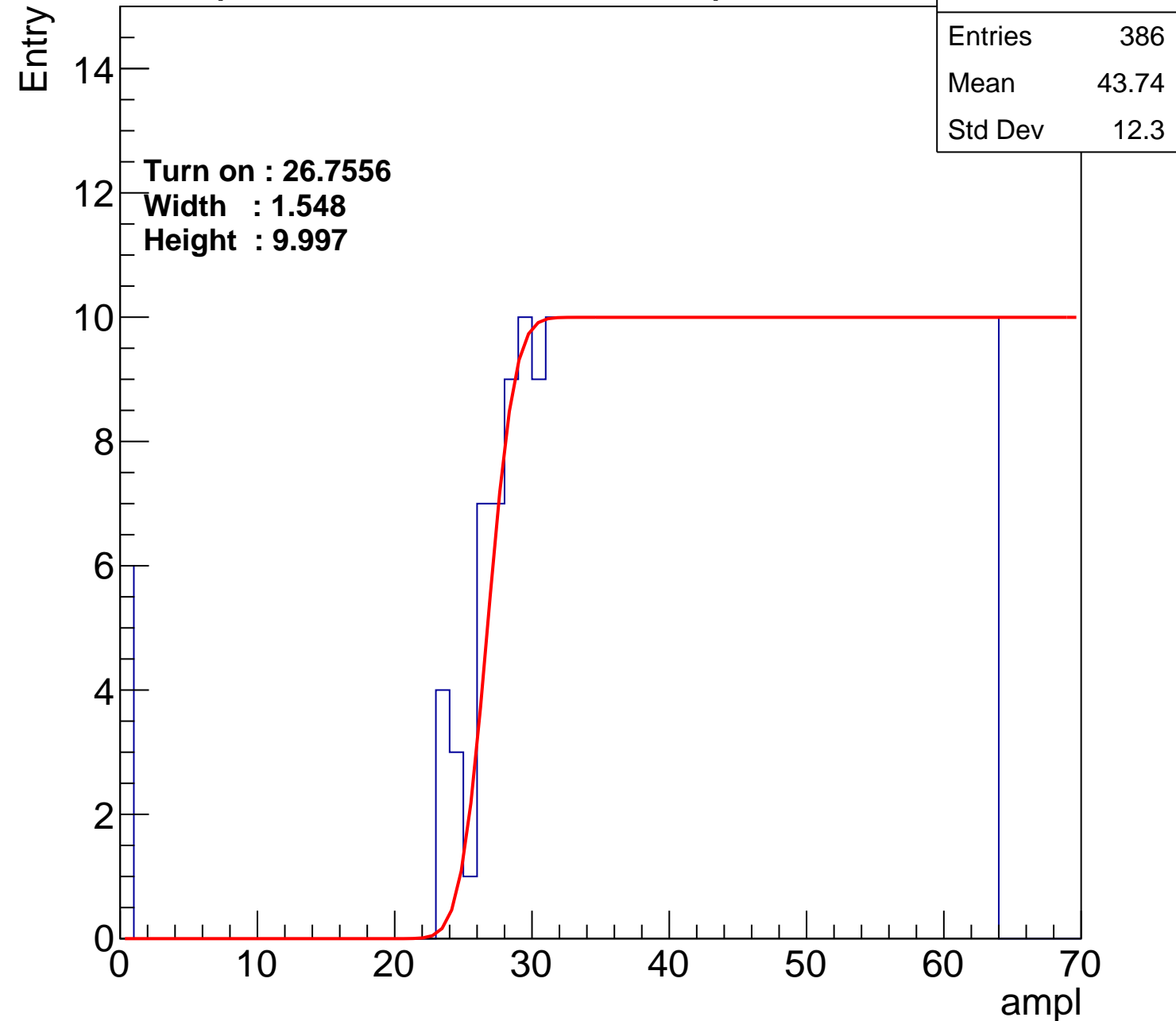
Width : 1.548

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch46

calib_packv5_042523_0143.root, FC#12, port B1

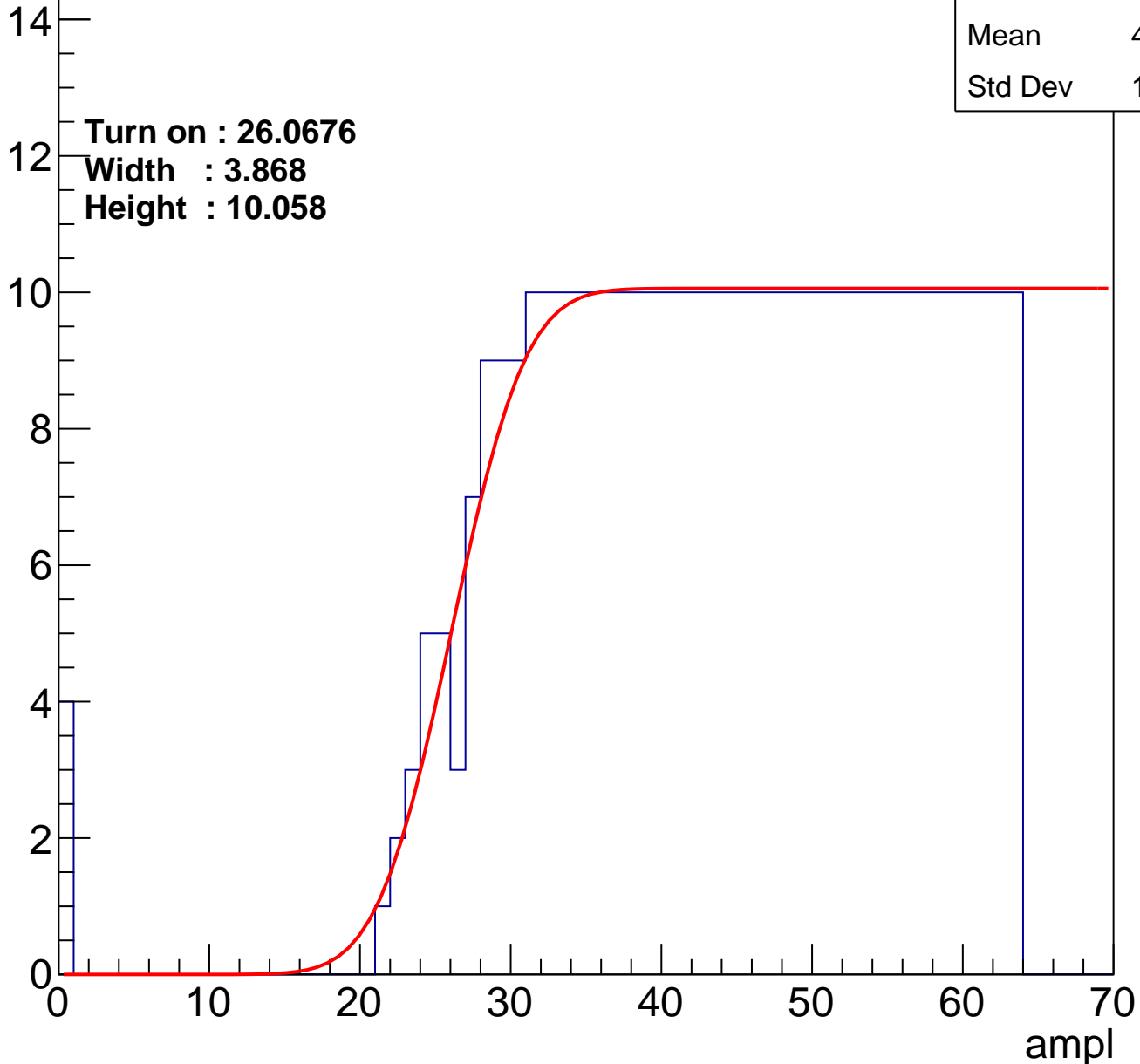
Entries	387
Mean	43.77
Std Dev	12.06

Turn on : 26.0676

Width : 3.868

Height : 10.058

Entry



B0L102S, U14-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.89
Std Dev	12.29

Turn on : 23.8972

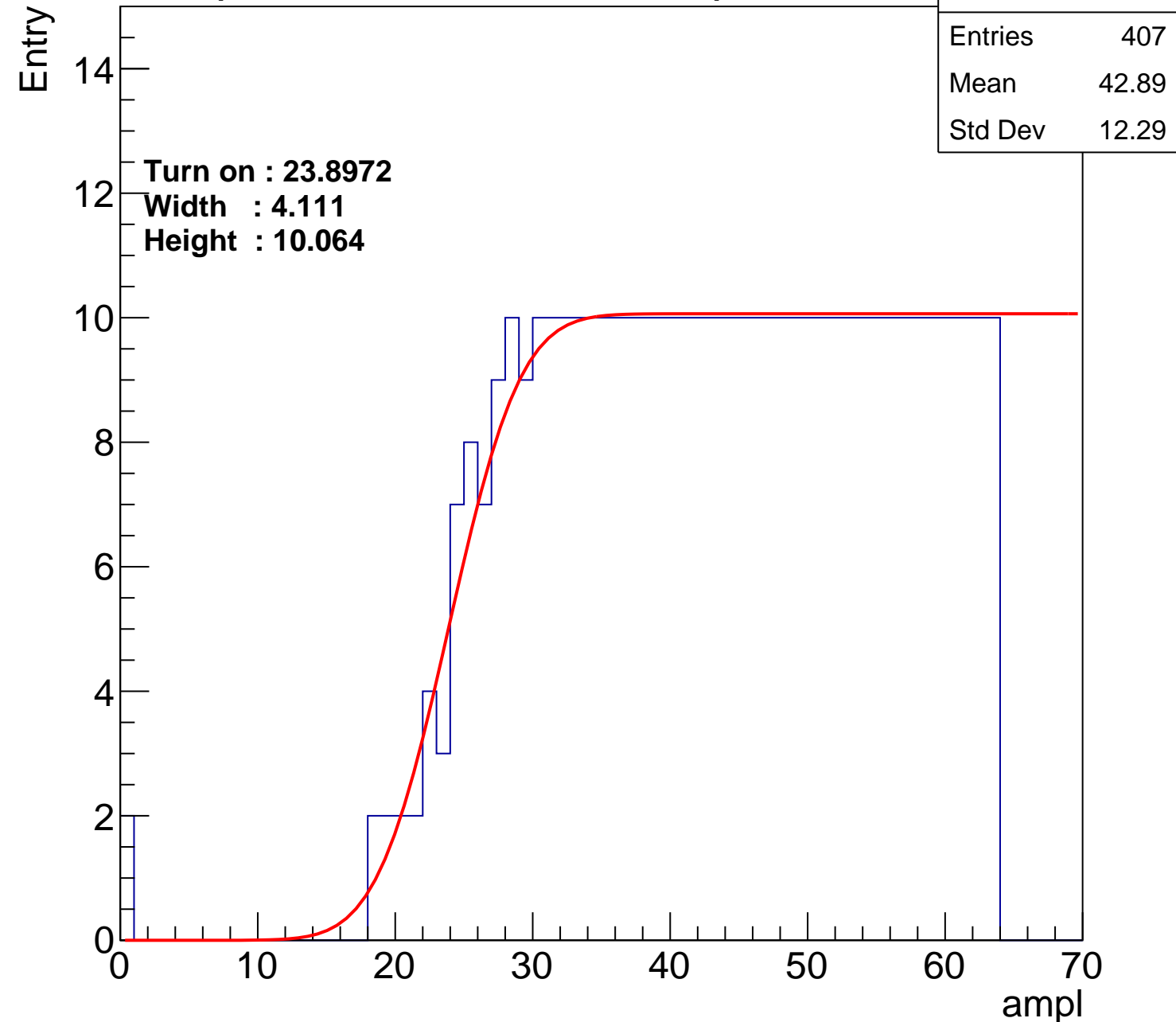
Width : 4.111

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	42.92
Std Dev	12.66

Turn on : 24.4810

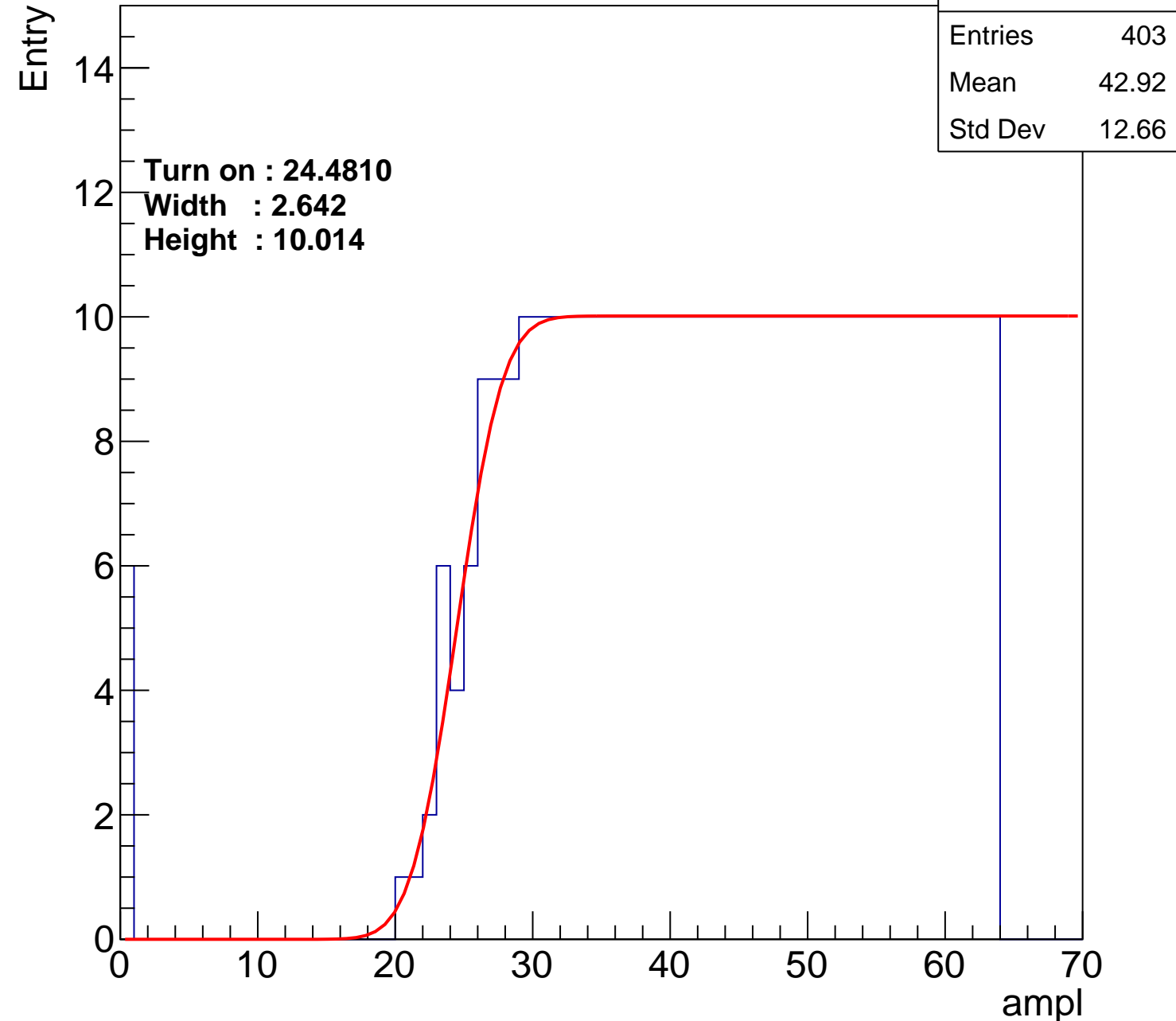
Width : 2.642

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.96
Std Dev	12.33

Turn on : 23.7832

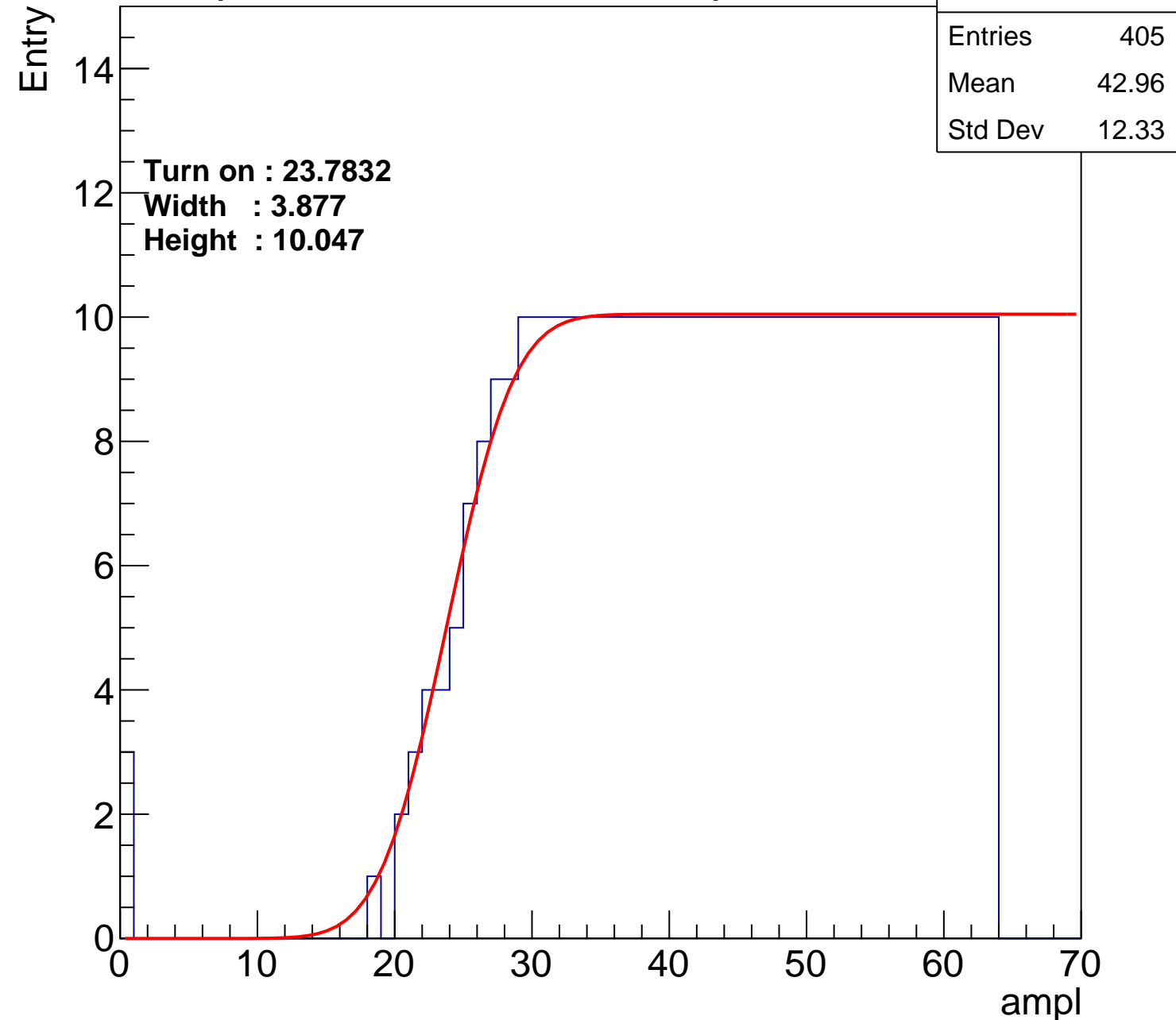
Width : 3.877

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch50

calib_packv5_042523_0143.root, FC#12, port B1

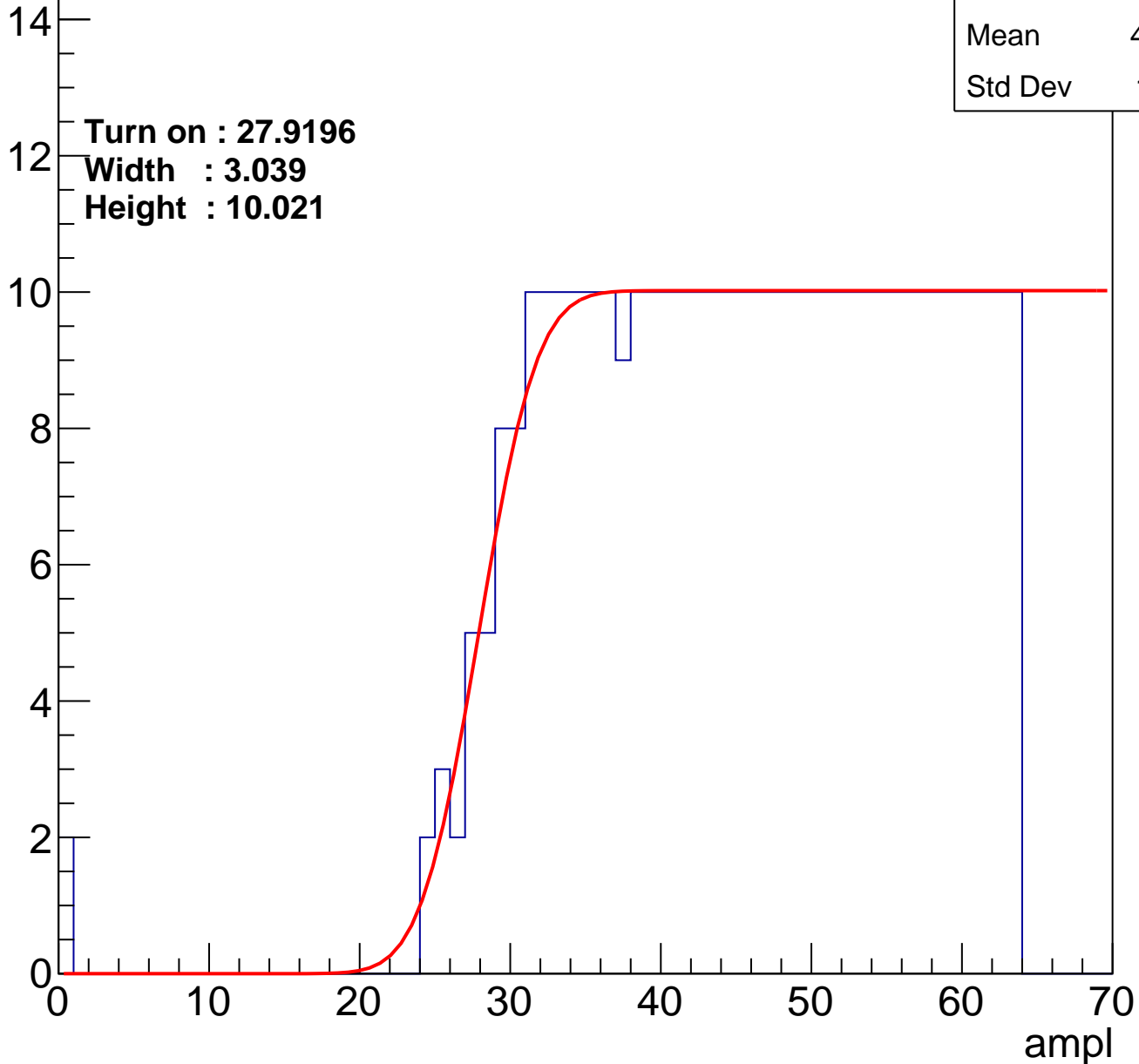
Entries	364
Mean	45.04
Std Dev	11.11

Turn on : 27.9196

Width : 3.039

Height : 10.021

Entry



B0L102S, U14-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.93
Std Dev	11.86

Turn on : 26.2112

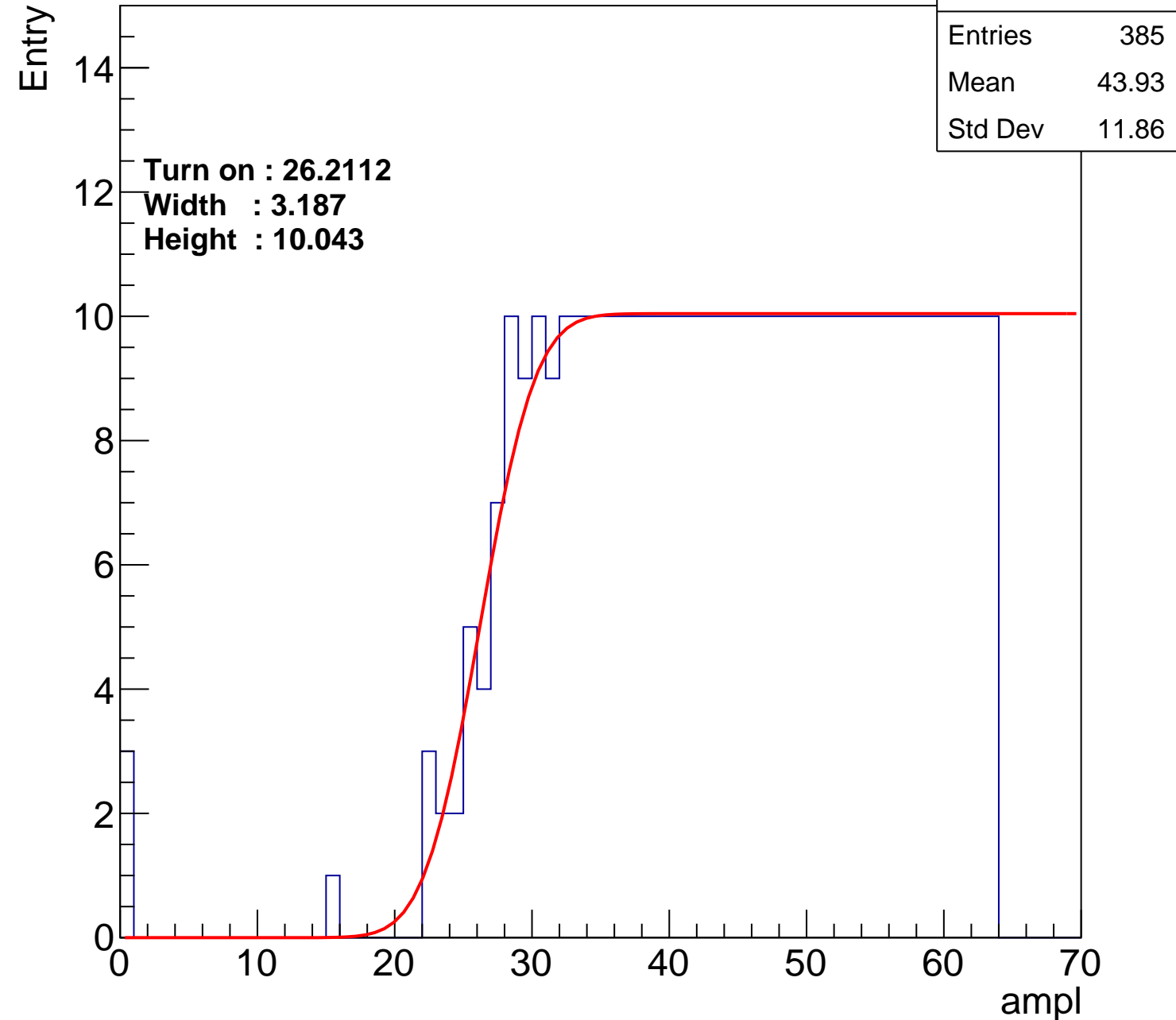
Width : 3.187

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.8009

Width : 3.114

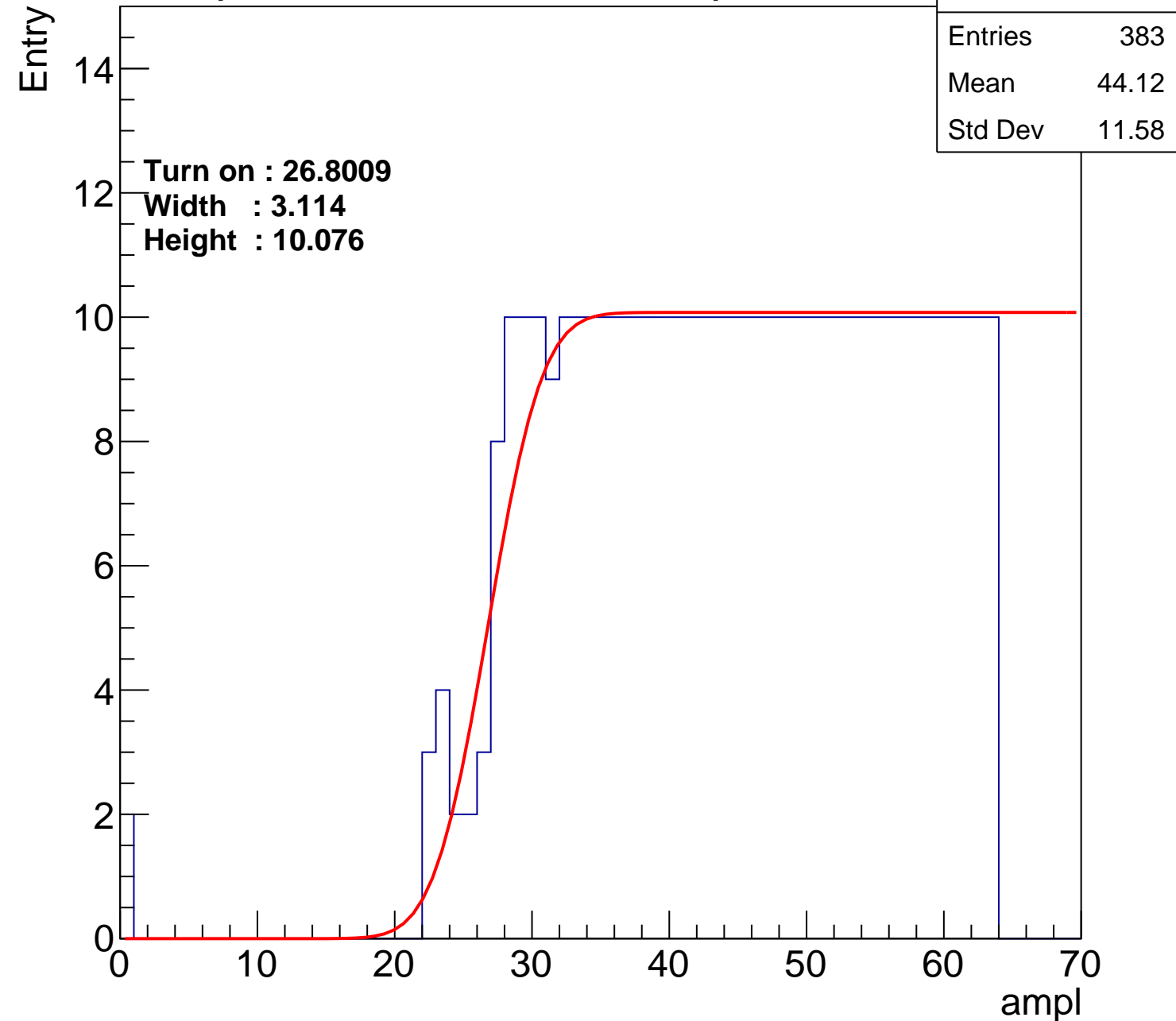
Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U14-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.19
Std Dev	12.18

Turn on : 24.0523

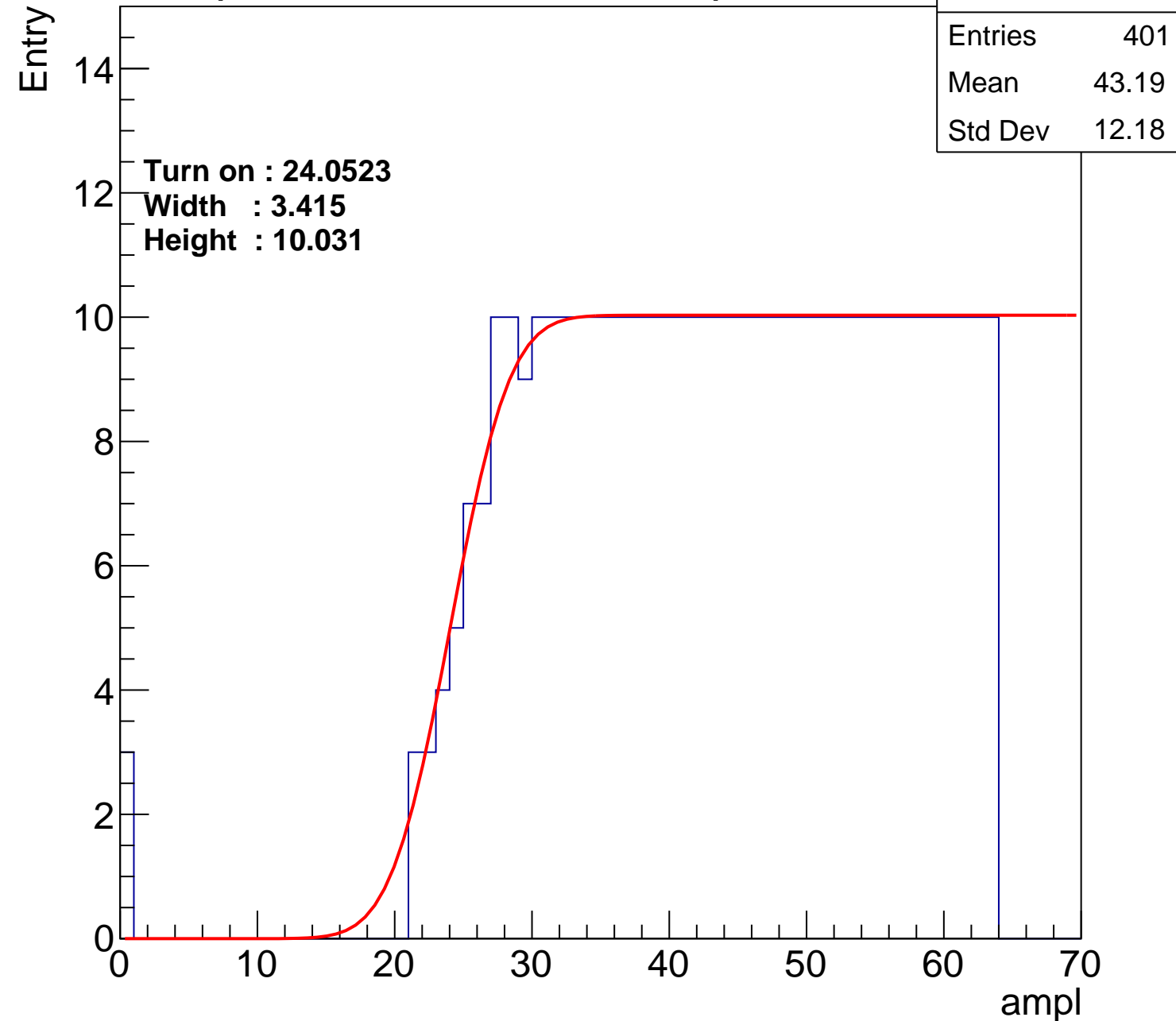
Width : 3.415

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.78
Std Dev	12.04

Turn on : 25.6319

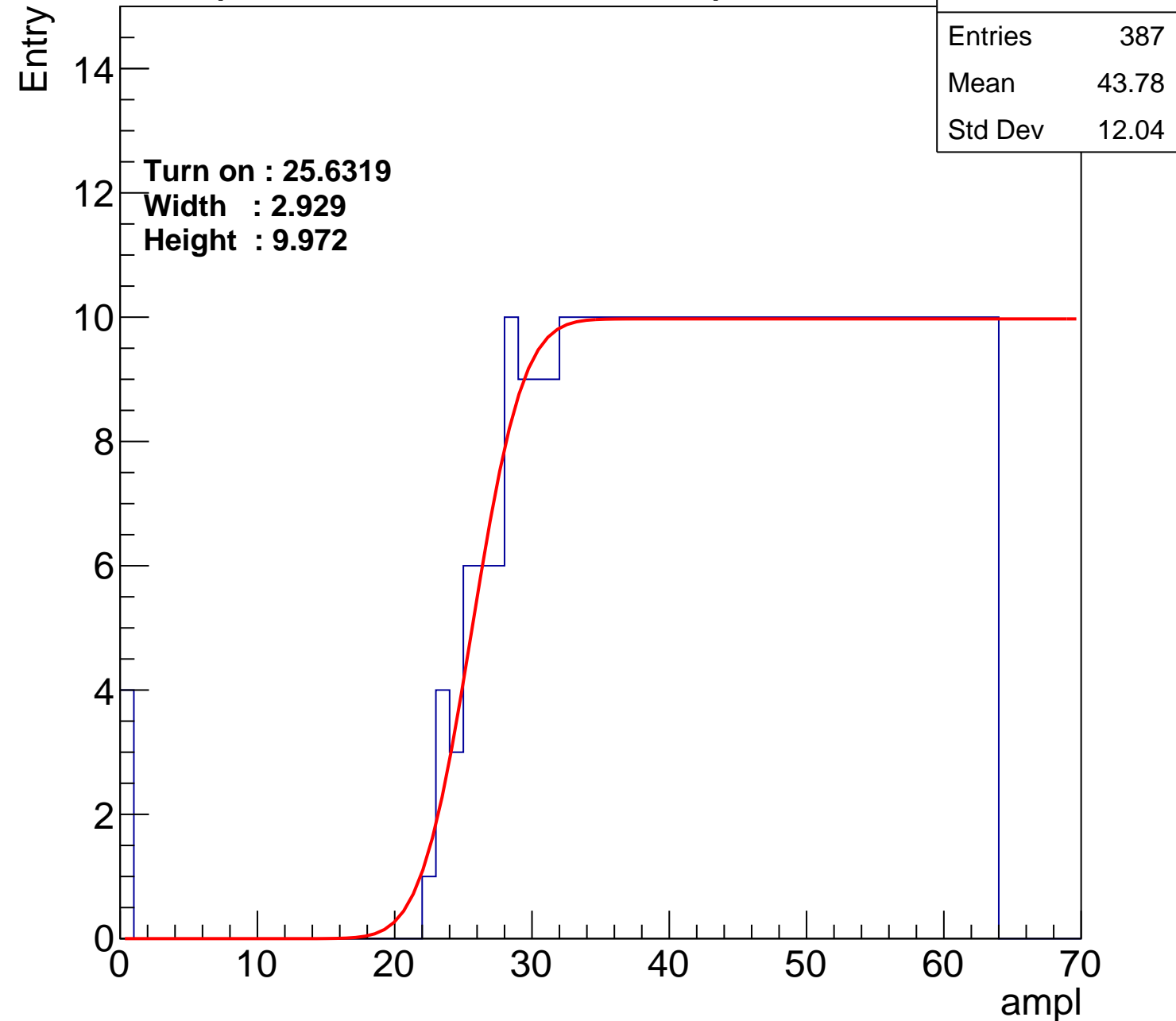
Width : 2.929

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.98
Std Dev	11.47

Turn on : 25.3508

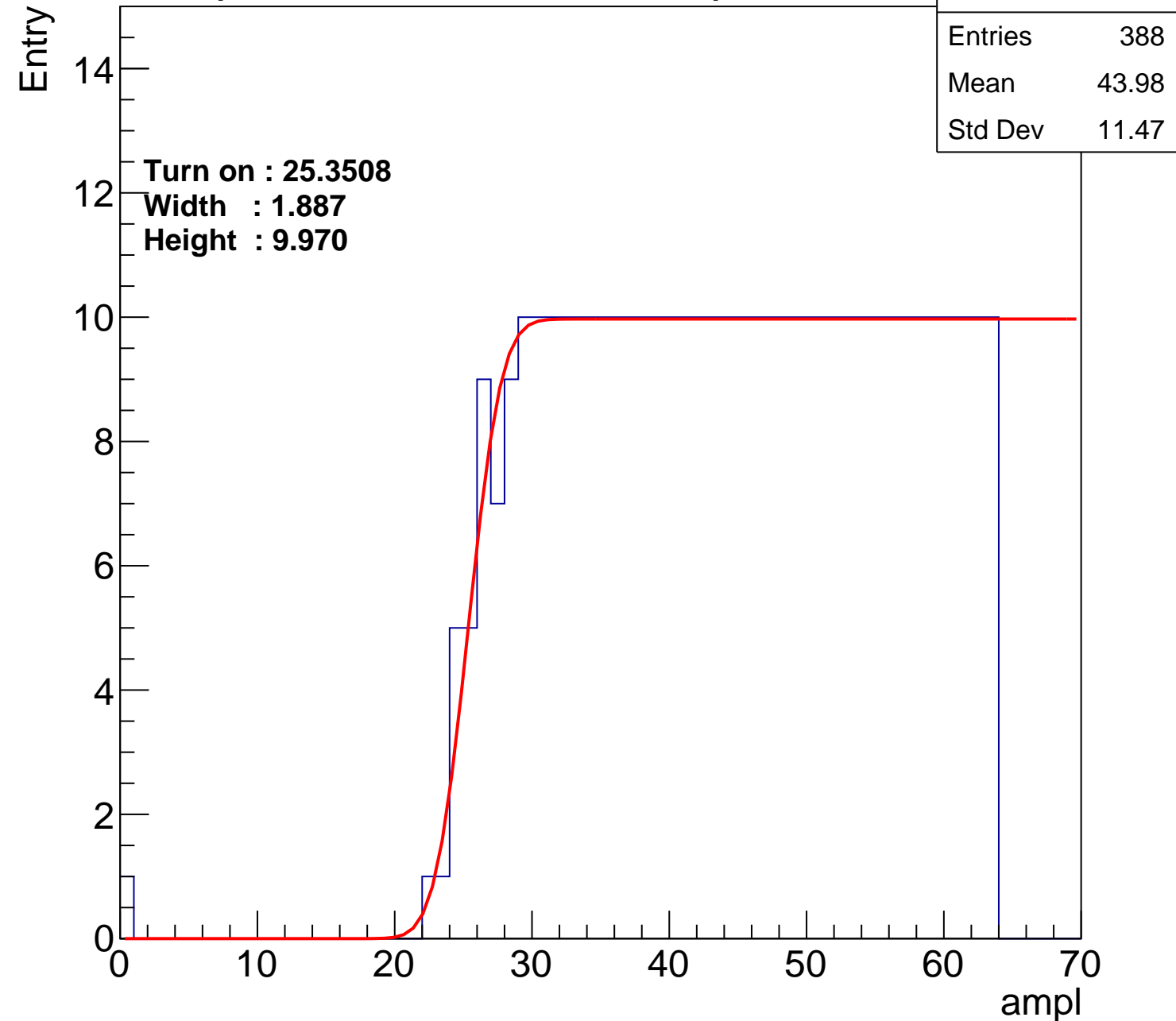
Width : 1.887

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch56

calib_packv5_042523_0143.root, FC#12, port B1

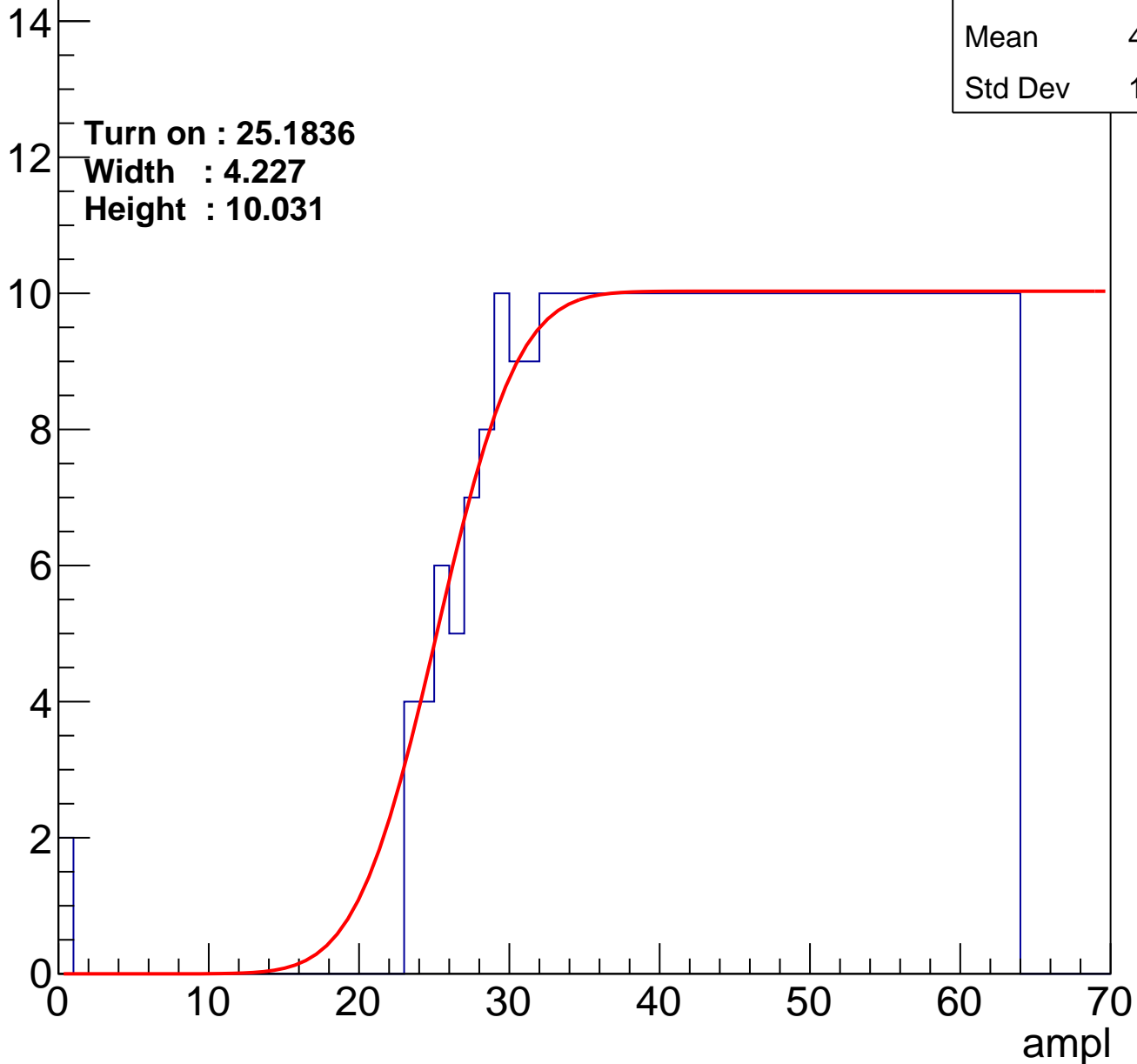
Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.1836

Width : 4.227

Height : 10.031

Entry



B0L102S, U14-ch57

calib_packv5_042523_0143.root, FC#12, port B1

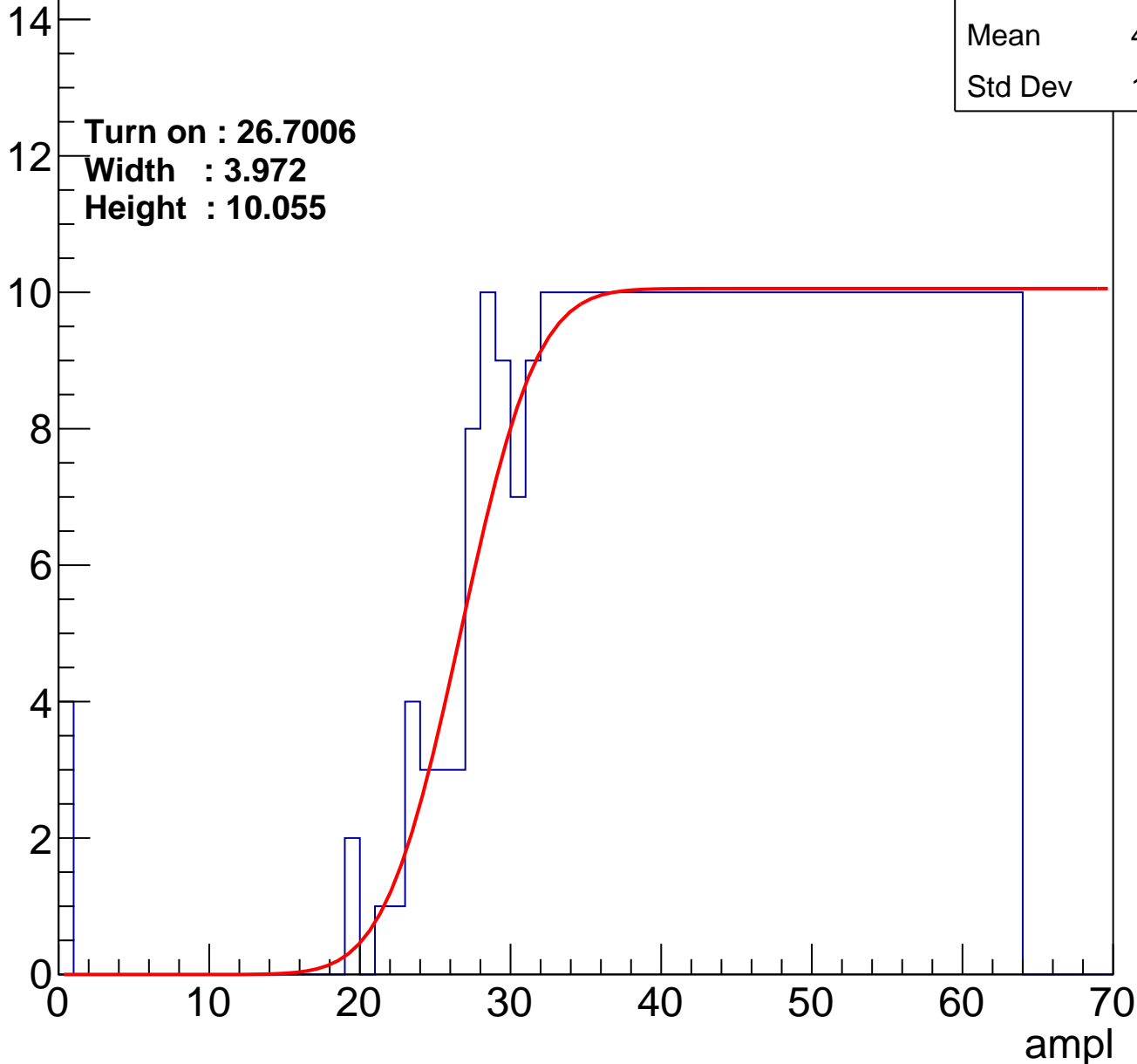
Entries	384
Mean	43.86
Std Dev	12.08

Turn on : 26.7006

Width : 3.972

Height : 10.055

Entry



B0L102S, U14-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.8215

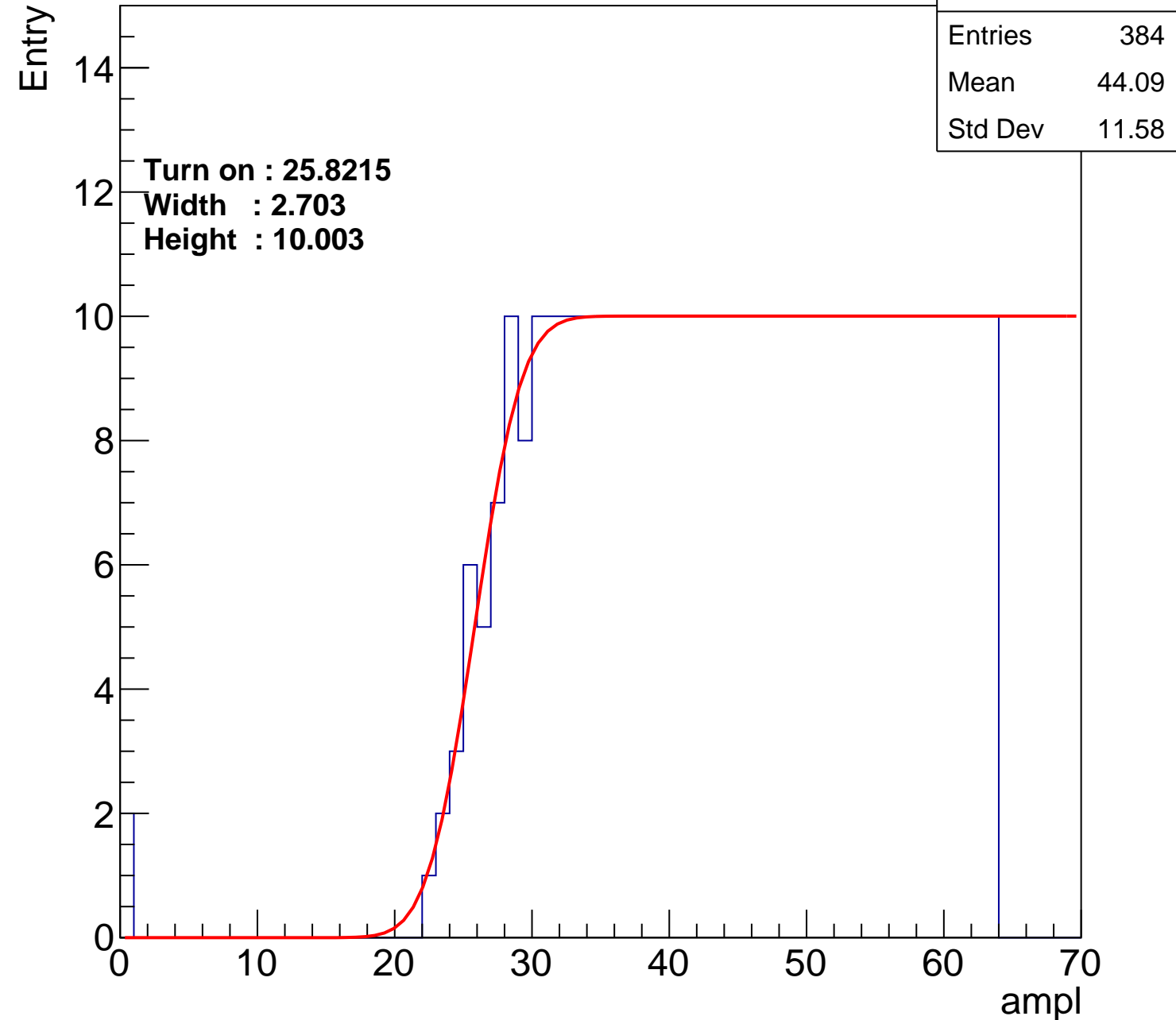
Width : 2.703

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.49
Std Dev	11.47

Turn on : 26.6831

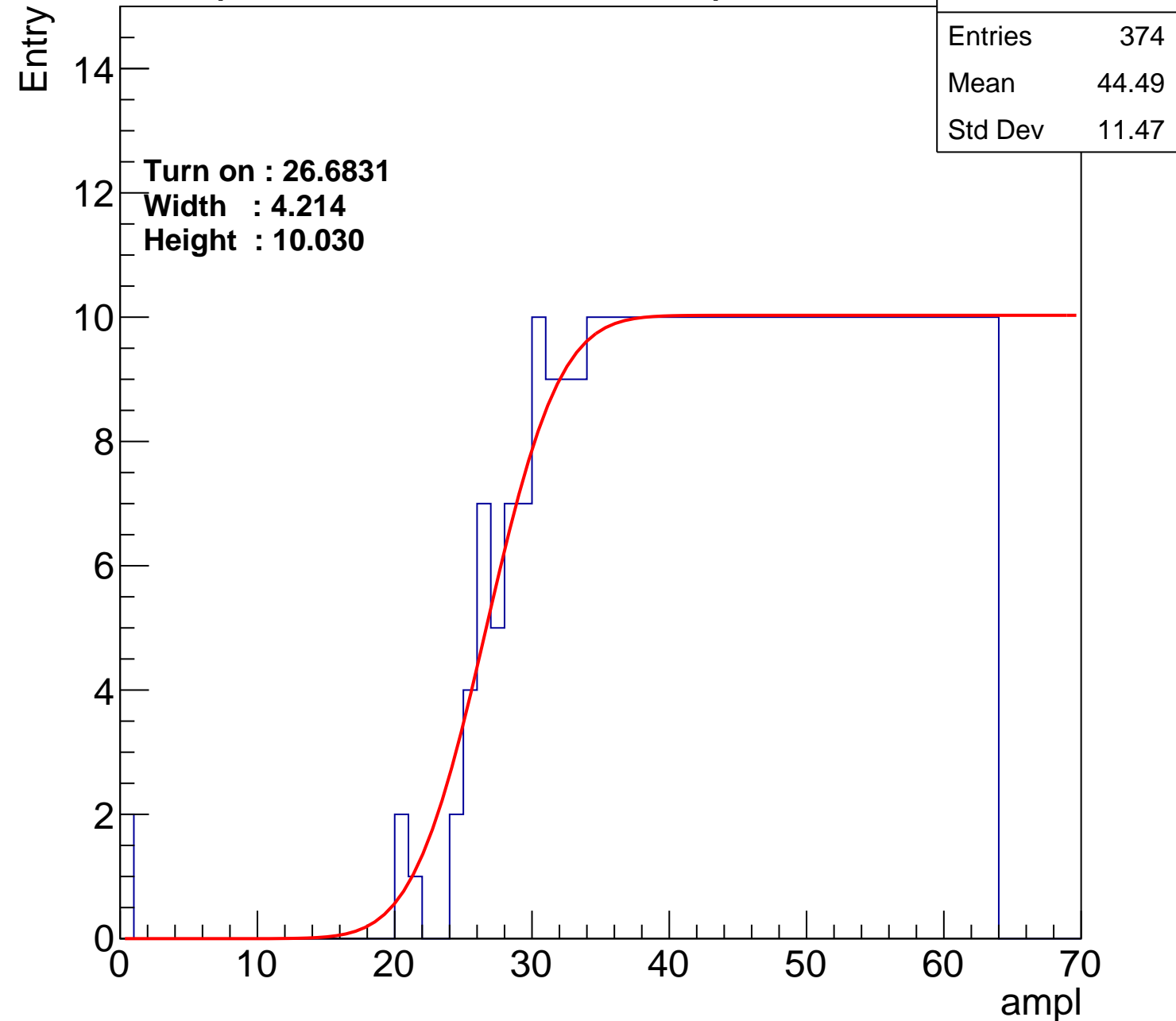
Width : 4.214

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.32
Std Dev	11.97

Turn on : 24.5514

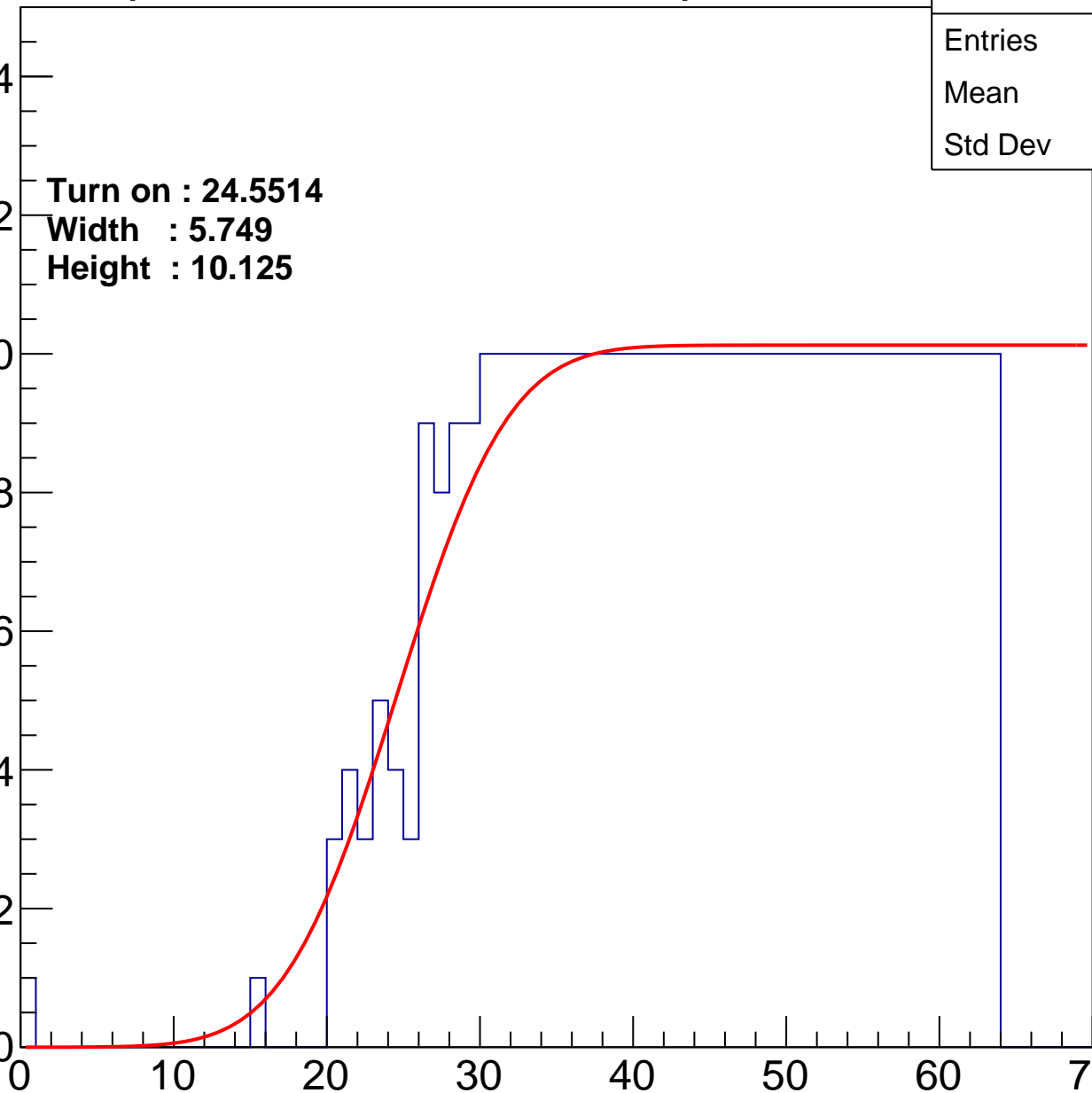
Width : 5.749

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 25.9359

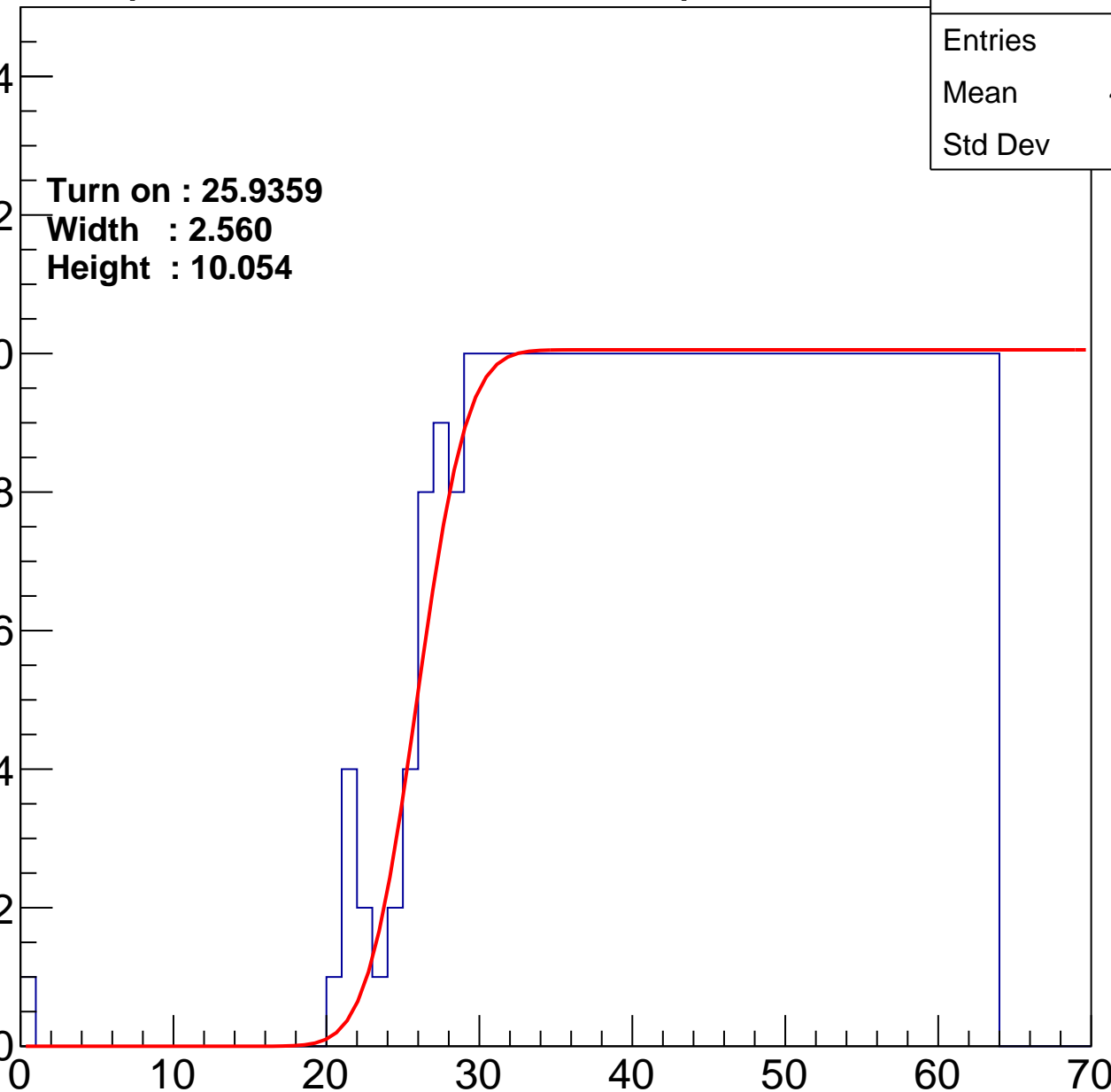
Width : 2.560

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.27
Std Dev	11.56

Turn on : 26.5793

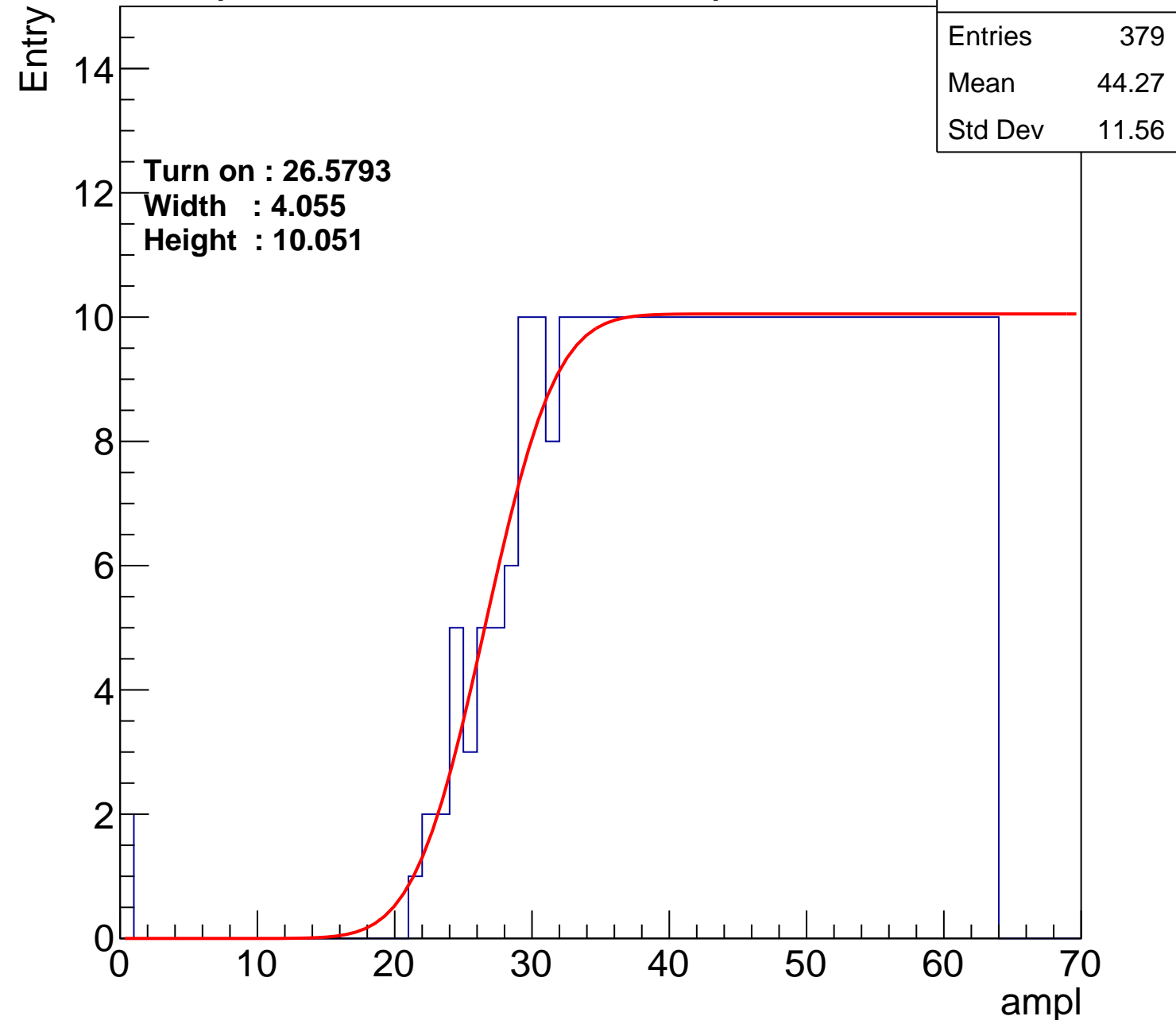
Width : 4.055

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 26.0021

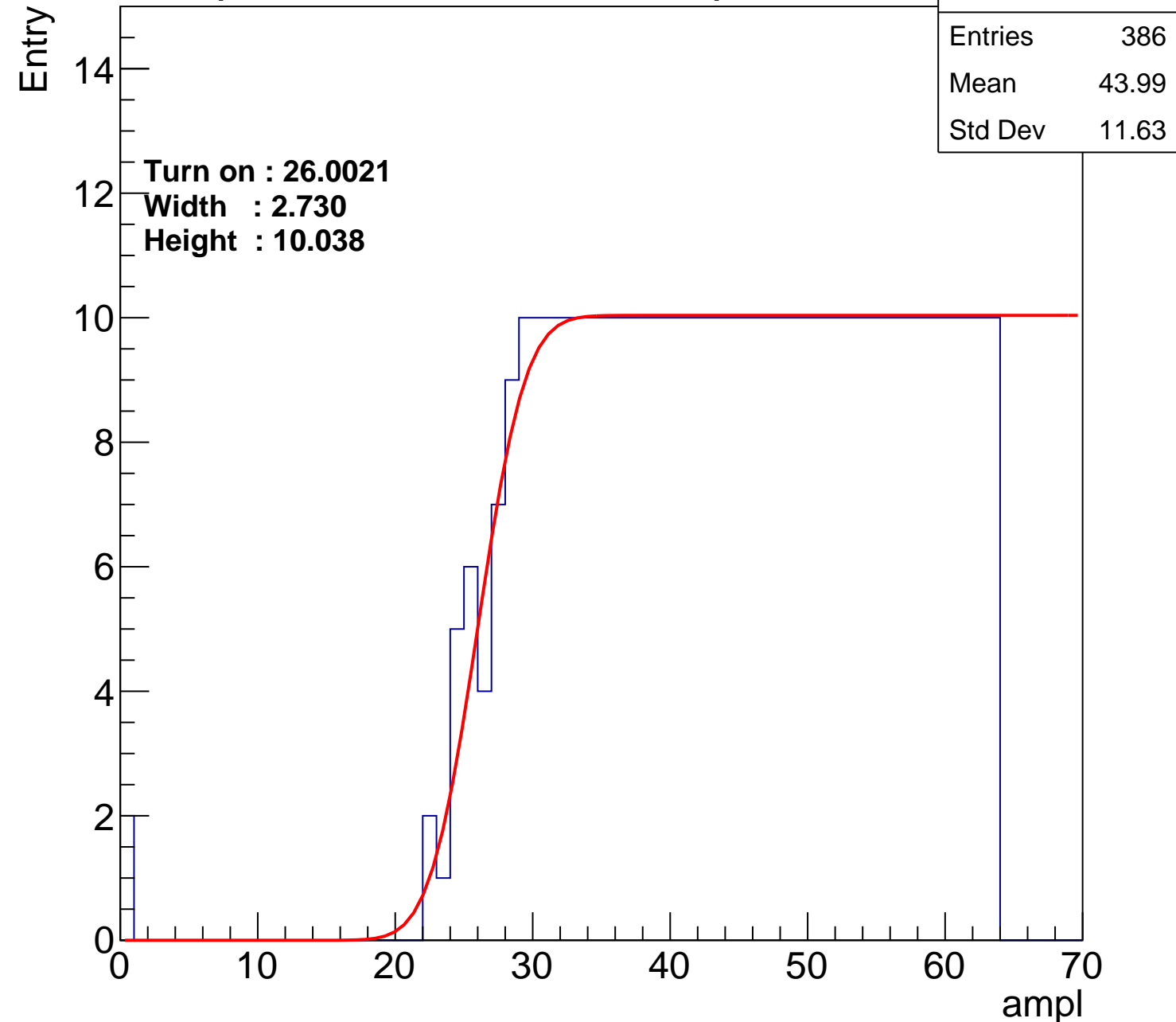
Width : 2.730

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch64

calib_packv5_042523_0143.root, FC#12, port B1

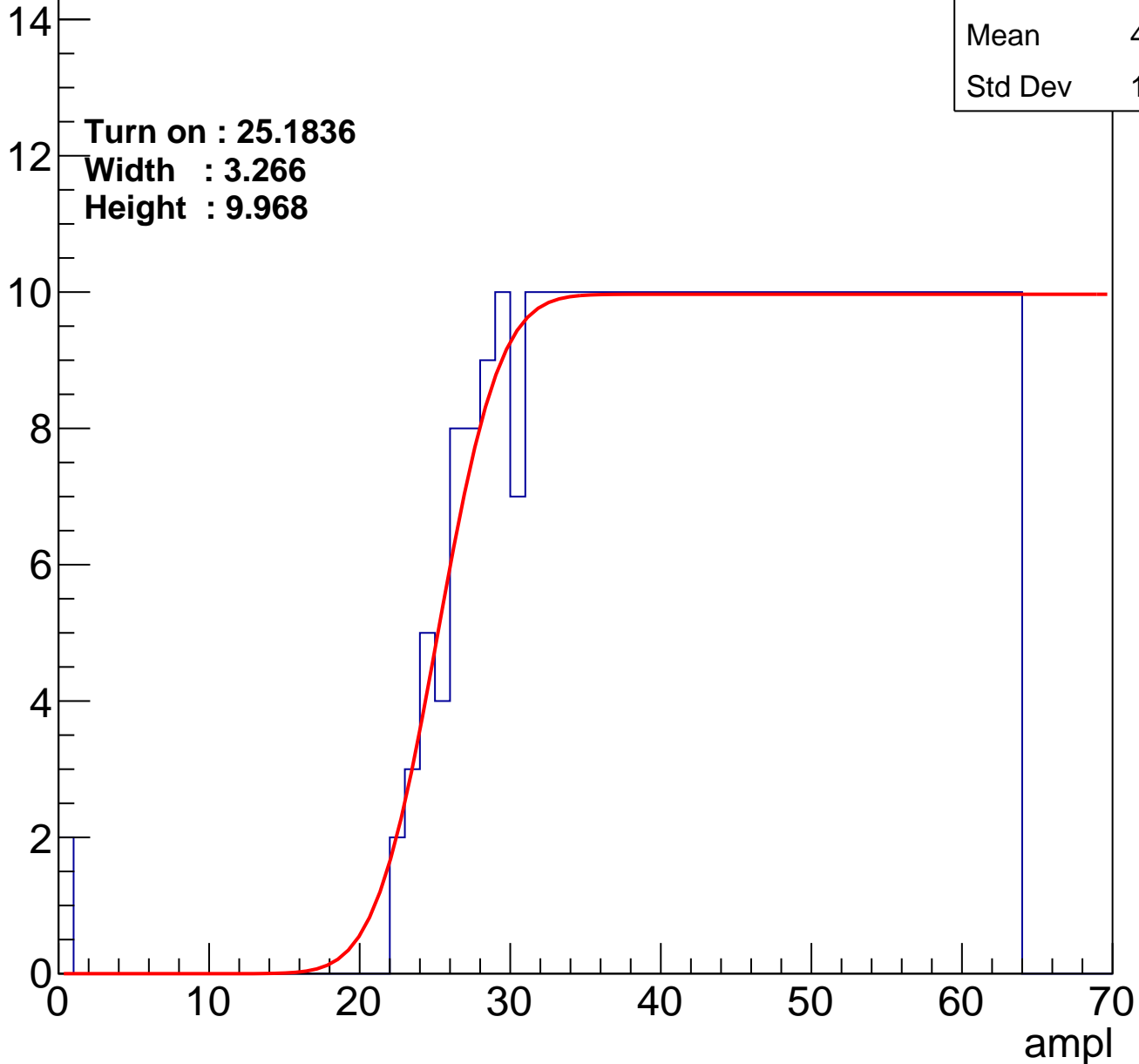
Entries	388
Mean	43.86
Std Dev	11.72

Turn on : 25.1836

Width : 3.266

Height : 9.968

Entry



B0L102S, U14-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.15
Std Dev	12.35

Turn on : 24.4945

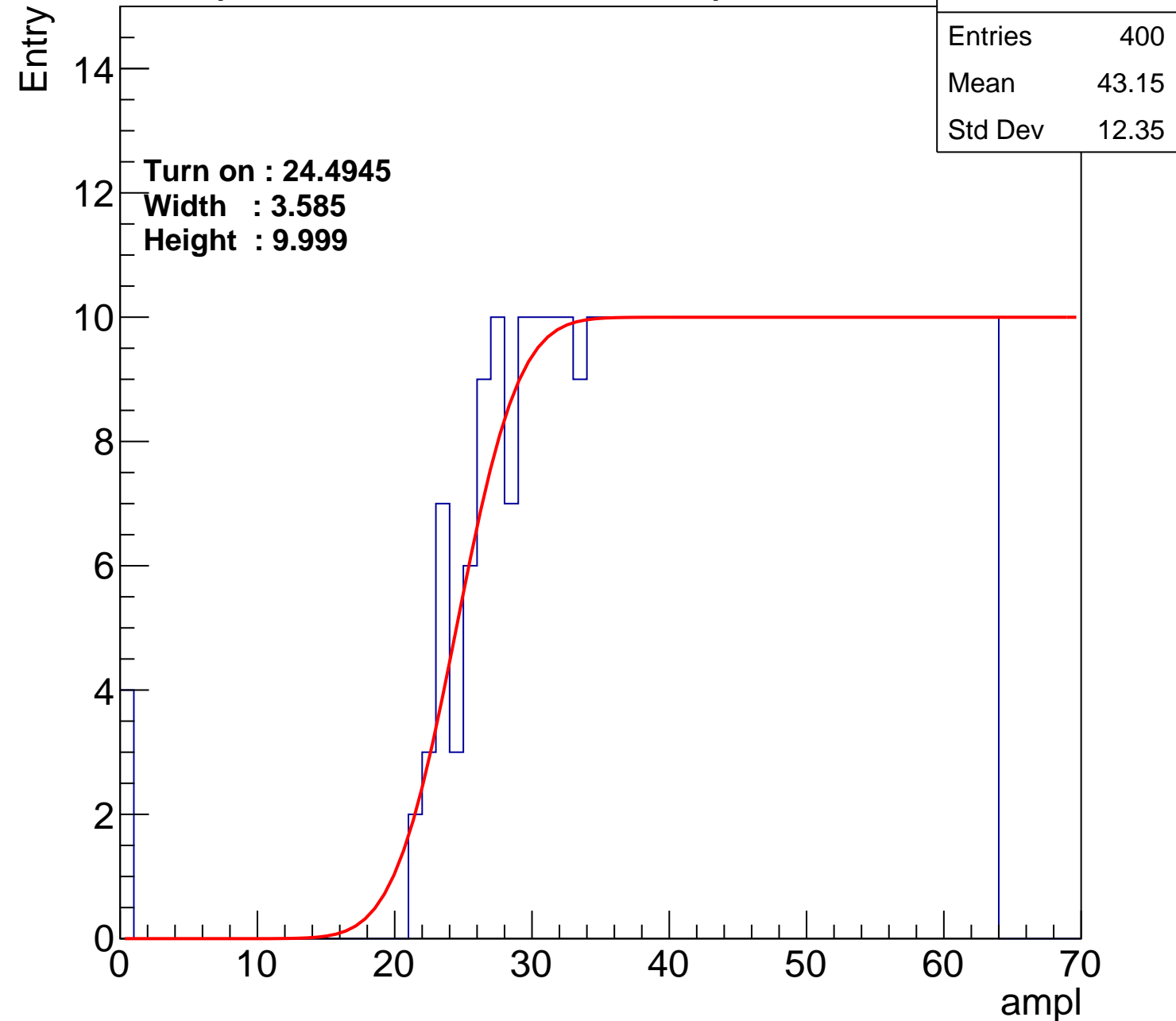
Width : 3.585

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.25
Std Dev	12.24

Turn on : 24.4994

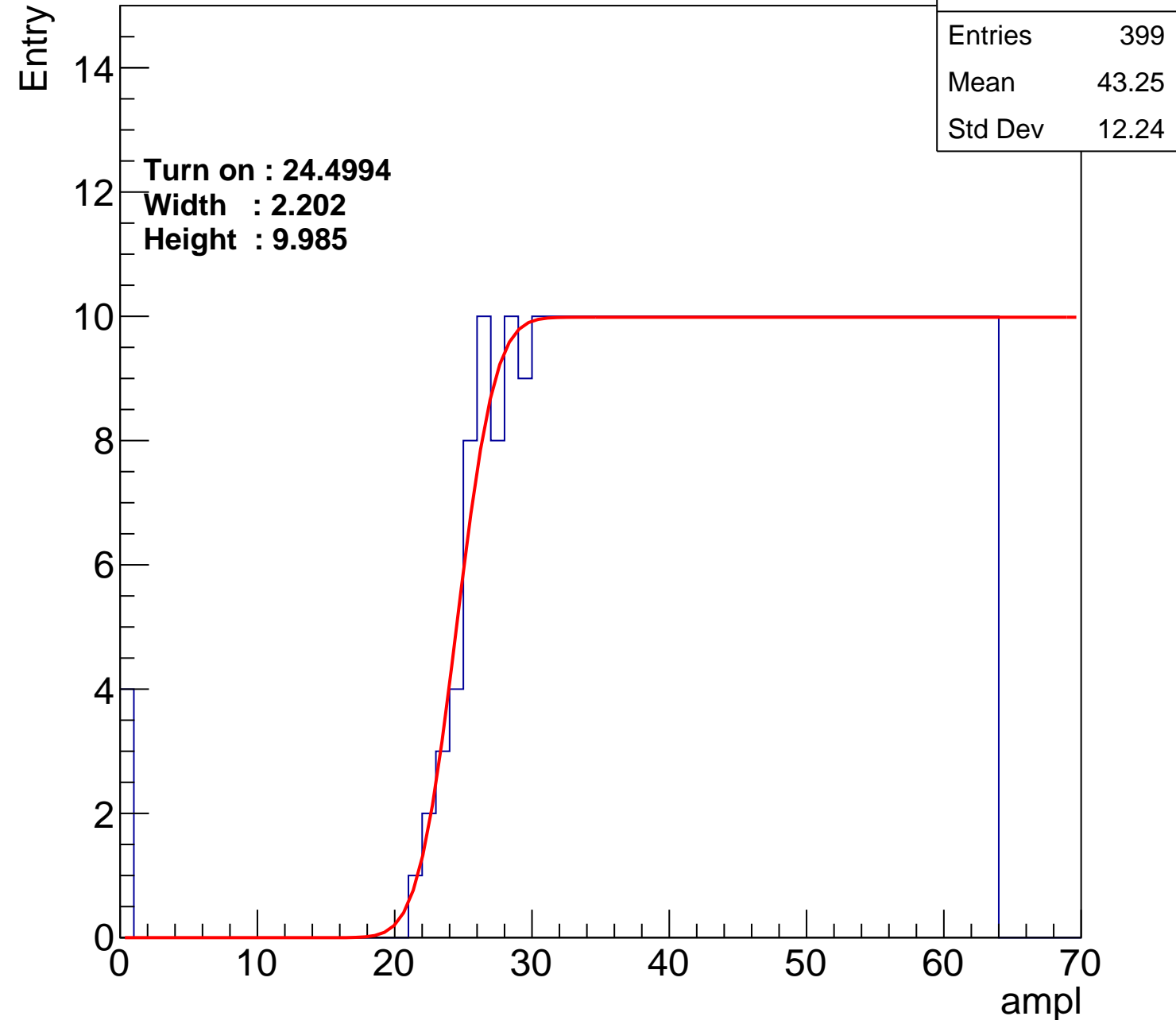
Width : 2.202

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 26.2423

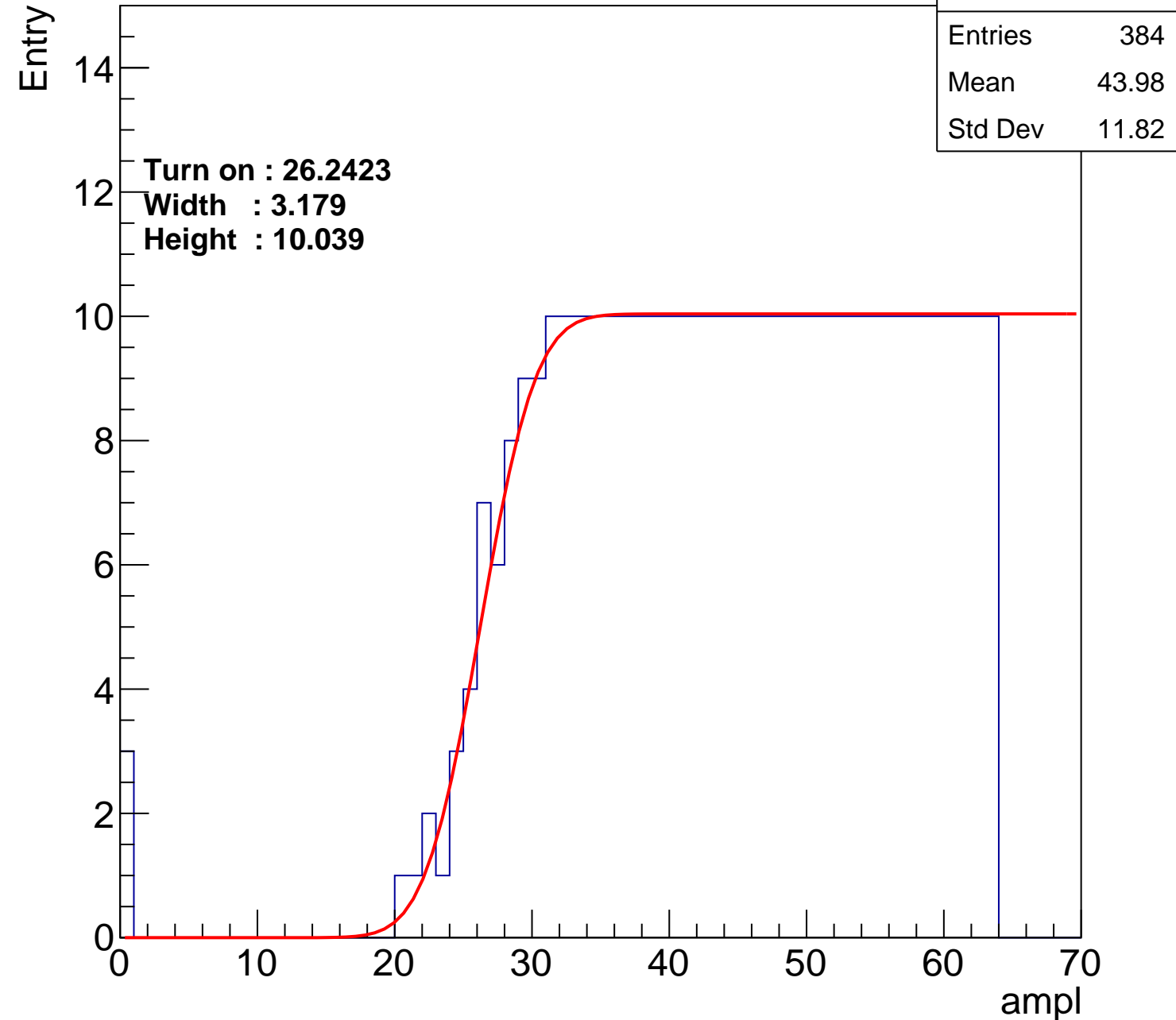
Width : 3.179

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.3
Std Dev	12.27

Turn on : 24.8603

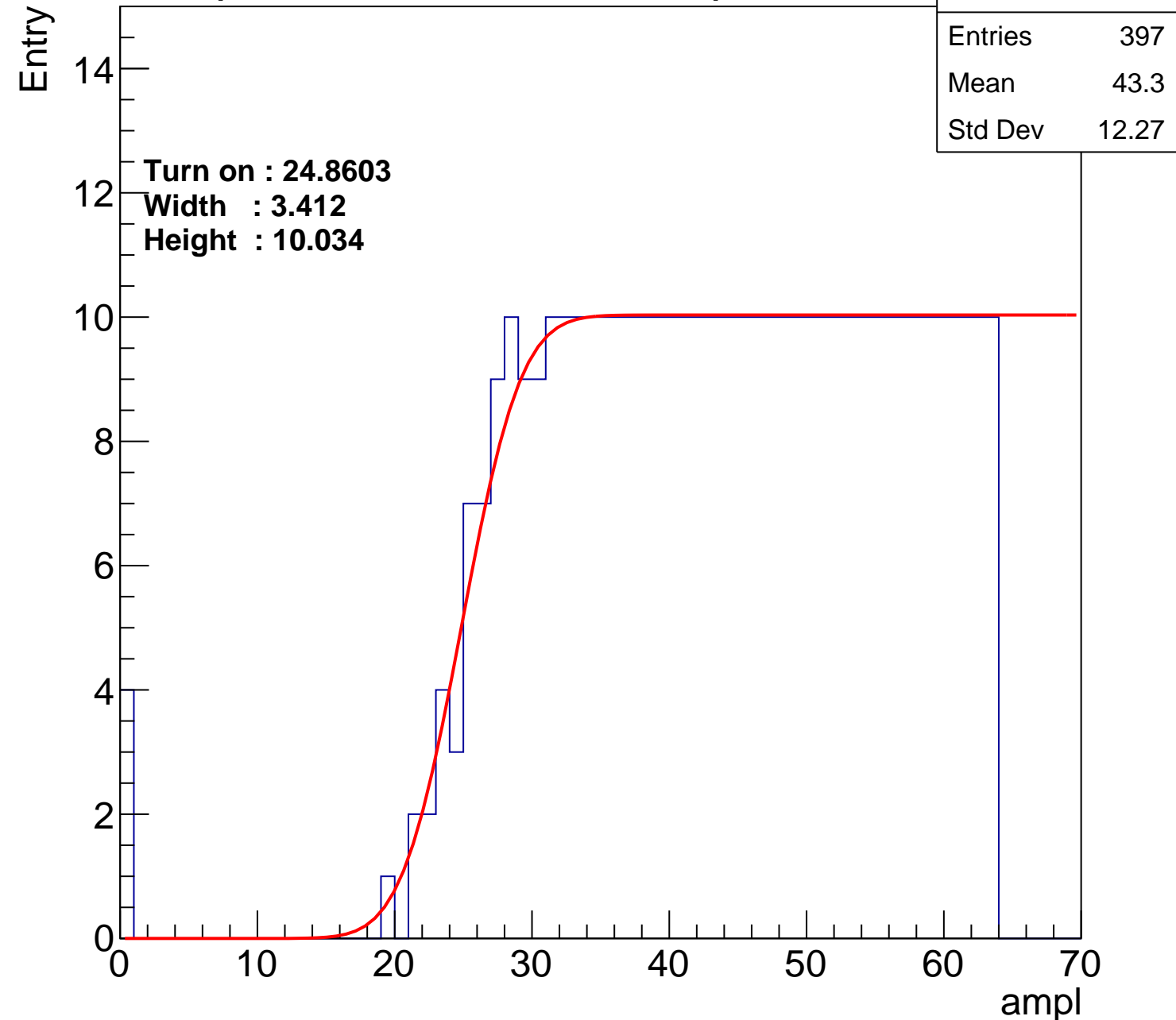
Width : 3.412

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch69

calib_packv5_042523_0143.root, FC#12, port B1

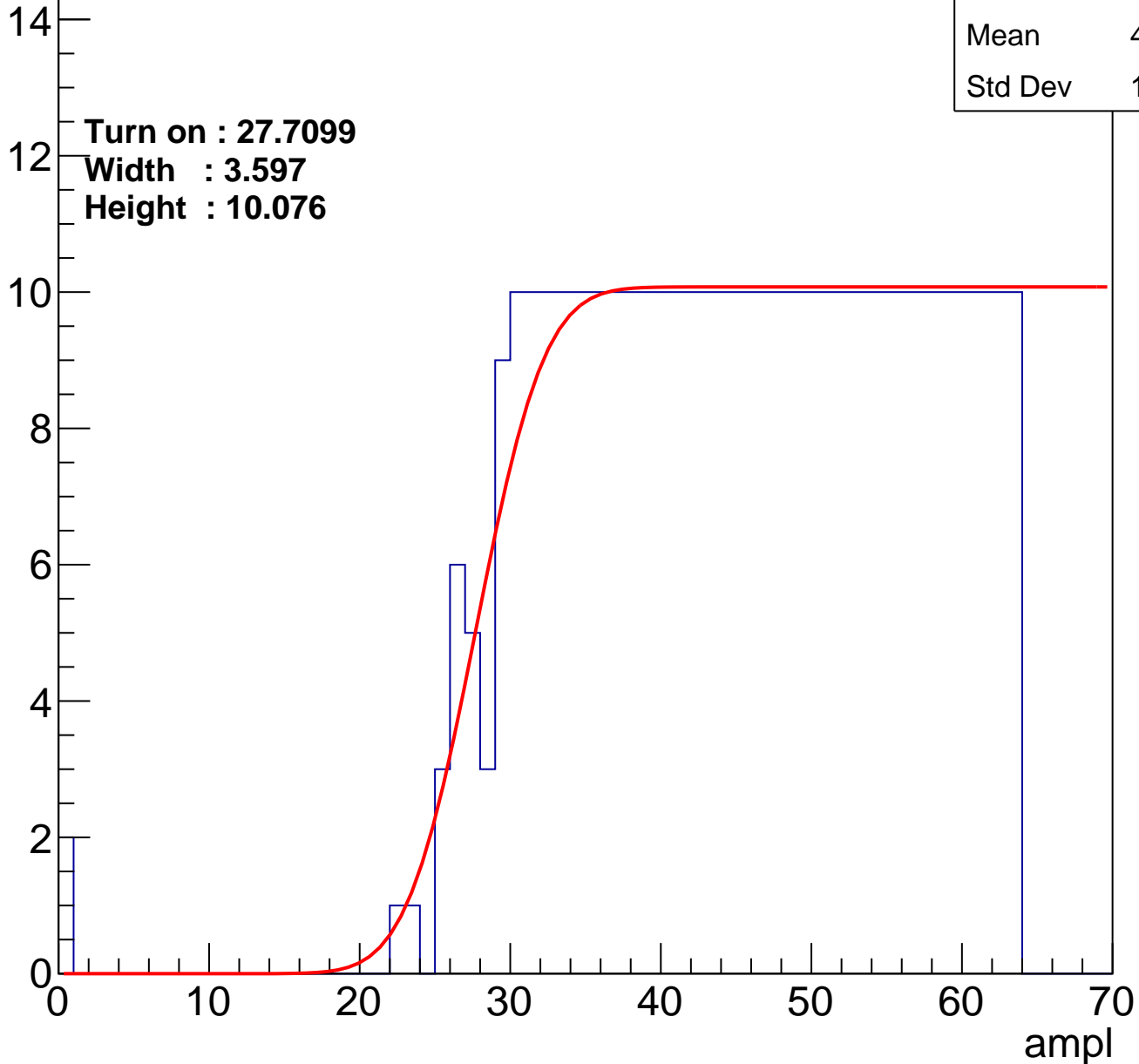
Entries	370
Mean	44.77
Std Dev	11.23

Turn on : 27.7099

Width : 3.597

Height : 10.076

Entry



B0L102S, U14-ch70

calib_packv5_042523_0143.root, FC#12, port B1

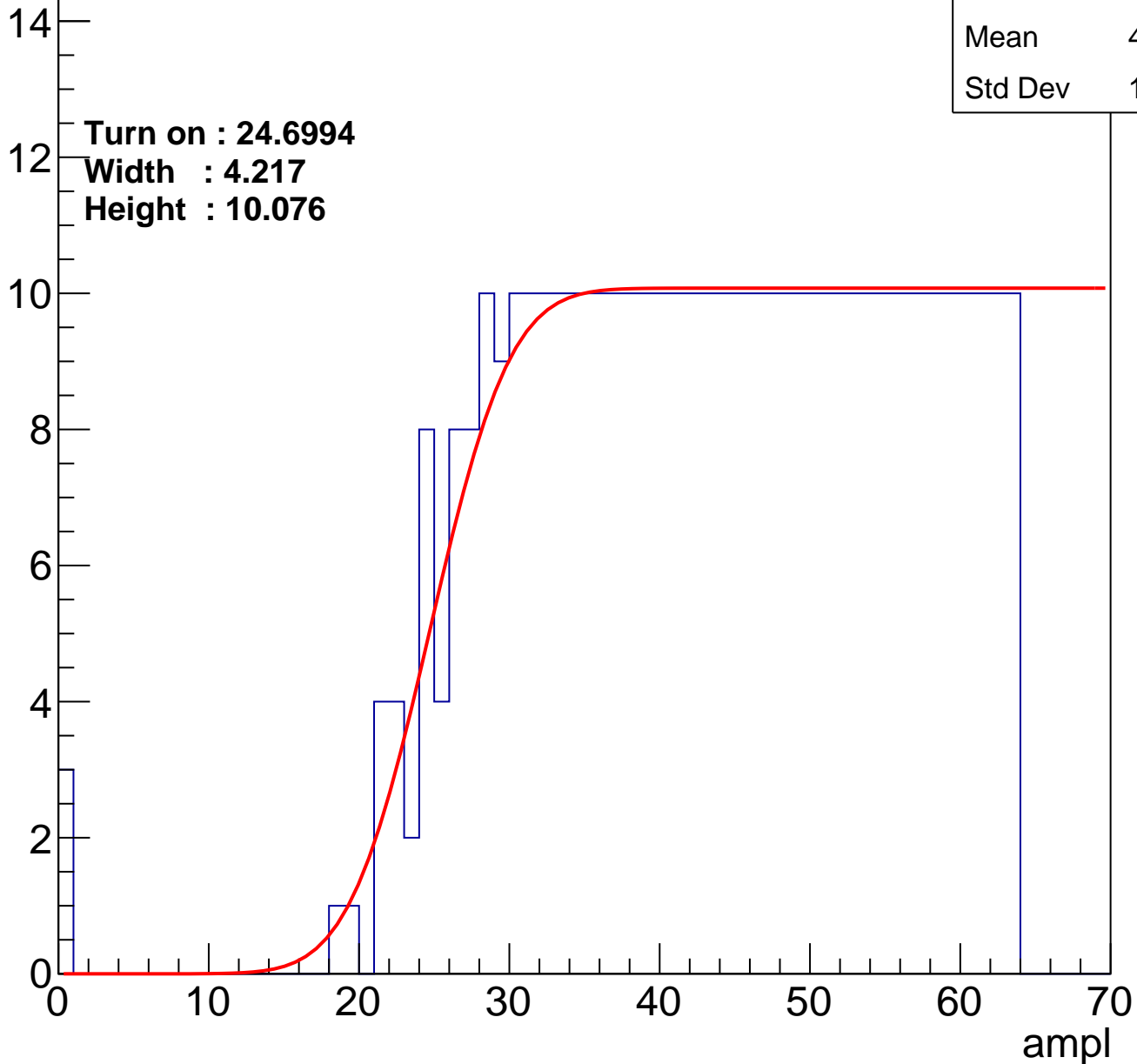
Entries	402
Mean	43.09
Std Dev	12.29

Turn on : 24.6994

Width : 4.217

Height : 10.076

Entry



B0L102S, U14-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.84
Std Dev	12.75

Turn on : 24.3051

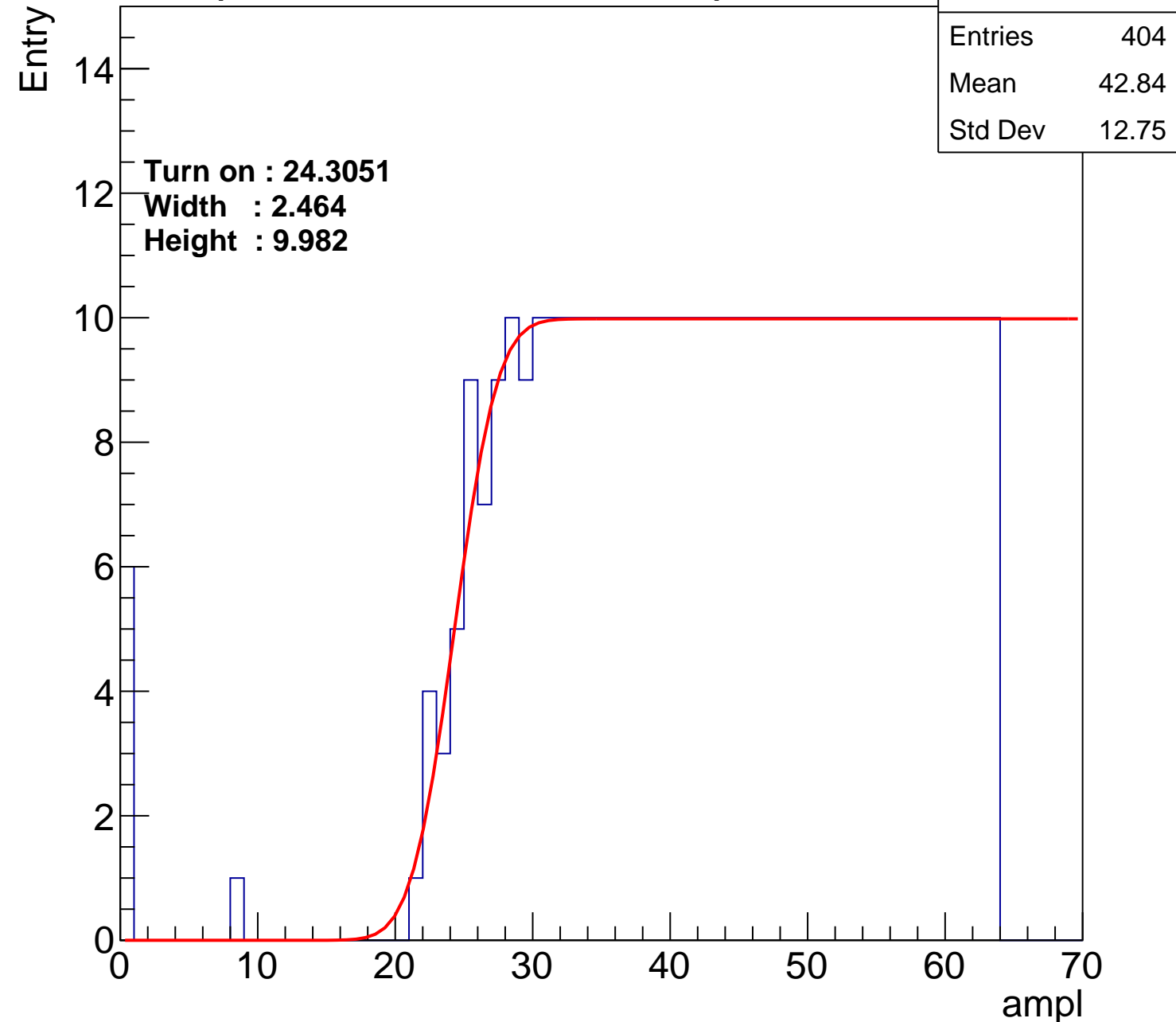
Width : 2.464

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	419
Mean	42.32
Std Dev	12.56

Turn on : 22.4120

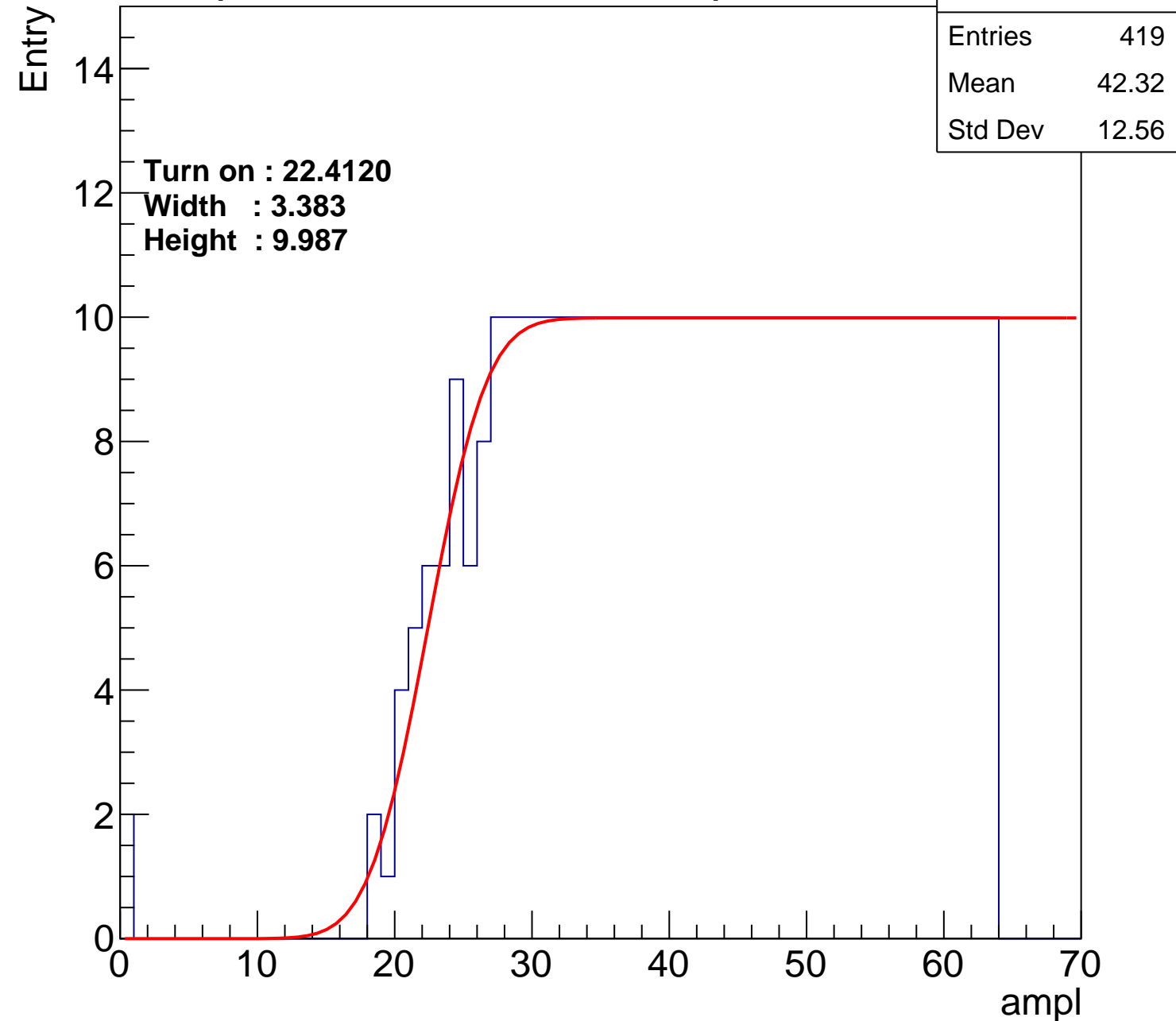
Width : 3.383

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.05
Std Dev	11.71

Turn on : 26.6850

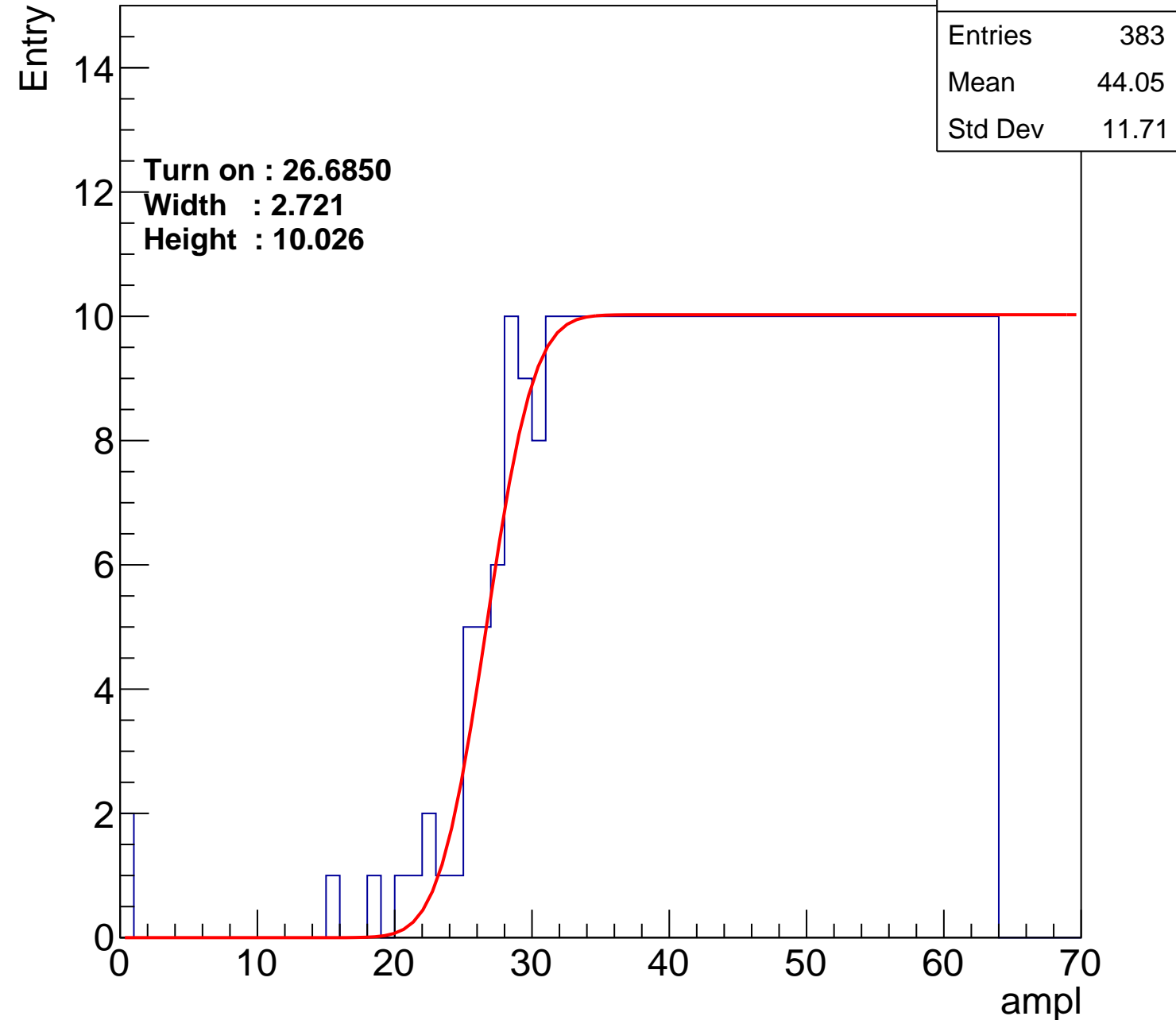
Width : 2.721

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.6706

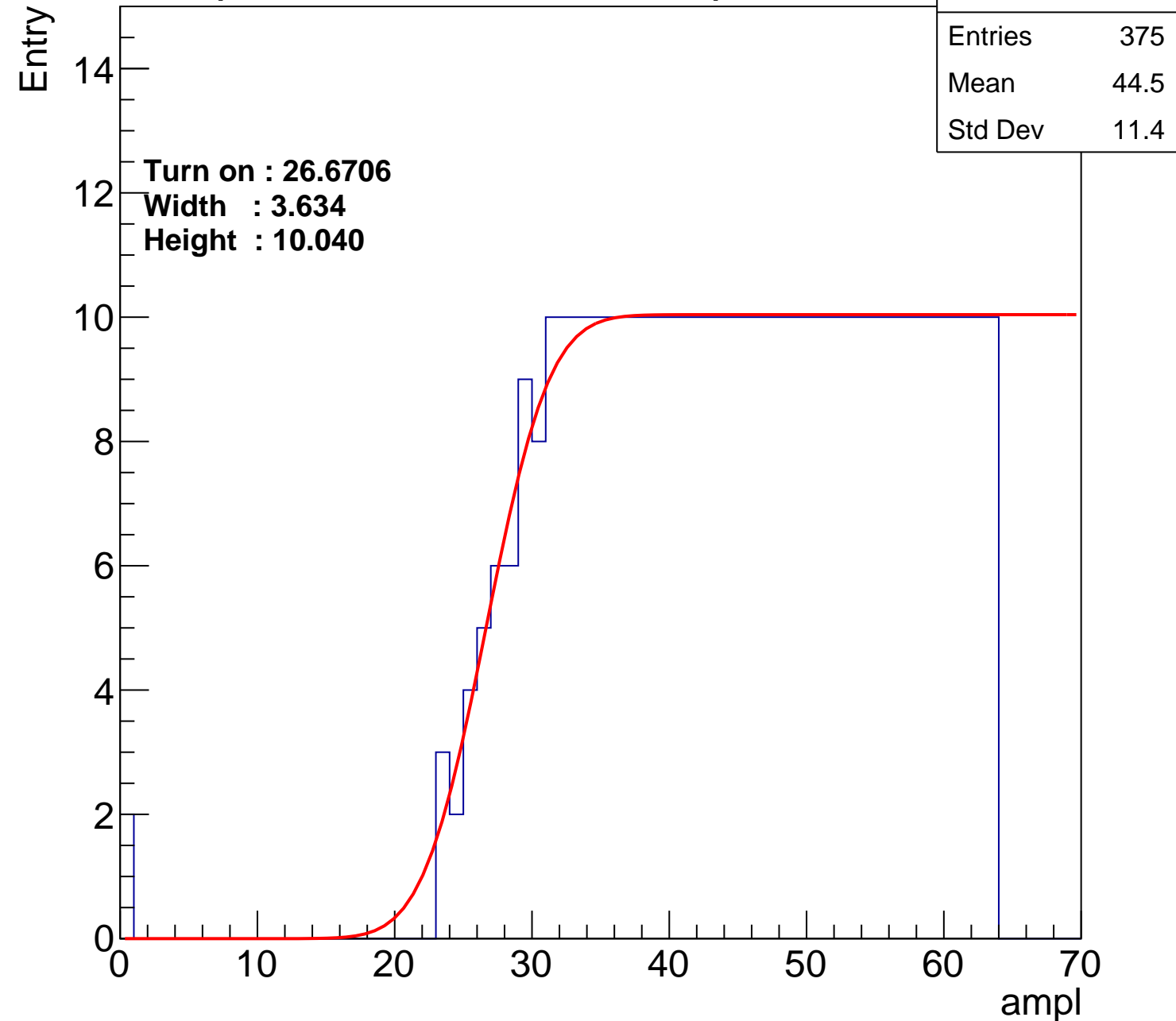
Width : 3.634

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.05
Std Dev	11.95

Turn on : 26.8159

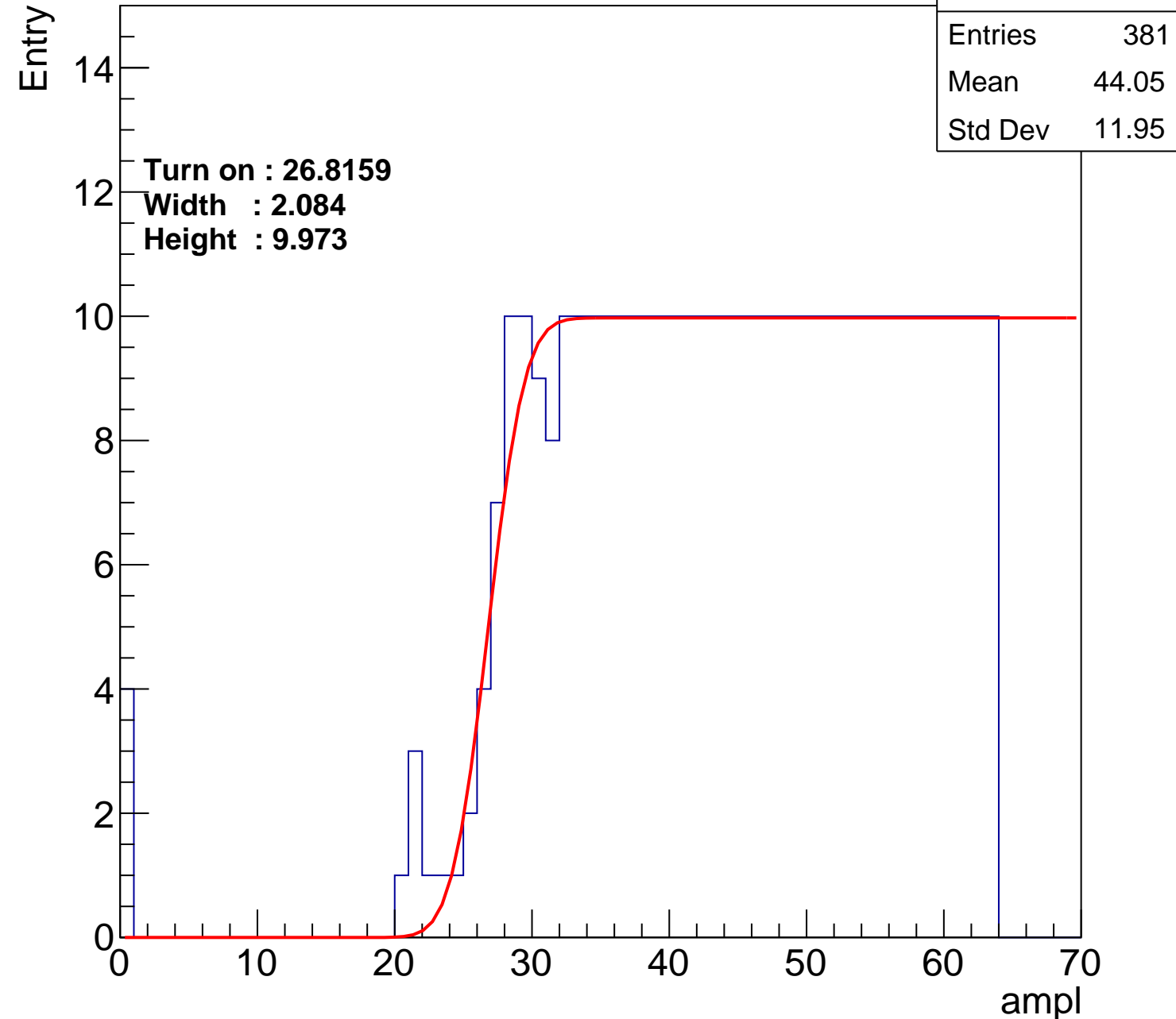
Width : 2.084

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.91
Std Dev	11.77

Turn on : 26.5945

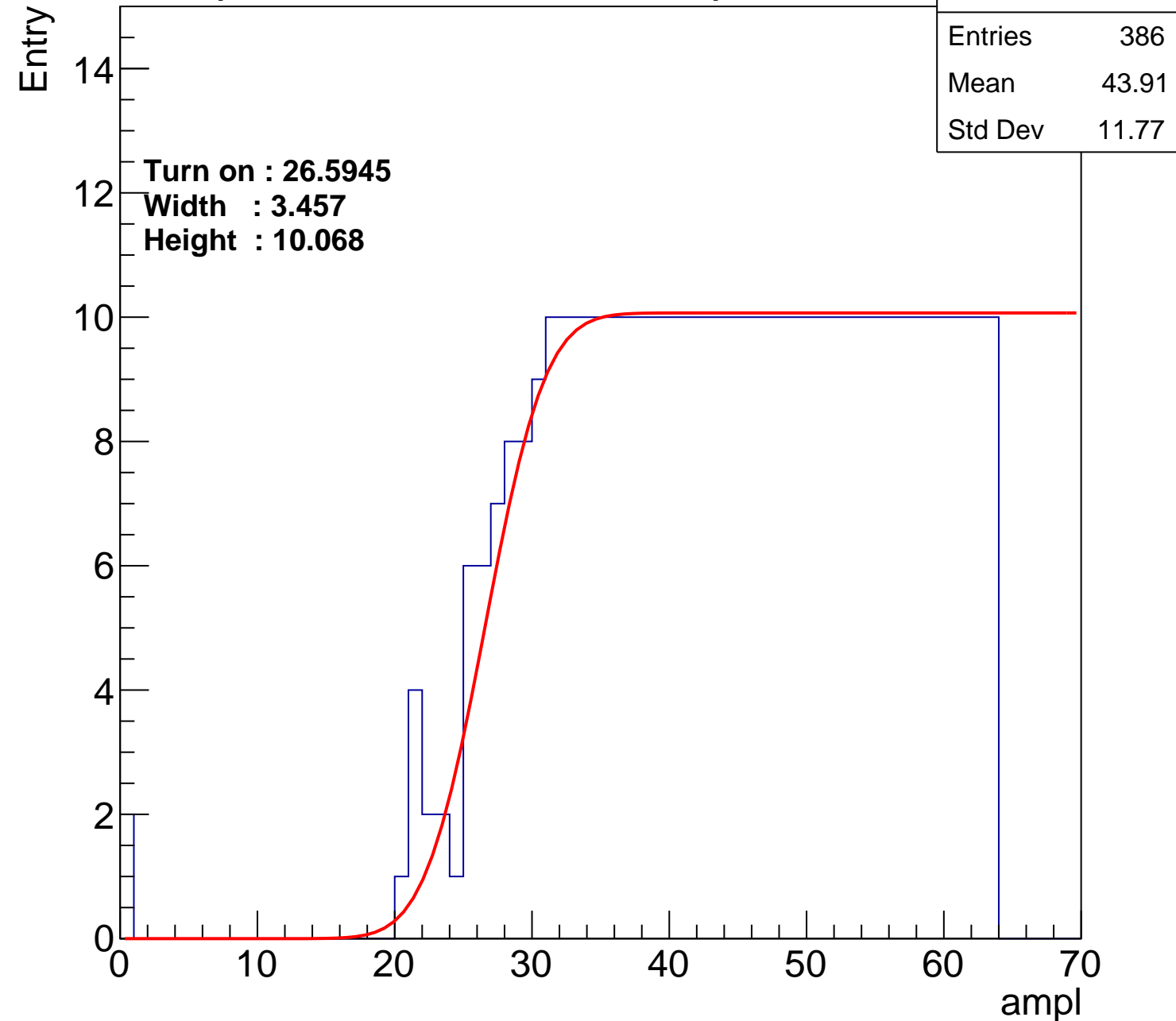
Width : 3.457

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.07
Std Dev	11.91

Turn on : 26.2088

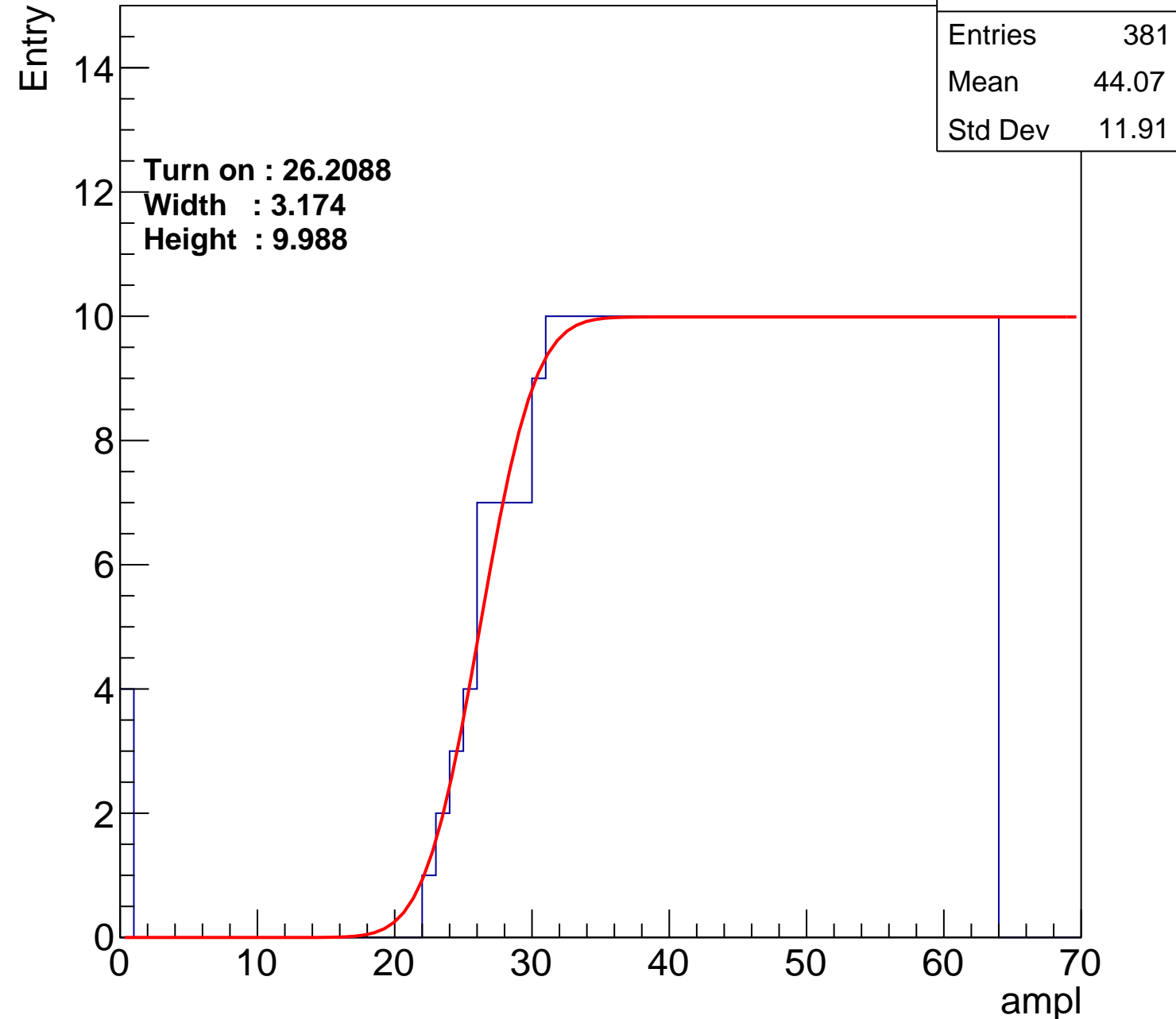
Width : 3.174

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch78

calib_packv5_042523_0143.root, FC#12, port B1

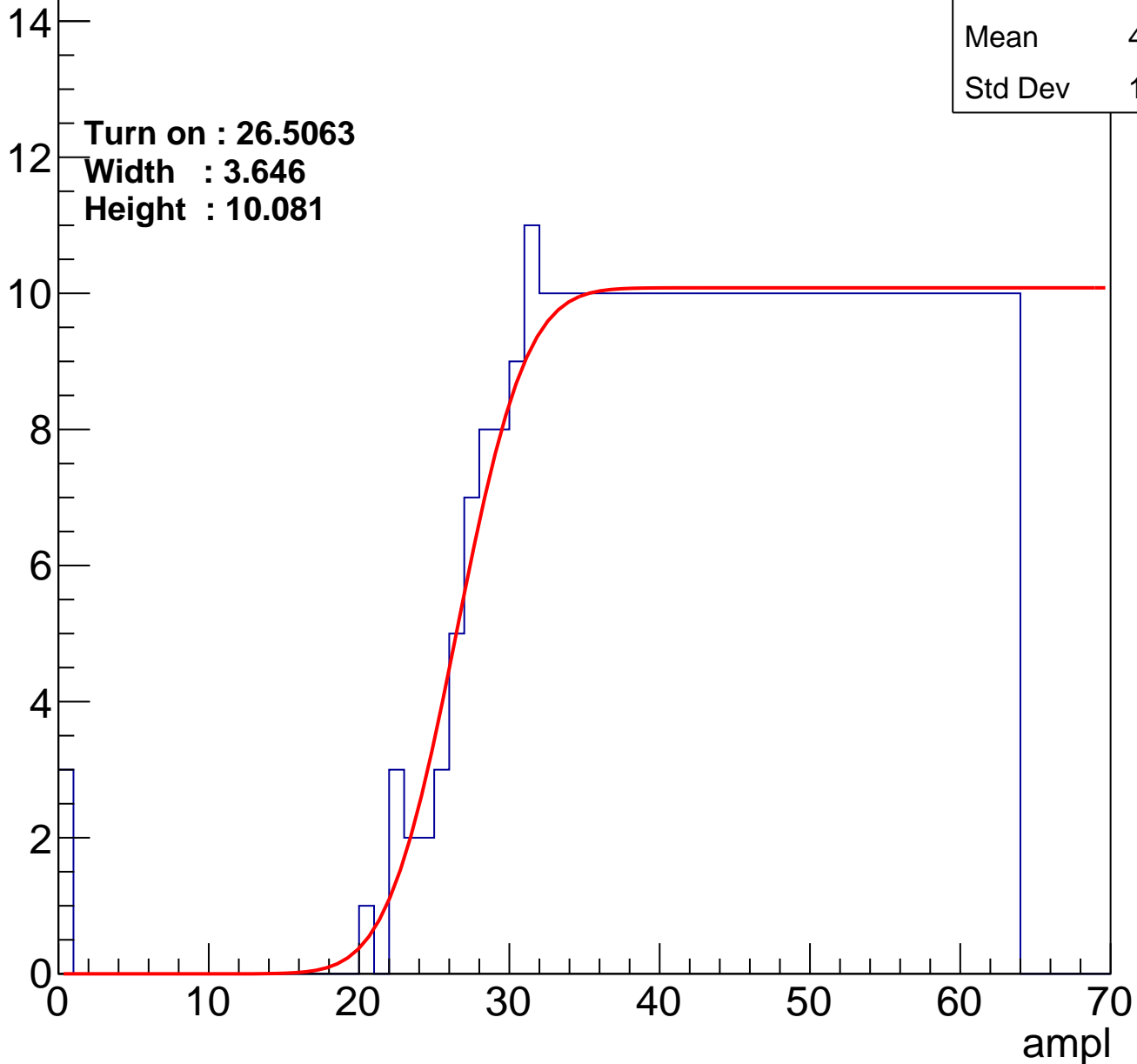
Entries	382
Mean	44.09
Std Dev	11.77

Turn on : 26.5063

Width : 3.646

Height : 10.081

Entry



B0L102S, U14-ch79

calib_packv5_042523_0143.root, FC#12, port B1

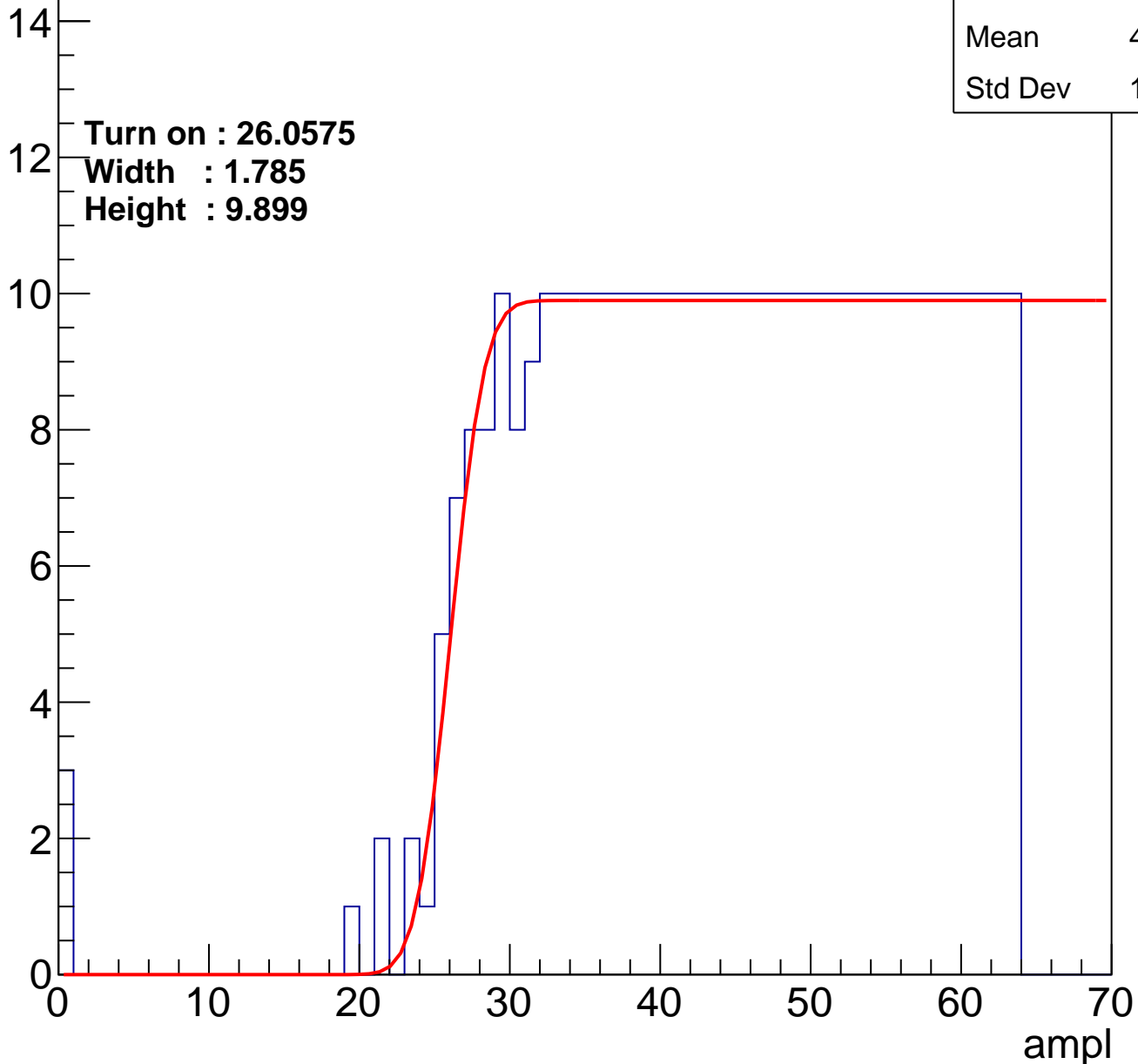
Entries	384
Mean	43.98
Std Dev	11.83

Turn on : 26.0575

Width : 1.785

Height : 9.899

Entry



B0L102S, U14-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.96
Std Dev	11.56

Turn on : 26.0283

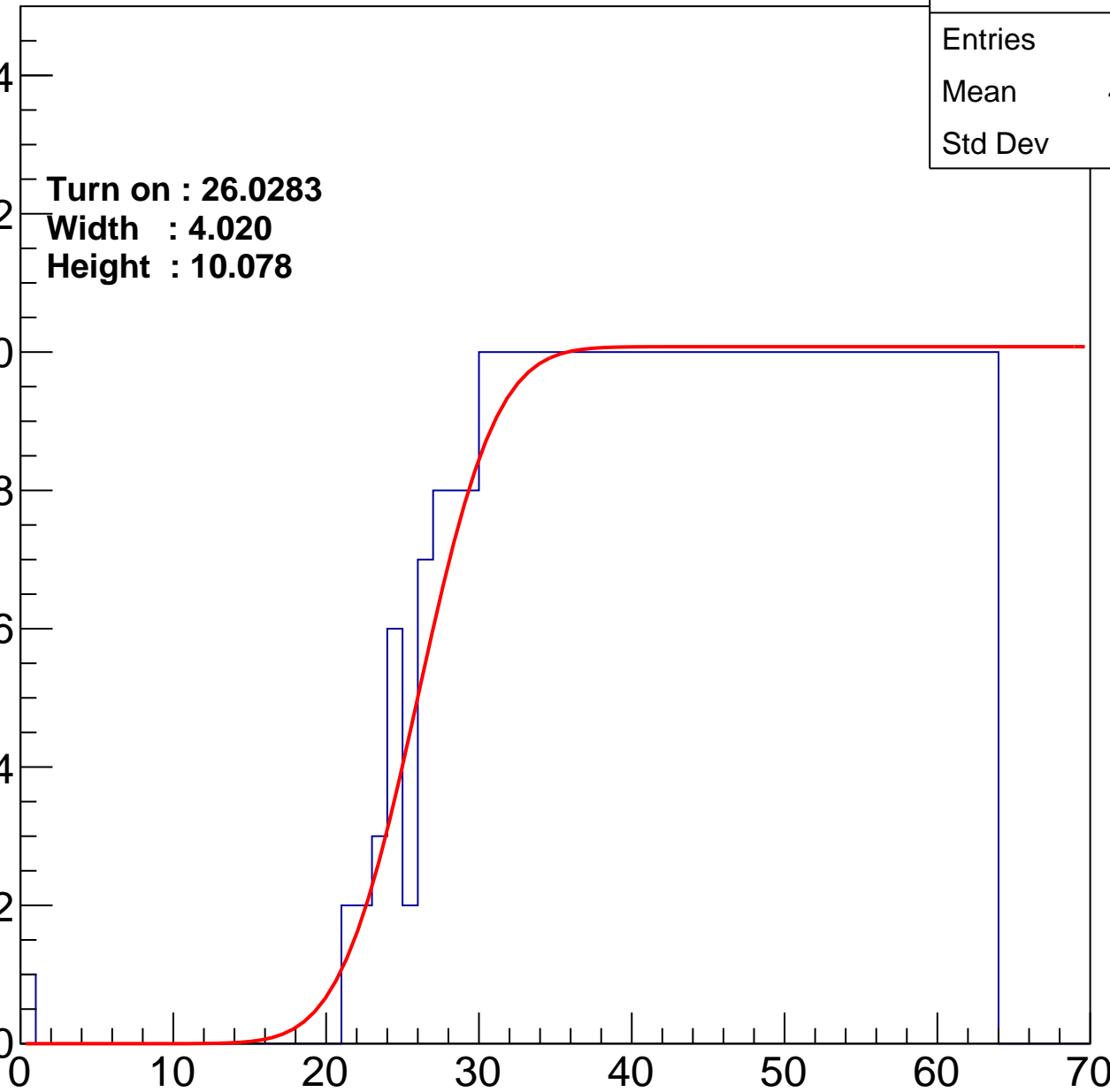
Width : 4.020

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.72
Std Dev	11.92

Turn on : 25.2428

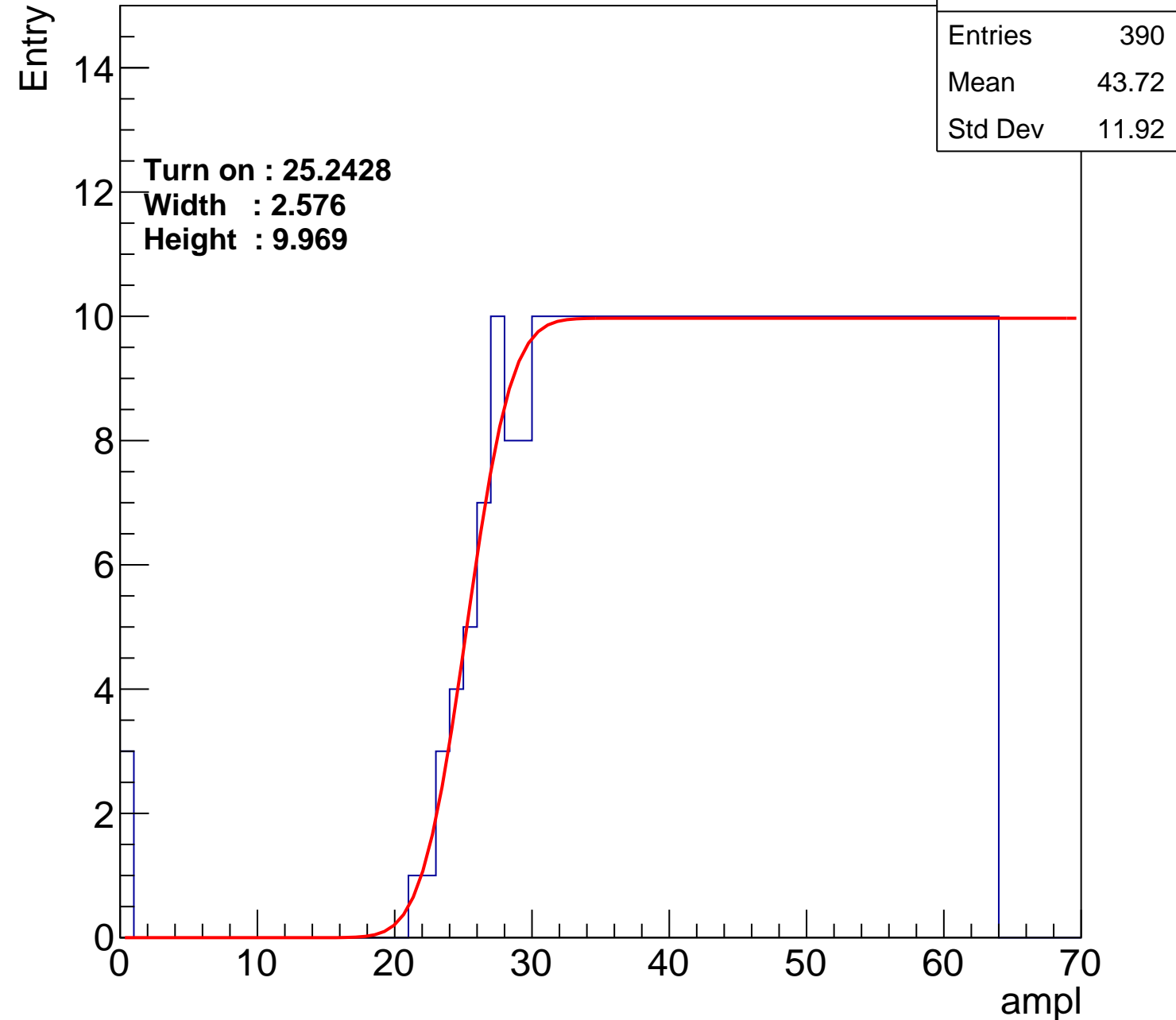
Width : 2.576

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	43.14
Std Dev	12.09

Turn on : 24.0668

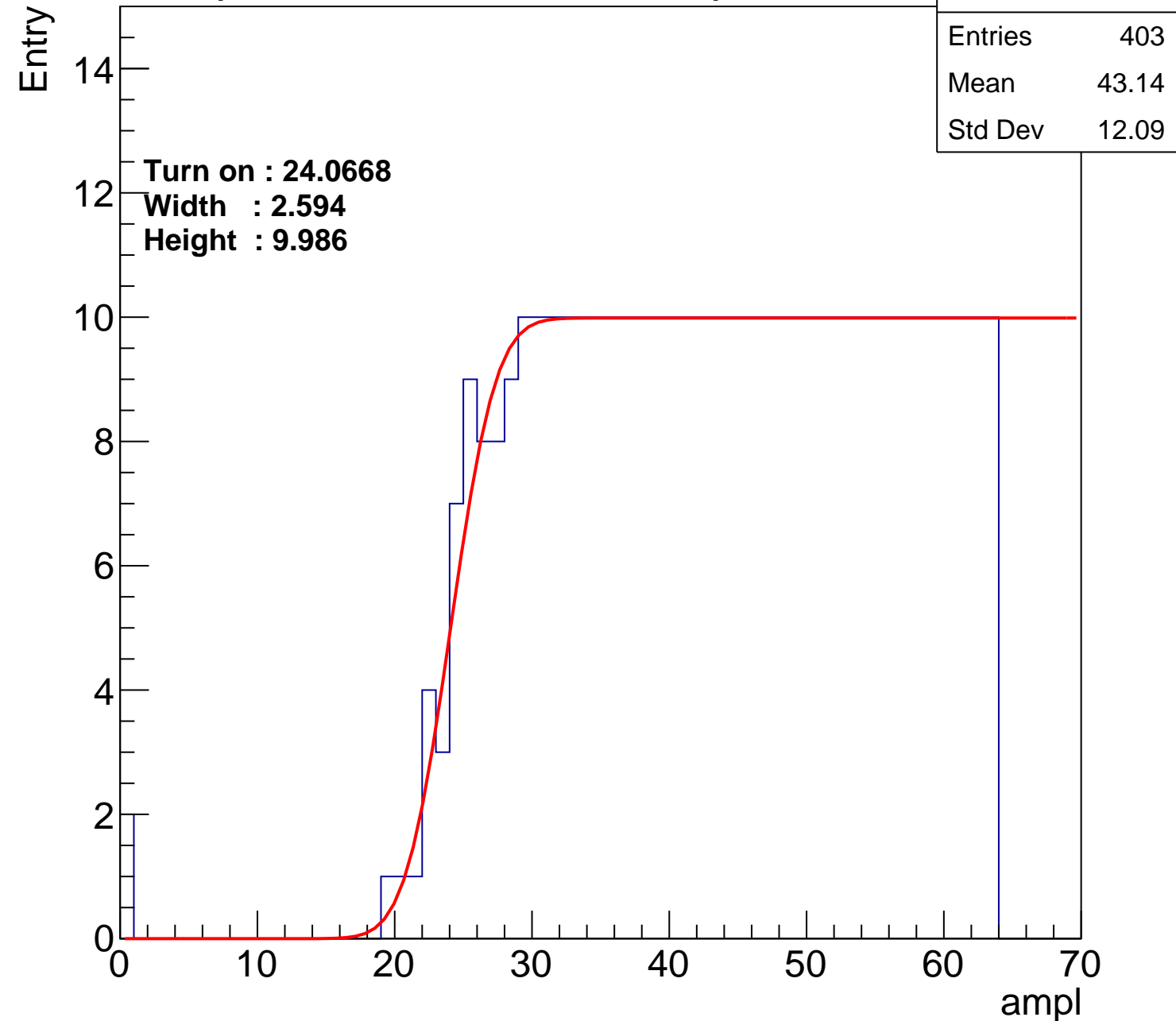
Width : 2.594

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.73
Std Dev	12.07

Turn on : 26.8564

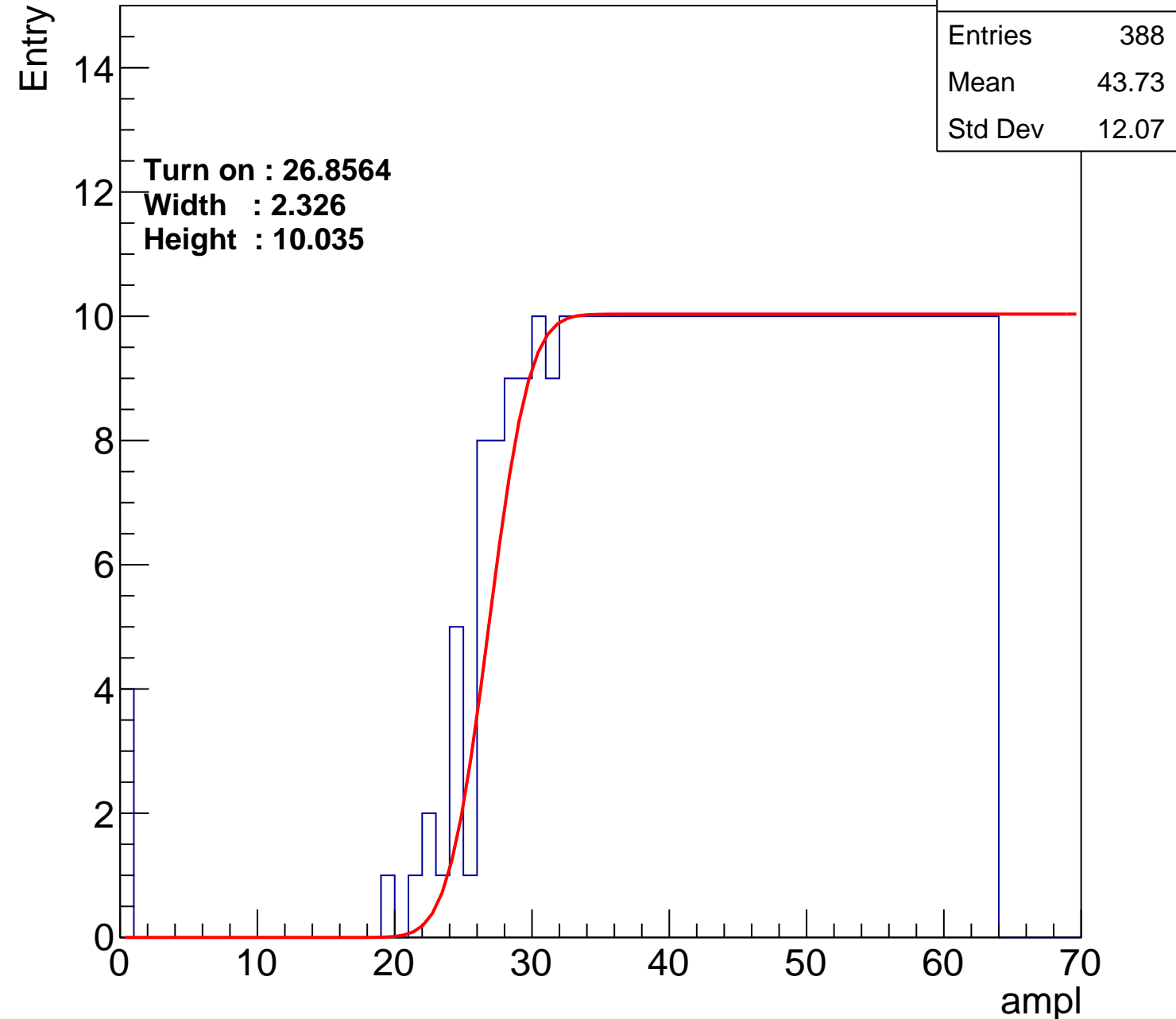
Width : 2.326

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch84

calib_packv5_042523_0143.root, FC#12, port B1

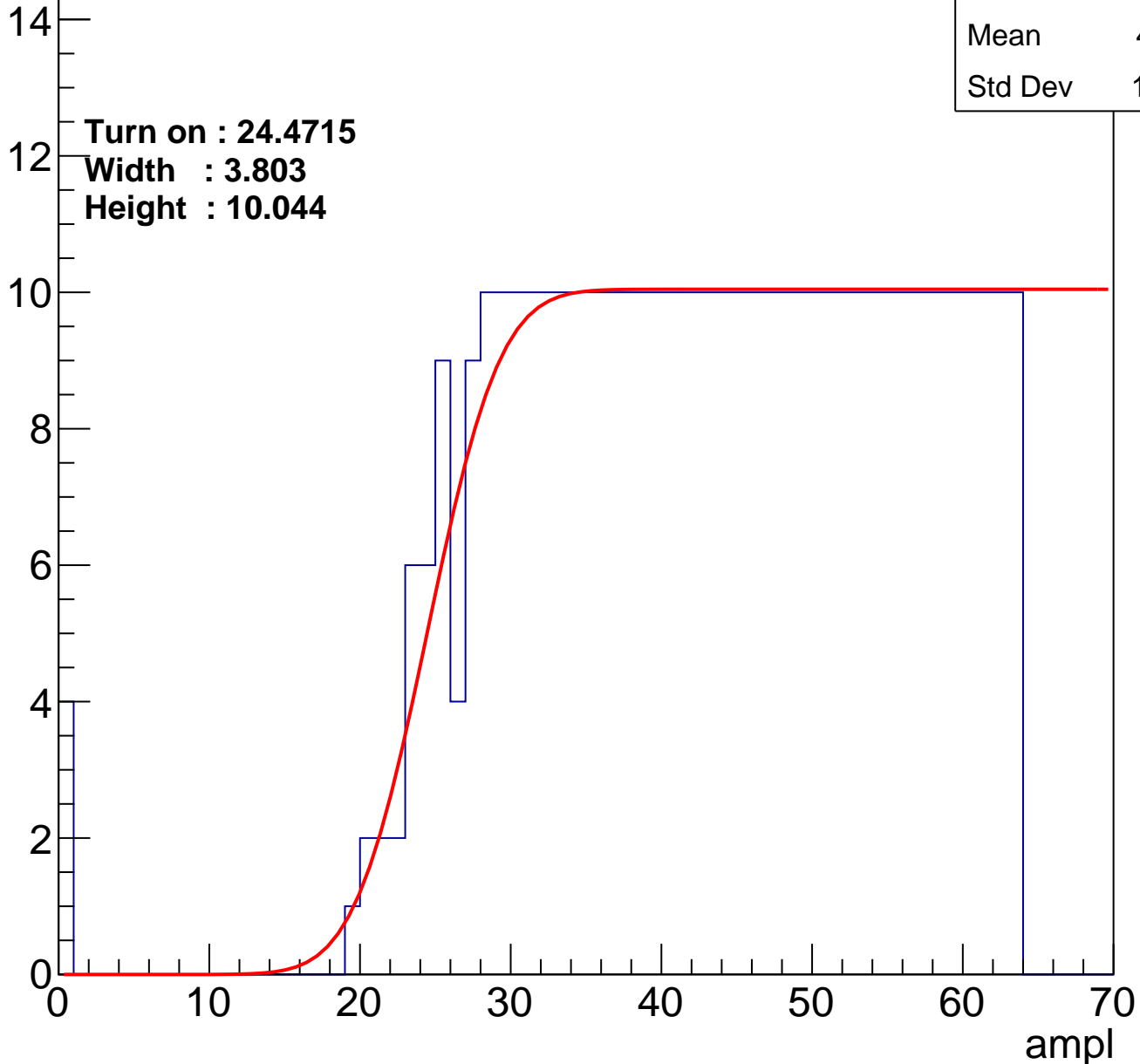
Entries	405
Mean	42.91
Std Dev	12.46

Turn on : 24.4715

Width : 3.803

Height : 10.044

Entry



B0L102S, U14-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 25.7397

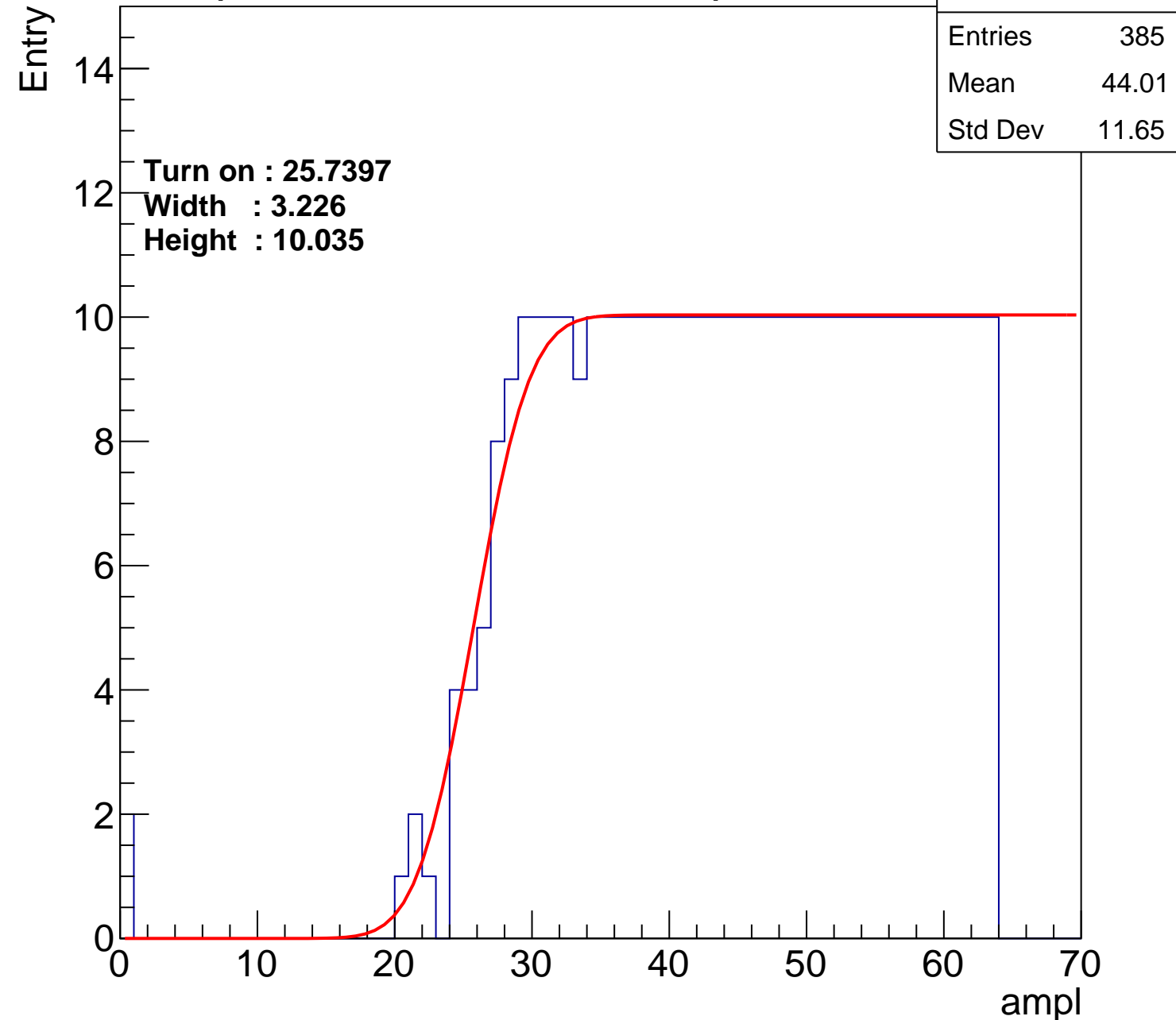
Width : 3.226

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch86

calib_packv5_042523_0143.root, FC#12, port B1

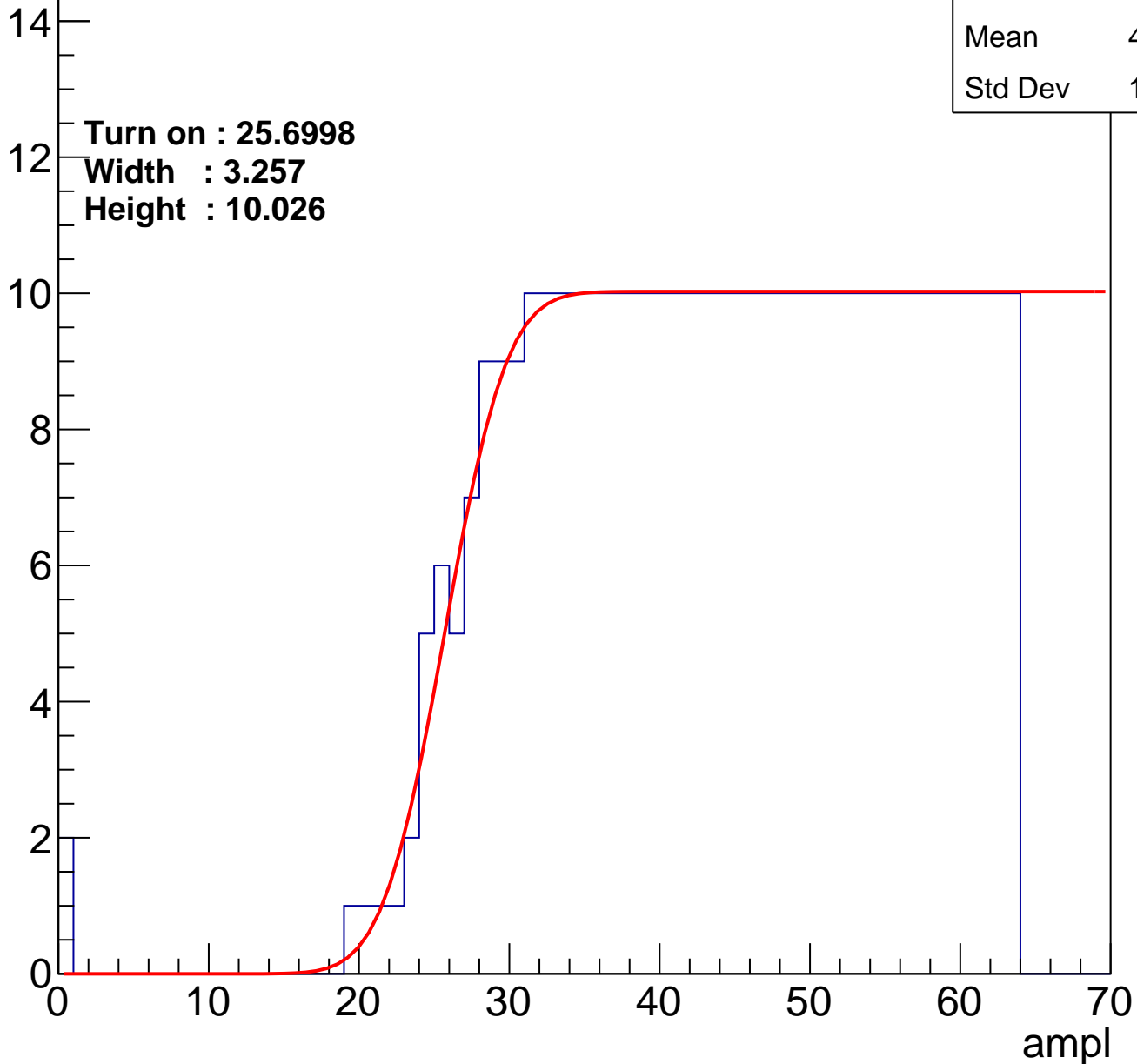
Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.6998

Width : 3.257

Height : 10.026

Entry



B0L102S, U14-ch87

calib_packv5_042523_0143.root, FC#12, port B1

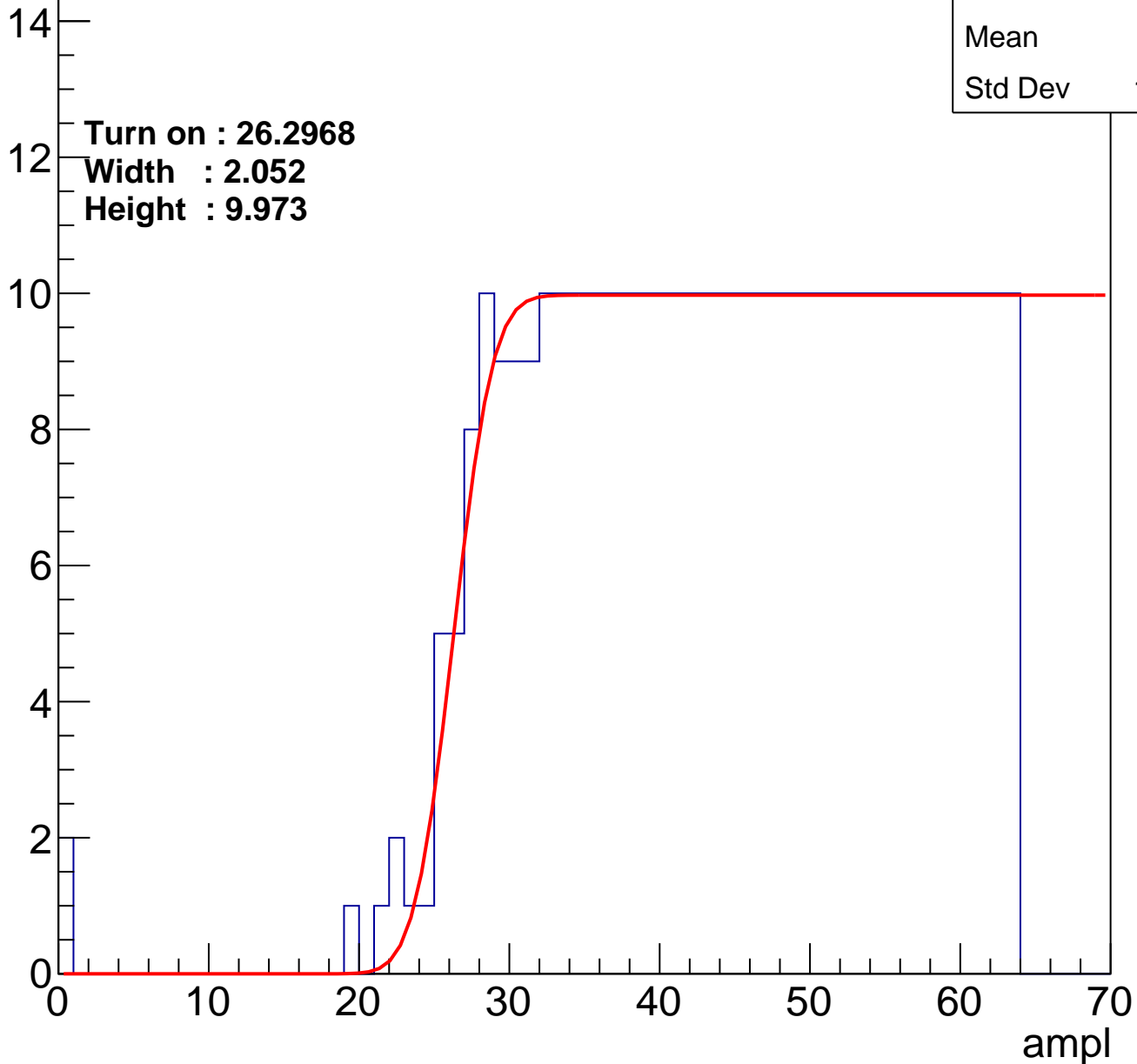
Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.2968

Width : 2.052

Height : 9.973

Entry



B0L102S, U14-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.67
Std Dev	11.94

Turn on : 25.3791

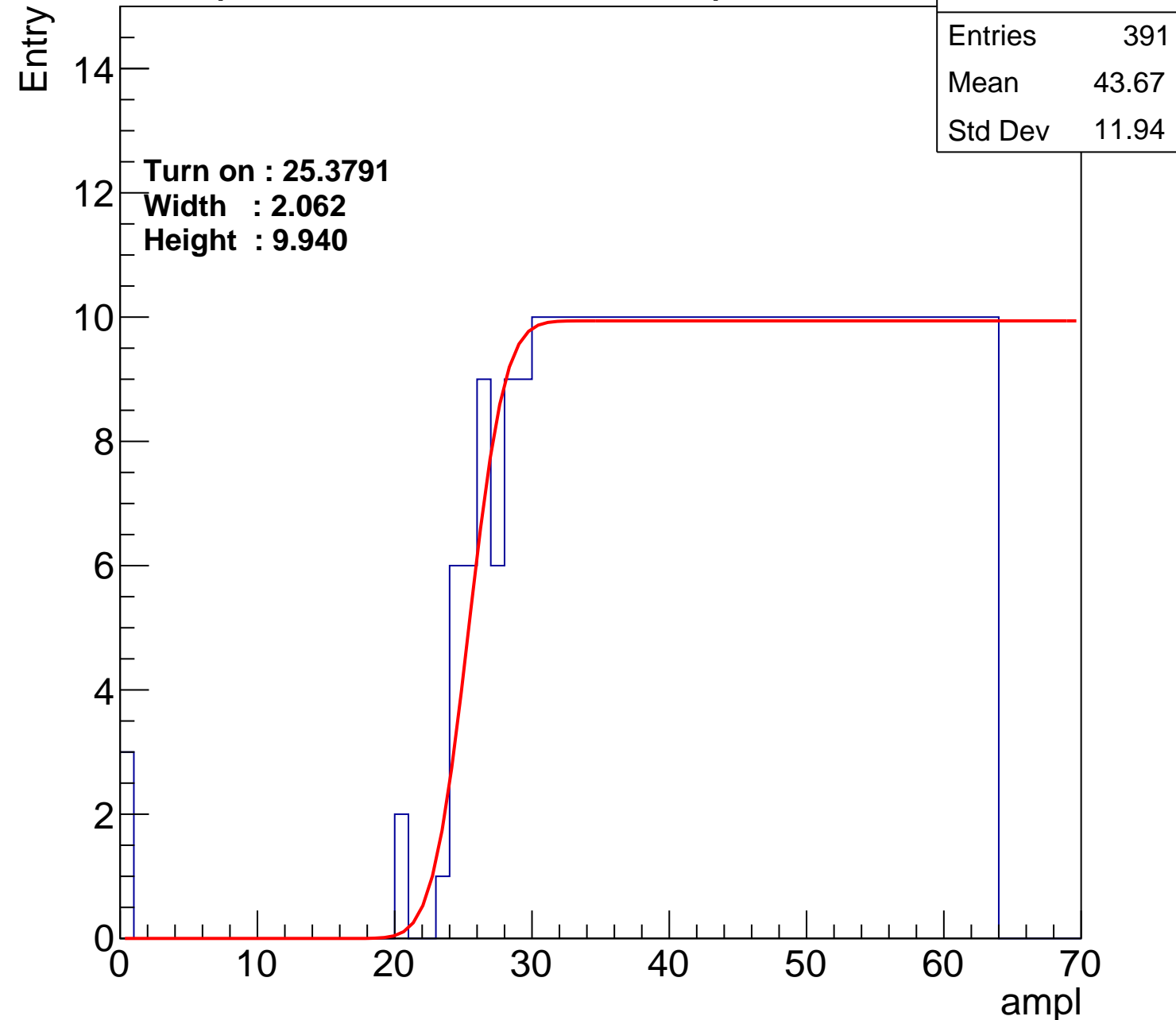
Width : 2.062

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch89

calib_packv5_042523_0143.root, FC#12, port B1

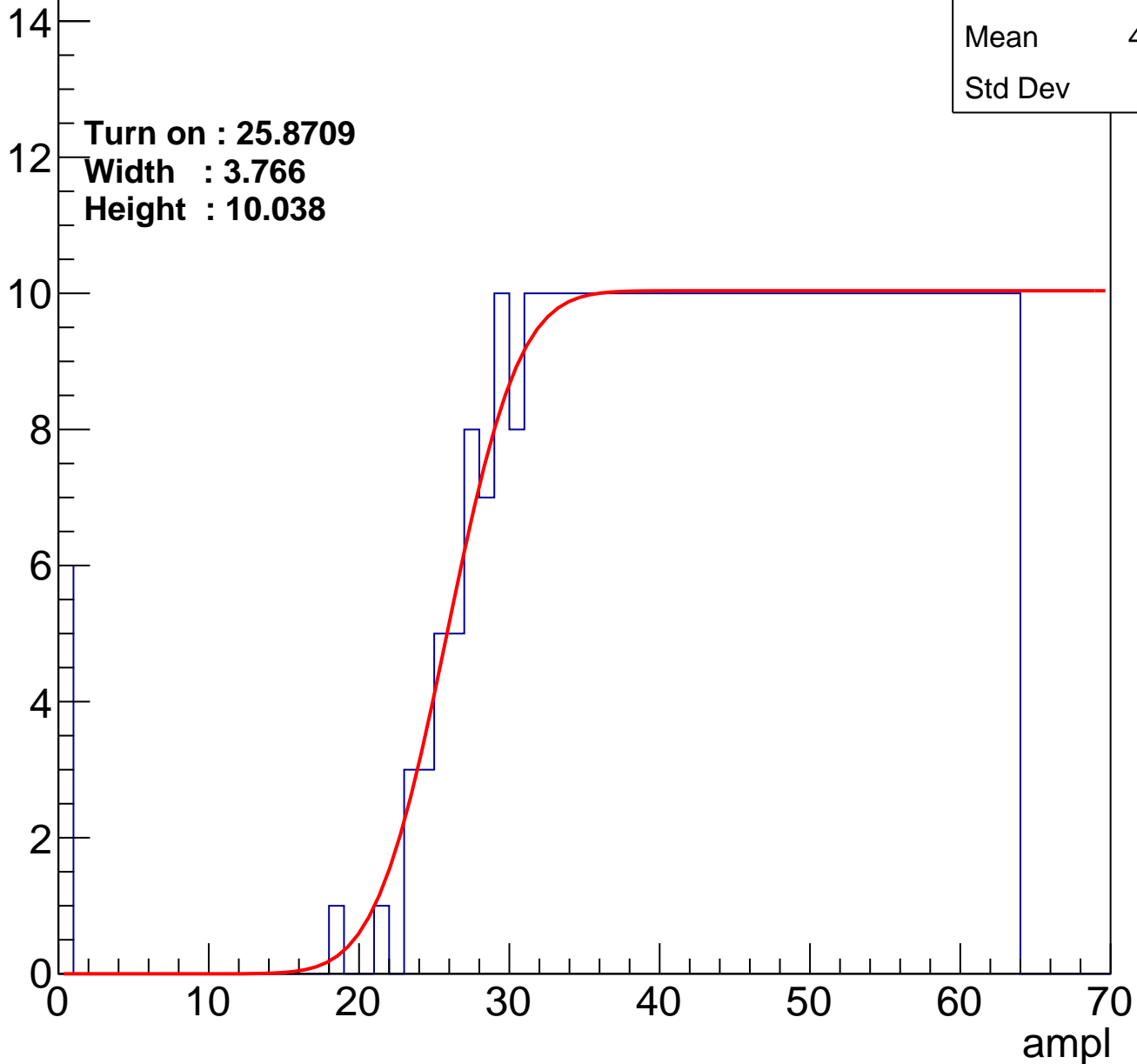
Entries	387
Mean	43.64
Std Dev	12.4

Turn on : 25.8709

Width : 3.766

Height : 10.038

Entry



B0L102S, U14-ch90

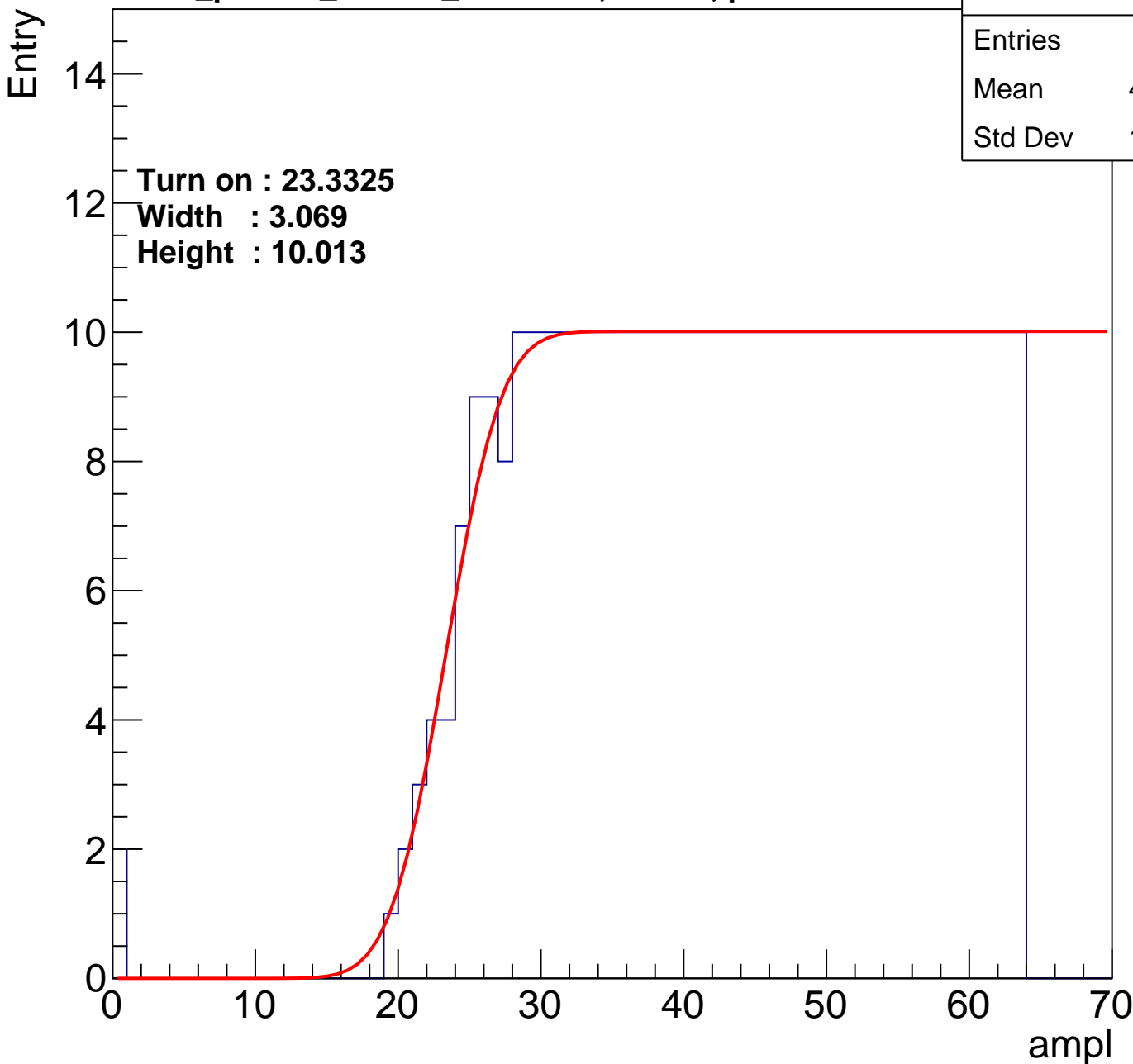
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 23.3325

Width : 3.069

Height : 10.013

Entries	409
Mean	42.85
Std Dev	12.25



B0L102S, U14-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.58
Std Dev	12.09

Turn on : 24.6250

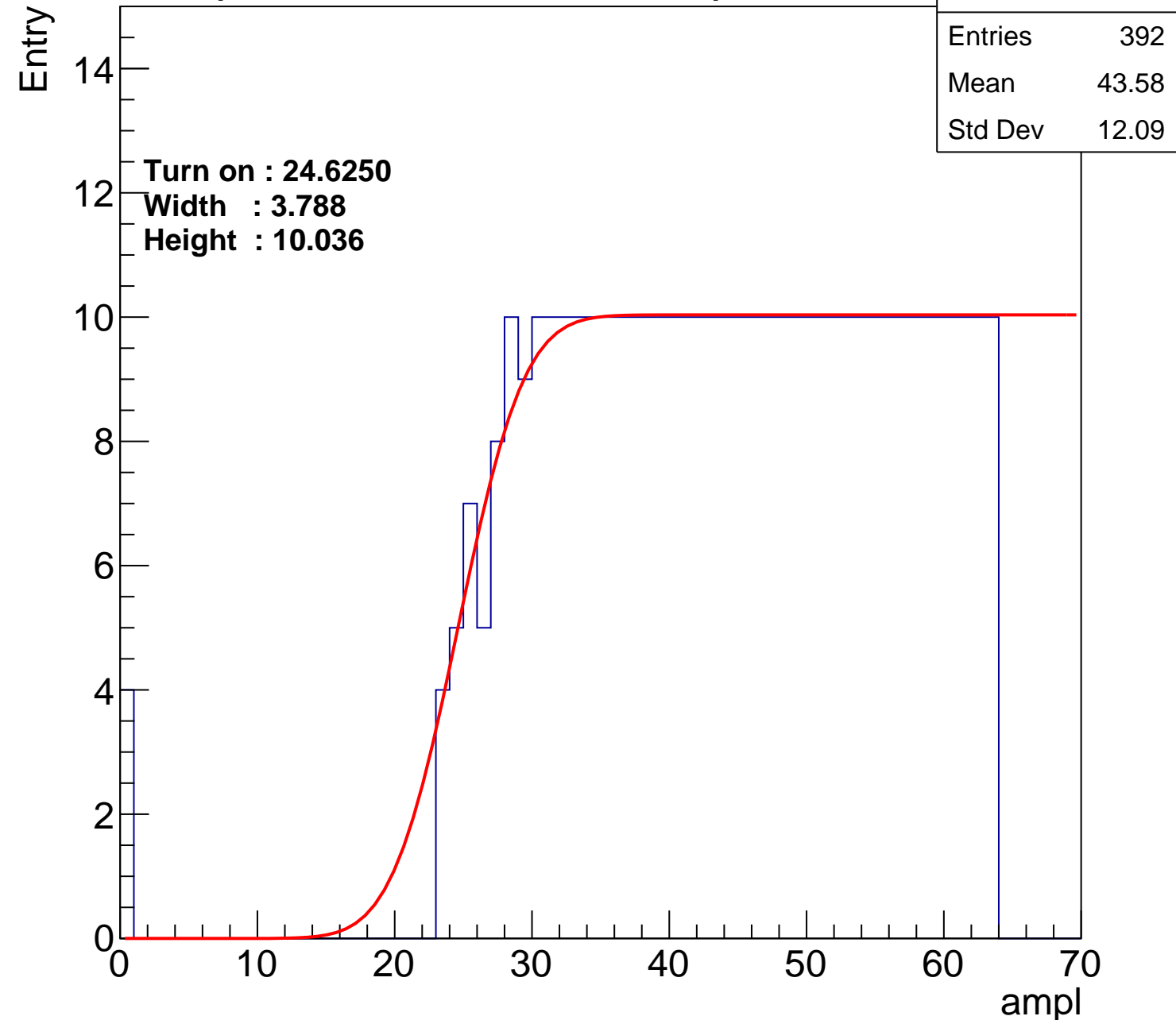
Width : 3.788

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch92

calib_packv5_042523_0143.root, FC#12, port B1

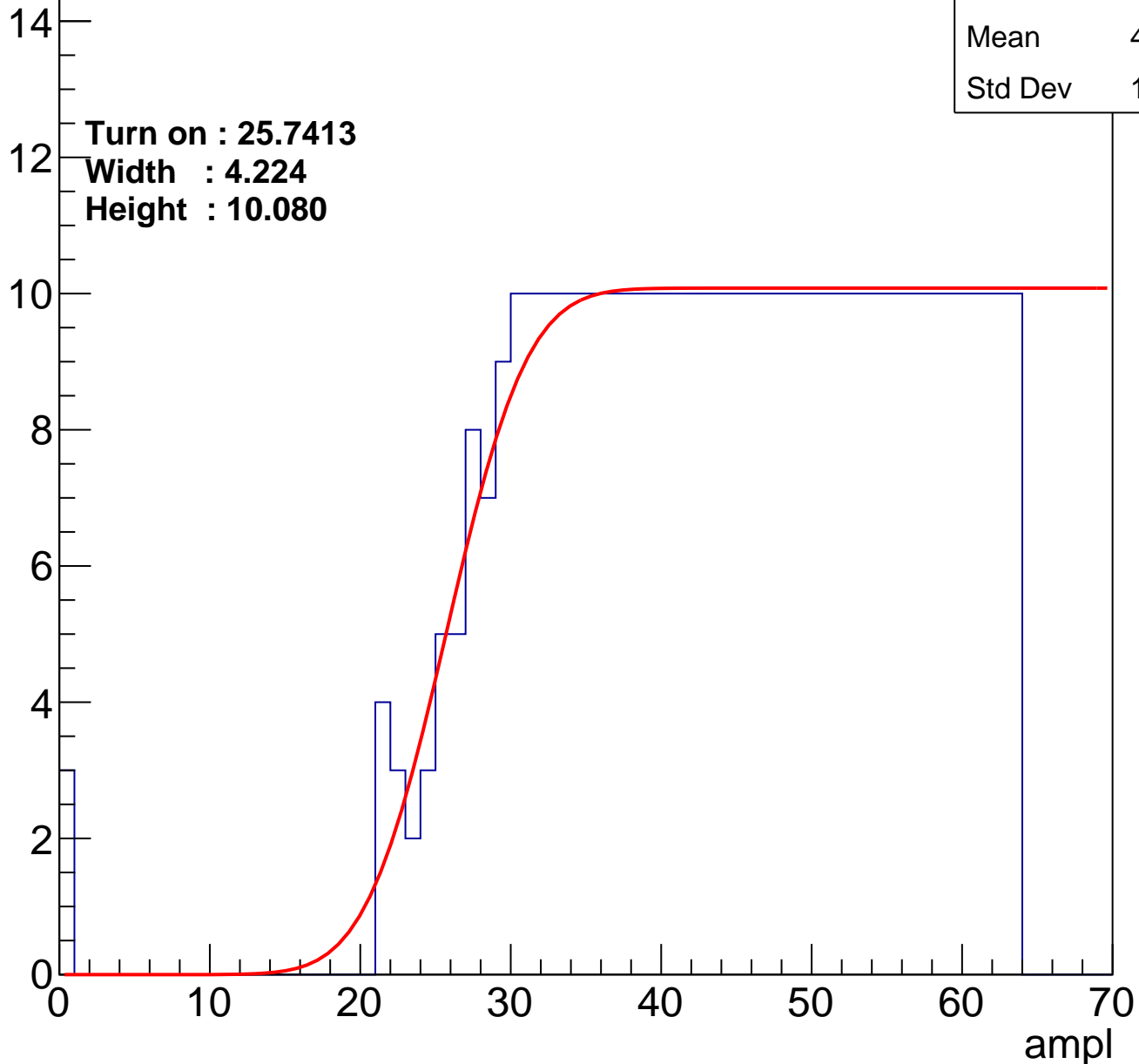
Entries	389
Mean	43.72
Std Dev	11.98

Turn on : 25.7413

Width : 4.224

Height : 10.080

Entry



B0L102S, U14-ch93

calib_packv5_042523_0143.root, FC#12, port B1

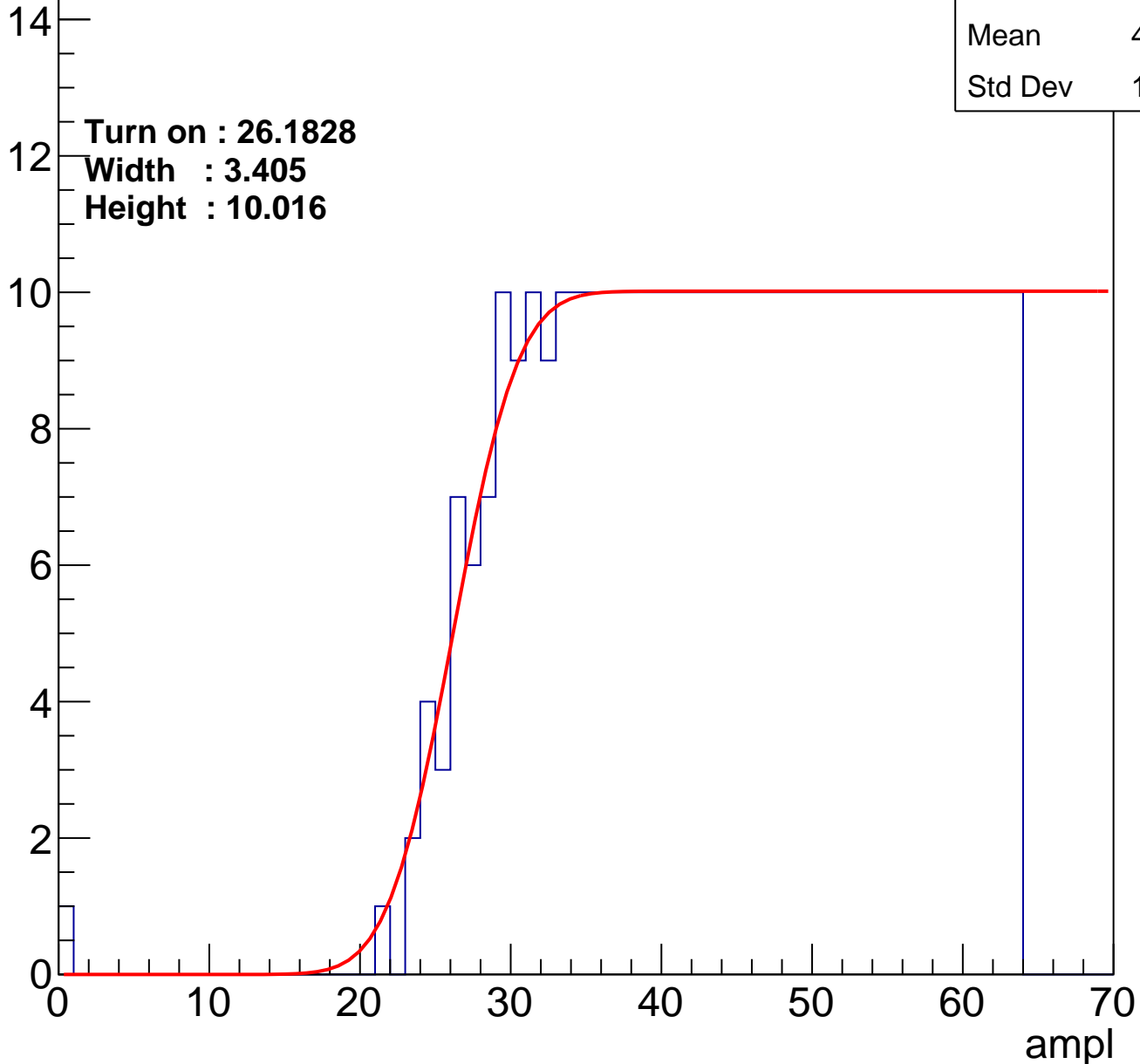
Entries	379
Mean	44.37
Std Dev	11.32

Turn on : 26.1828

Width : 3.405

Height : 10.016

Entry

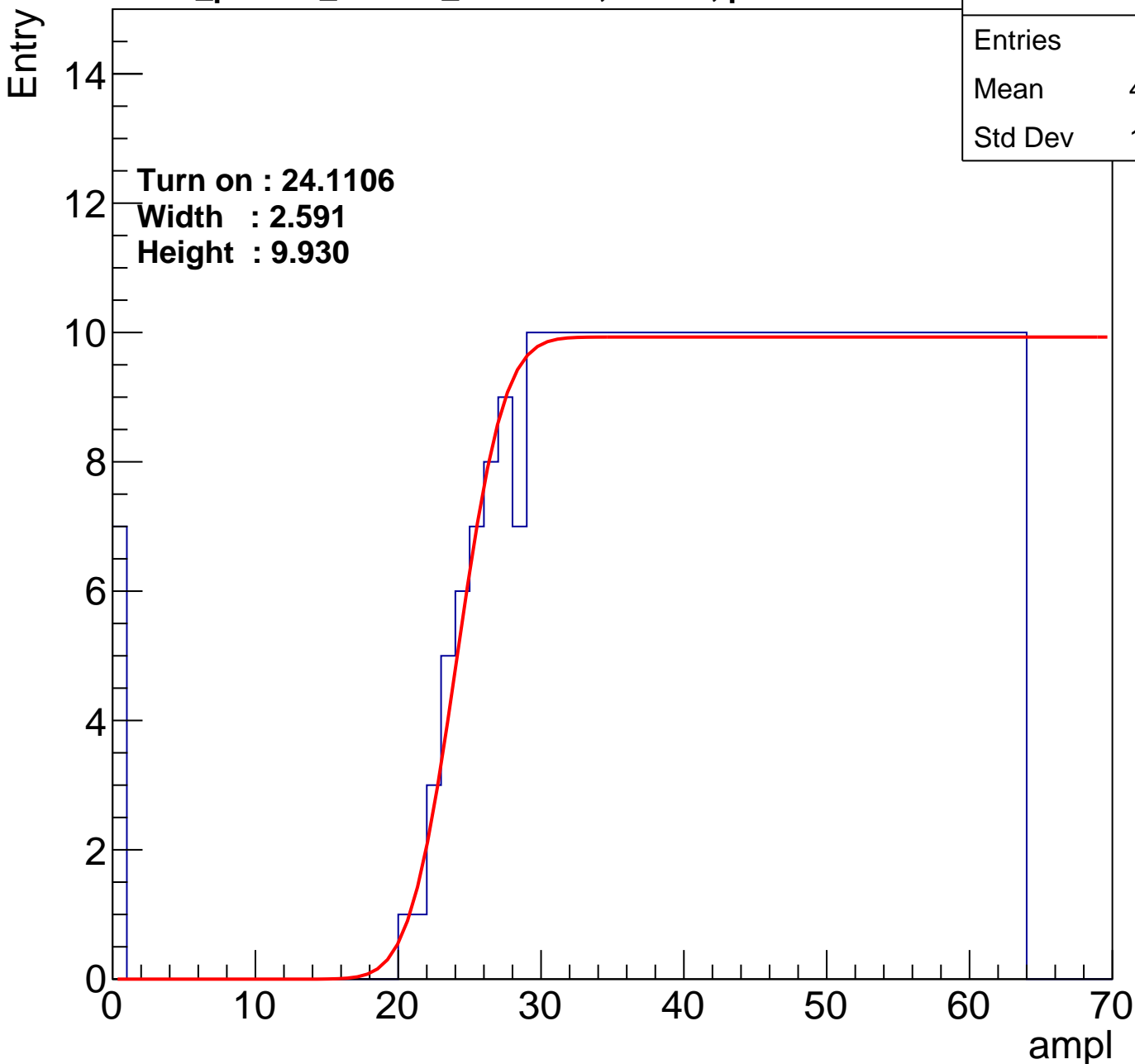


calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.79
Std Dev	12.85

Height : 9.930



B0L102S, U14-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.98
Std Dev	12

Turn on : 26.7941

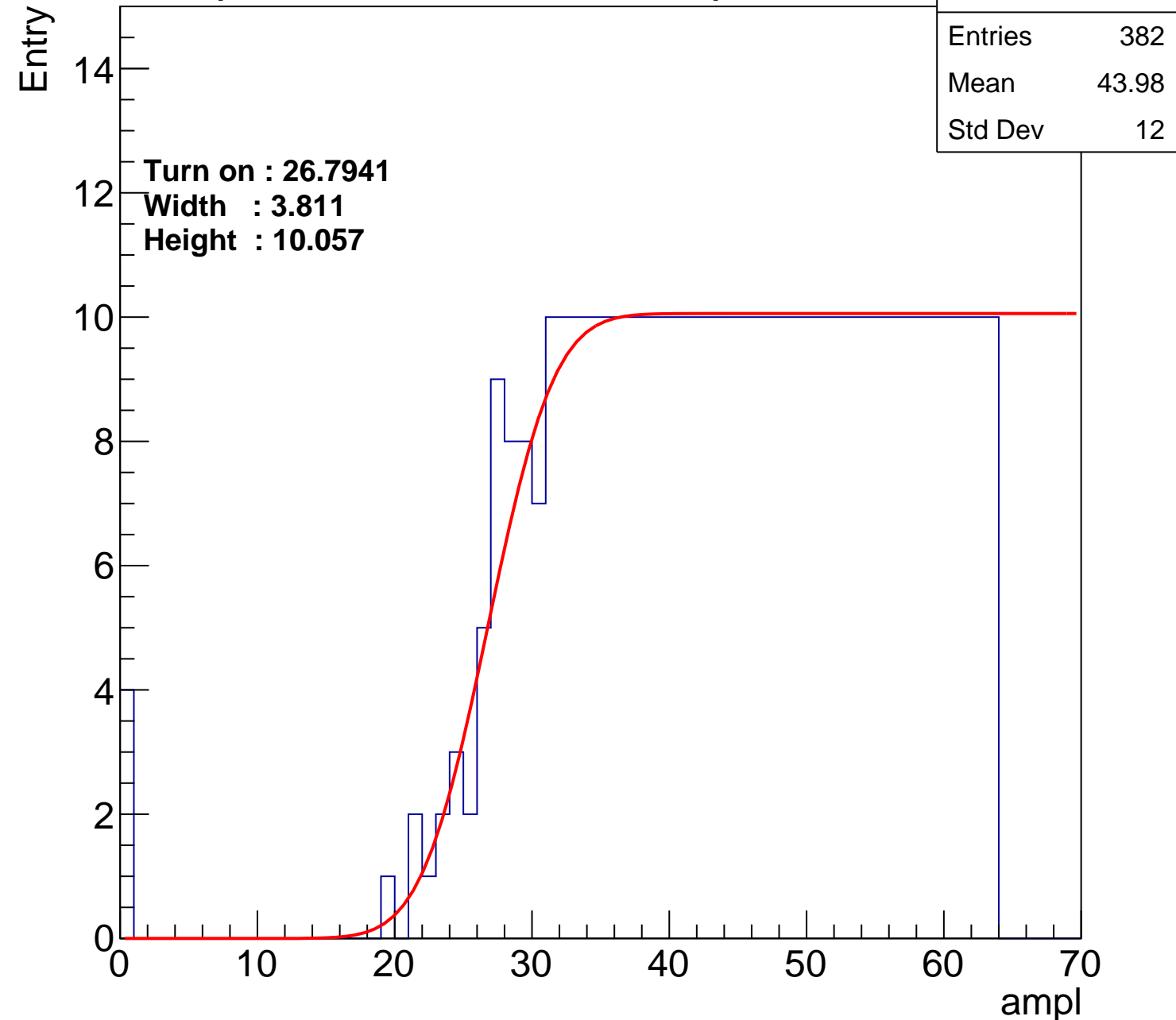
Width : 3.811

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.05
Std Dev	12.57

Turn on : 24.8621

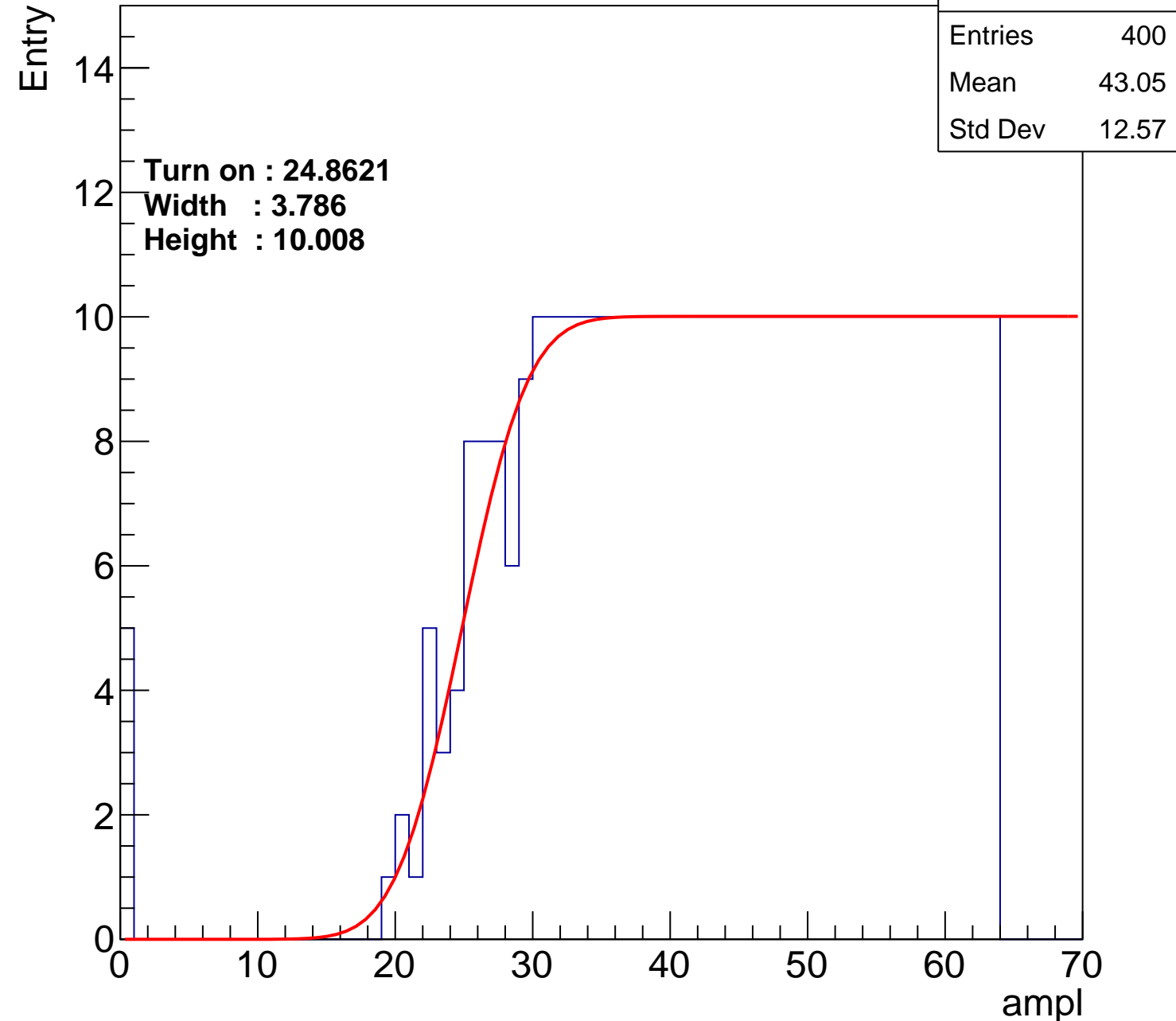
Width : 3.786

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.55
Std Dev	12.17

Turn on : 25.1137

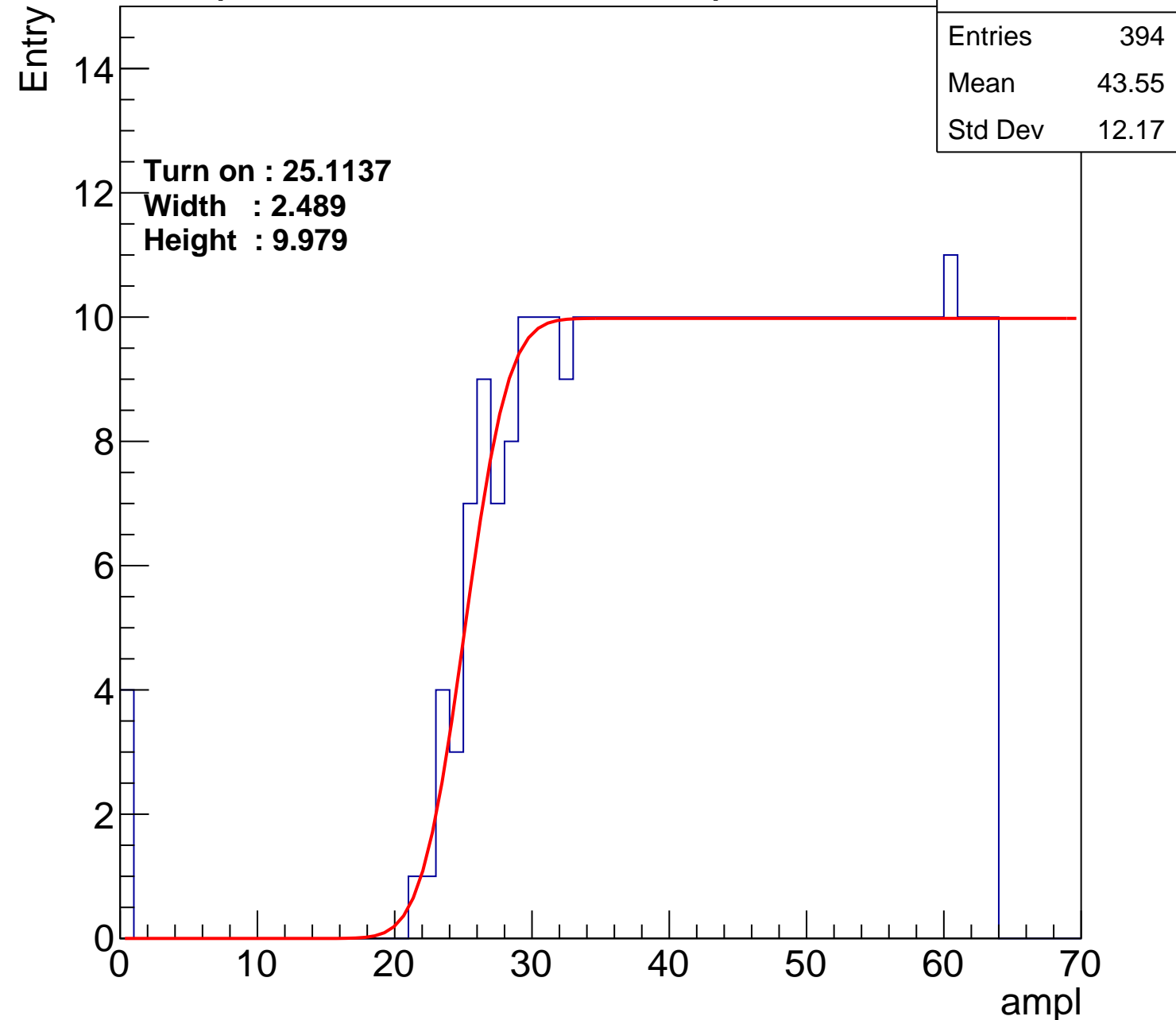
Width : 2.489

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.13
Std Dev	12.38

Turn on : 25.0634

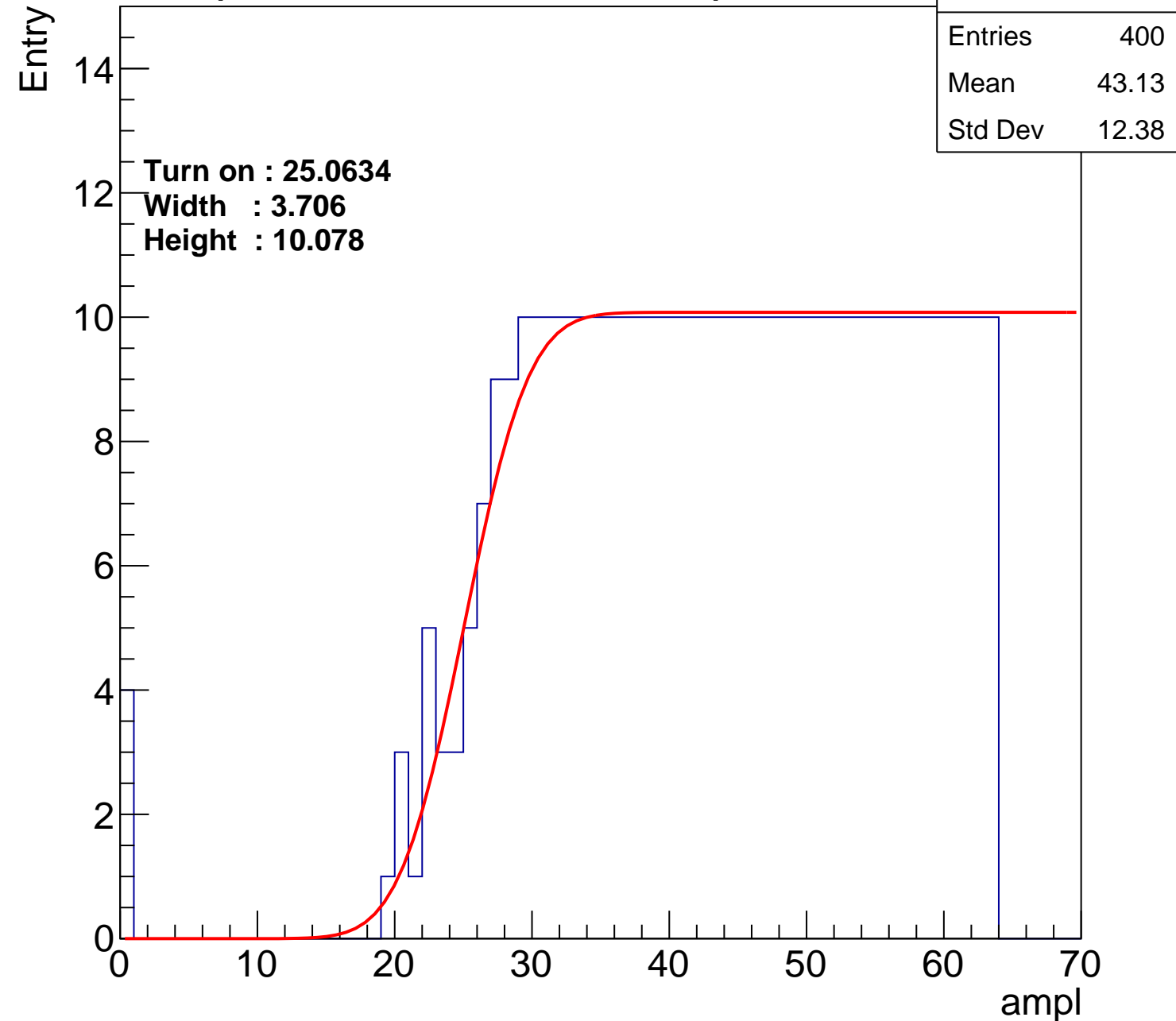
Width : 3.706

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.8893

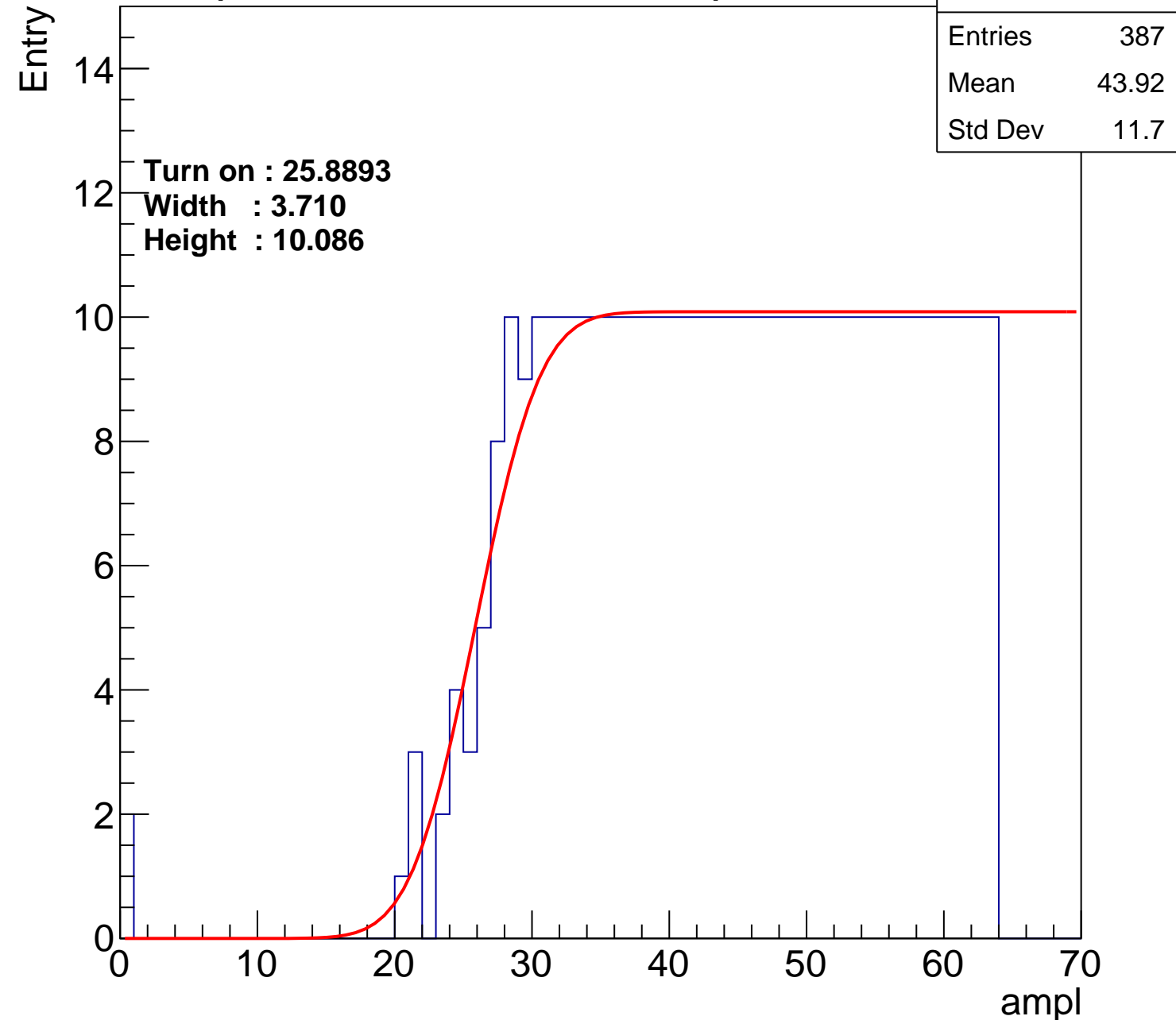
Width : 3.710

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.7
Std Dev	12.07

Turn on : 25.7499

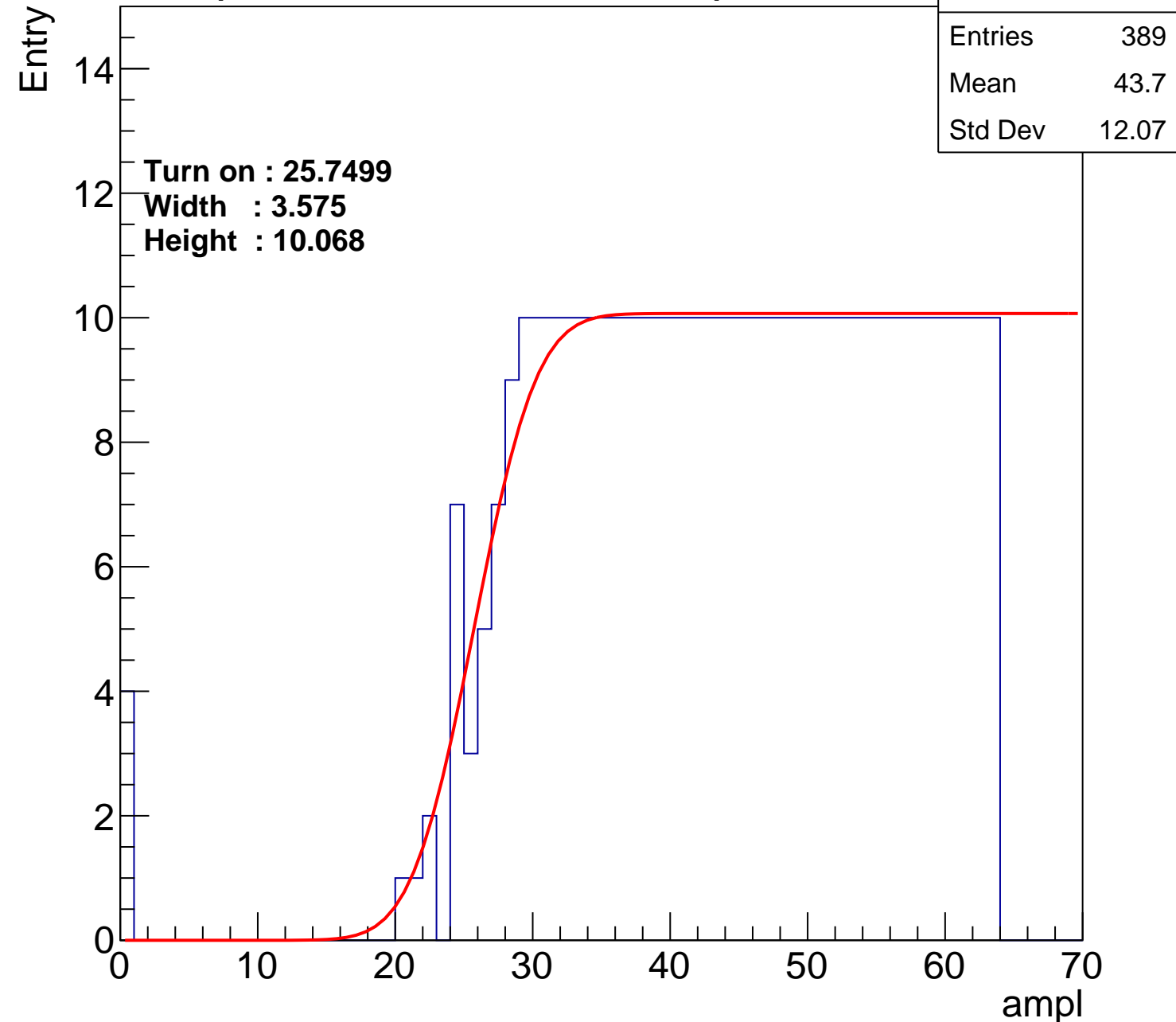
Width : 3.575

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.62
Std Dev	12.15

Turn on : 25.4628

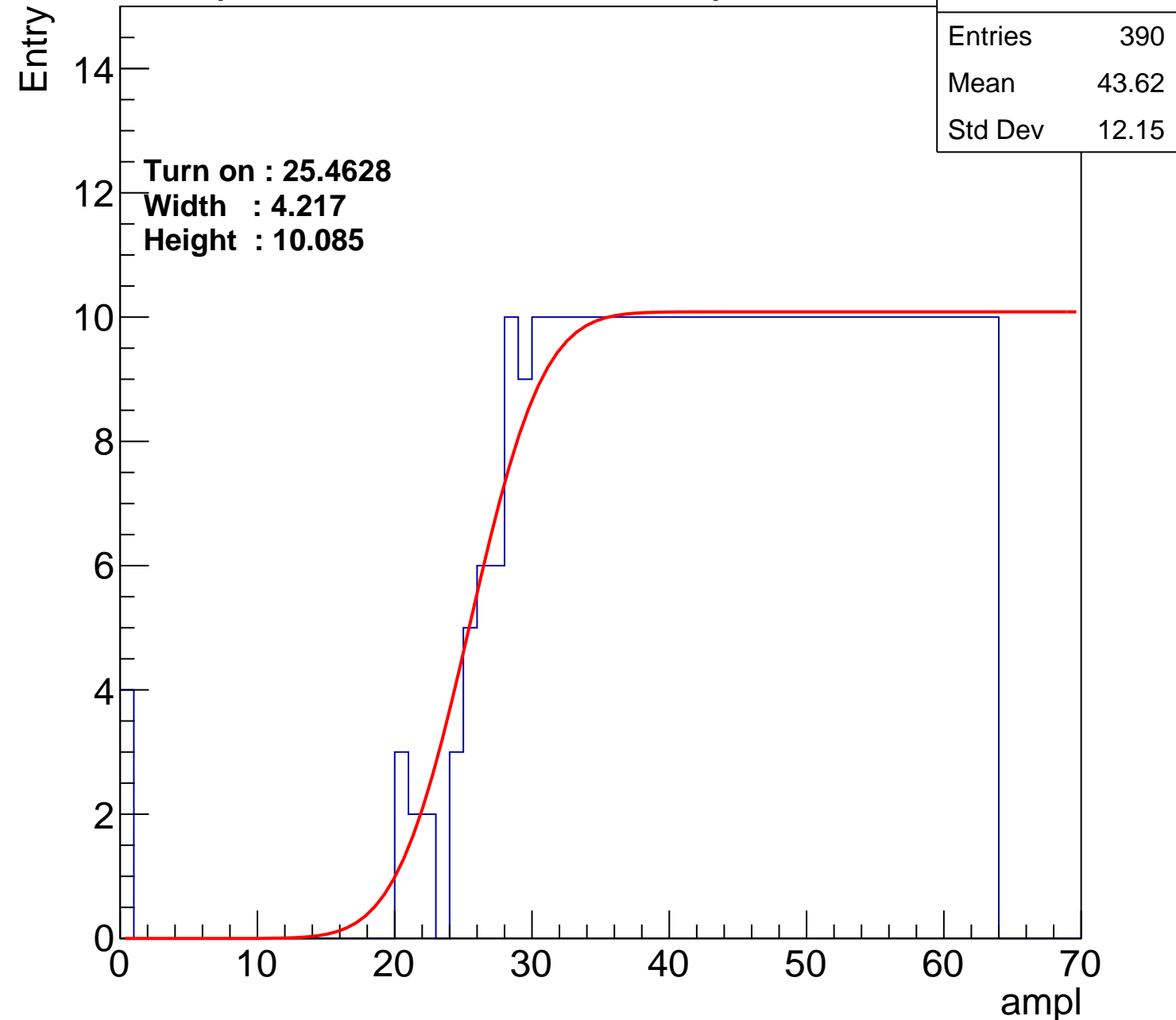
Width : 4.217

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.53
Std Dev	12.29

Turn on : 25.6815

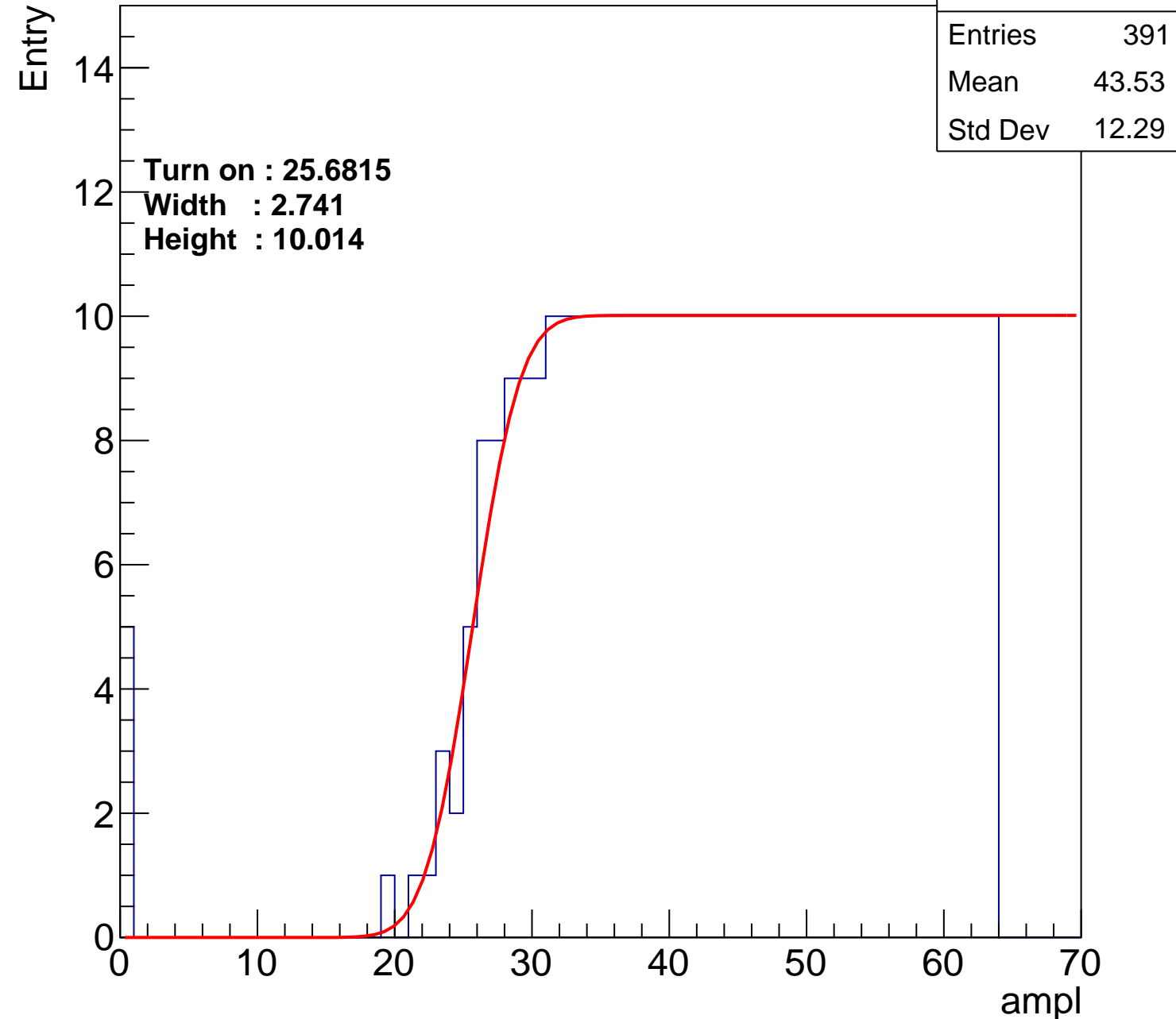
Width : 2.741

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.58
Std Dev	12.09

Turn on : 24.1005

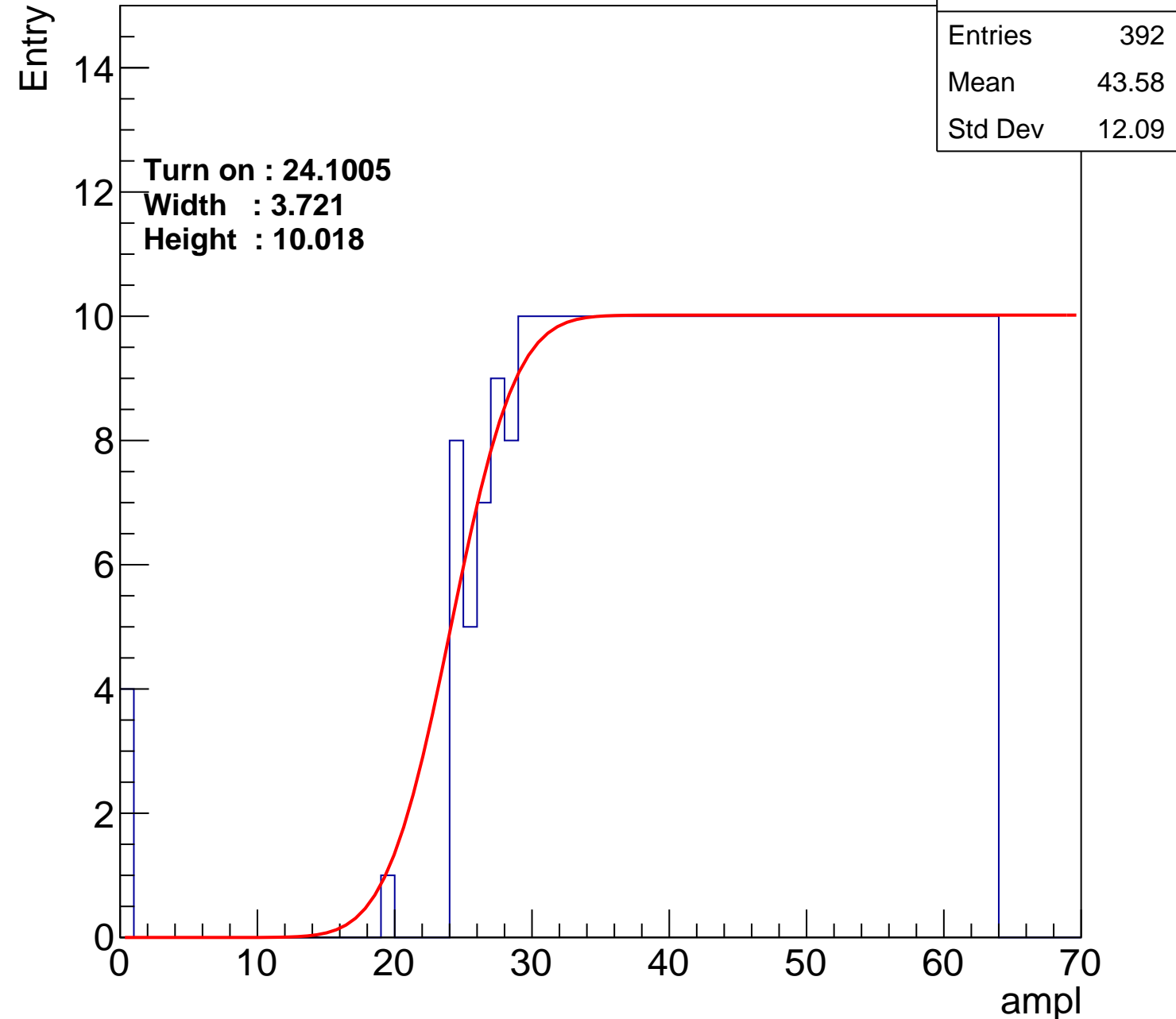
Width : 3.721

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch104

calib_packv5_042523_0143.root, FC#12, port B1

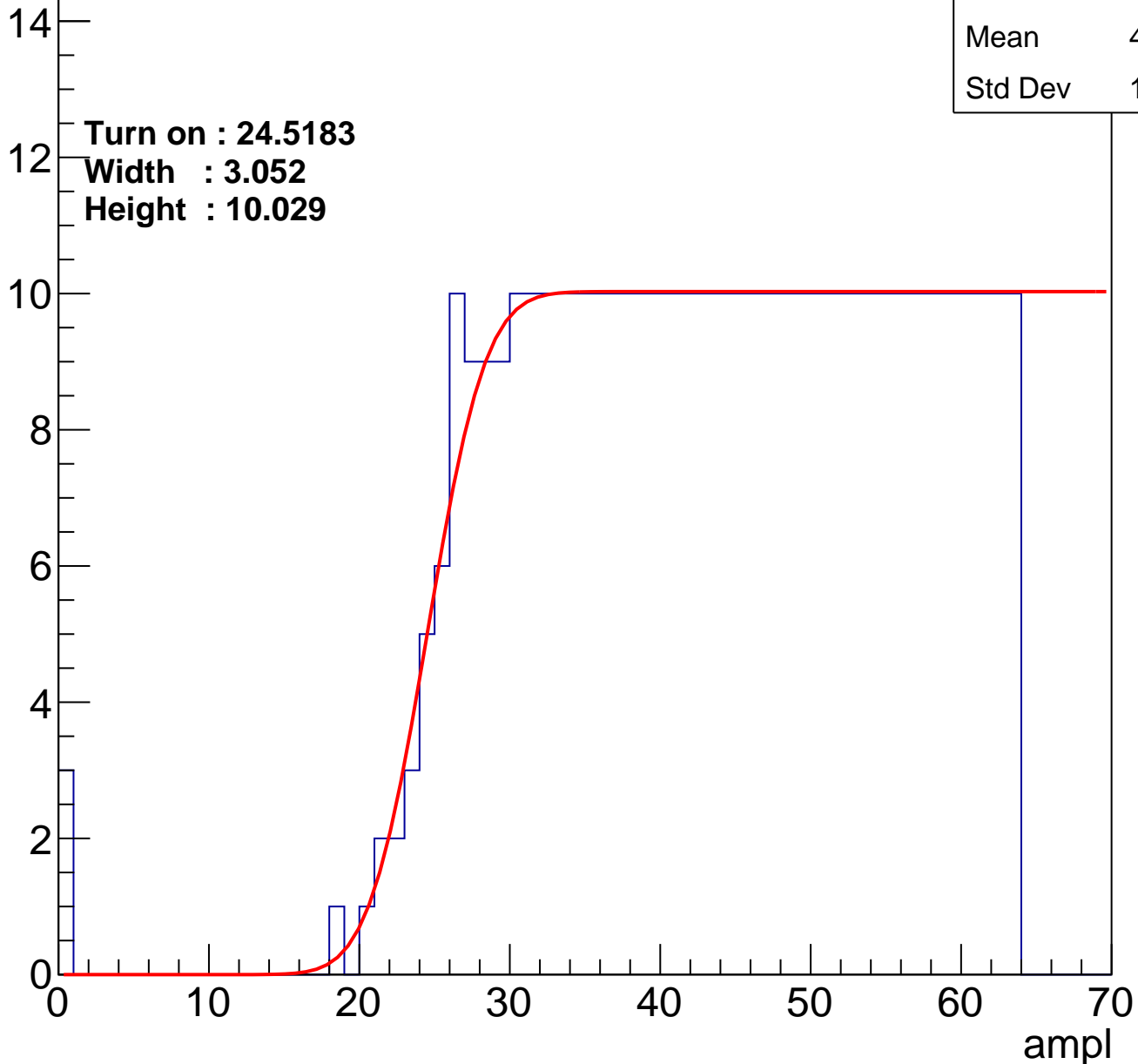
Entries	400
Mean	43.22
Std Dev	12.18

Turn on : 24.5183

Width : 3.052

Height : 10.029

Entry



B0L102S, U14-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.04
Std Dev	11.53

Turn on : 26.2462

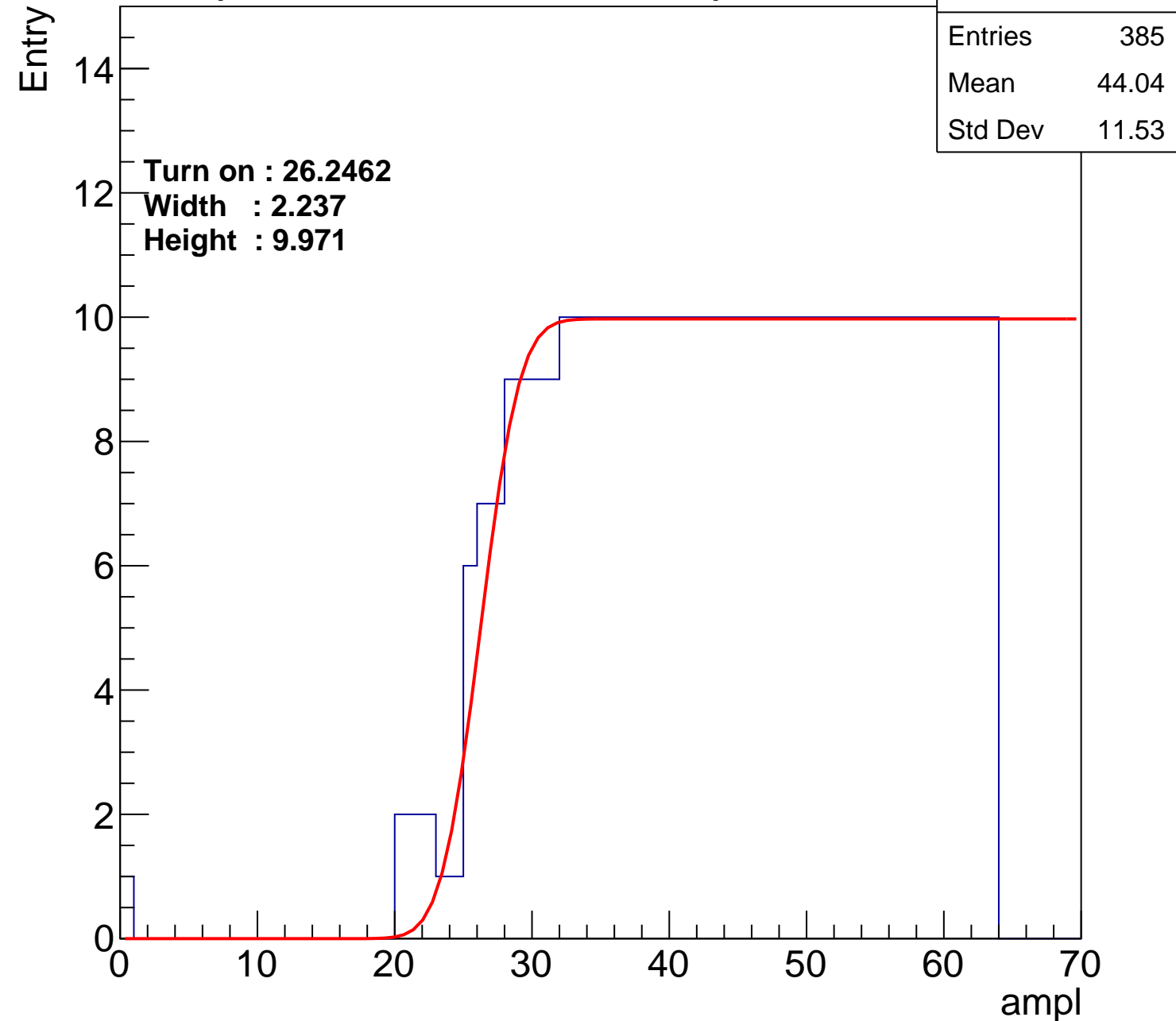
Width : 2.237

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.92
Std Dev	11.75

Turn on : 26.5241

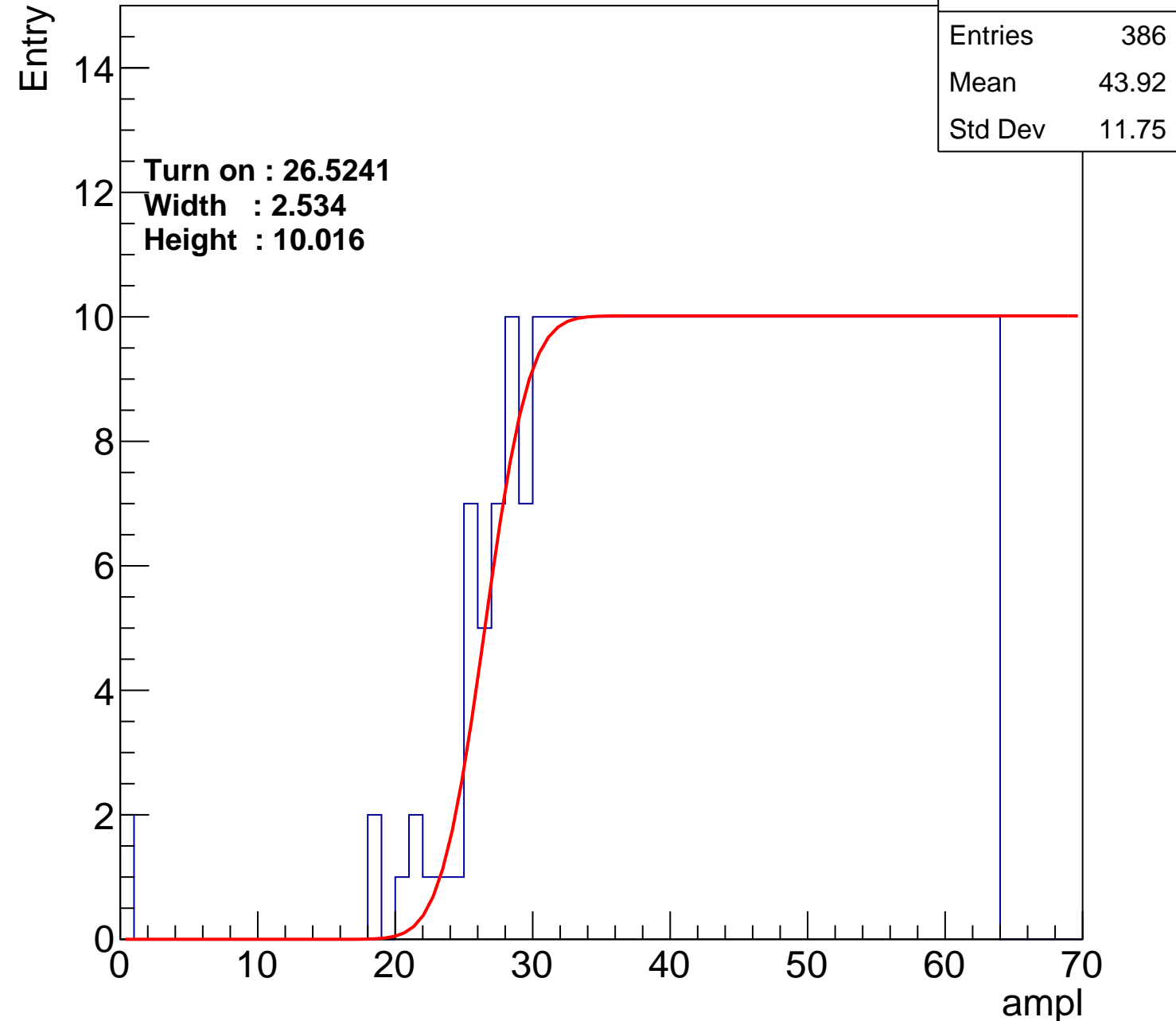
Width : 2.534

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.9
Std Dev	11.9

Turn on : 26.1992

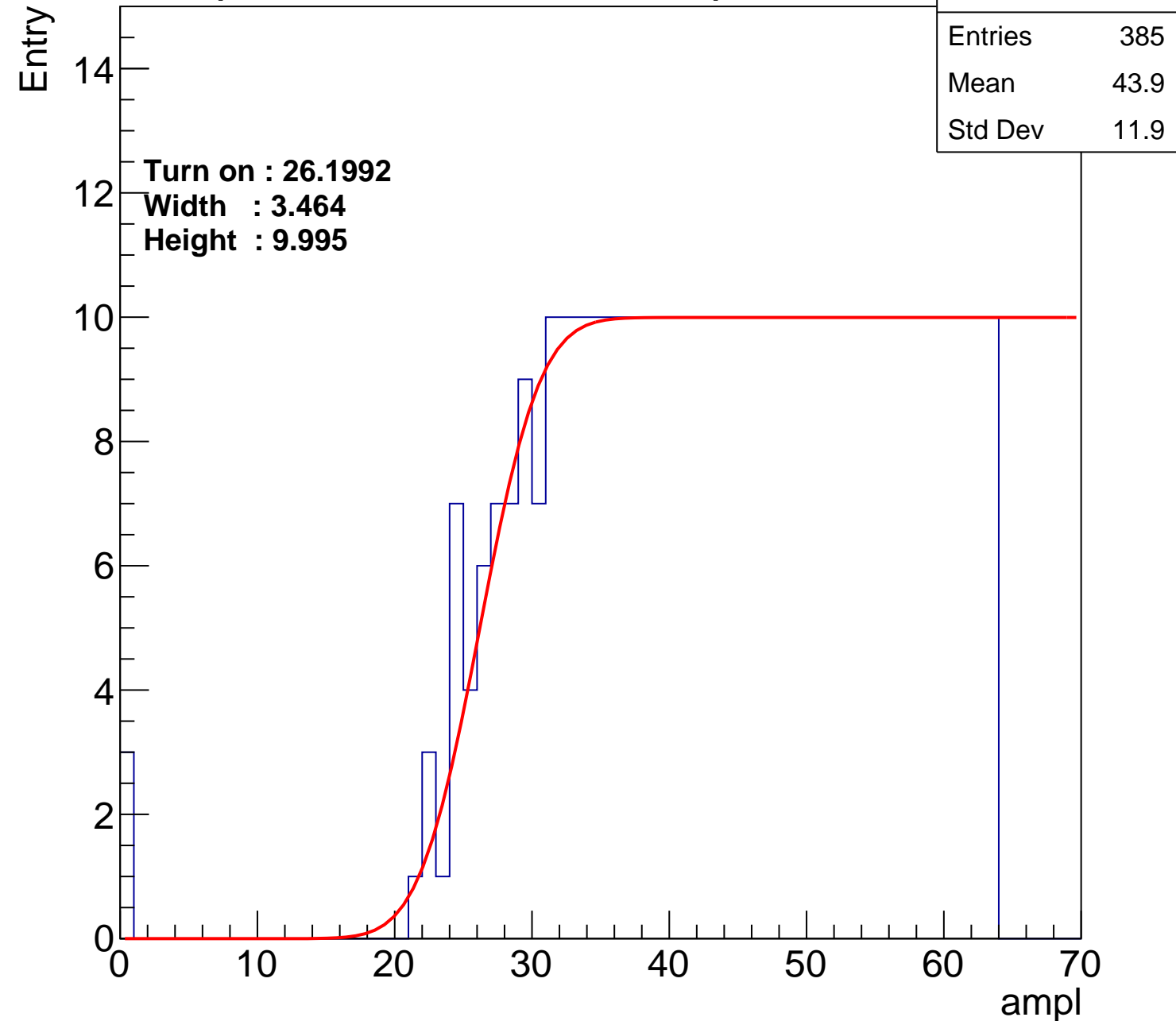
Width : 3.464

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.39
Std Dev	11.85

Turn on : 24.1329

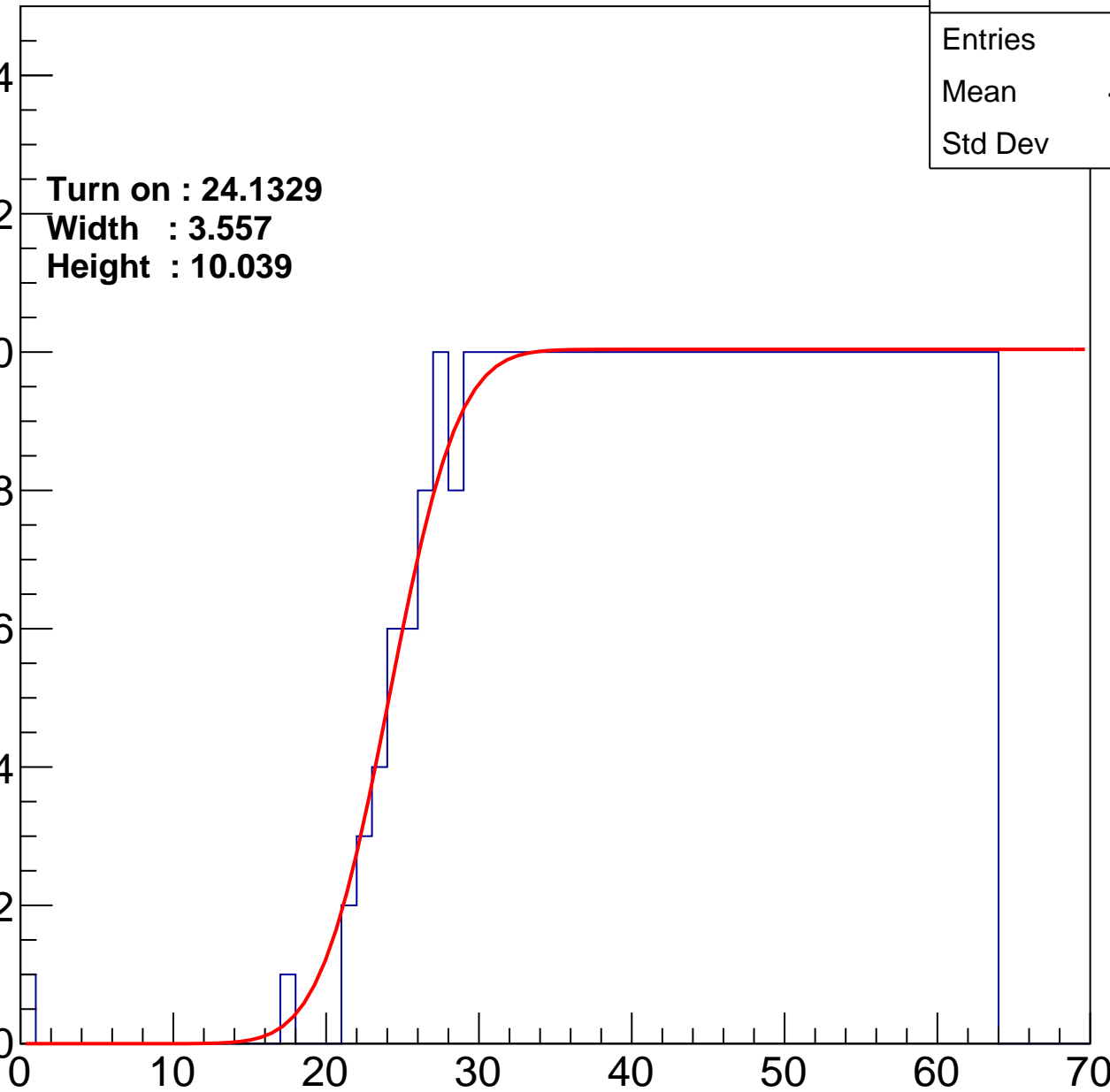
Width : 3.557

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.4780

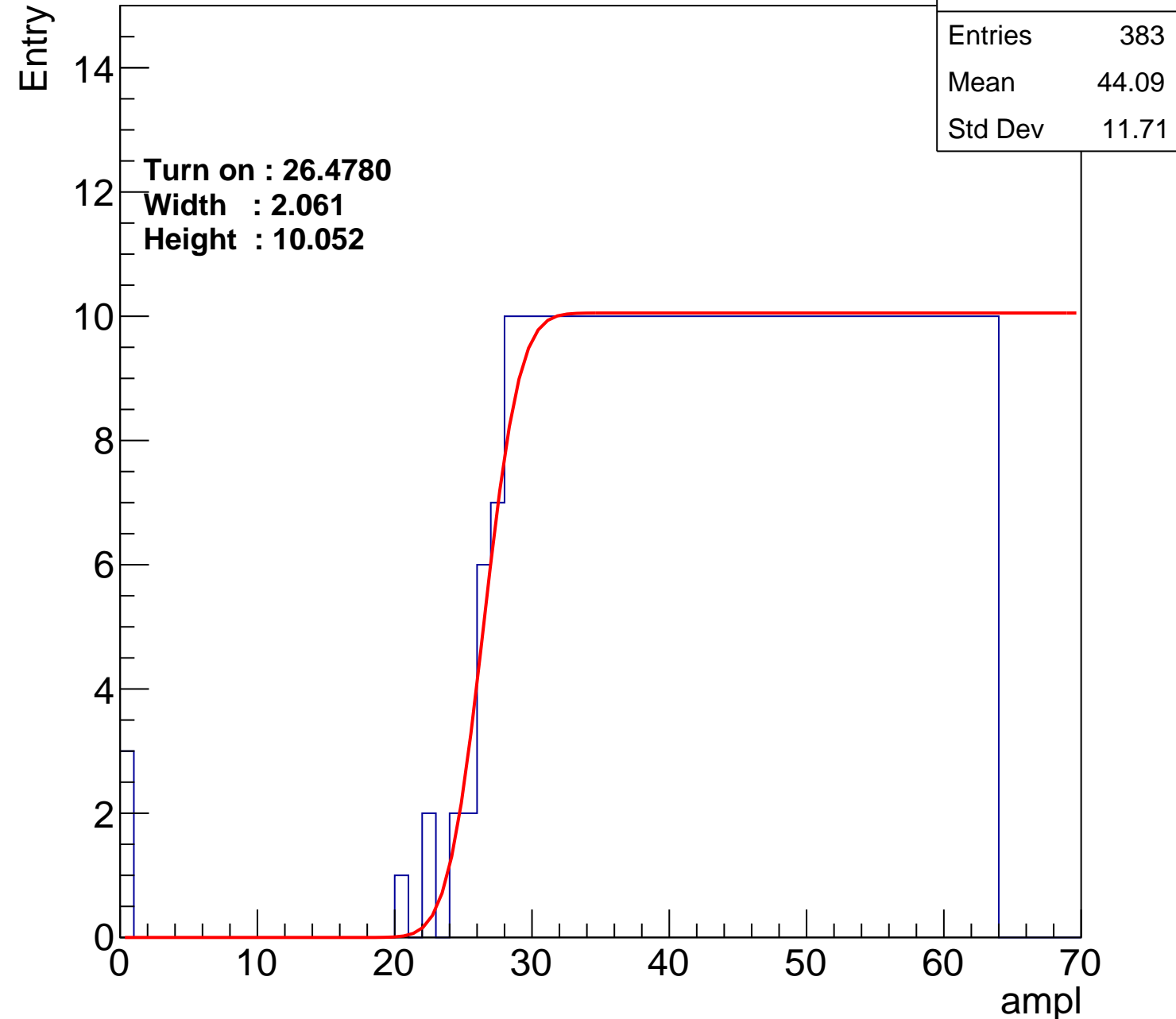
Width : 2.061

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch110

calib_packv5_042523_0143.root, FC#12, port B1

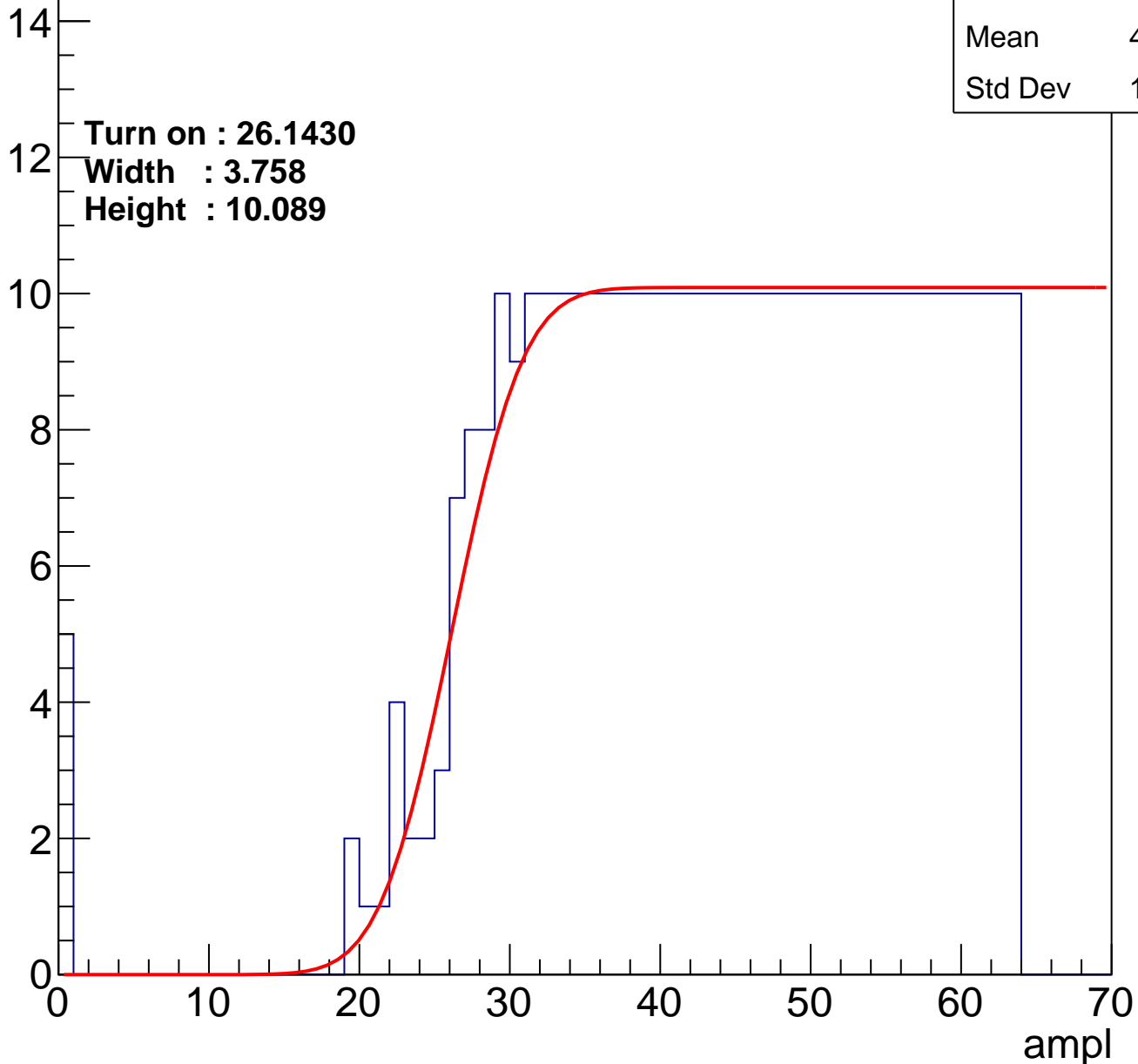
Entries	392
Mean	43.44
Std Dev	12.39

Turn on : 26.1430

Width : 3.758

Height : 10.089

Entry



B0L102S, U14-ch111

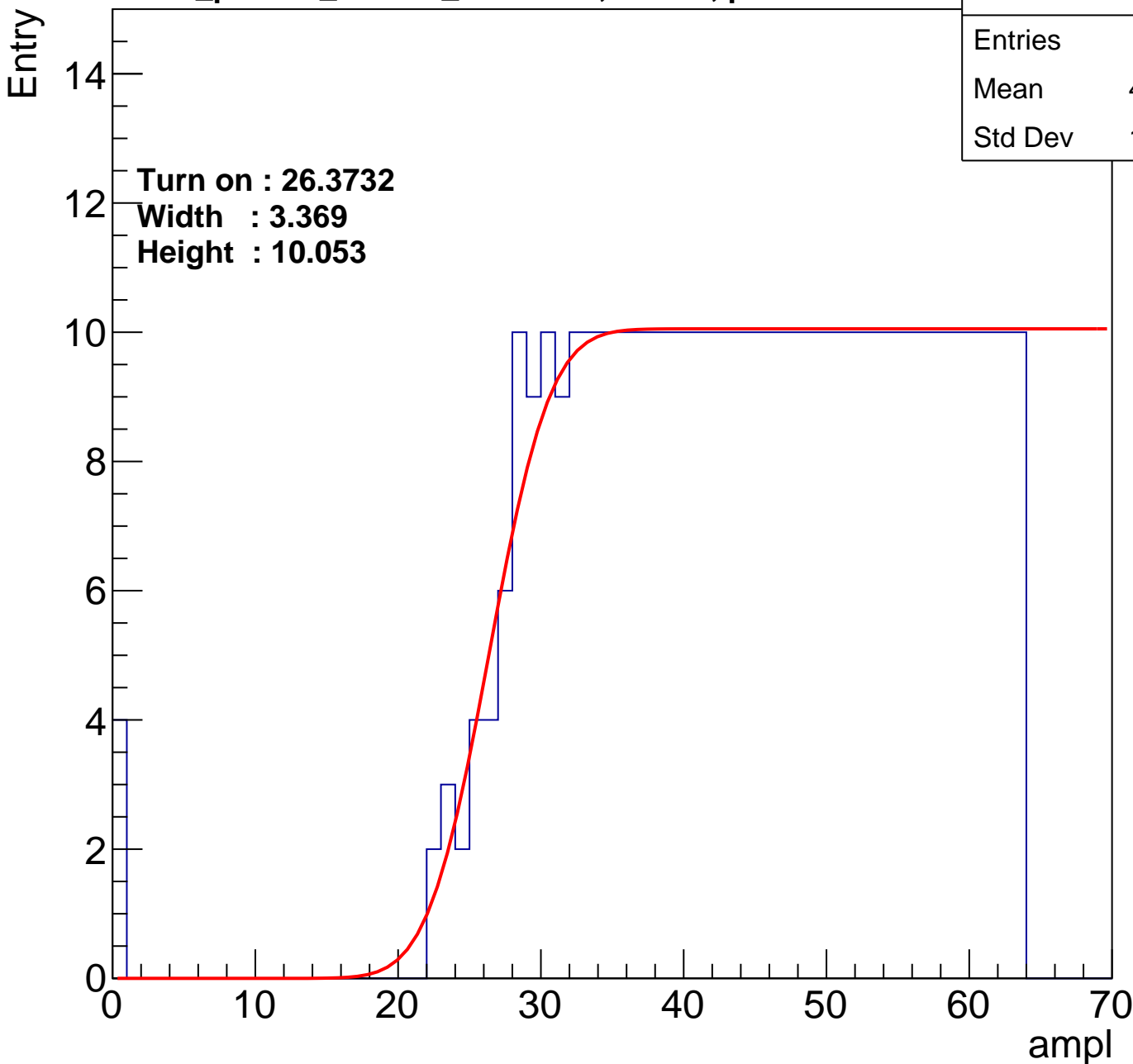
calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.99
Std Dev	11.94

Turn on : 26.3732

Width : 3.369

Height : 10.053



B0L102S, U14-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.18
Std Dev	12.14

Turn on : 24.4357

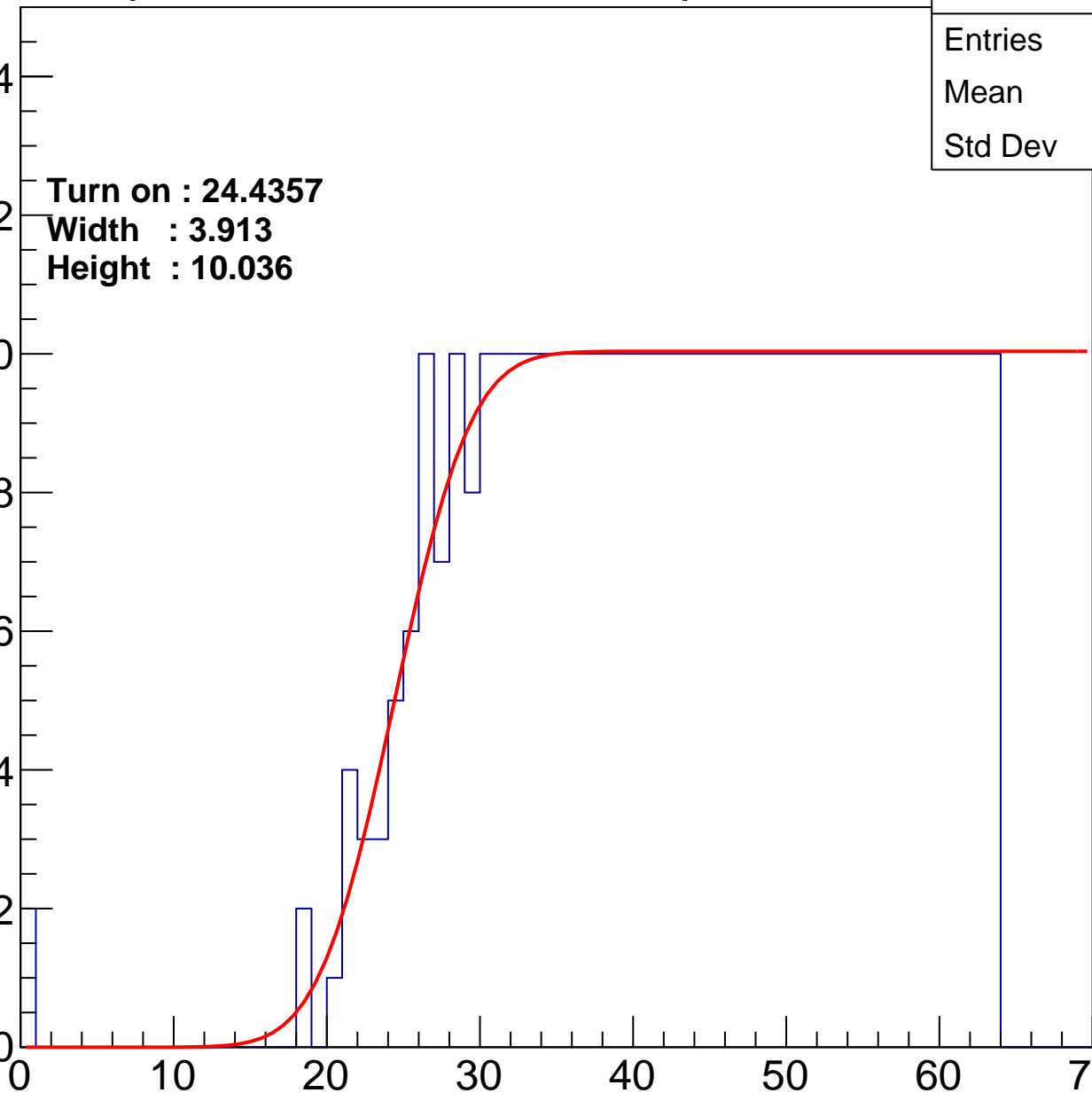
Width : 3.913

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.72
Std Dev	12.55

Turn on : 23.7028

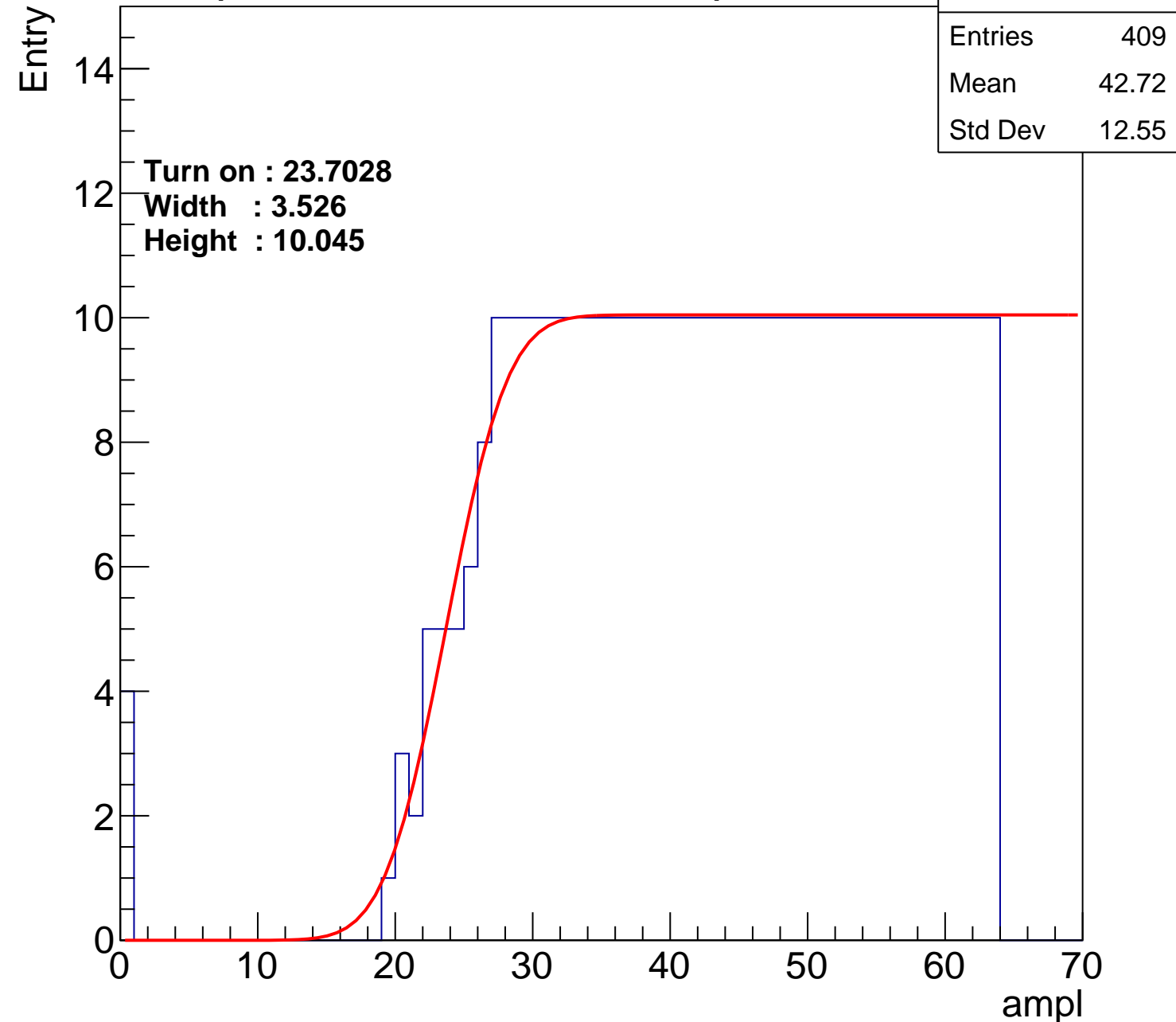
Width : 3.526

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch114

calib_packv5_042523_0143.root, FC#12, port B1

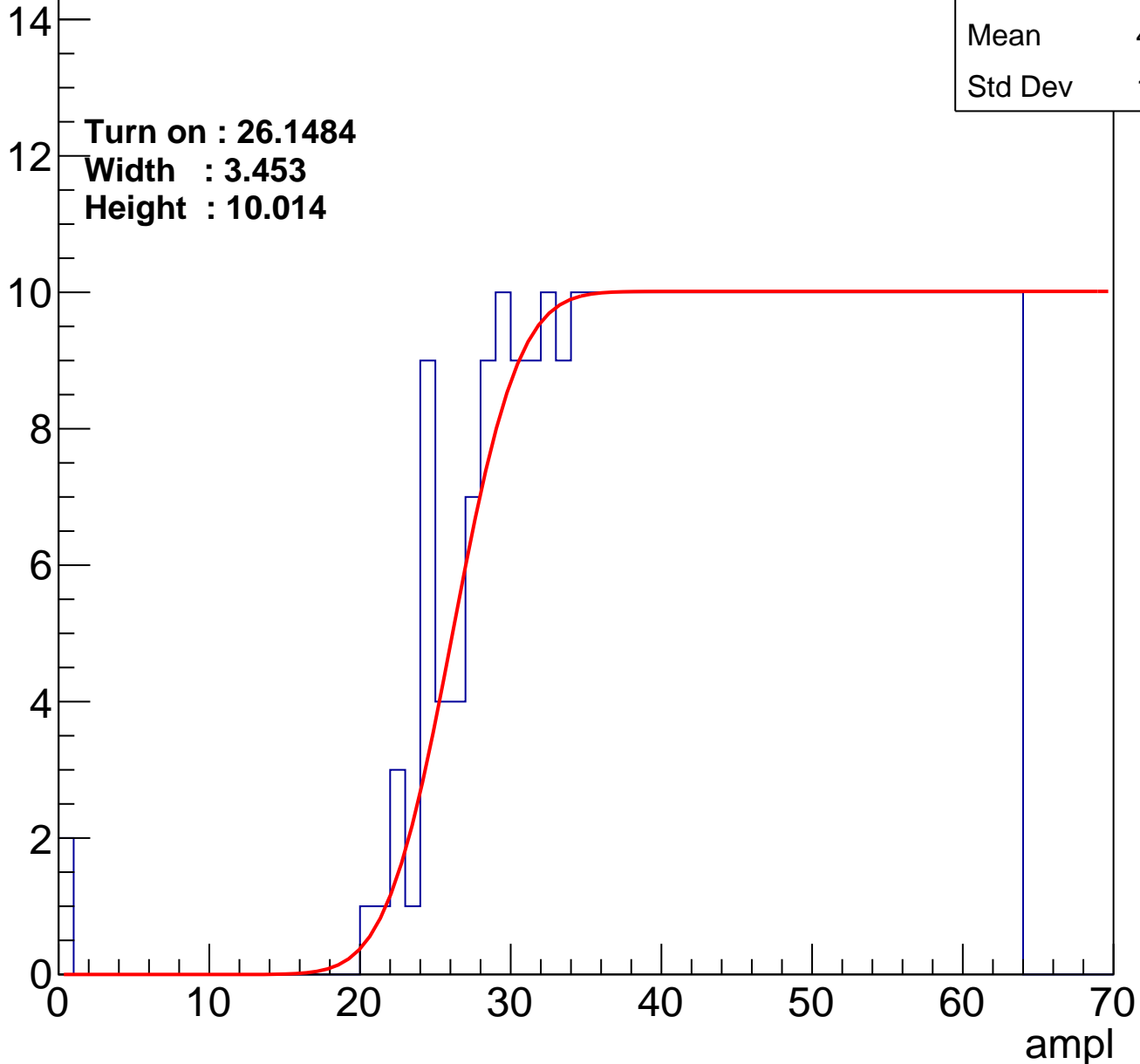
Entries	388
Mean	43.81
Std Dev	11.81

Turn on : 26.1484

Width : 3.453

Height : 10.014

Entry



B0L102S, U14-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	42.66
Std Dev	13.26

Turn on : 24.8932

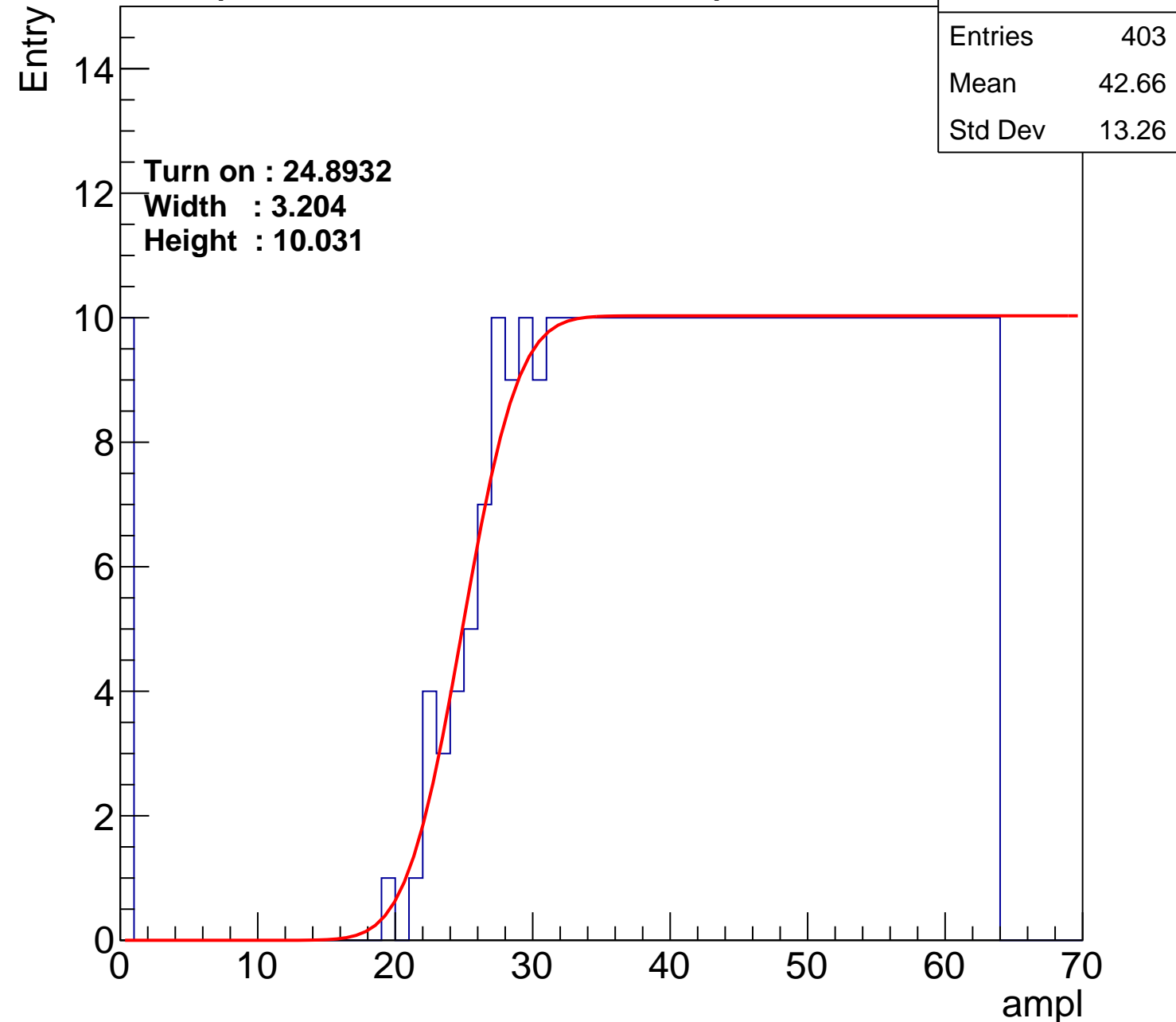
Width : 3.204

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch116

calib_packv5_042523_0143.root, FC#12, port B1

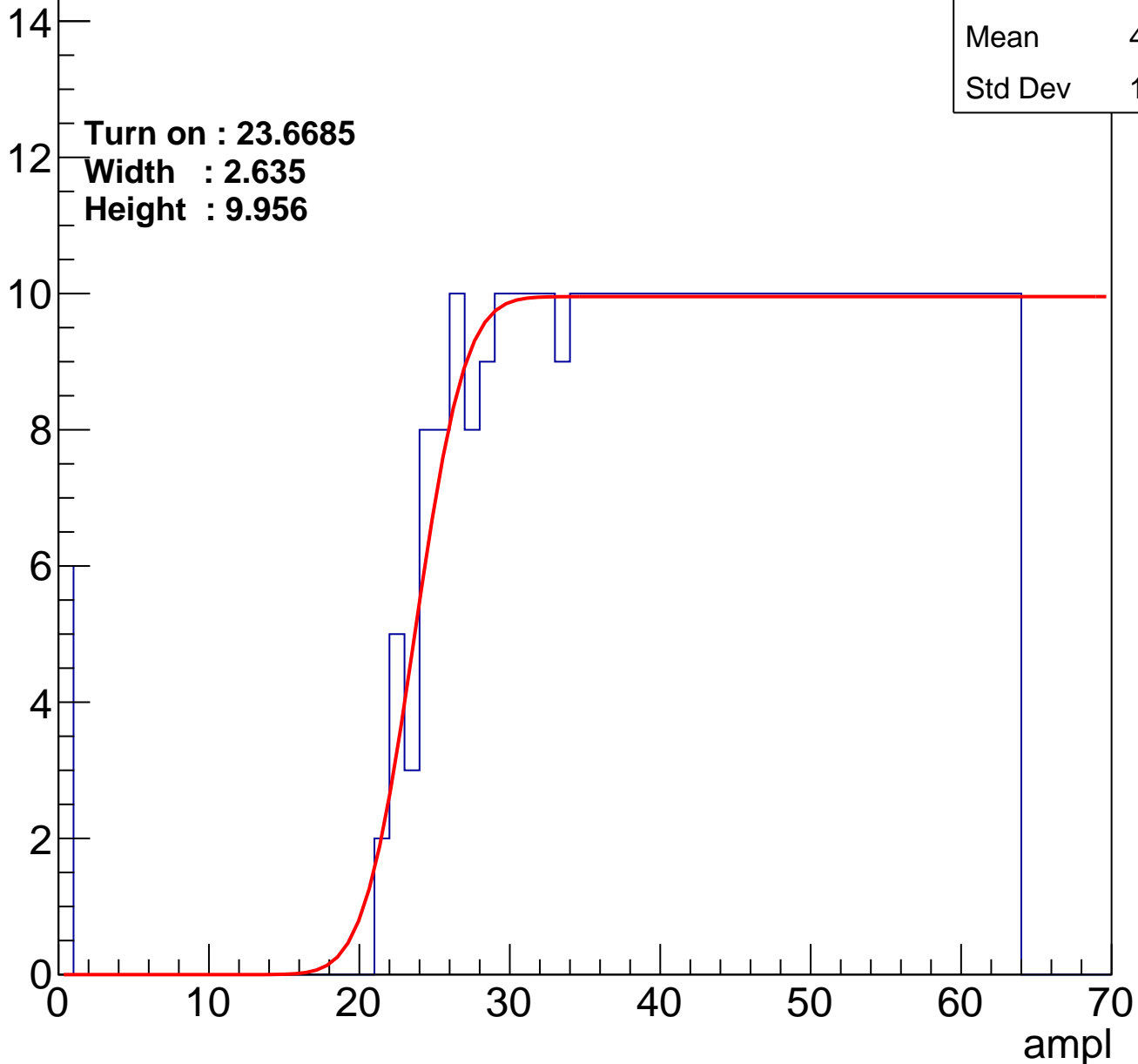
Entries	408
Mean	42.67
Std Dev	12.78

Turn on : 23.6685

Width : 2.635

Height : 9.956

Entry



B0L102S, U14-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.45
Std Dev	12.03

Turn on : 24.8447

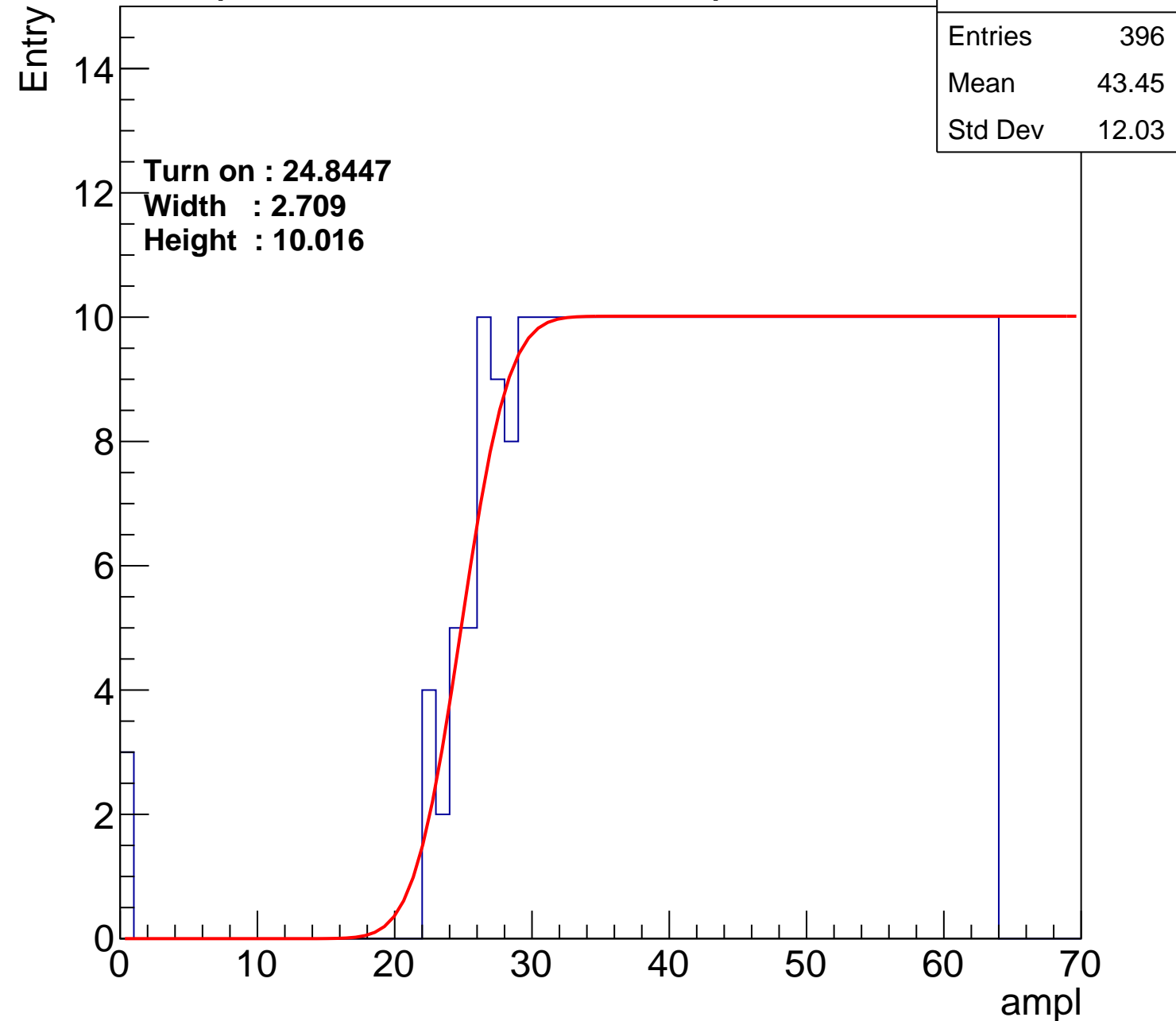
Width : 2.709

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch118

calib_packv5_042523_0143.root, FC#12, port B1

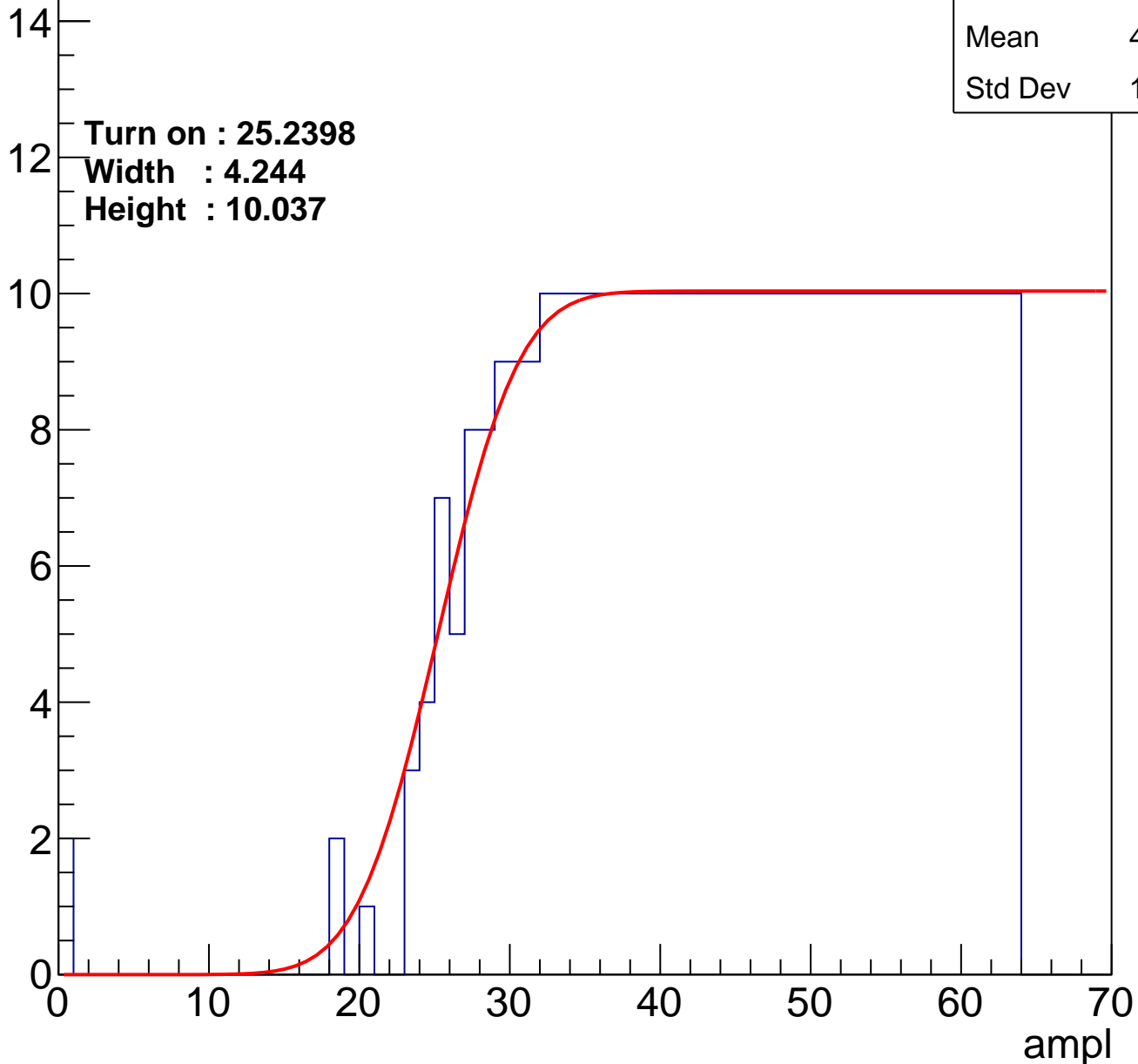
Entries	387
Mean	43.87
Std Dev	11.78

Turn on : 25.2398

Width : 4.244

Height : 10.037

Entry



B0L102S, U14-ch119

calib_packv5_042523_0143.root, FC#12, port B1

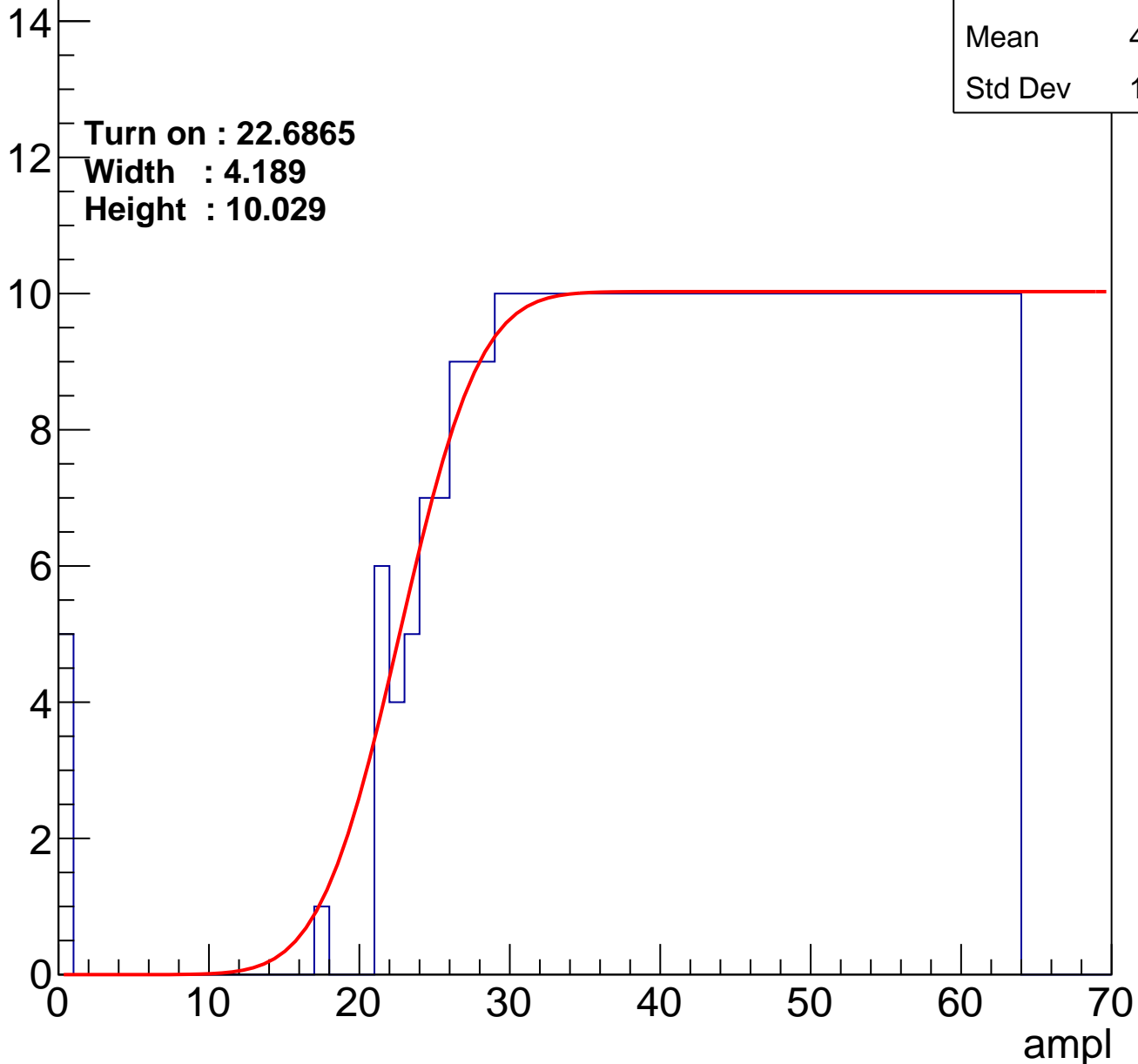
Entries	412
Mean	42.52
Std Dev	12.76

Turn on : 22.6865

Width : 4.189

Height : 10.029

Entry



B0L102S, U14-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.48
Std Dev	13.03

Turn on : 24.2585

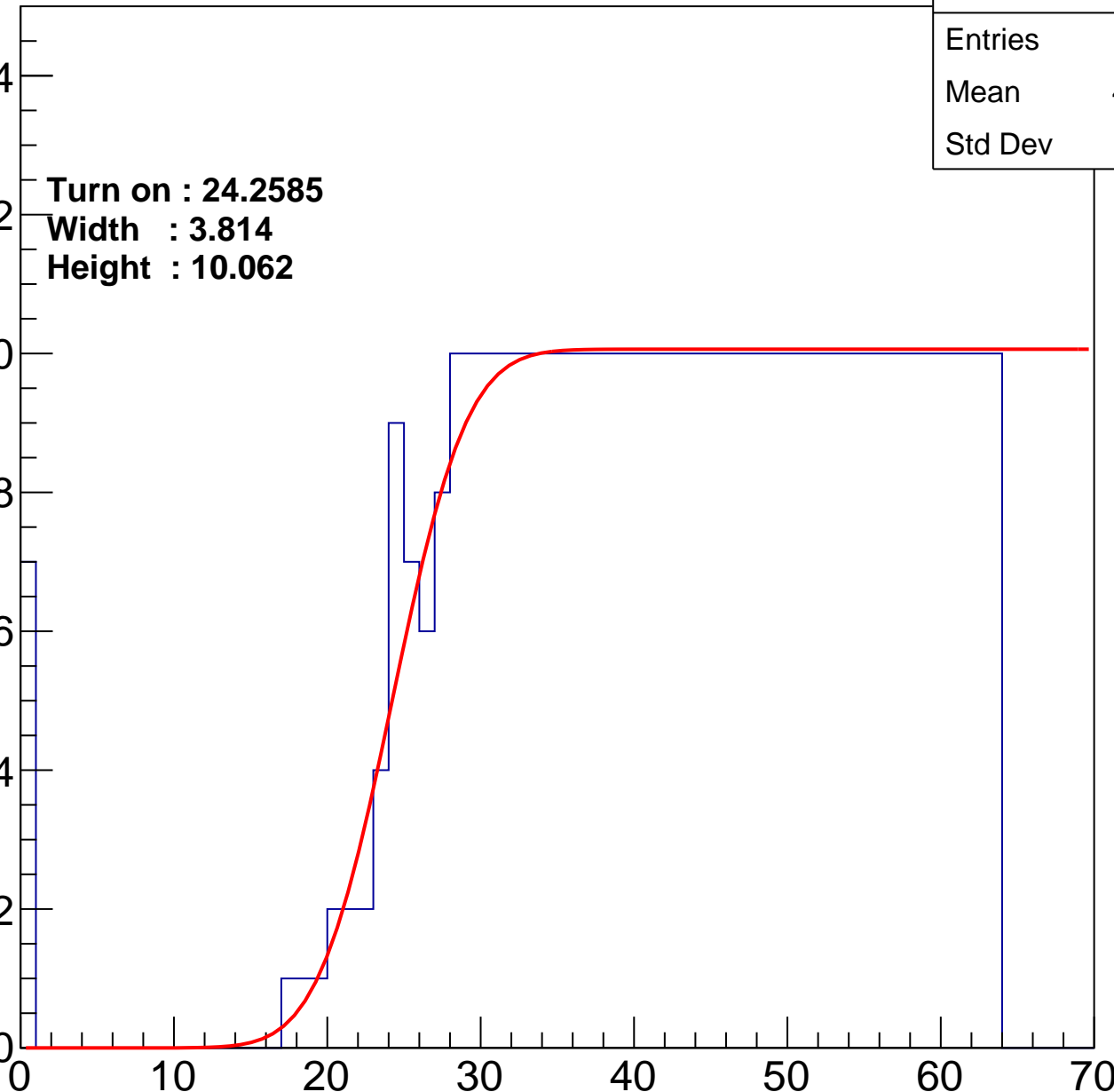
Width : 3.814

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.42
Std Dev	11.85

Turn on : 24.4897

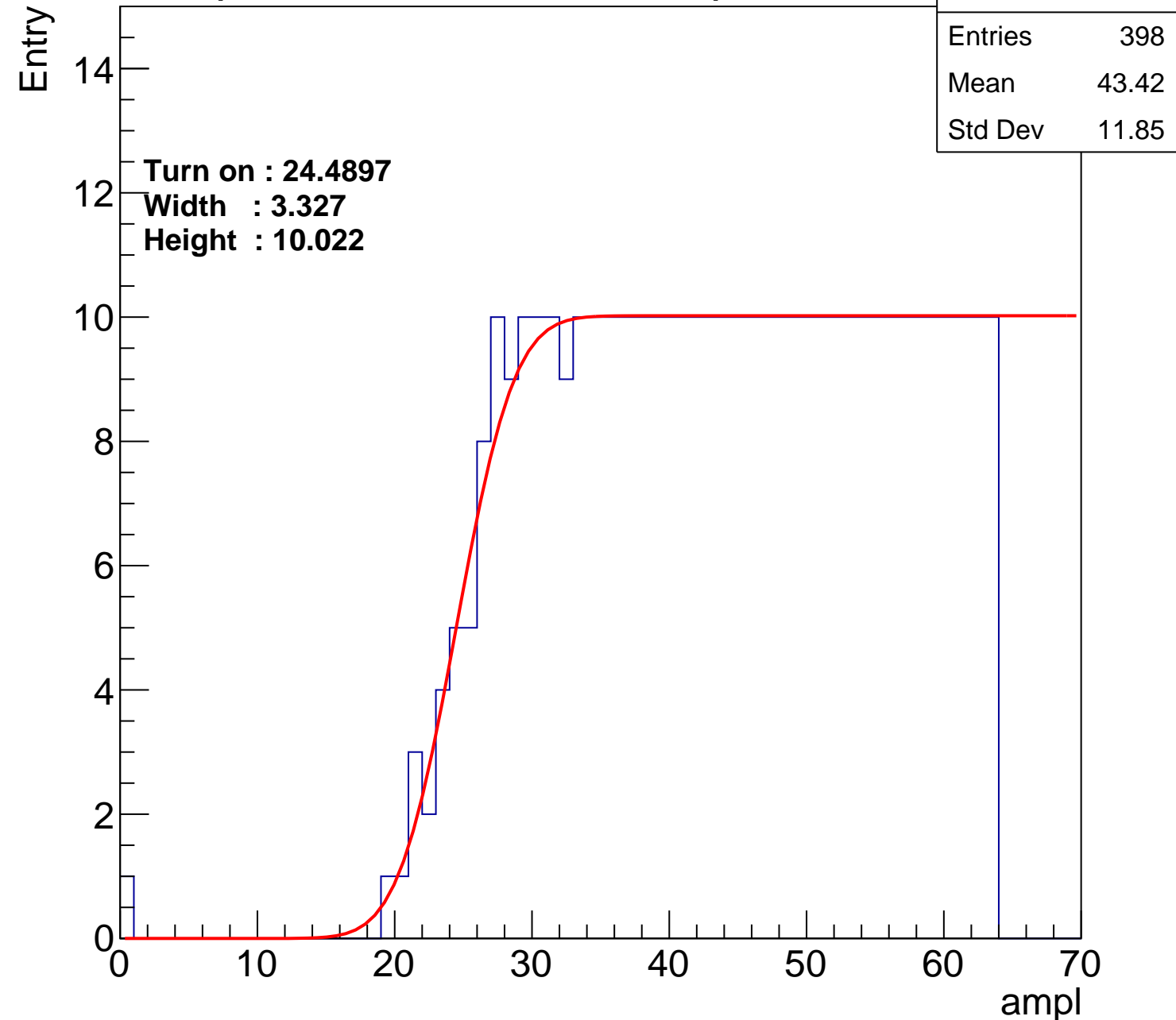
Width : 3.327

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch122

calib_packv5_042523_0143.root, FC#12, port B1

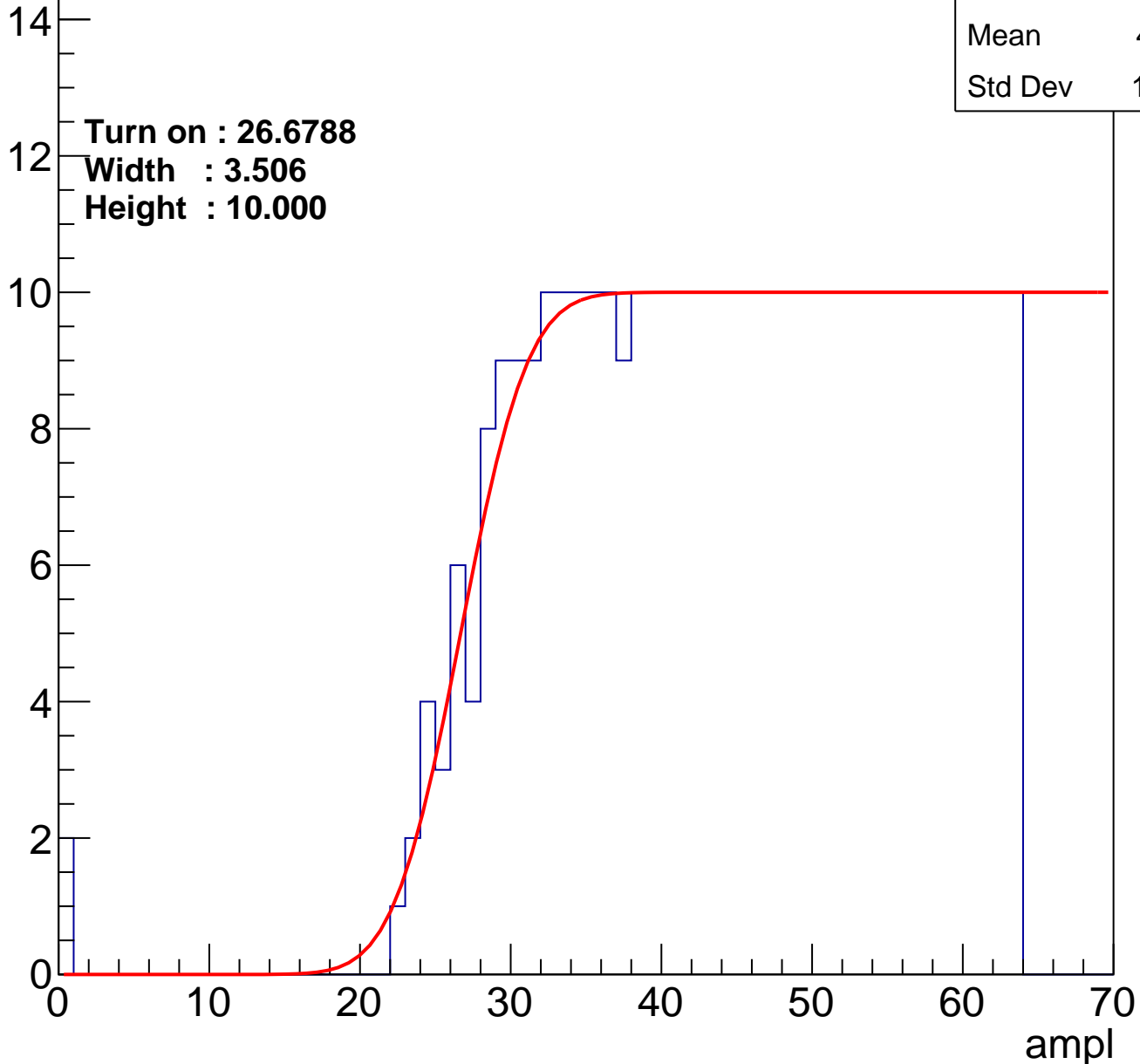
Entries	376
Mean	44.41
Std Dev	11.47

Turn on : 26.6788

Width : 3.506

Height : 10.000

Entry



B0L102S, U14-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.98
Std Dev	12.34

Turn on : 23.7144

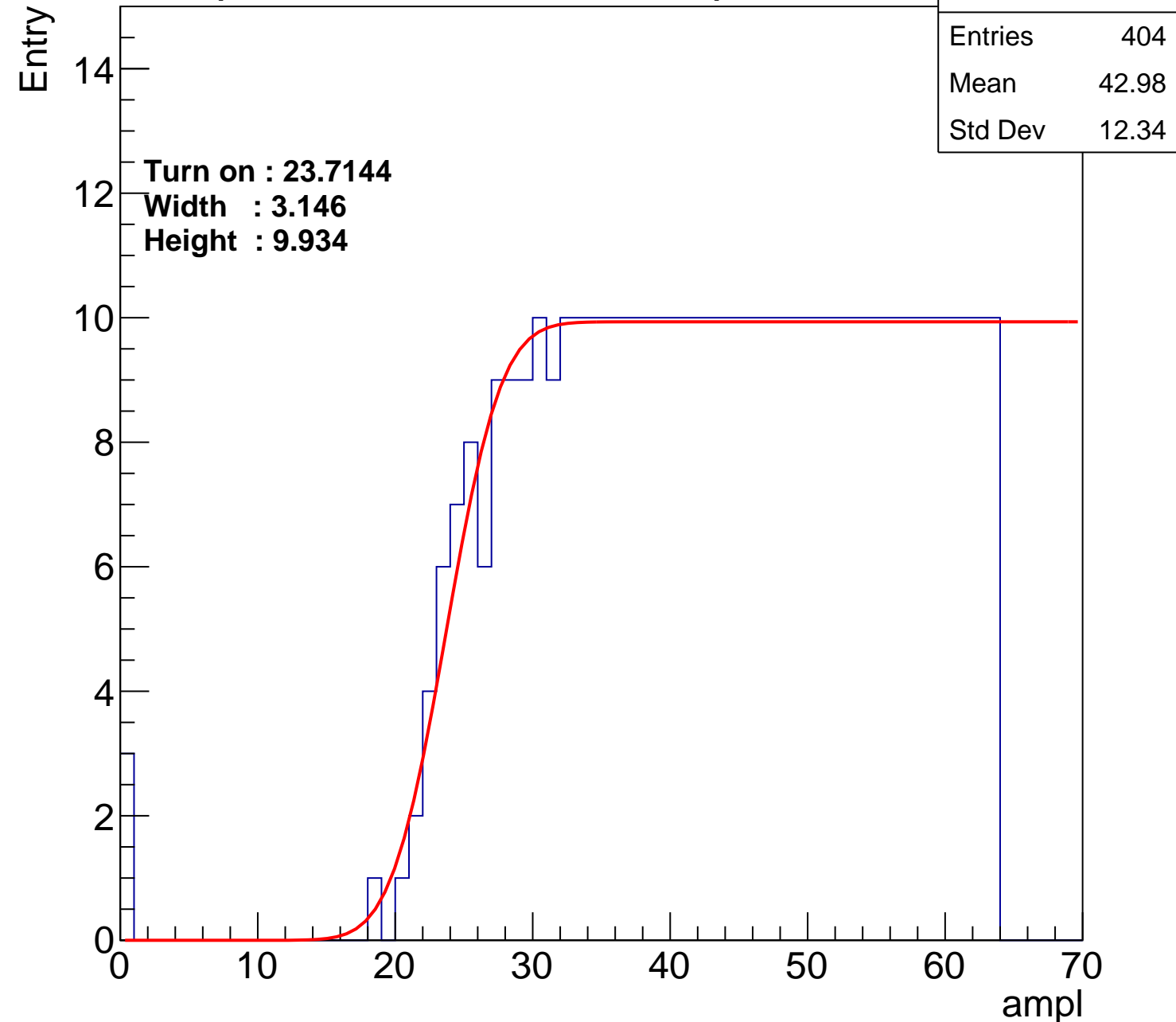
Width : 3.146

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.85
Std Dev	11.81

Turn on : 27.3901

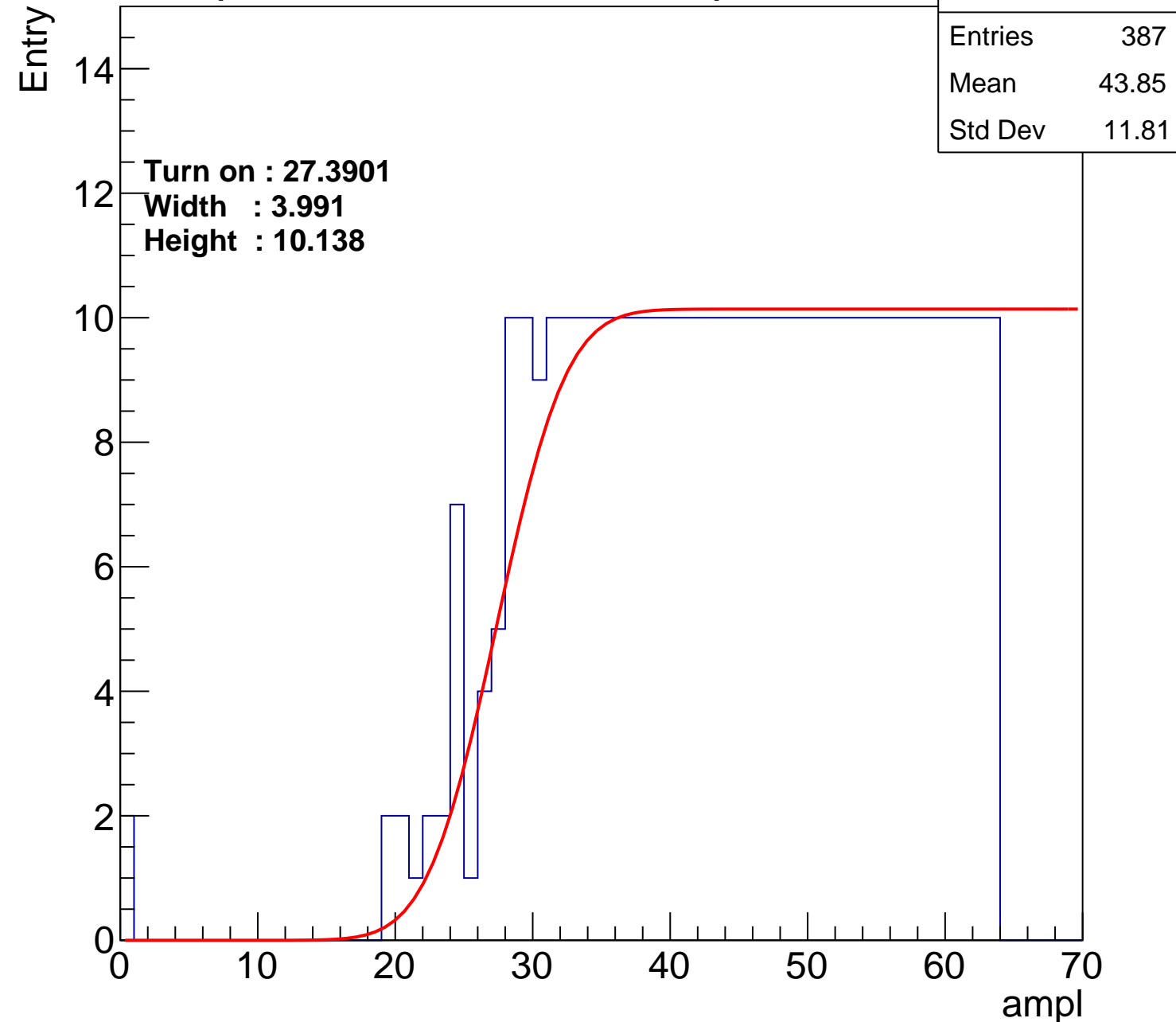
Width : 3.991

Height : 10.138

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.44
Std Dev	12.92

Turn on : 23.9520

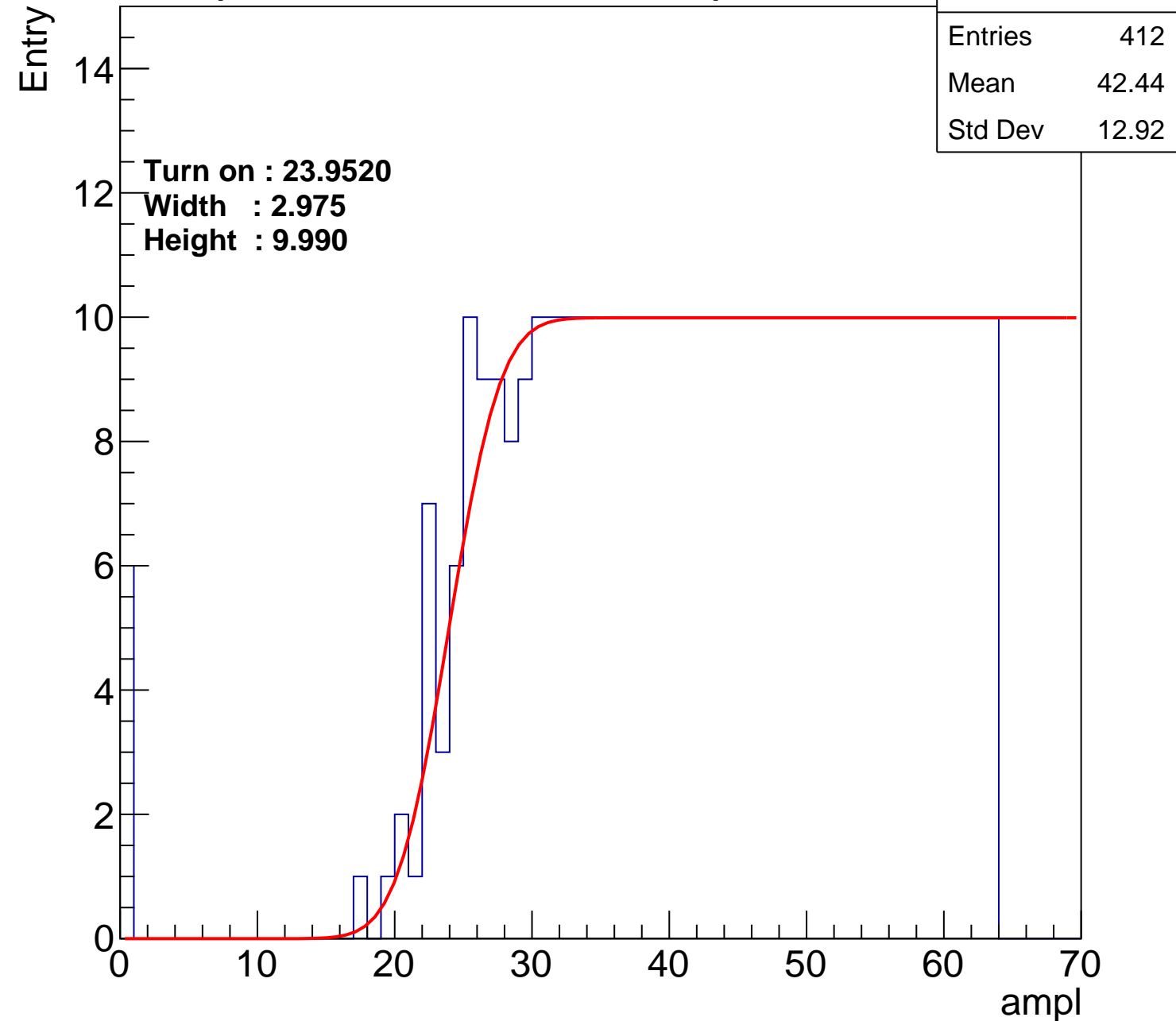
Width : 2.975

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	427
Mean	41.84
Std Dev	12.99

Turn on : 21.8006

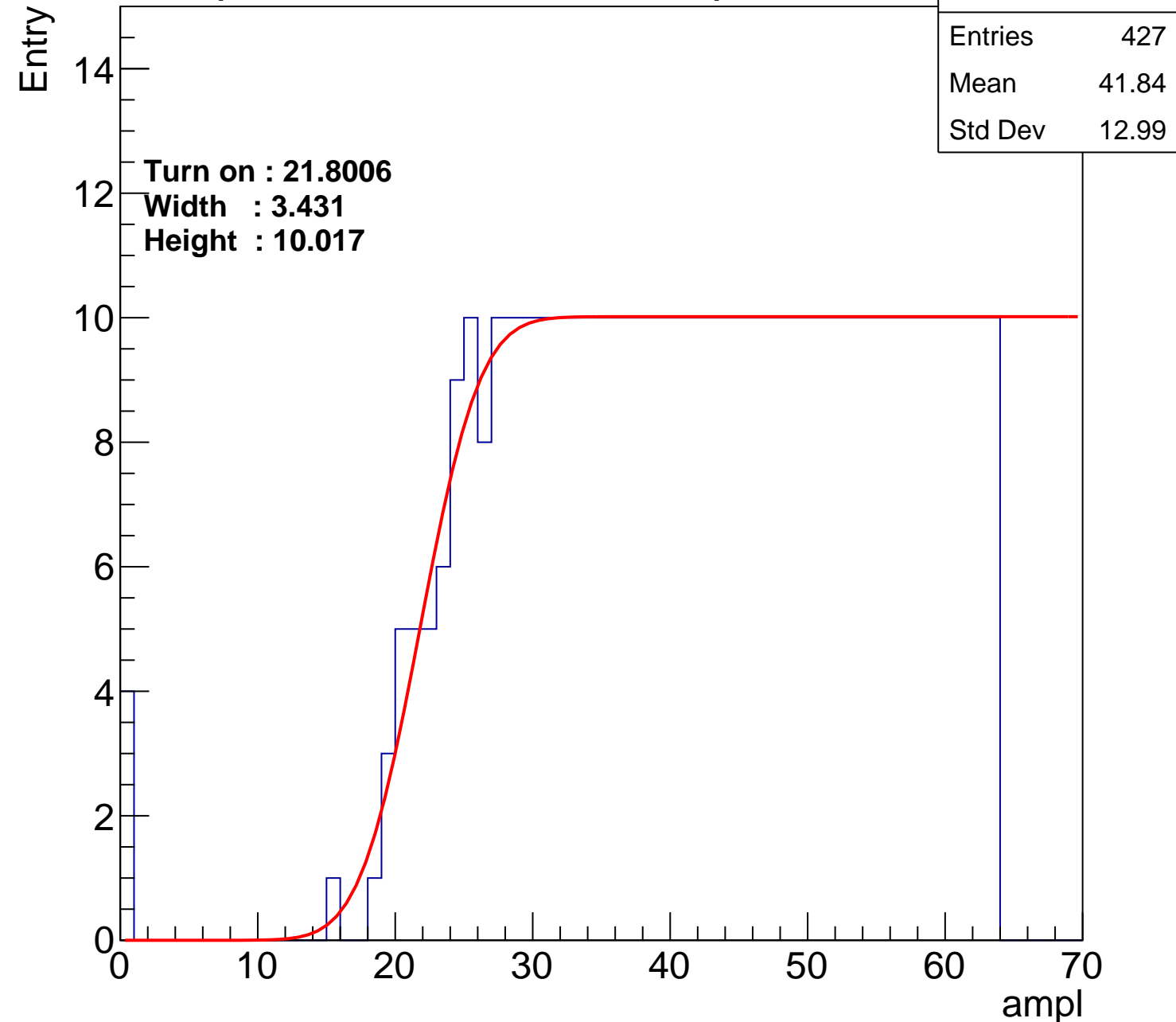
Width : 3.431

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.14
Std Dev	12.37

Turn on : 25.0984

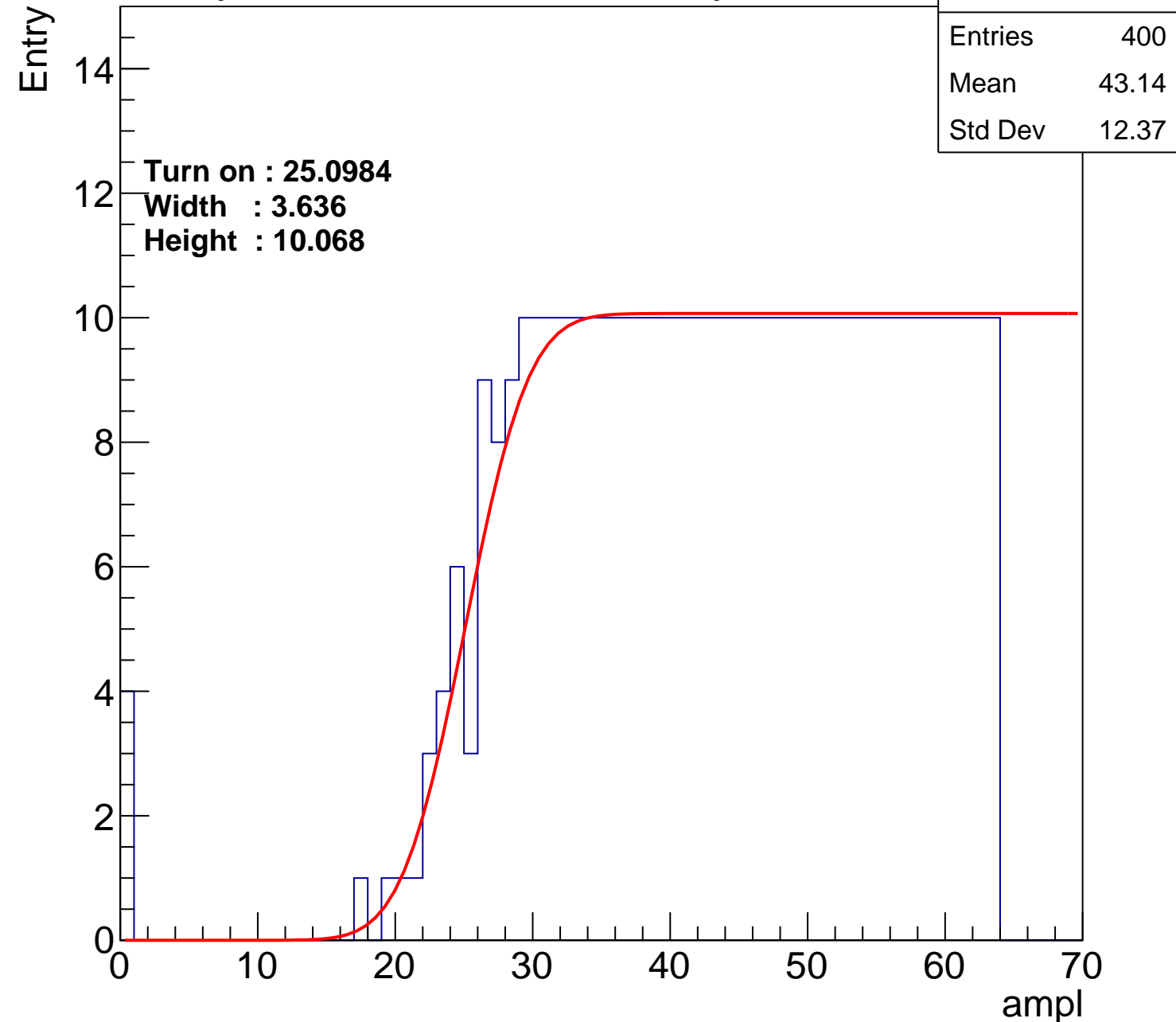
Width : 3.636

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U14-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.14
Std Dev	12.37

Turn on : 25.0984

Width : 3.636

Height : 10.068

Entry

