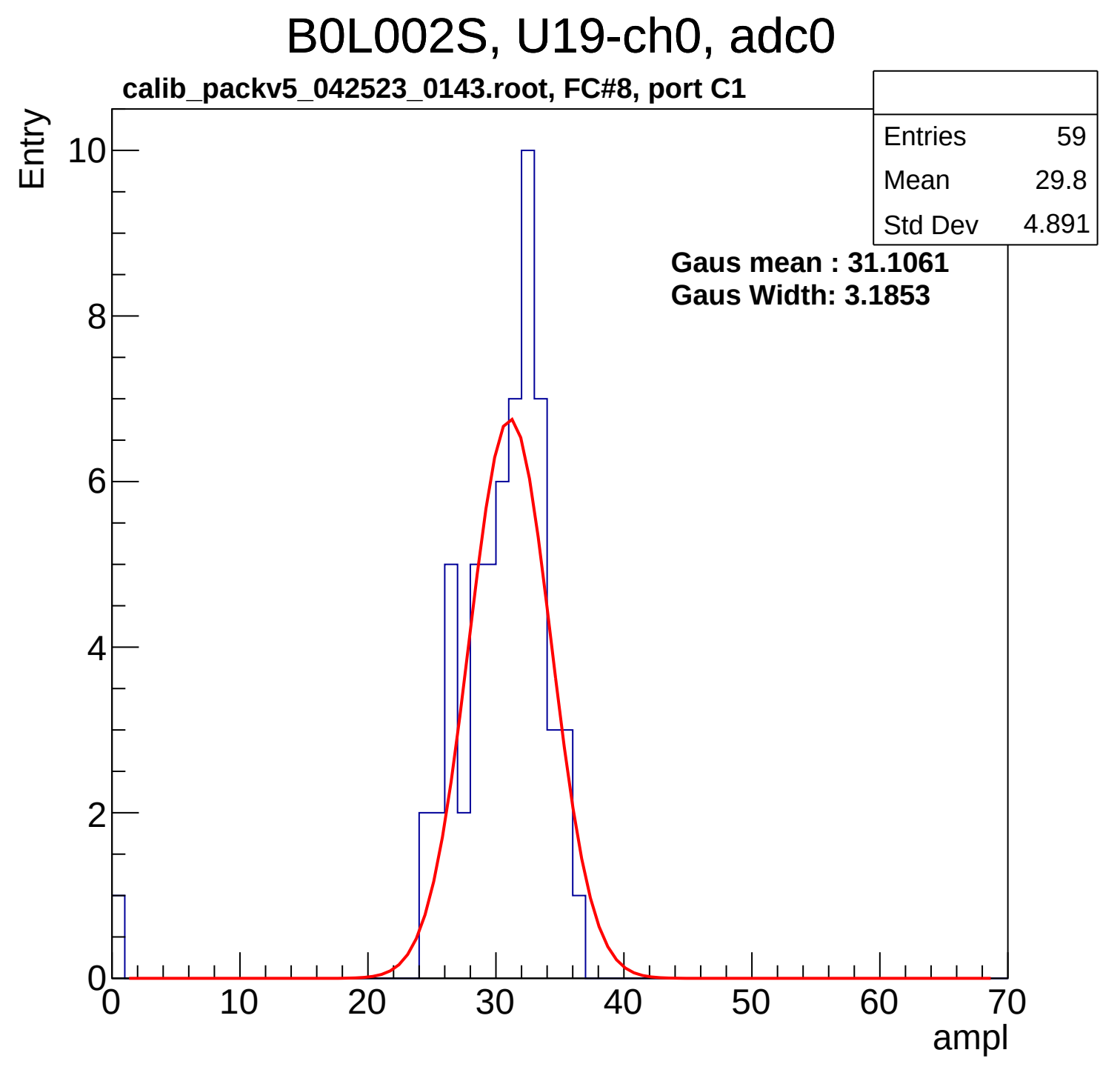
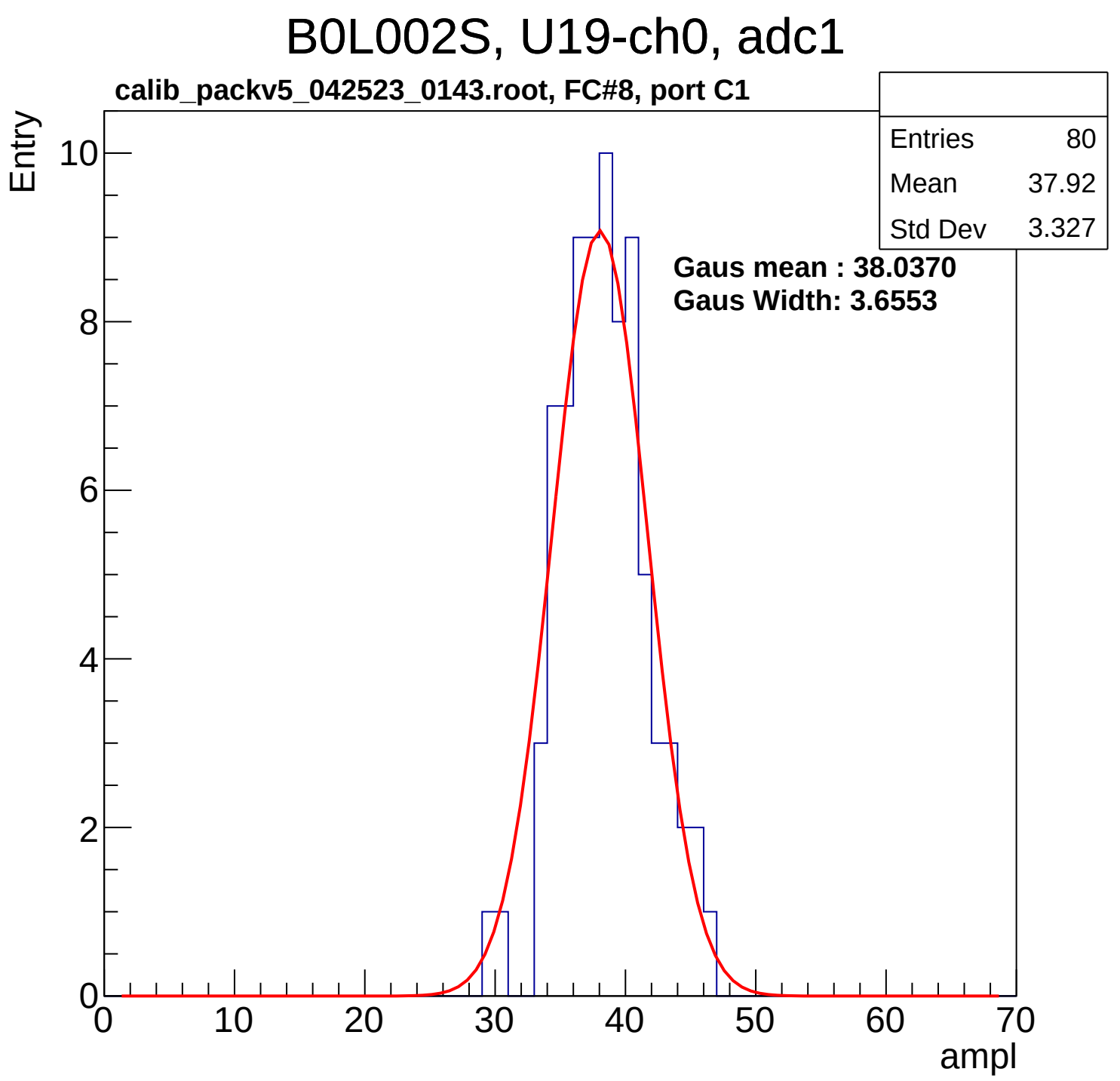
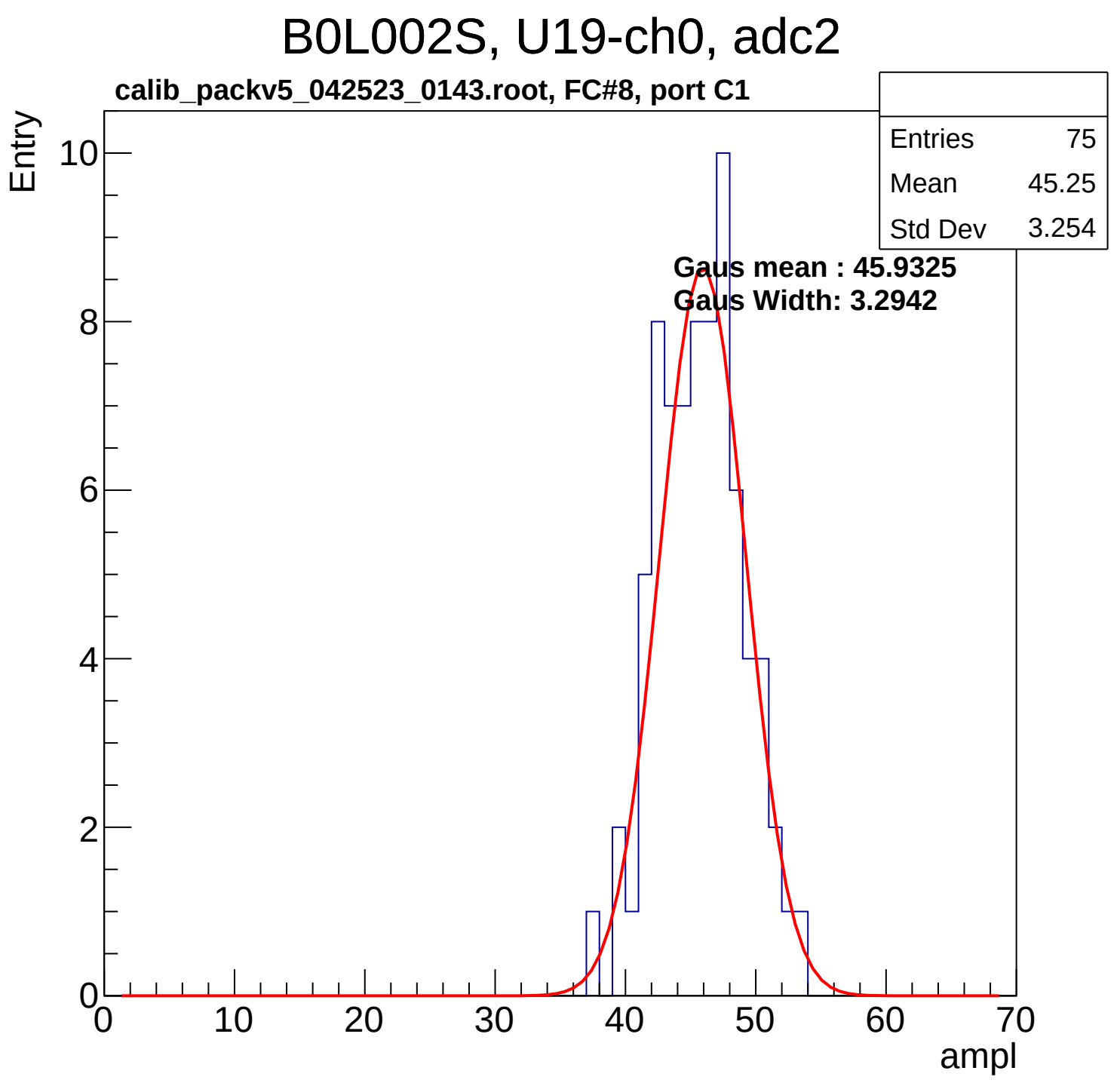
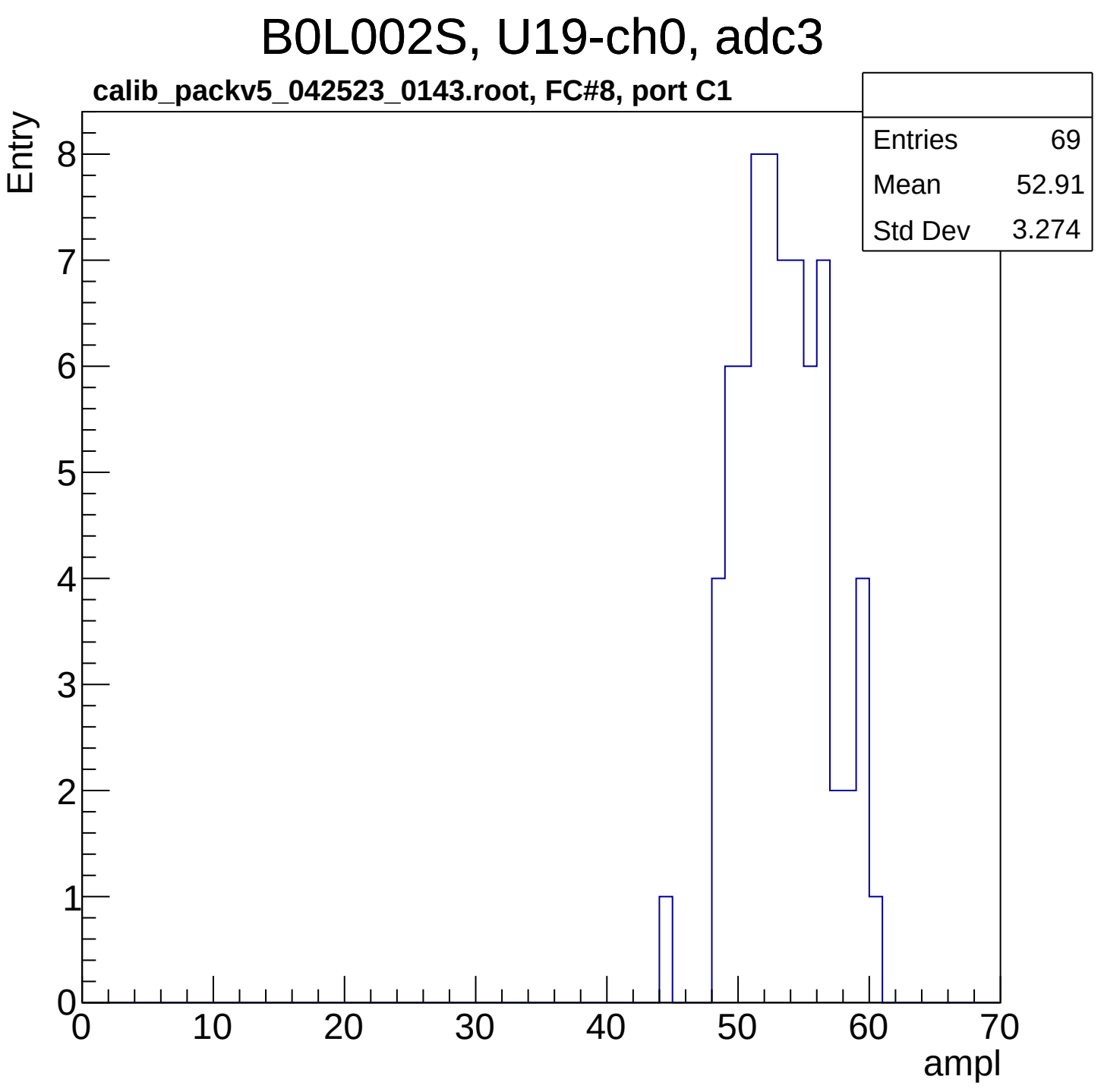
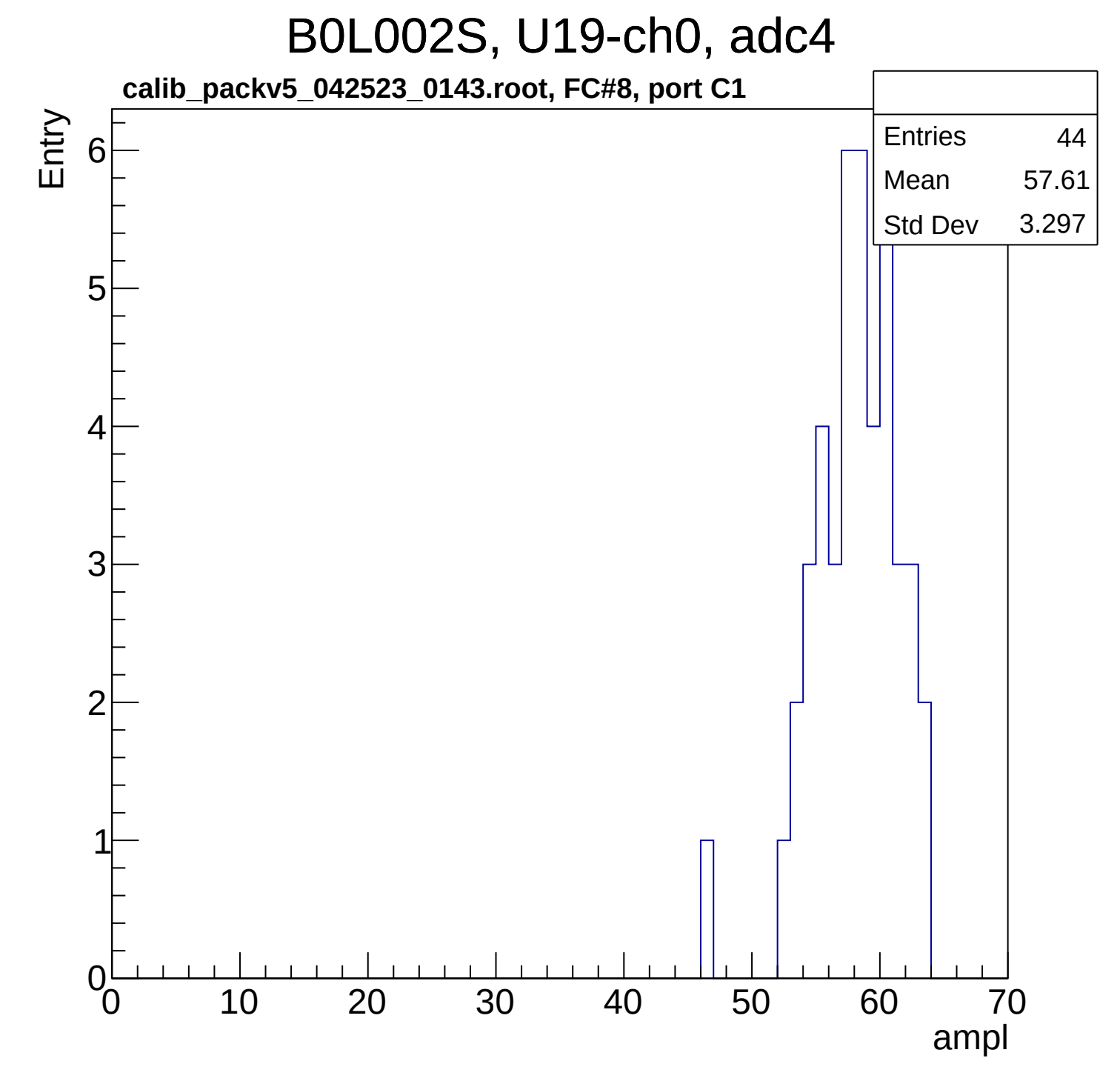
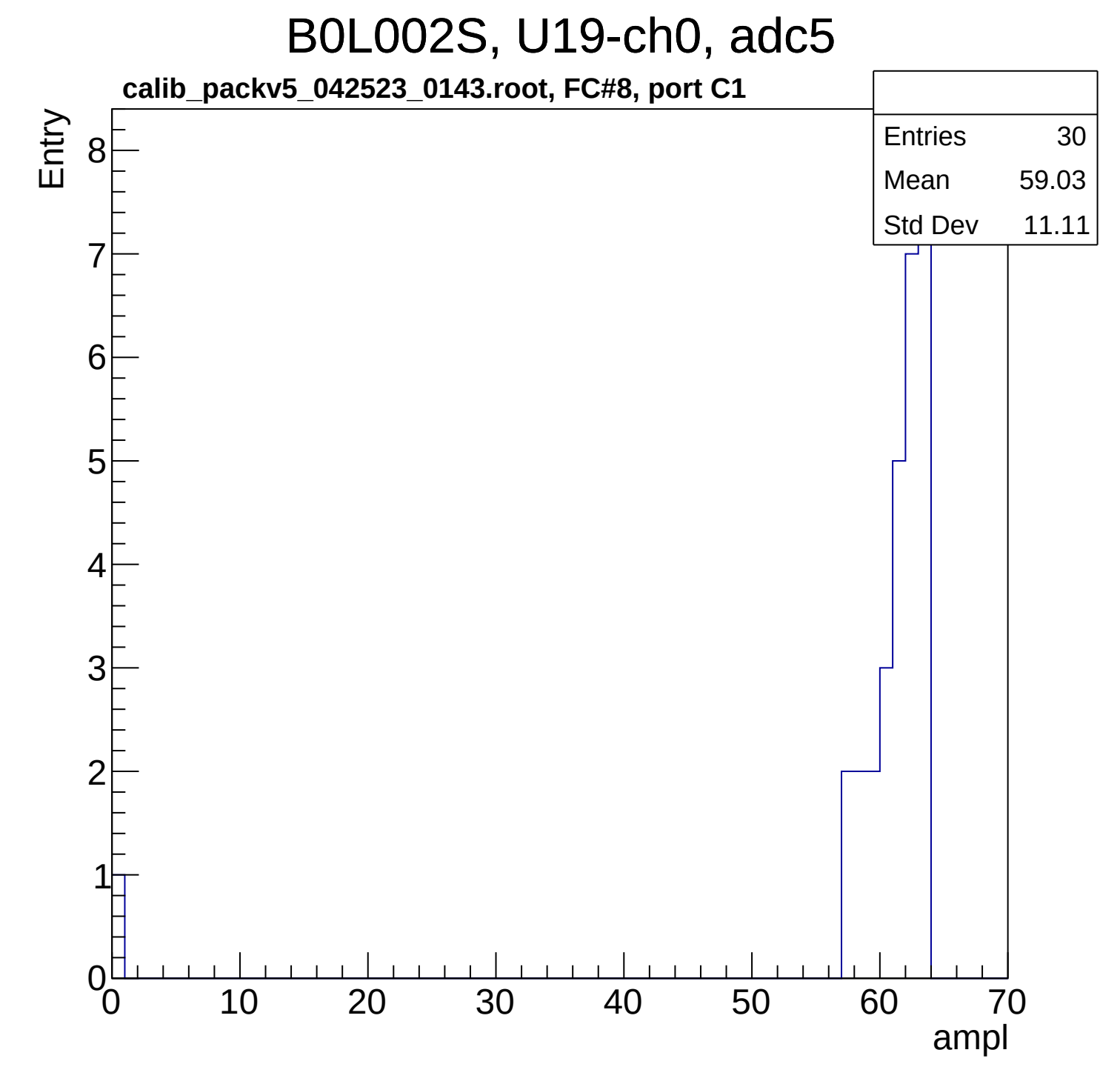

B0L002S, U19-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 29.8
Std Dev 4.891
Gaus mean : 31.1061
Gaus Width: 3.1853
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

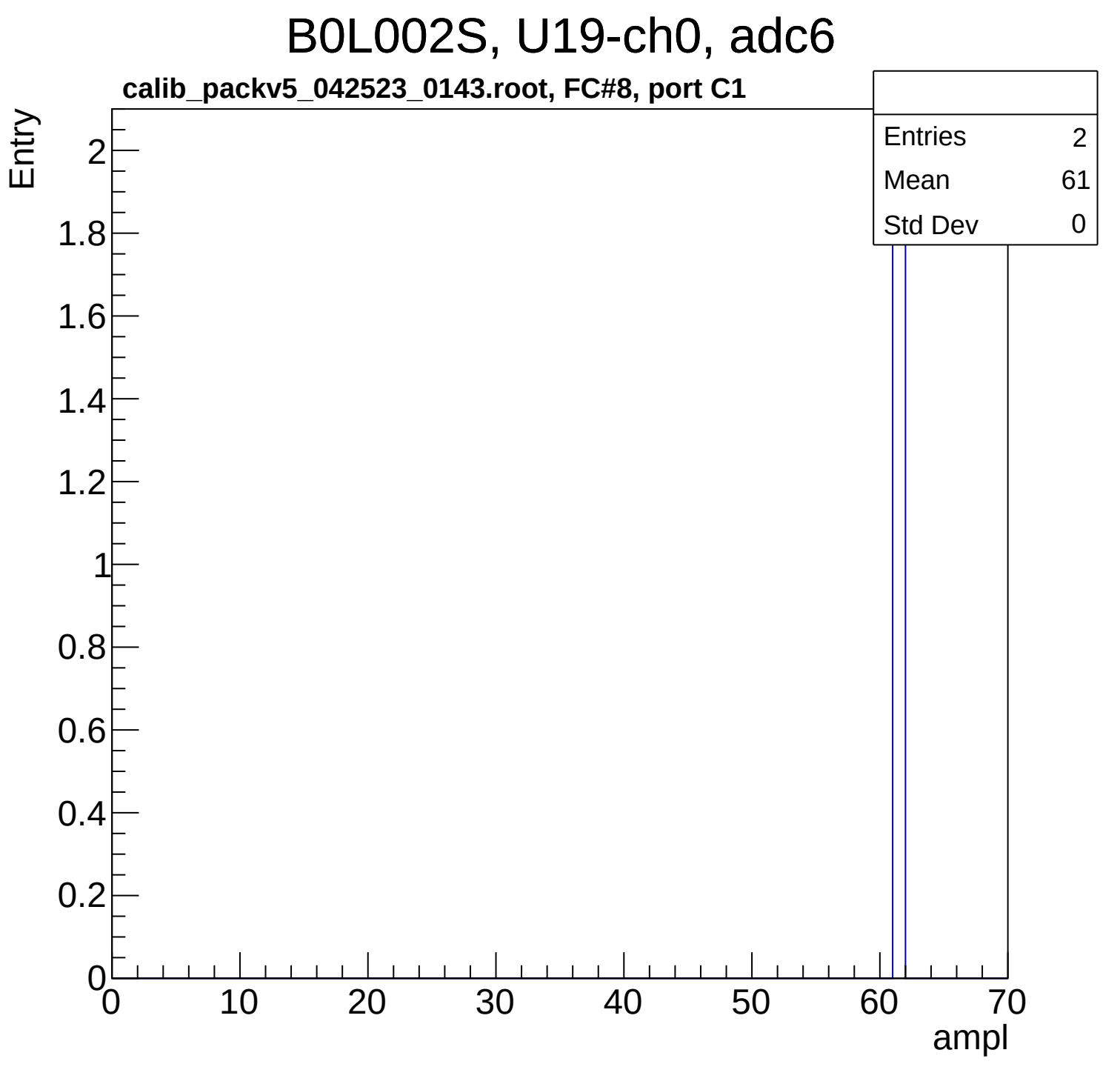

B0L002S, U19-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.92
Std Dev 3.327
Gaus mean : 38.0370
Gaus Width: 3.6553
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

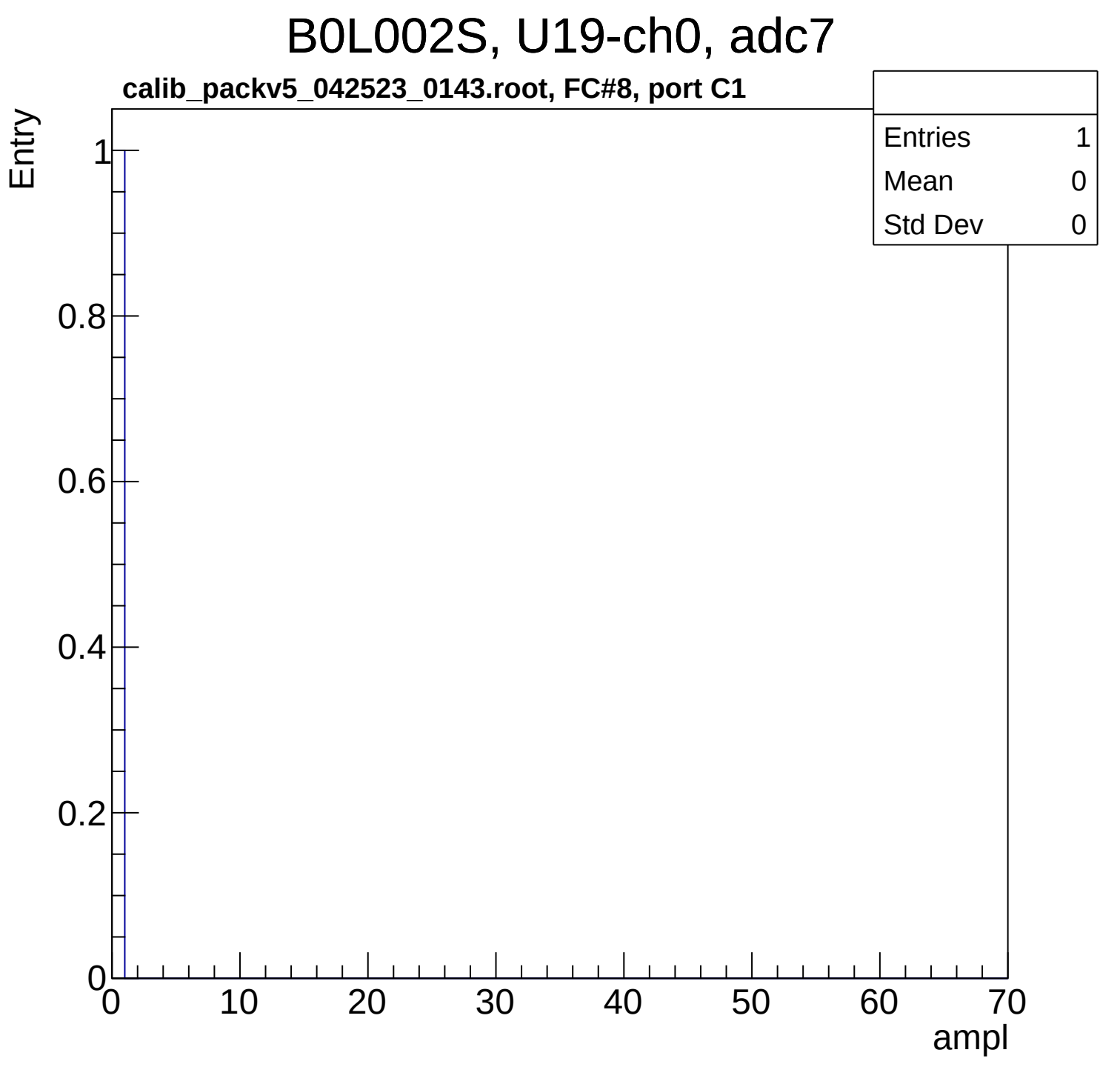

B0L002S, U19-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.25
Std Dev 3.254
Gaus mean : 45.9325
Gaus Width: 3.2942
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

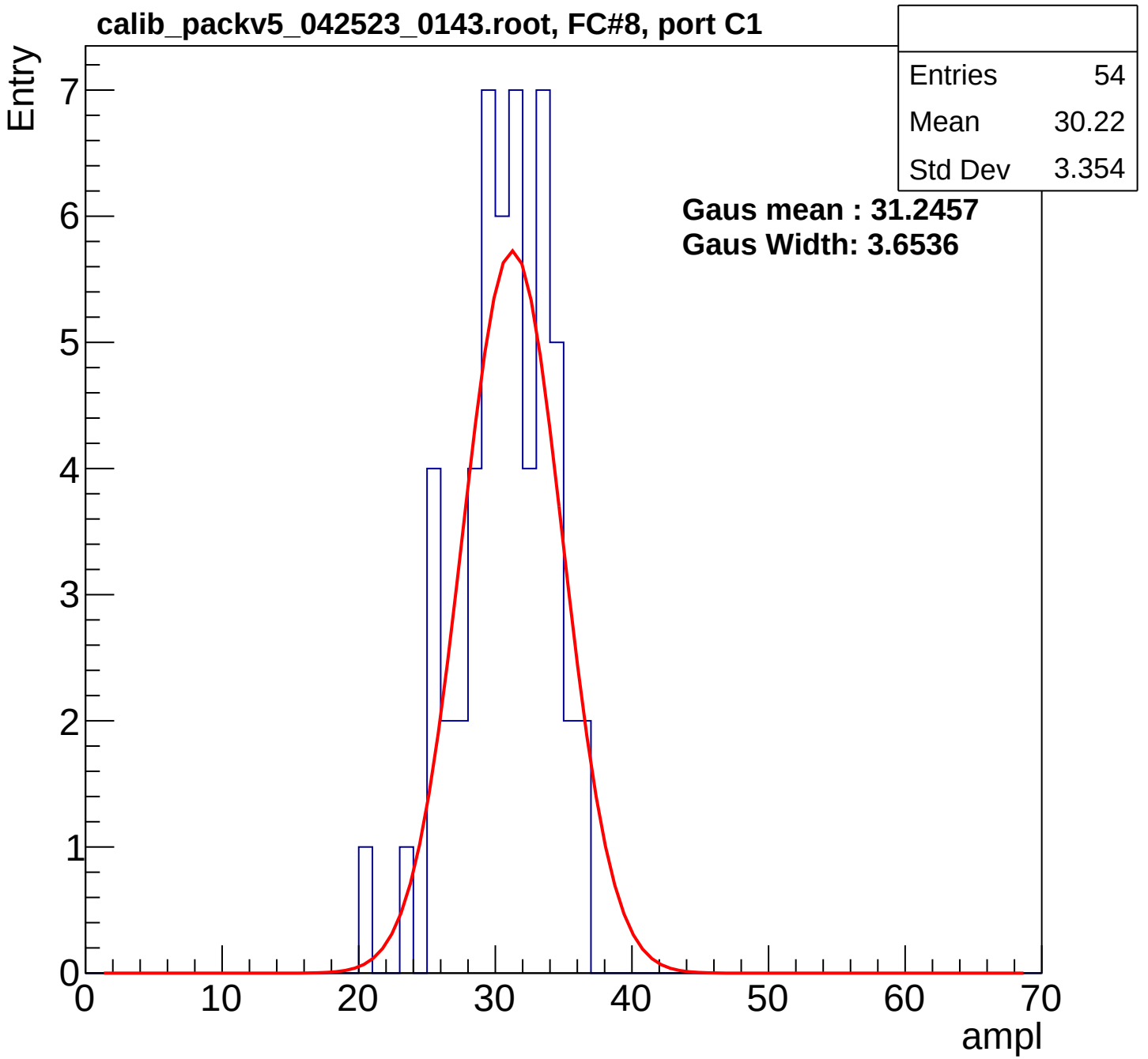

B0L002S, U19-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.91
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

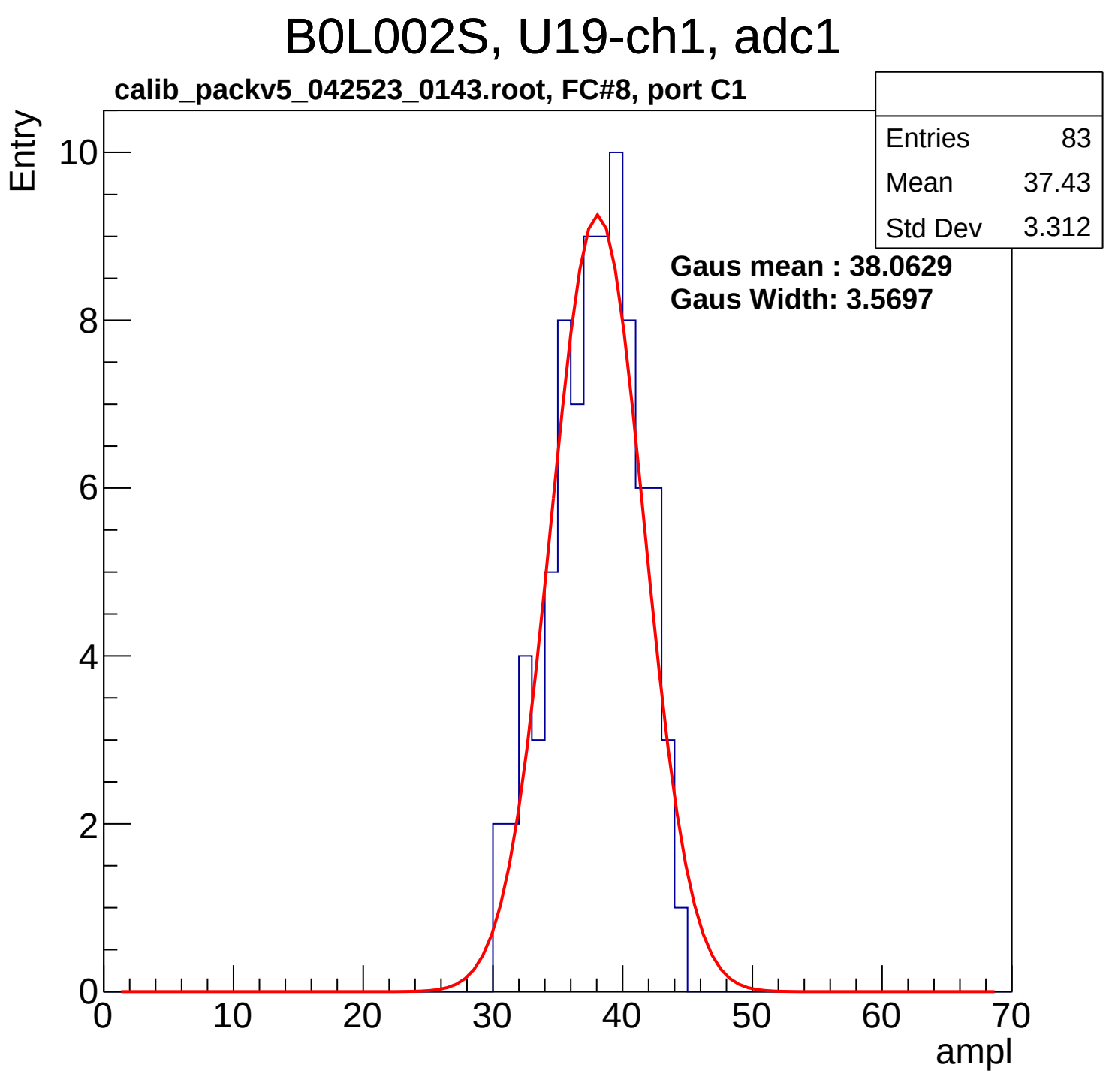

B0L002S, U19-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 57.61
Std Dev 3.297
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

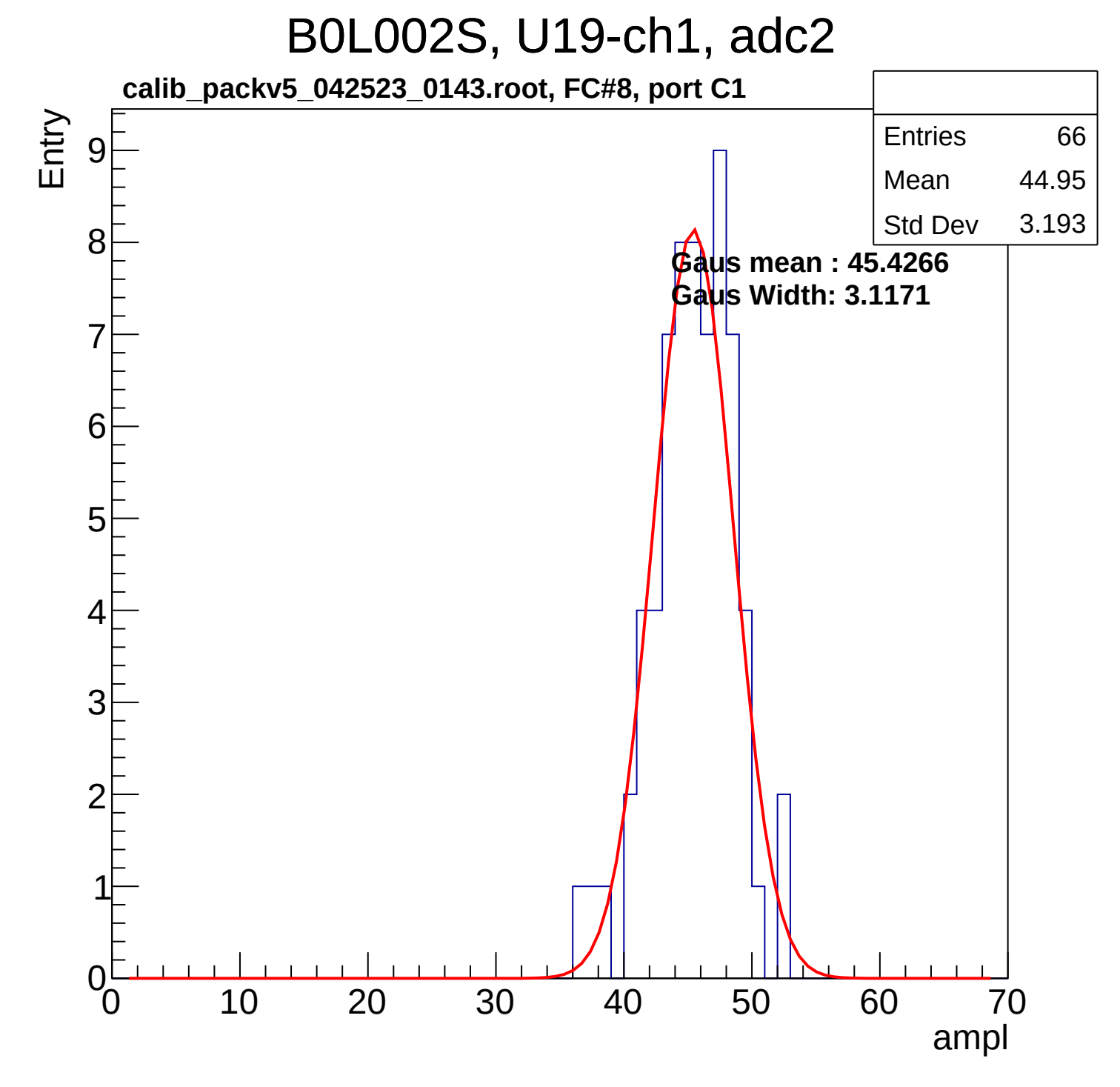

B0L002S, U19-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.03
Std Dev 11.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

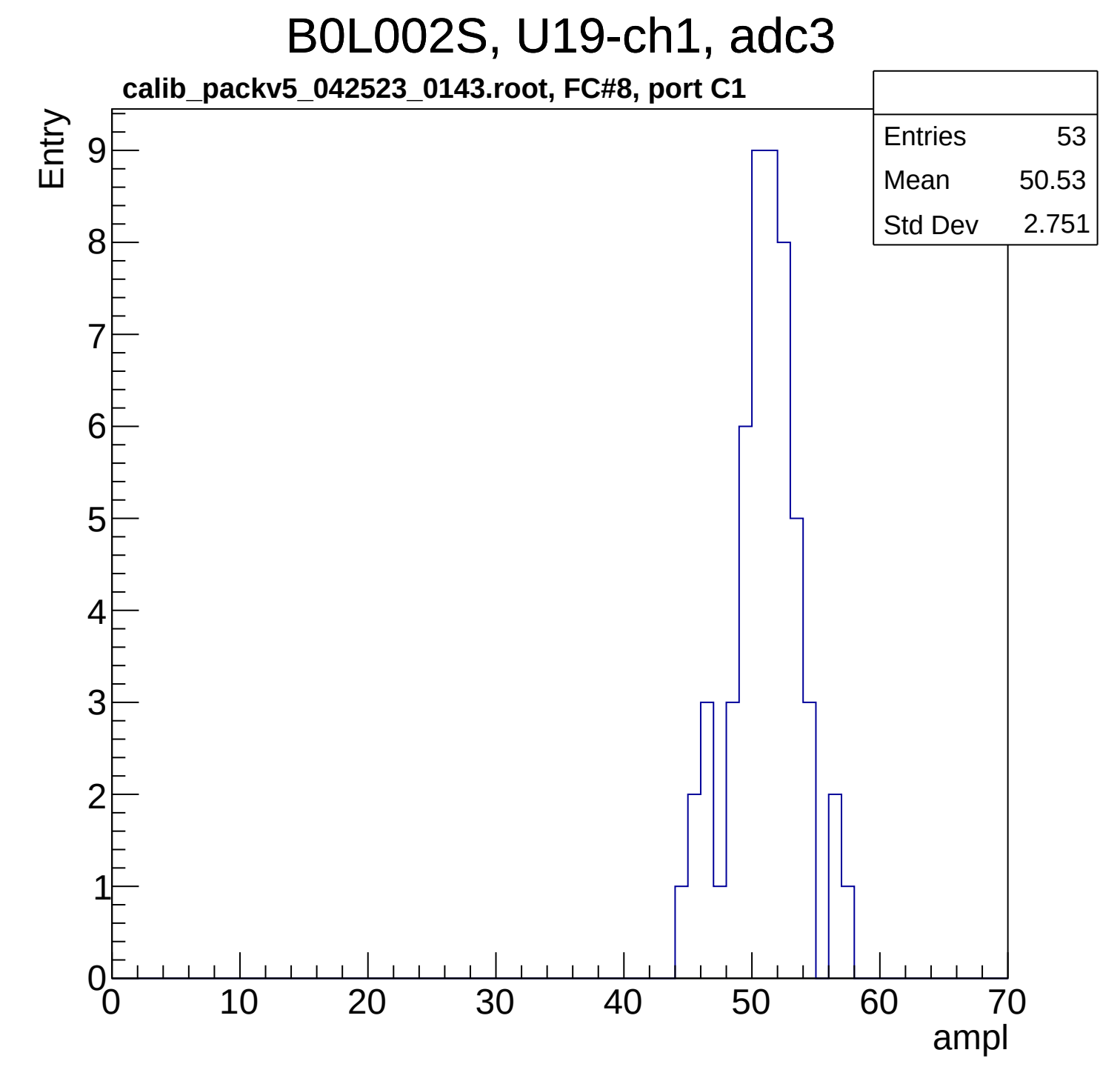

B0L002S, U19-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

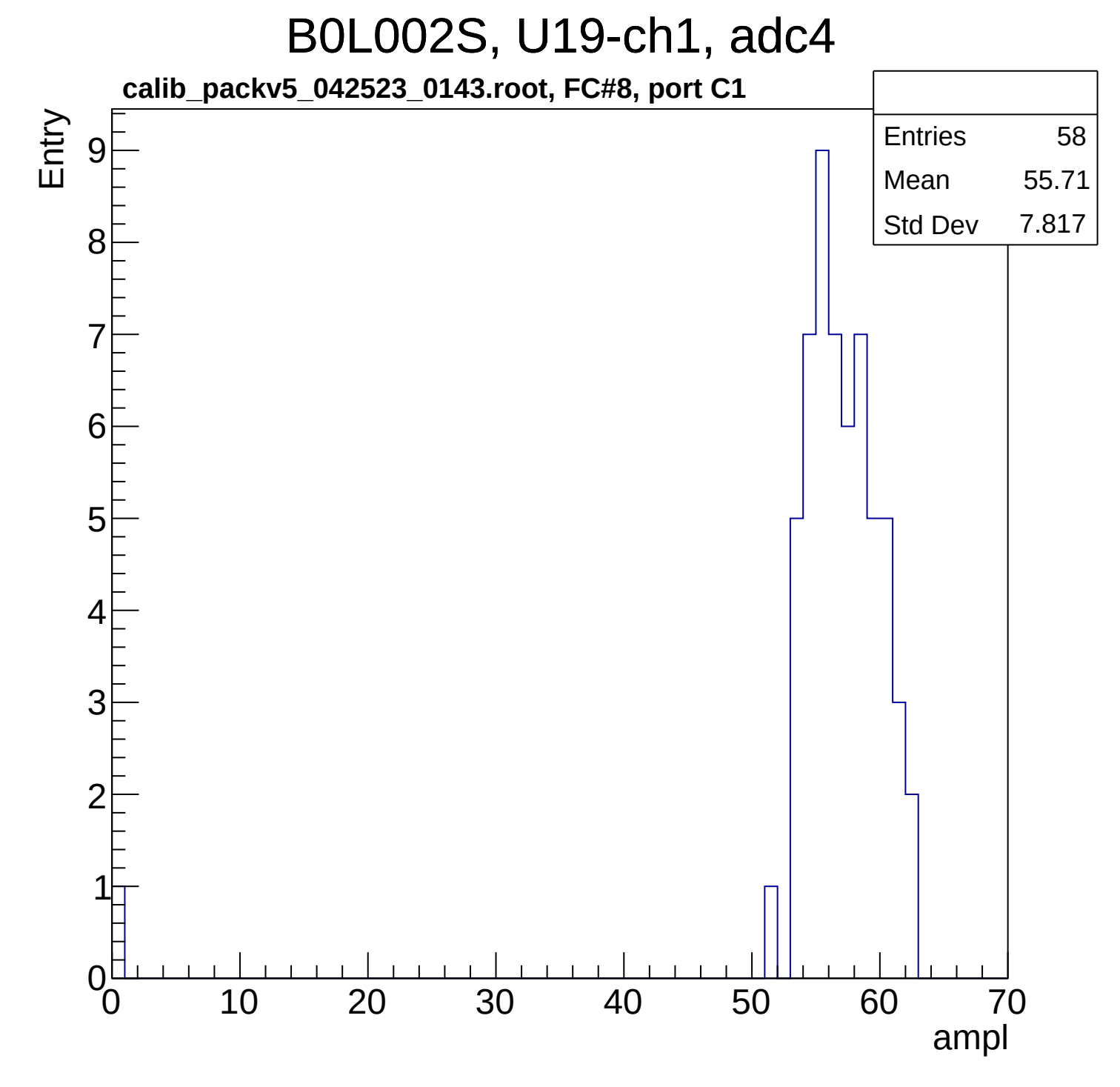

B0L002S, U19-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

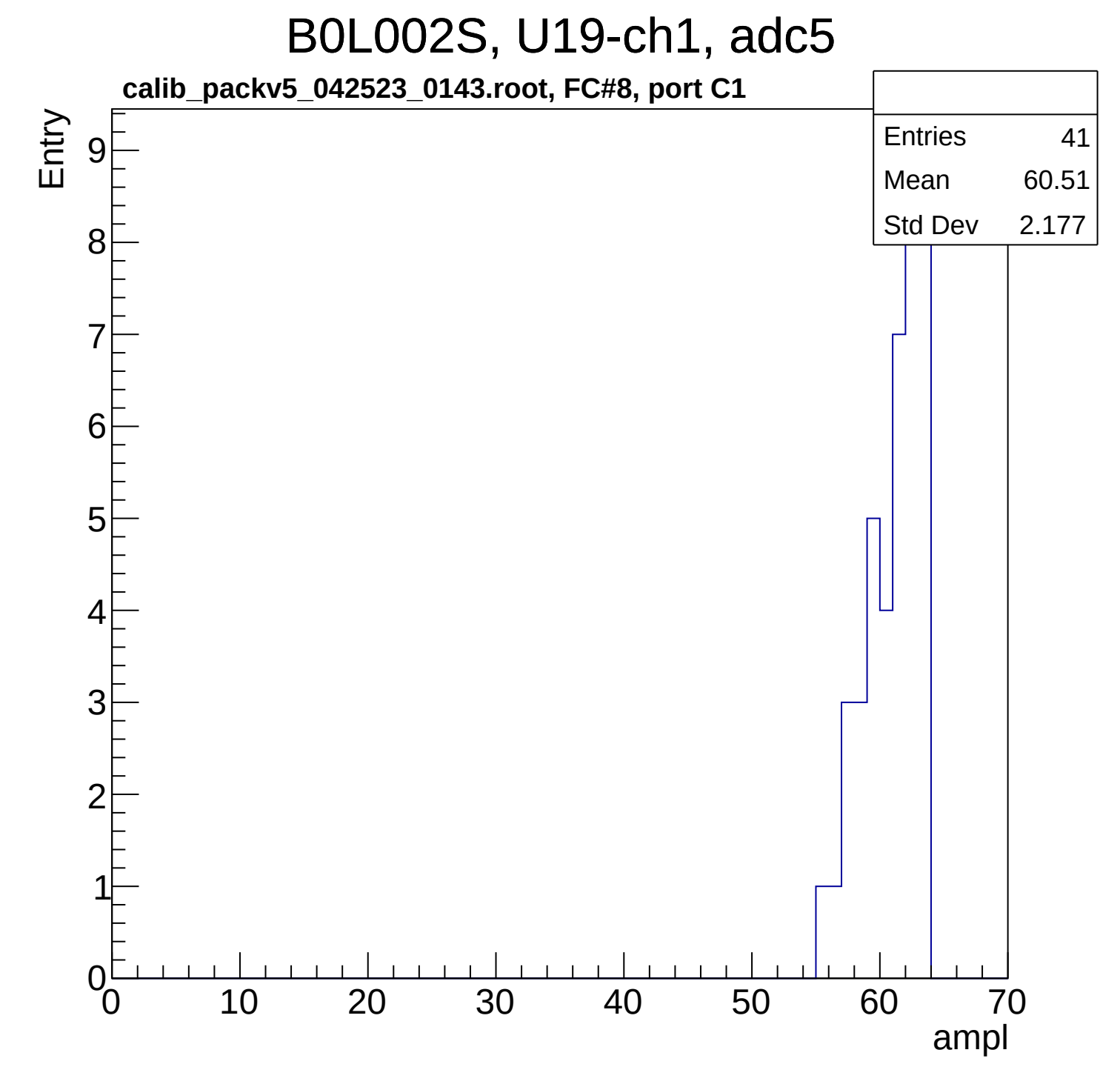

B0L002S, U19-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 30.22
Std Dev 3.354
Gaus mean : 31.2457
Gaus Width: 3.6536
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

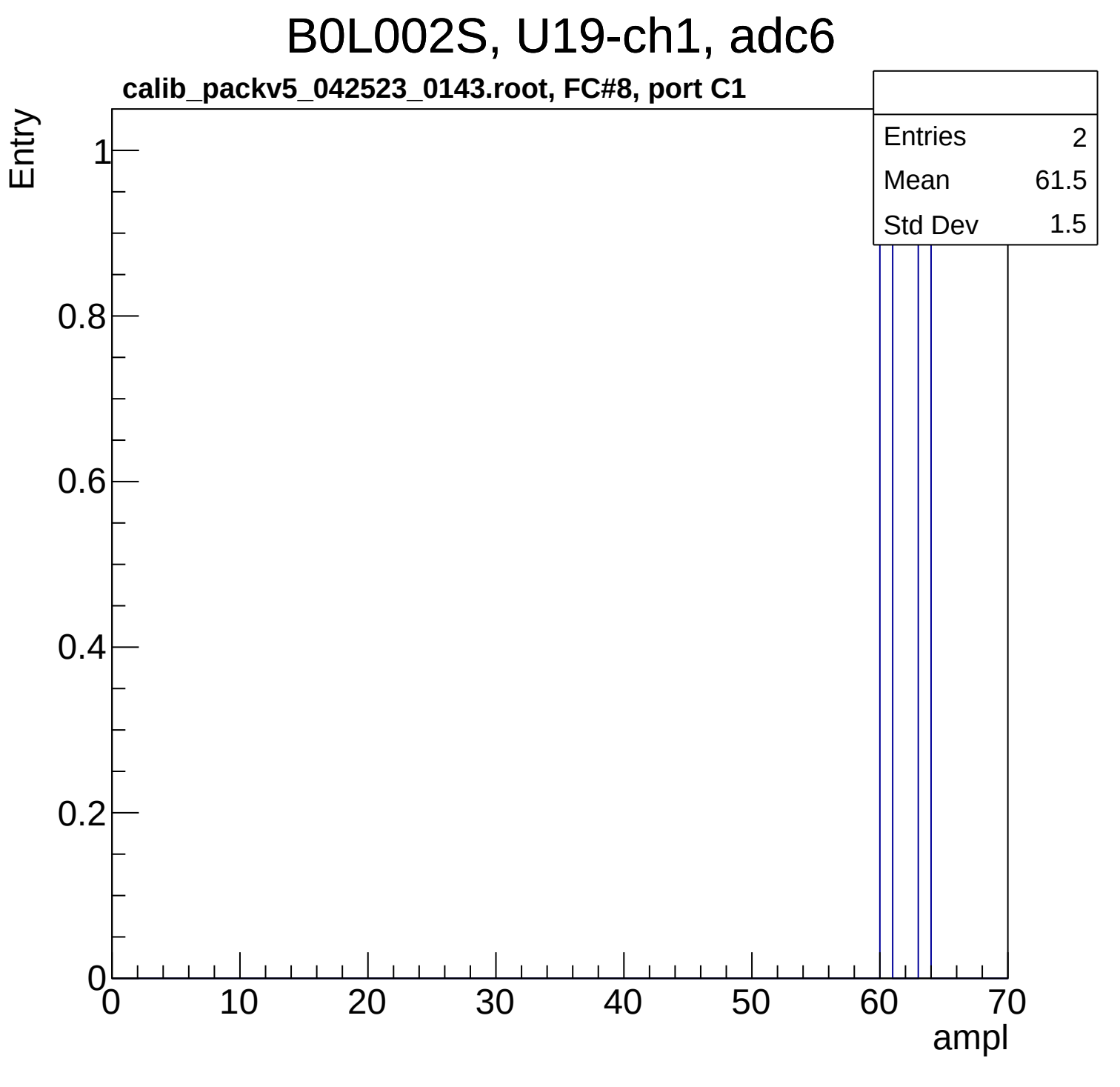

B0L002S, U19-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 37.43
Std Dev 3.312
Gaus mean : 38.0629
Gaus Width: 3.5697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

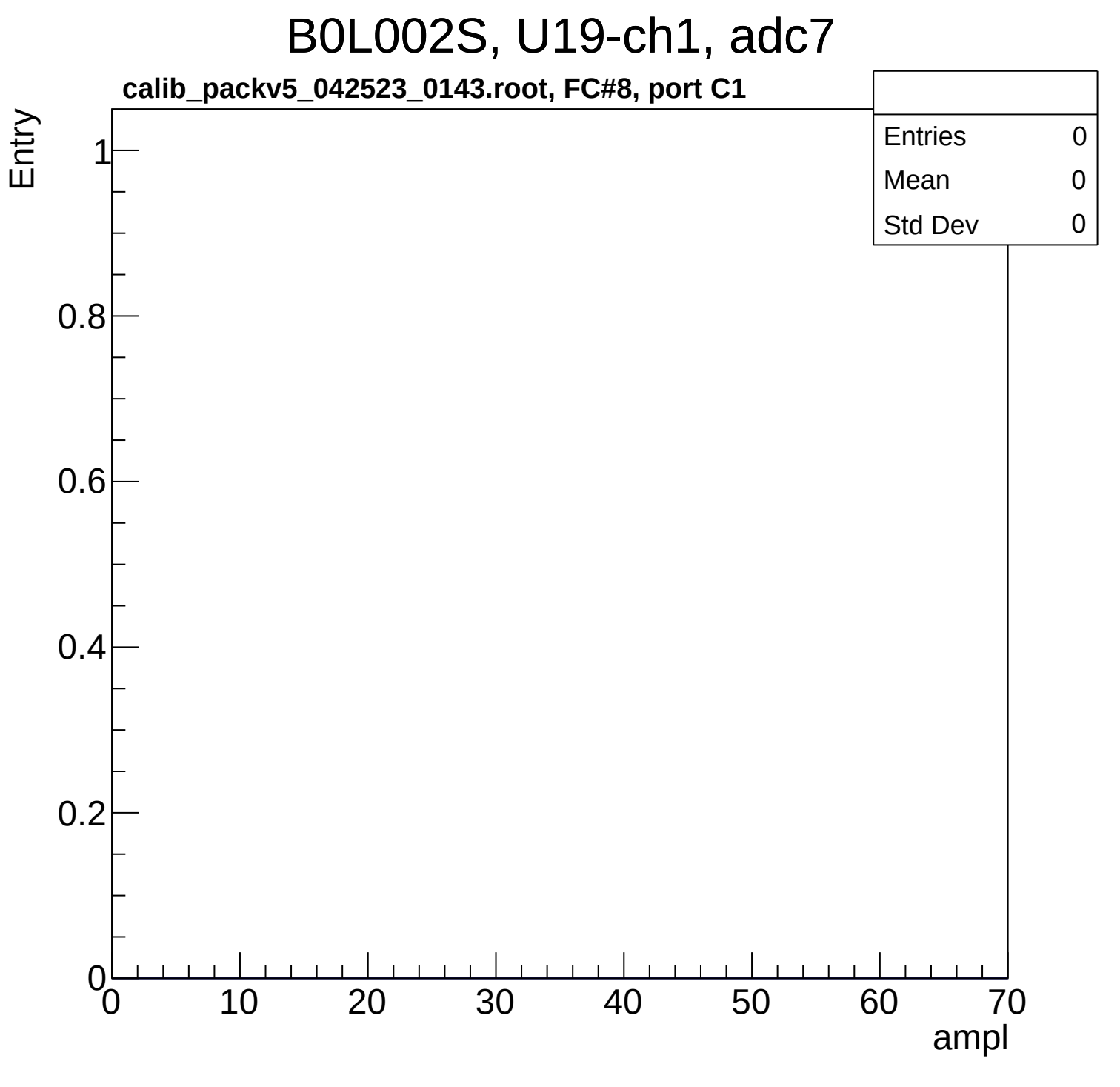

B0L002S, U19-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.95
Std Dev 3.193
Gaus mean : 45.4266
Gaus Width: 3.1171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

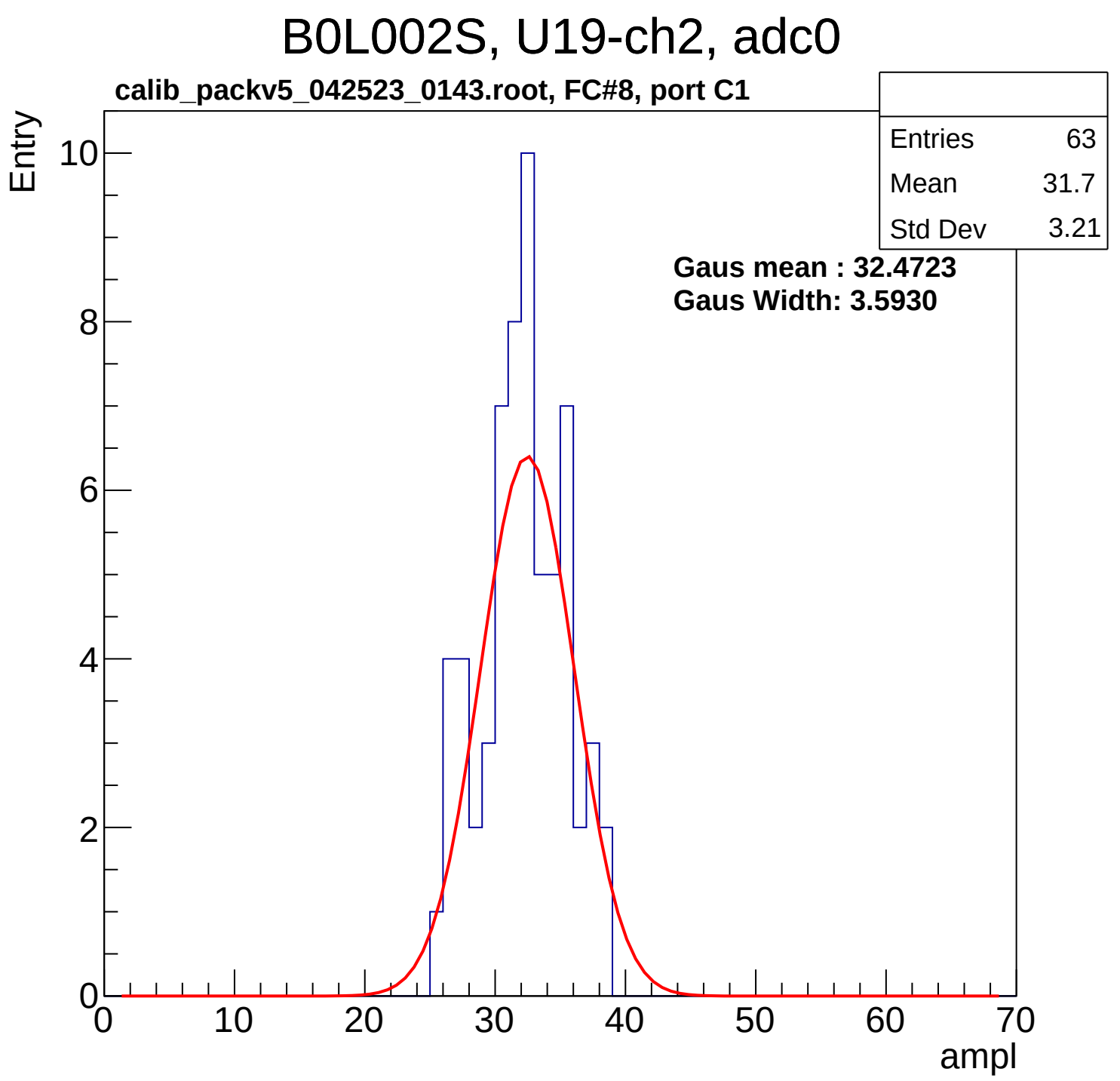

B0L002S, U19-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 50.53
Std Dev 2.751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

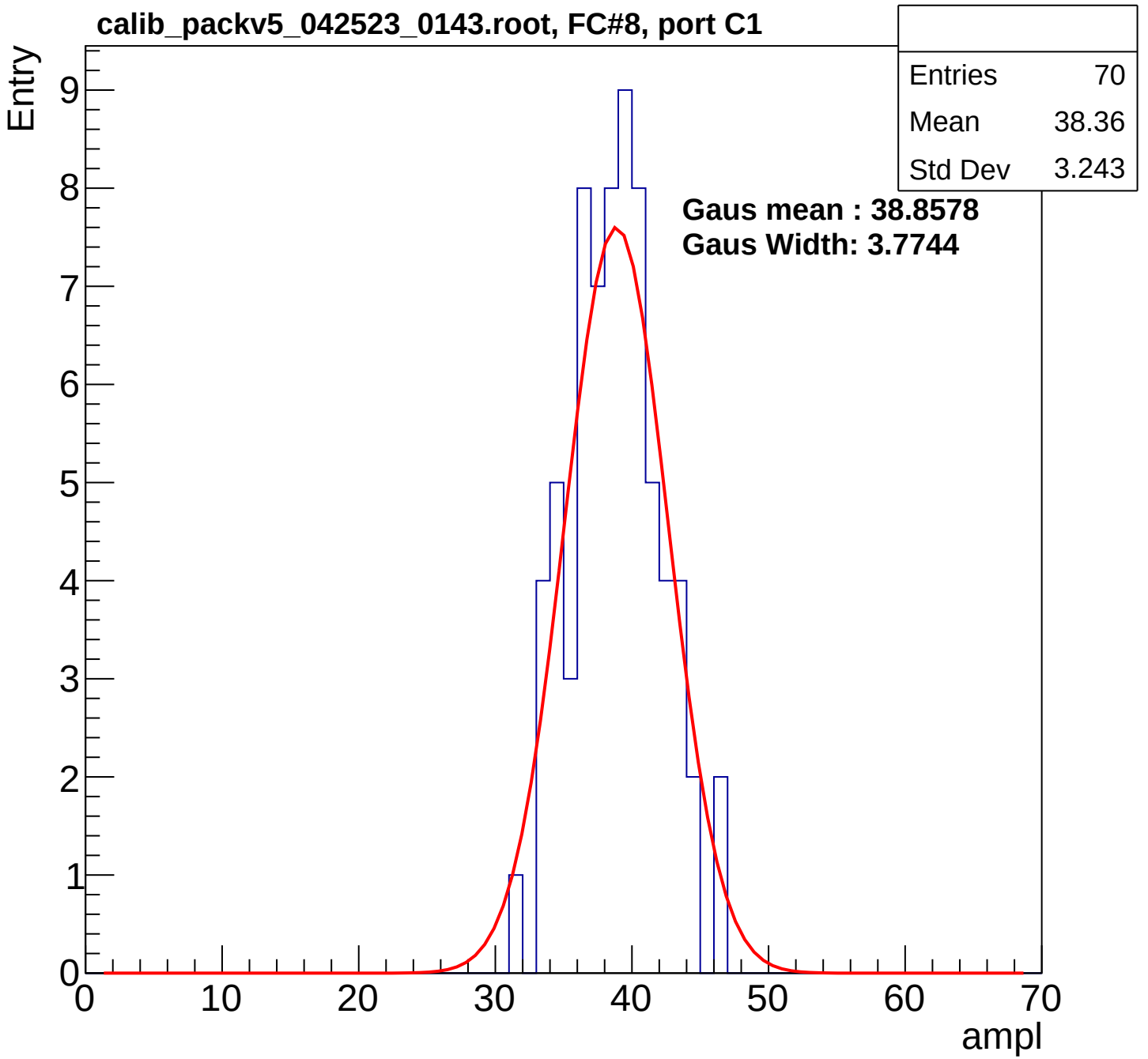

B0L002S, U19-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.71
Std Dev 7.817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

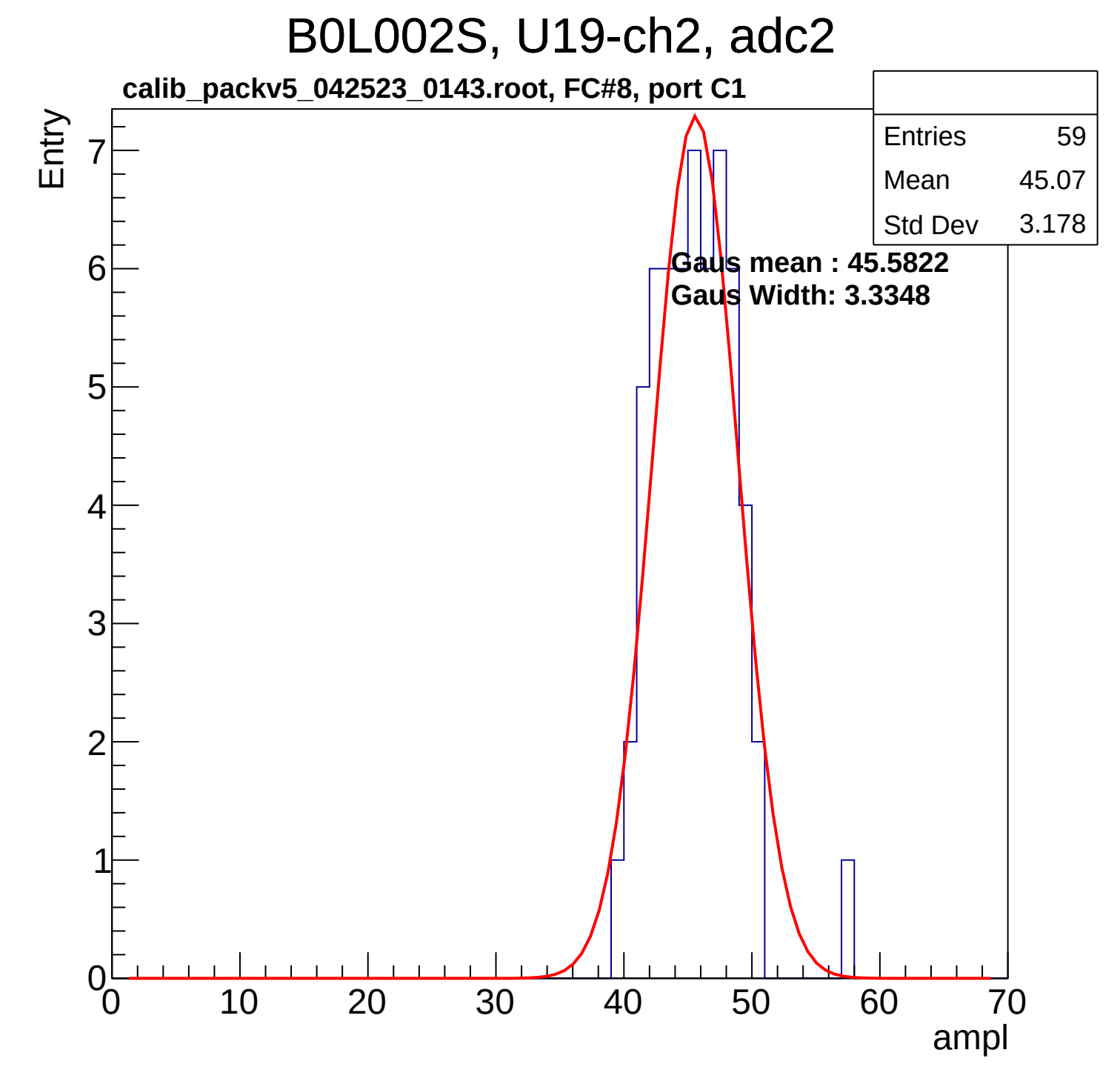

B0L002S, U19-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 60.51
Std Dev 2.177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

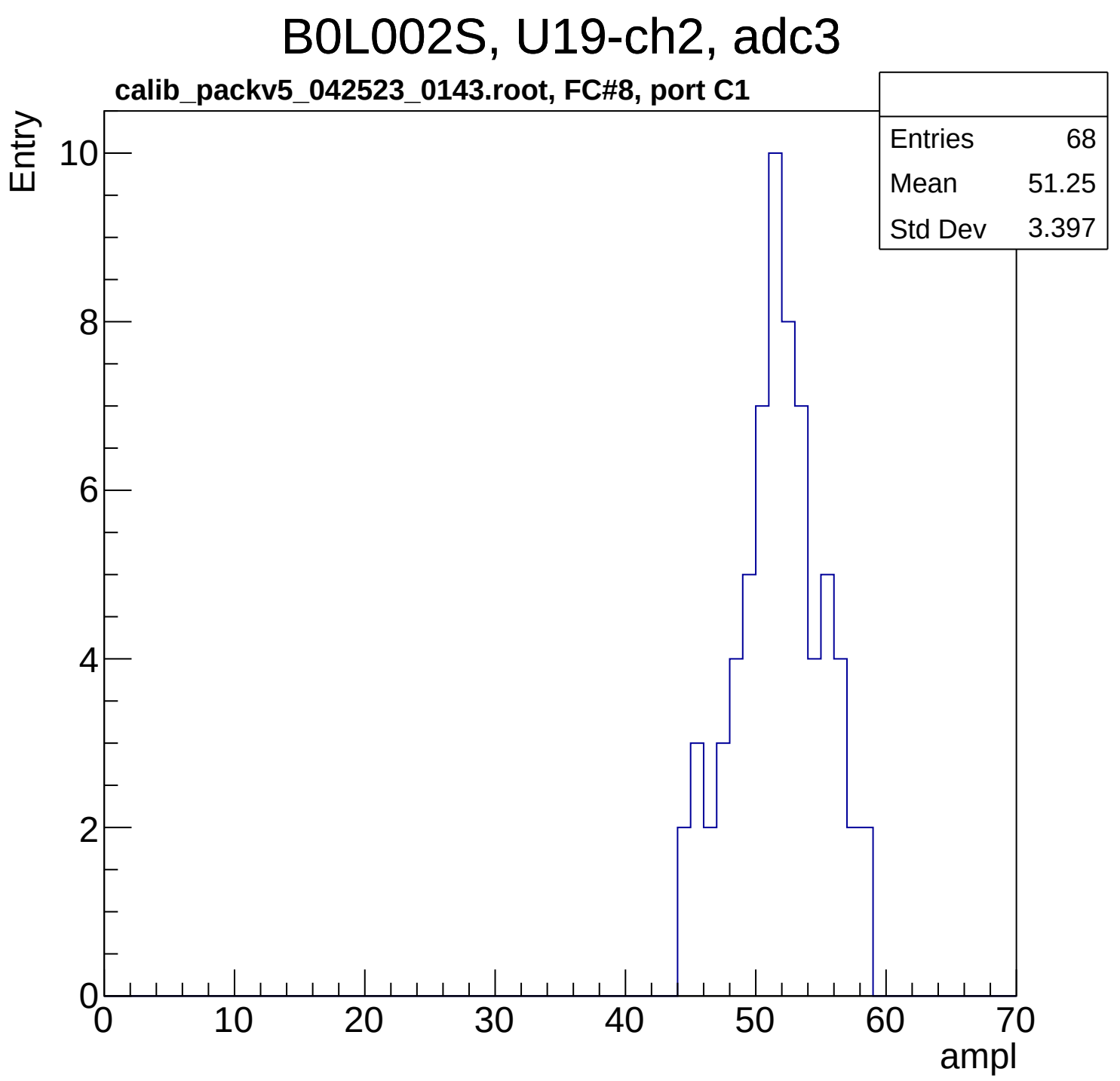

B0L002S, U19-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

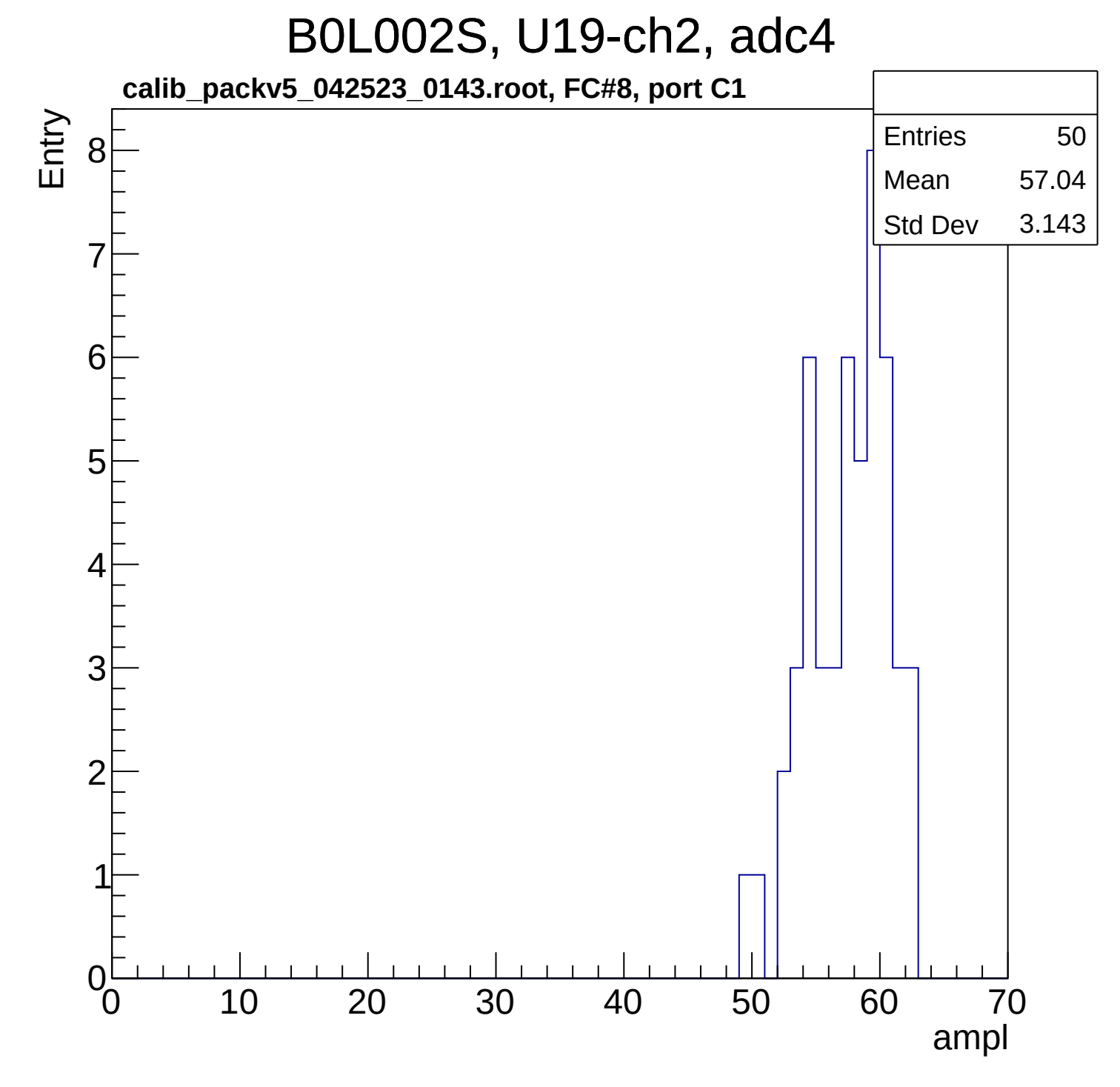

B0L002S, U19-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

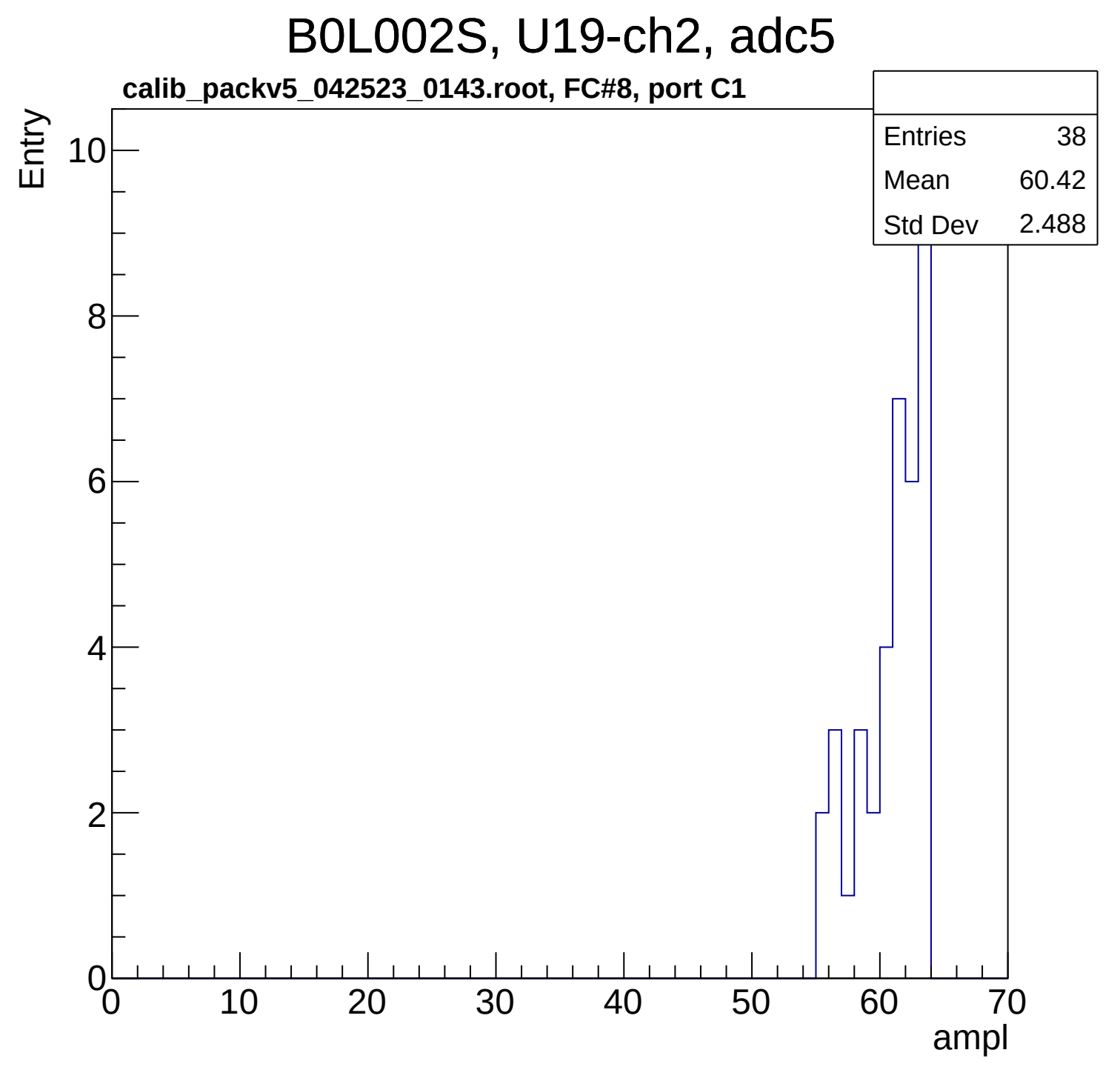

B0L002S, U19-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.7
Std Dev 3.21
Gaus mean : 32.4723
Gaus Width: 3.5930
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

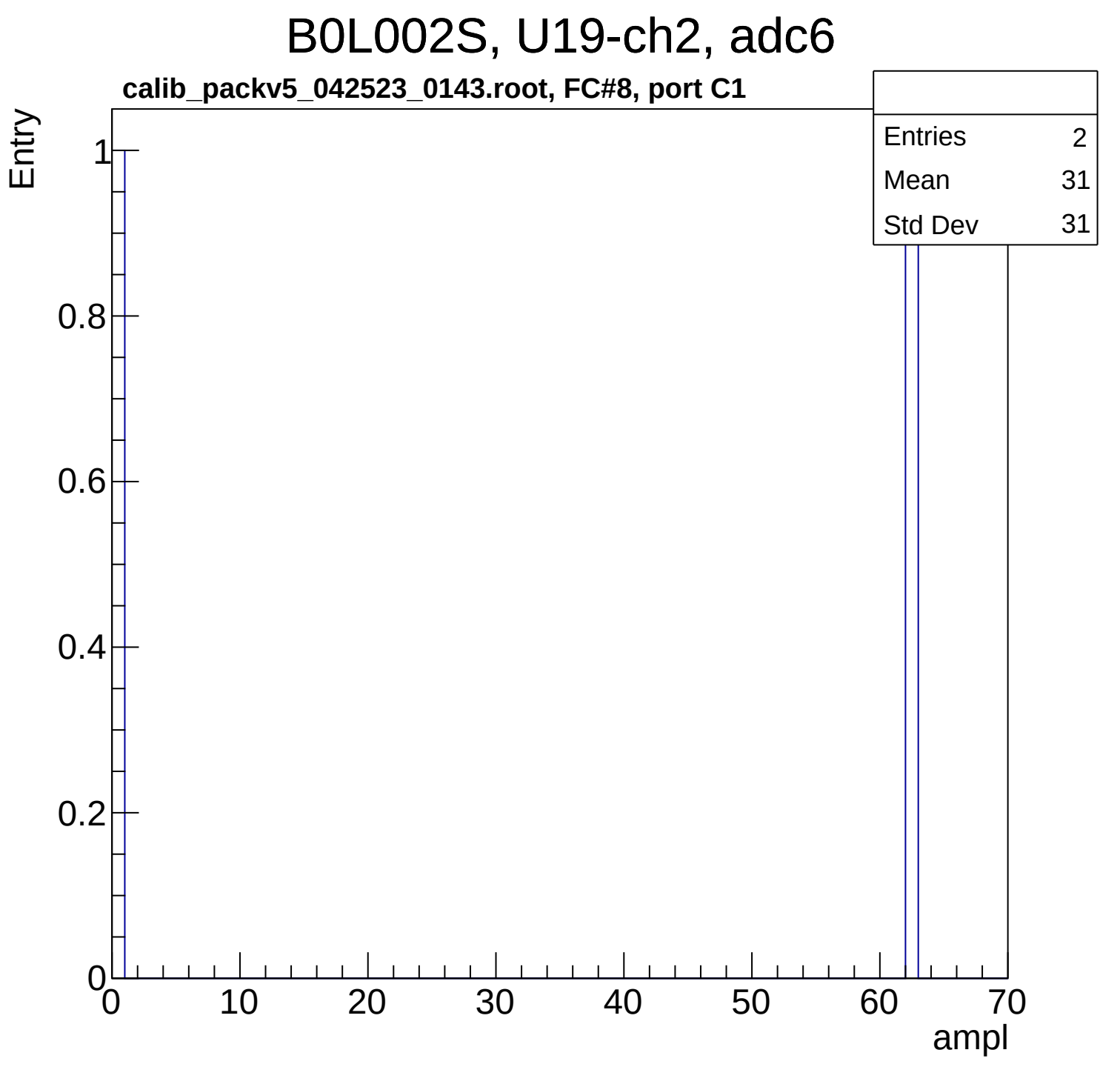

B0L002S, U19-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.36
Std Dev 3.243
Gaus mean : 38.8578
Gaus Width: 3.7744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

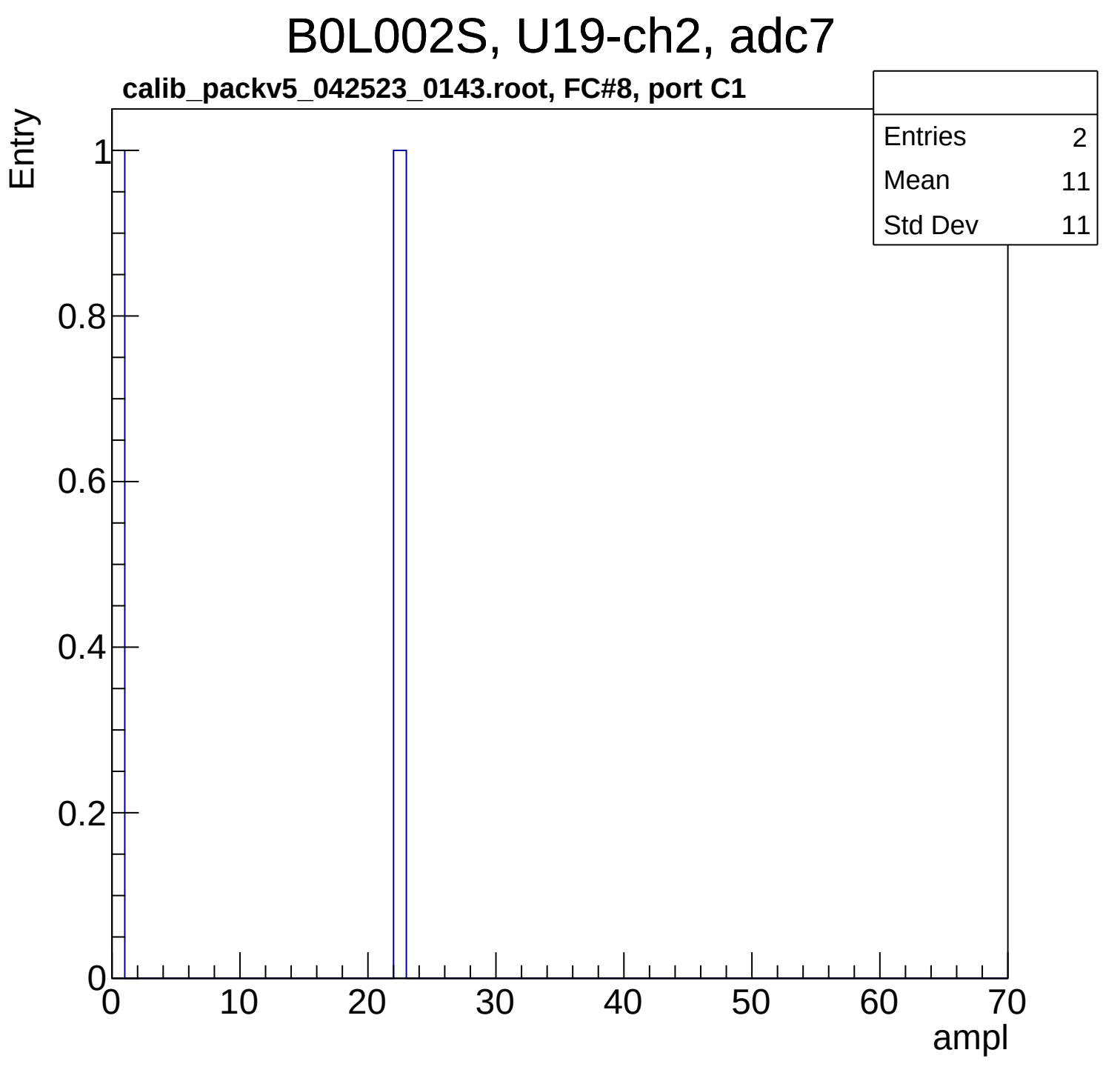

B0L002S, U19-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 45.07
Std Dev 3.178
Gaus mean : 45.5822
Gaus Width: 3.3348
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

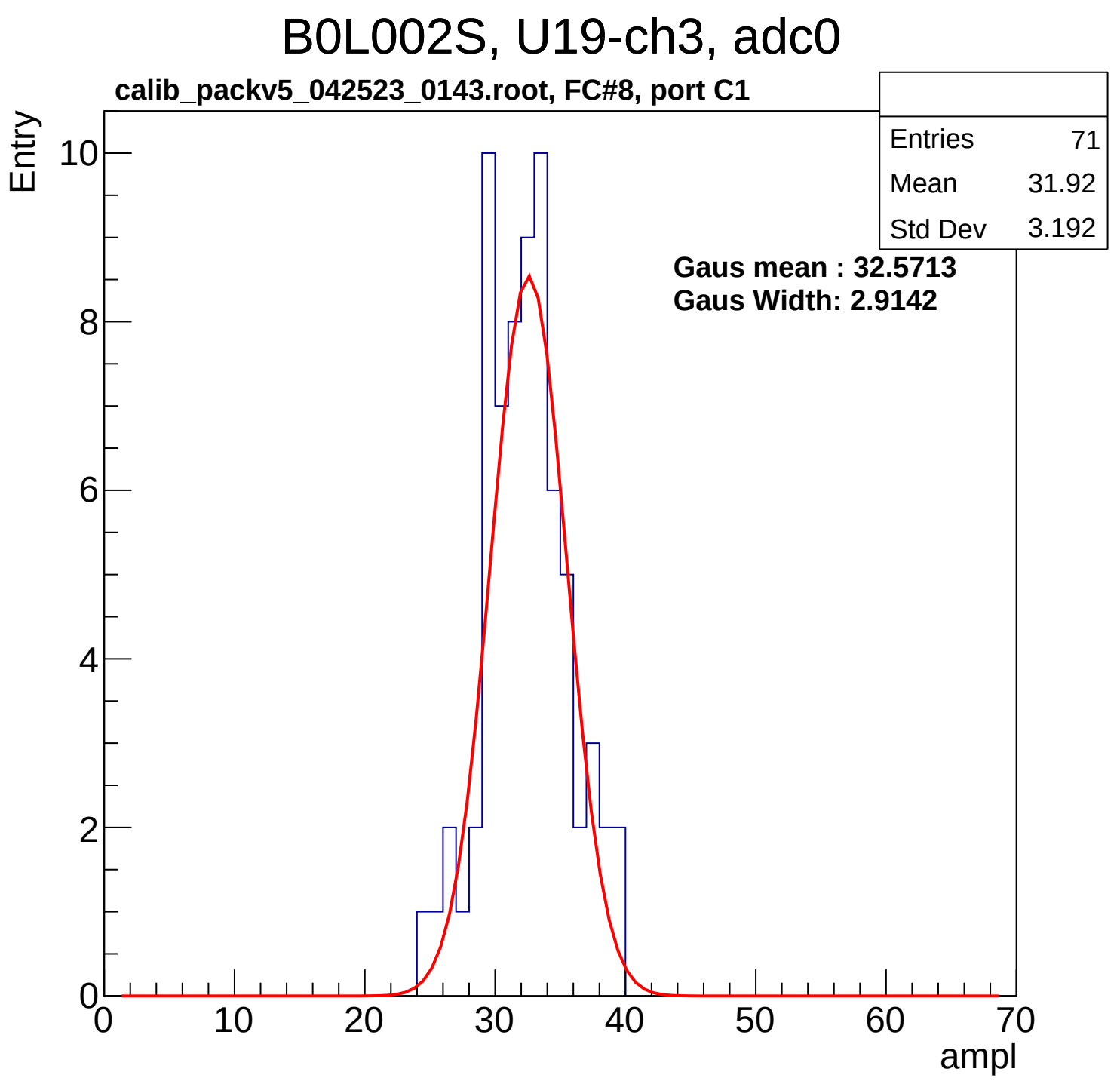

B0L002S, U19-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.25
Std Dev 3.397
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

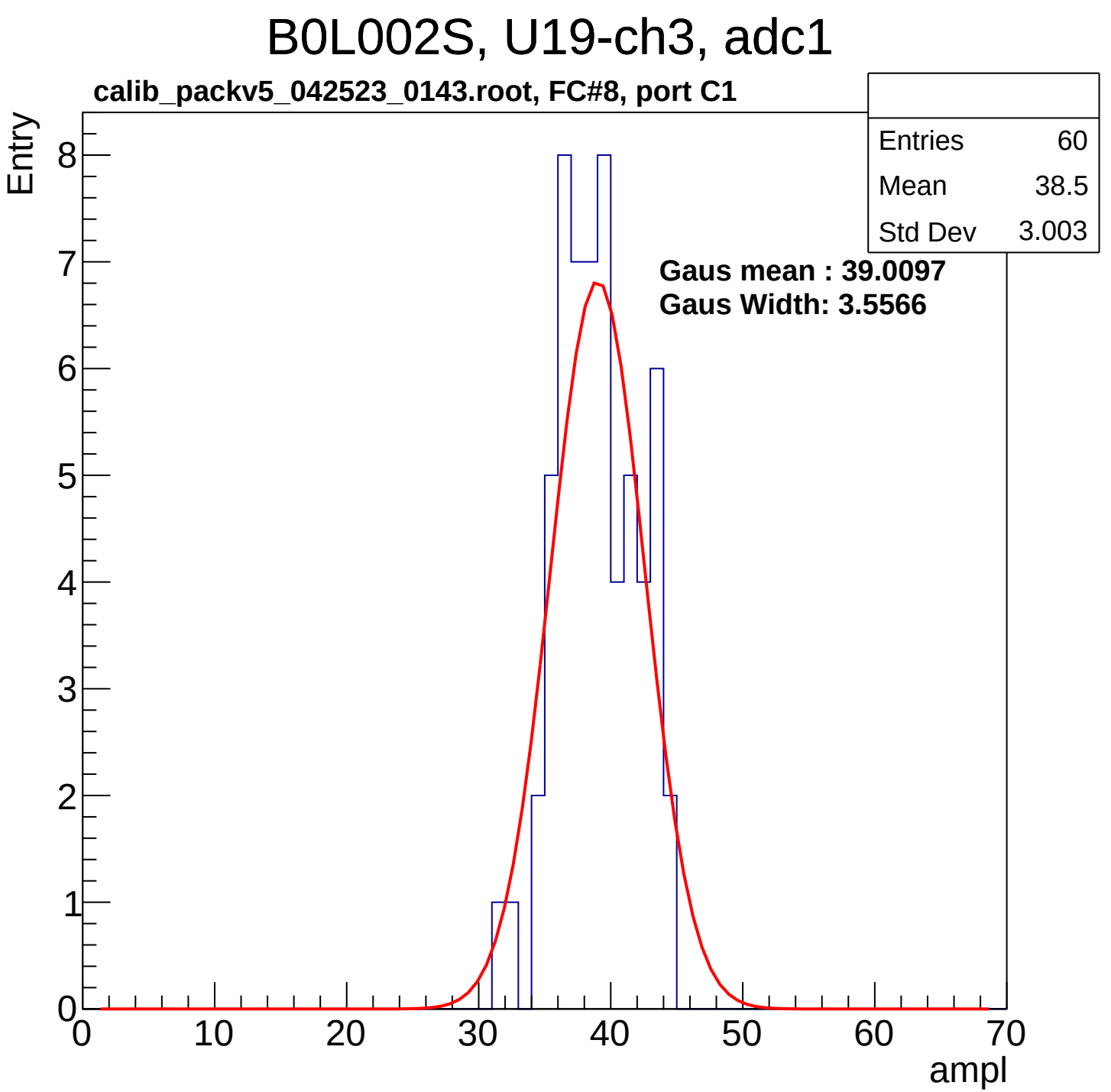

B0L002S, U19-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.04
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

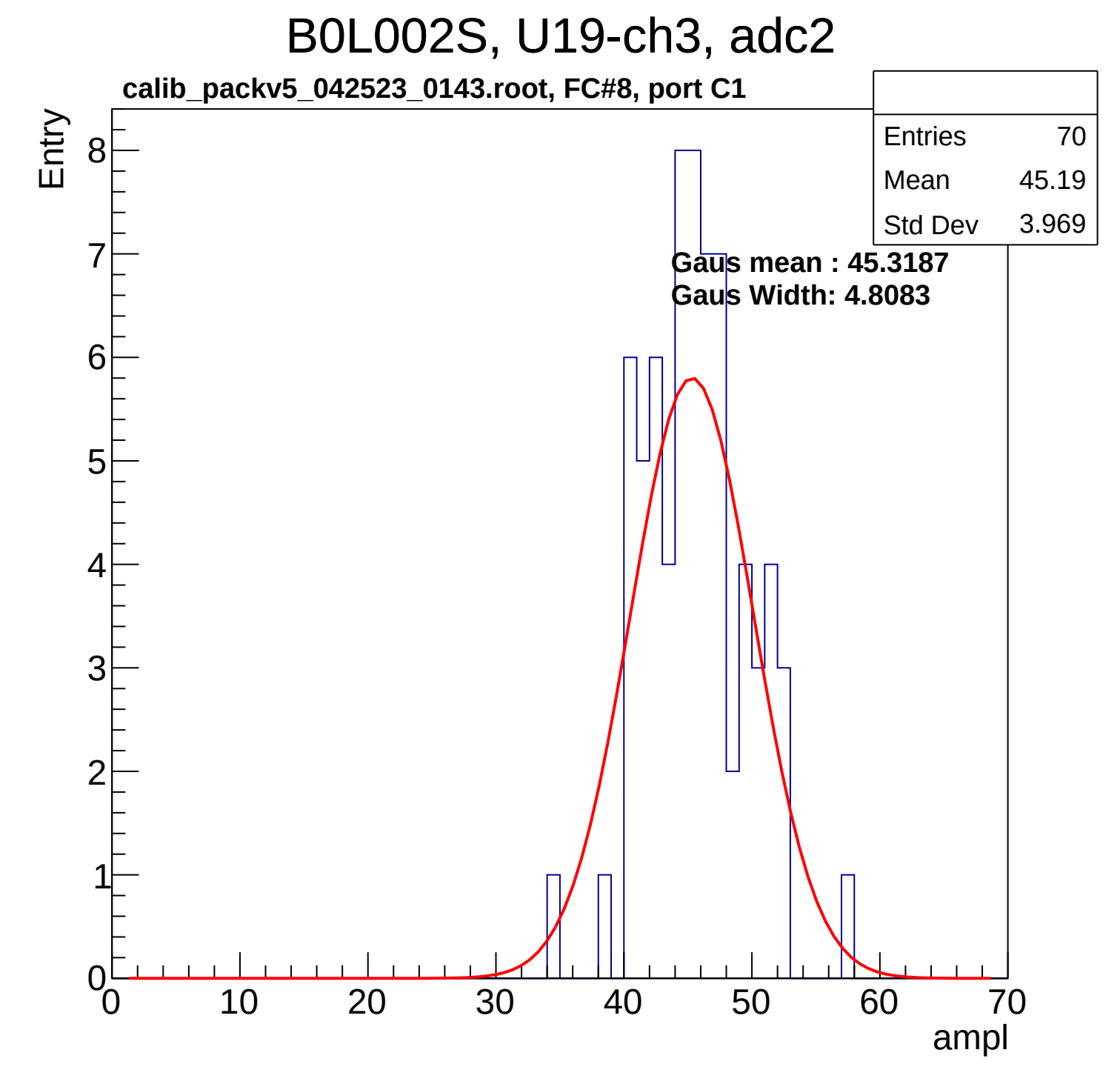

B0L002S, U19-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.42
Std Dev 2.488
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

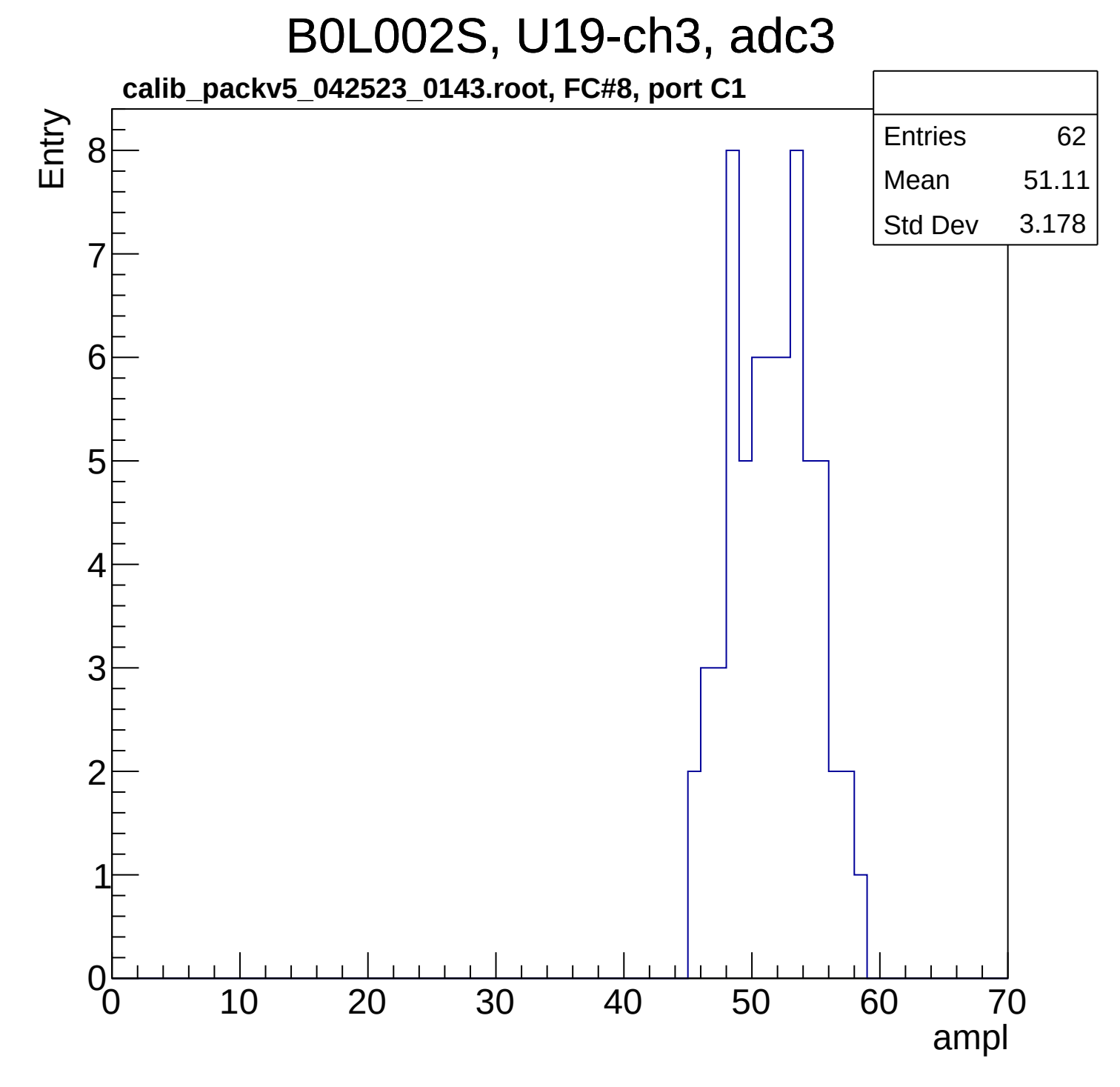

B0L002S, U19-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

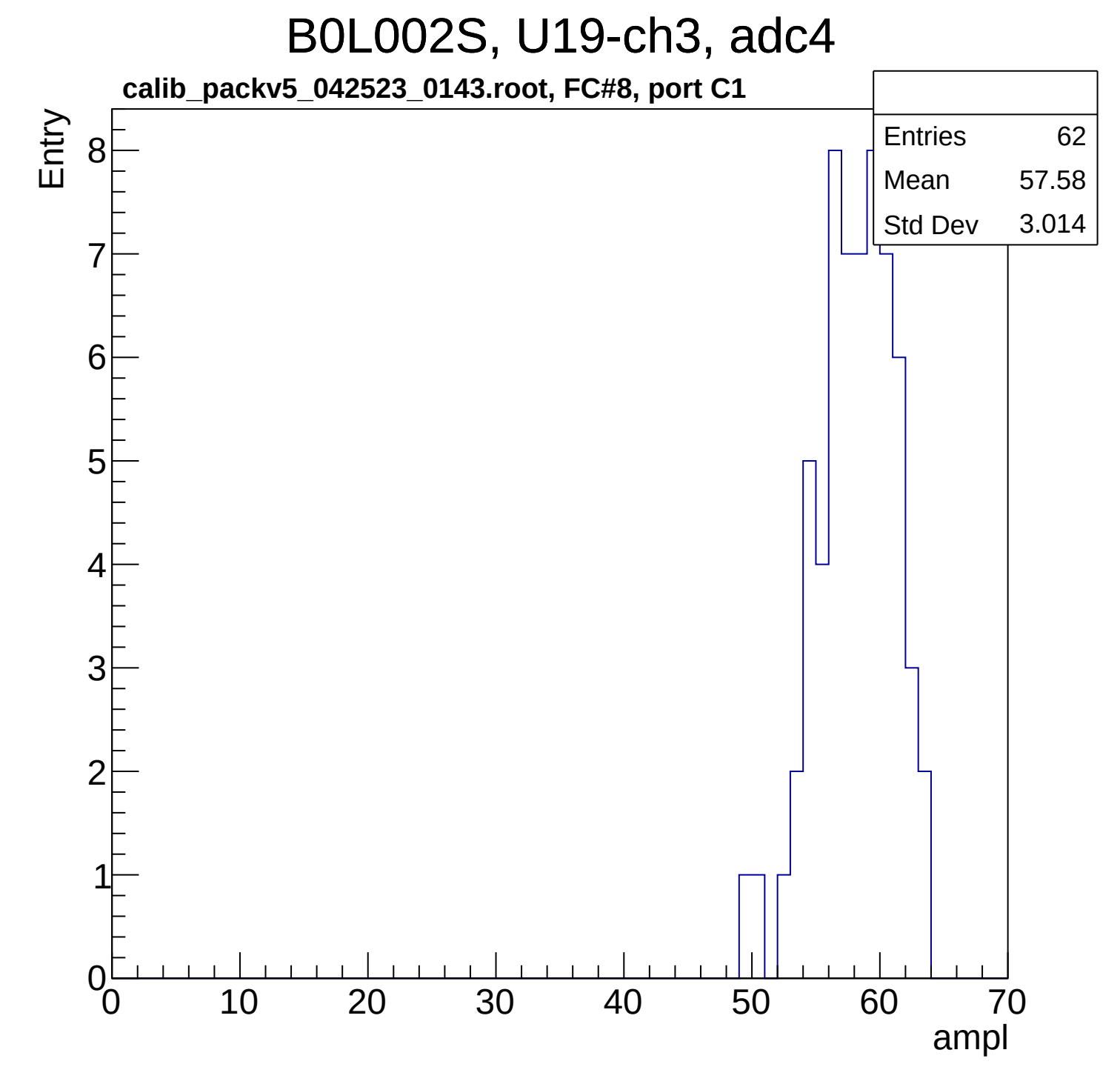

B0L002S, U19-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

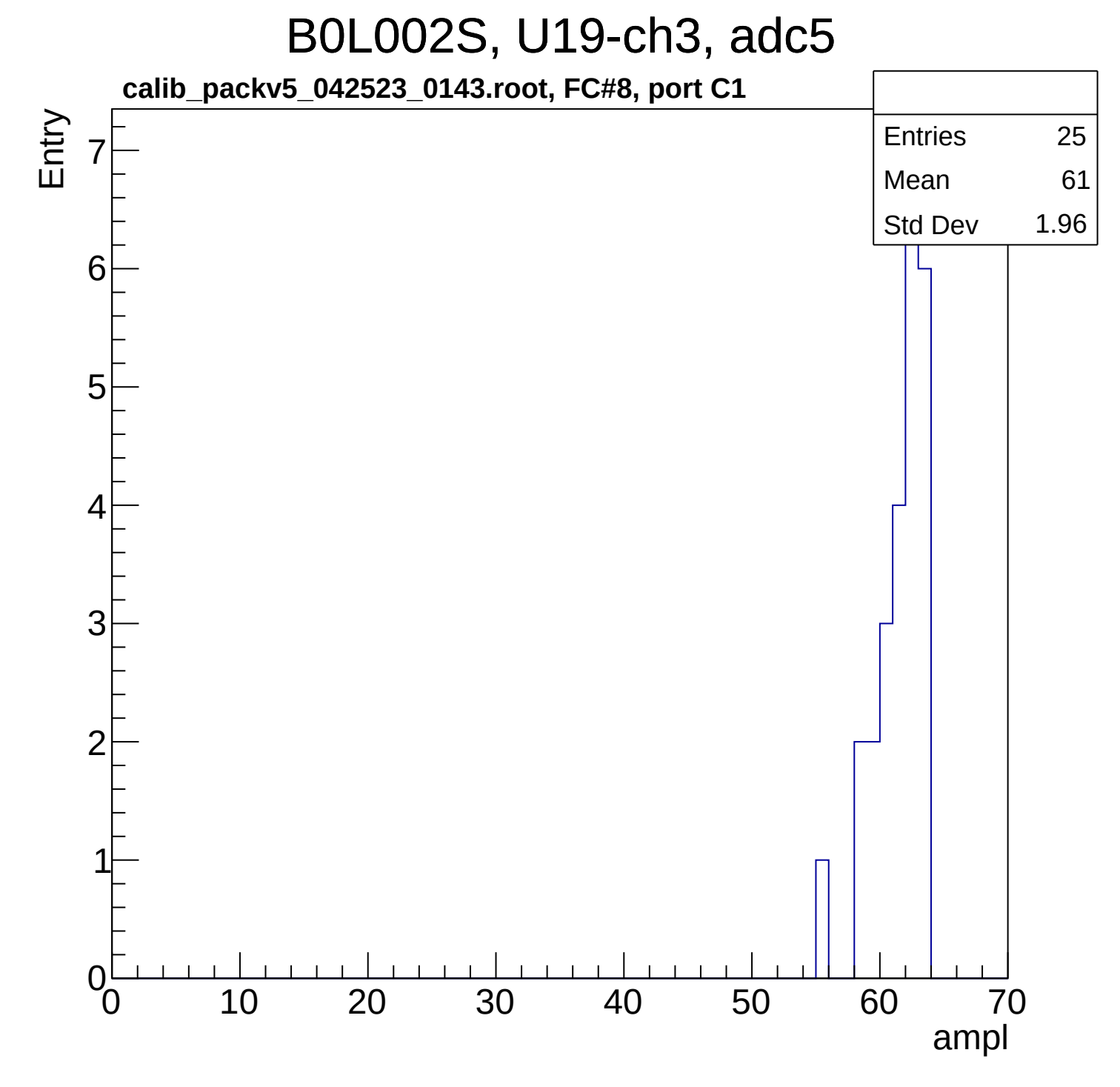

B0L002S, U19-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.92
Std Dev 3.192
Gaus mean : 32.5713
Gaus Width: 2.9142
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

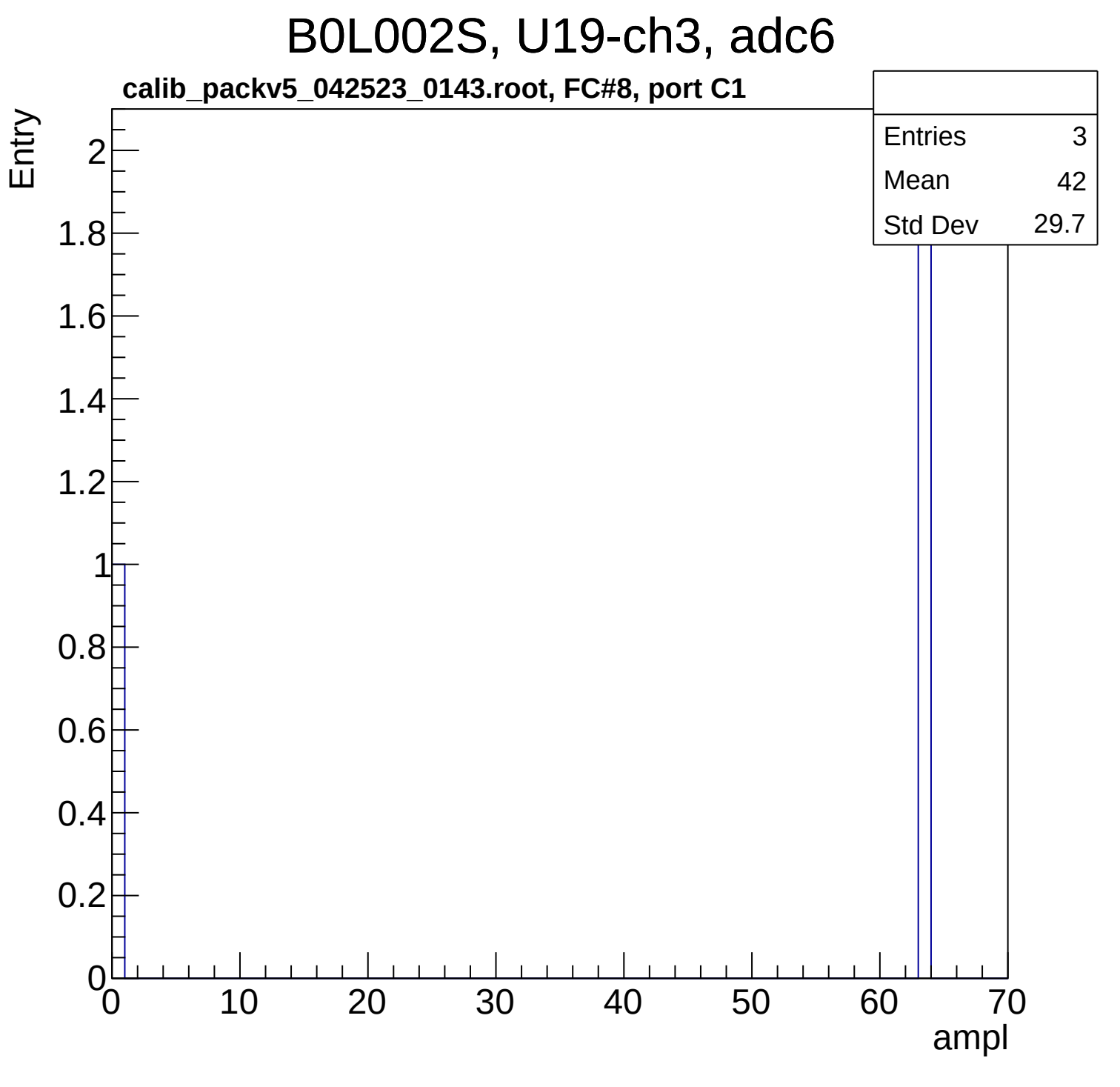

B0L002S, U19-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.5
Std Dev 3.003
Gaus mean : 39.0097
Gaus Width: 3.5566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

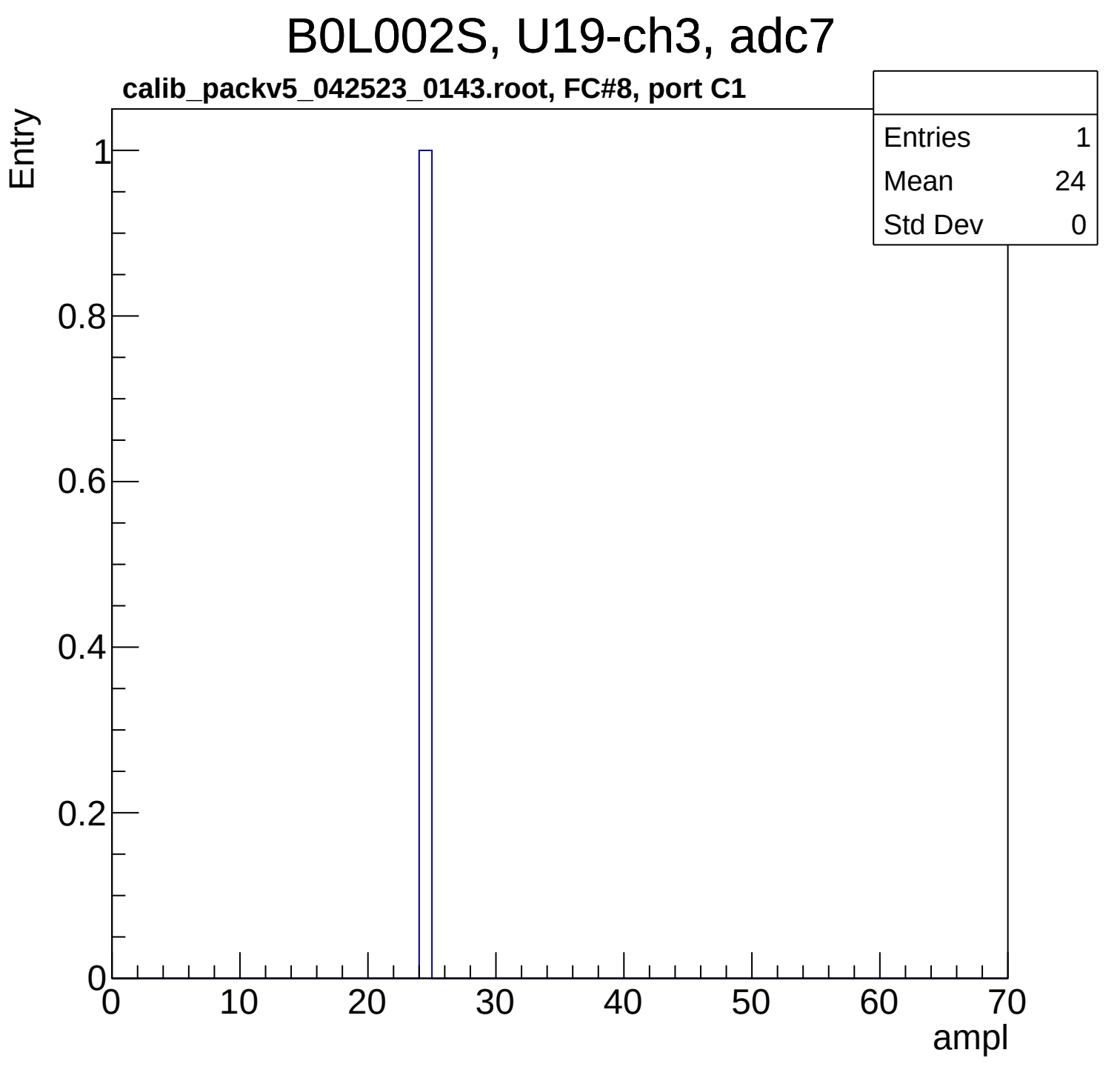

B0L002S, U19-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 45.19
Std Dev 3.969
Gaus mean : 45.3187
Gaus Width: 4.8083
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

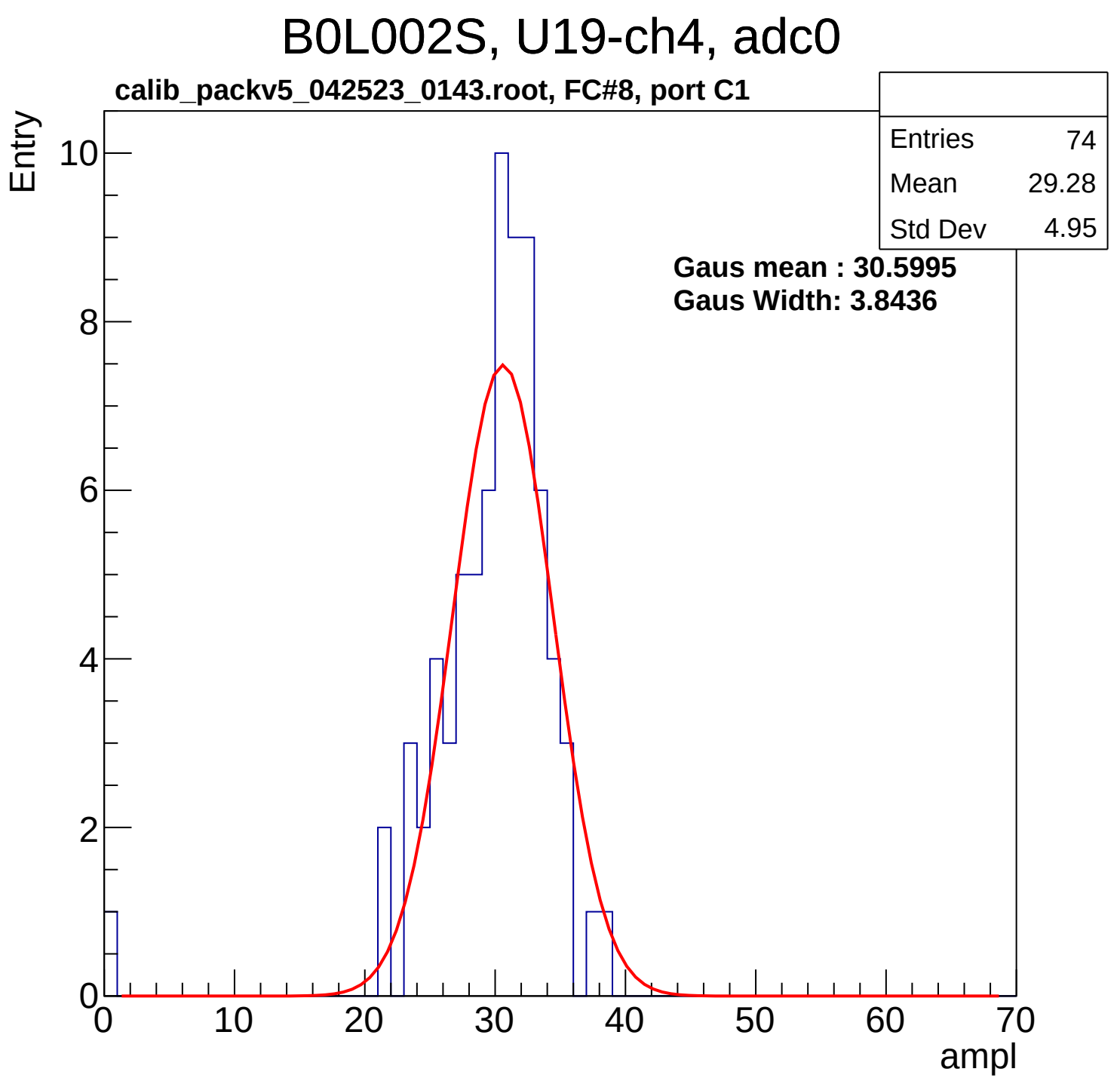

B0L002S, U19-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.11
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

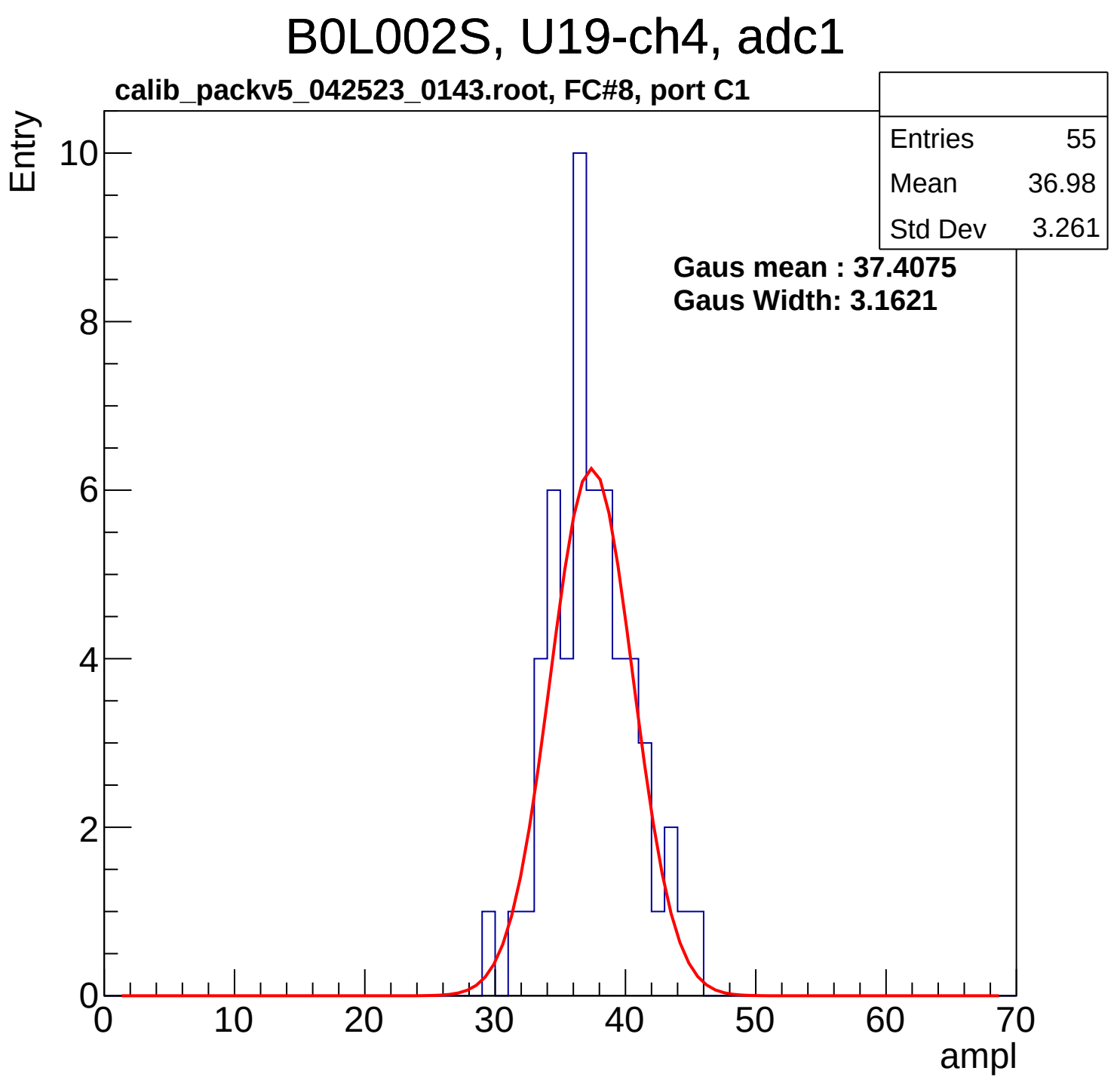

B0L002S, U19-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.58
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

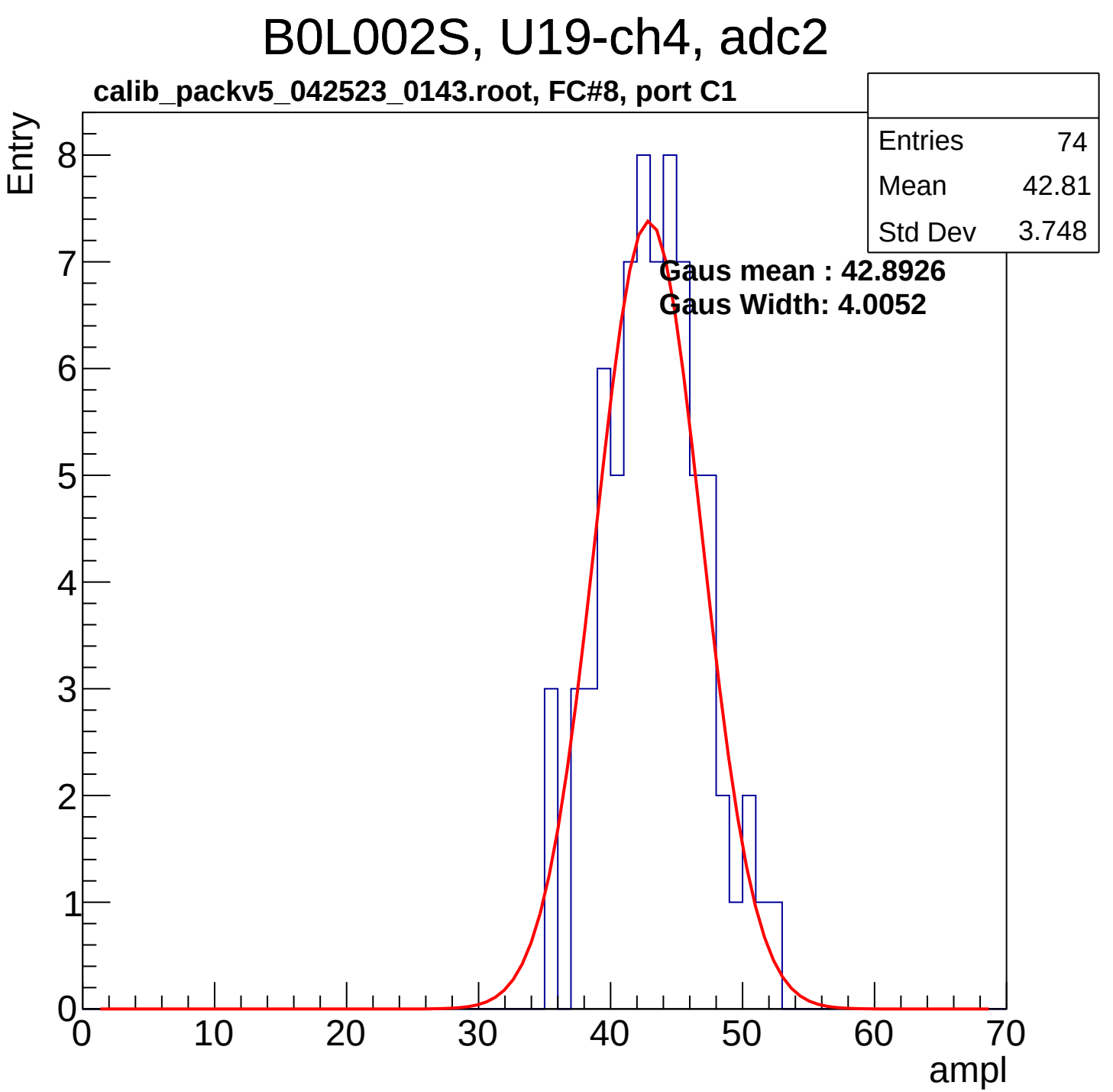

B0L002S, U19-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61
Std Dev 1.96
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

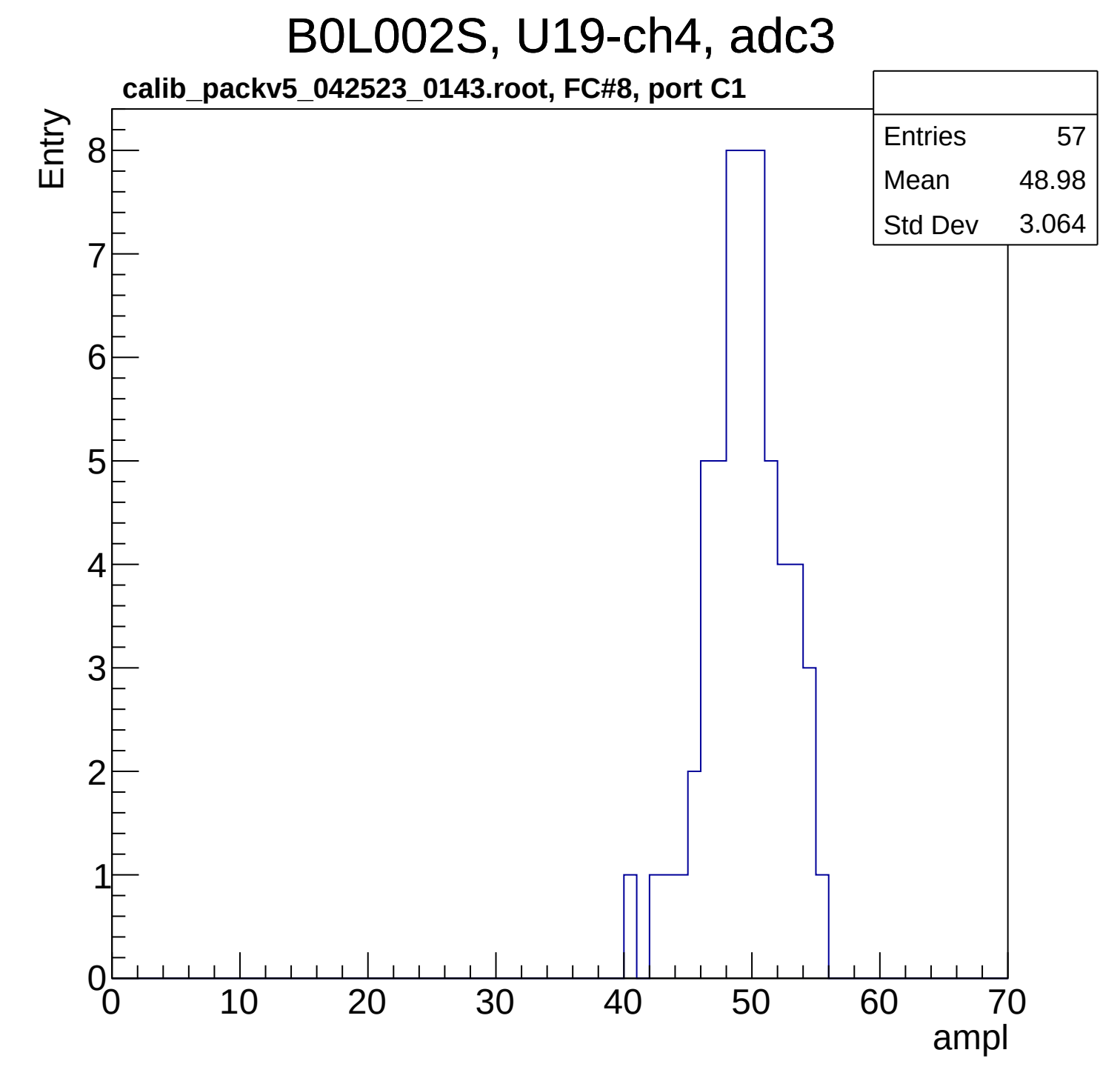

B0L002S, U19-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

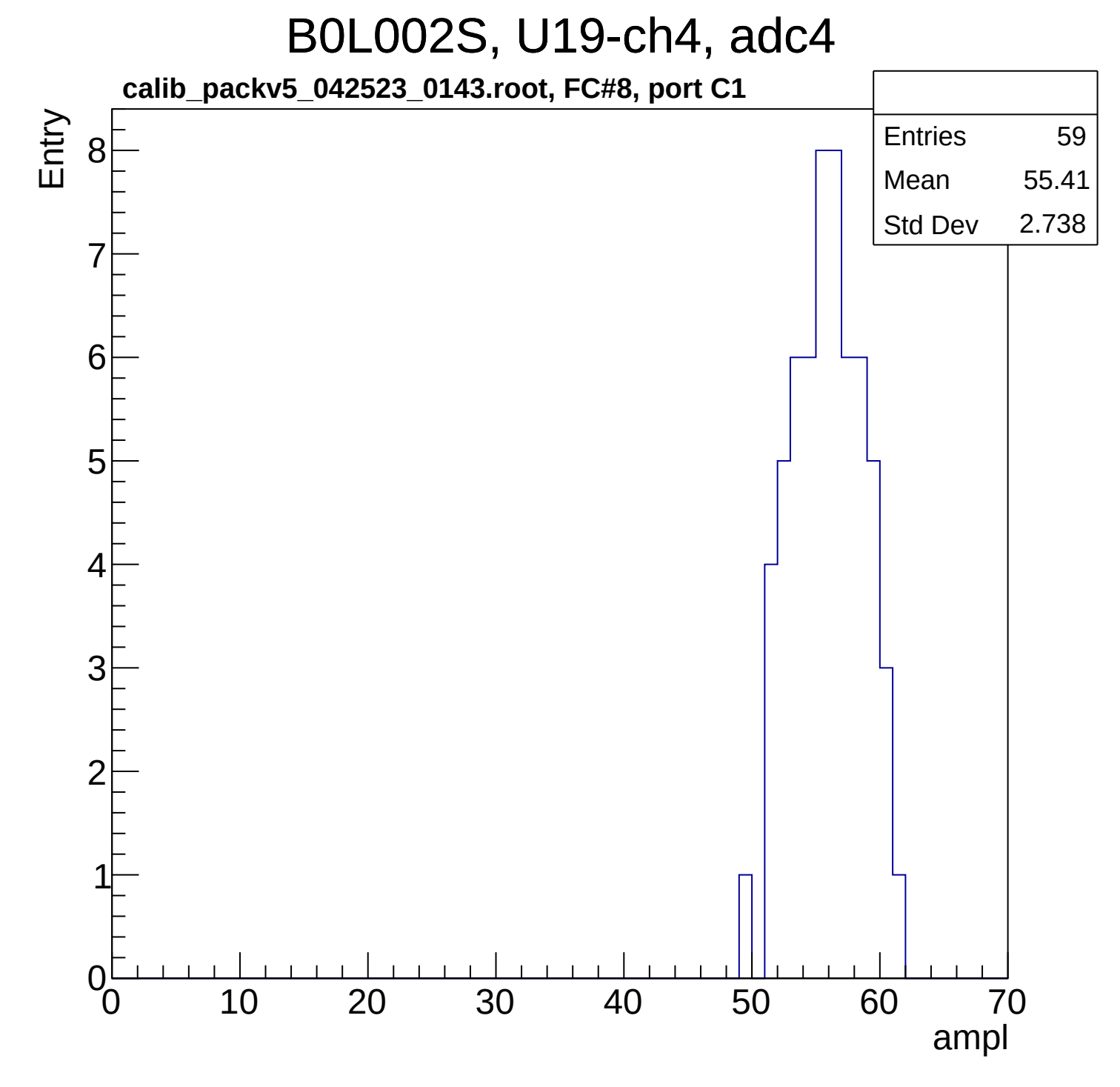

B0L002S, U19-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

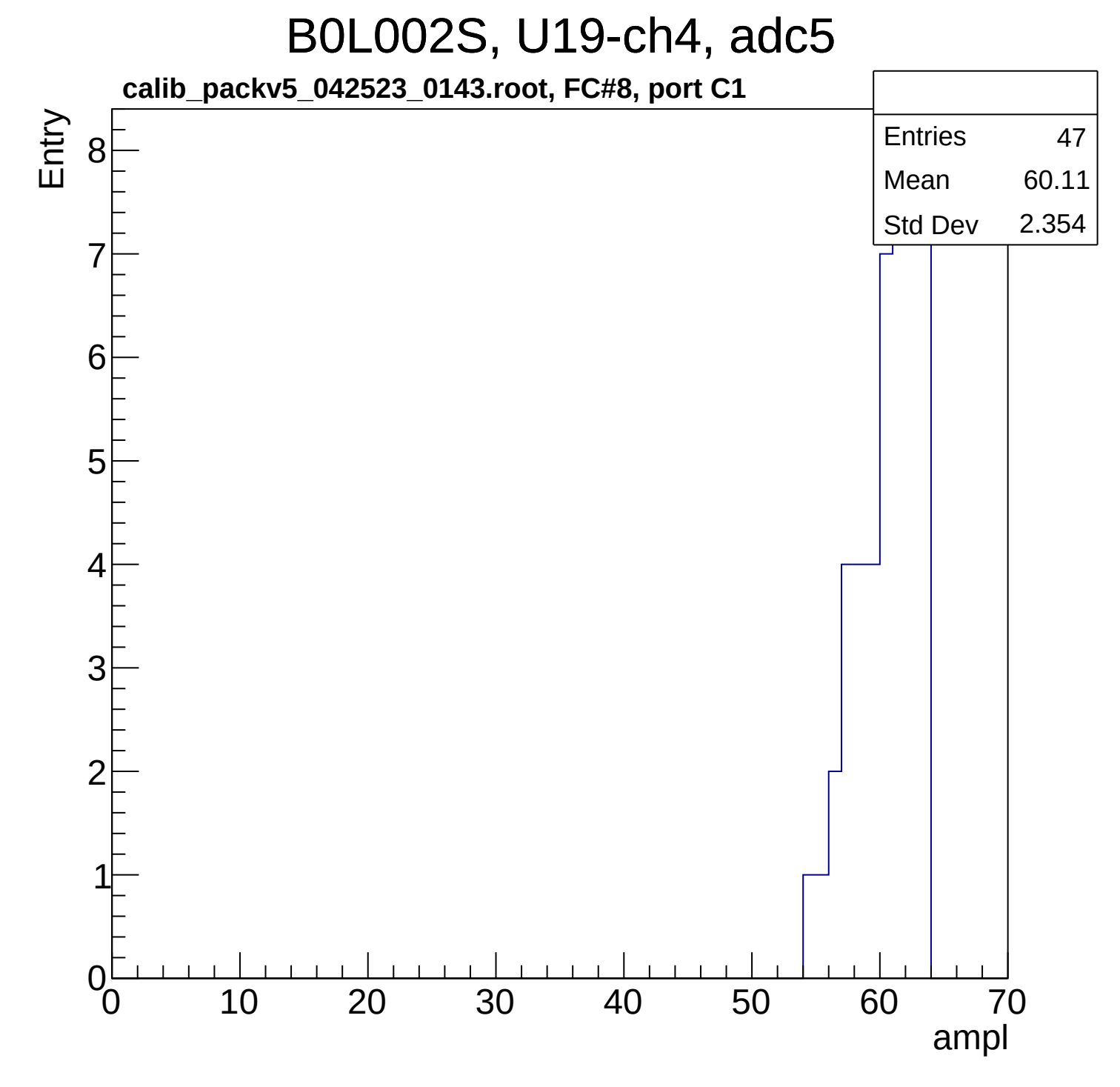

B0L002S, U19-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.28
Std Dev 4.95
Gaus mean : 30.5995
Gaus Width: 3.8436
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

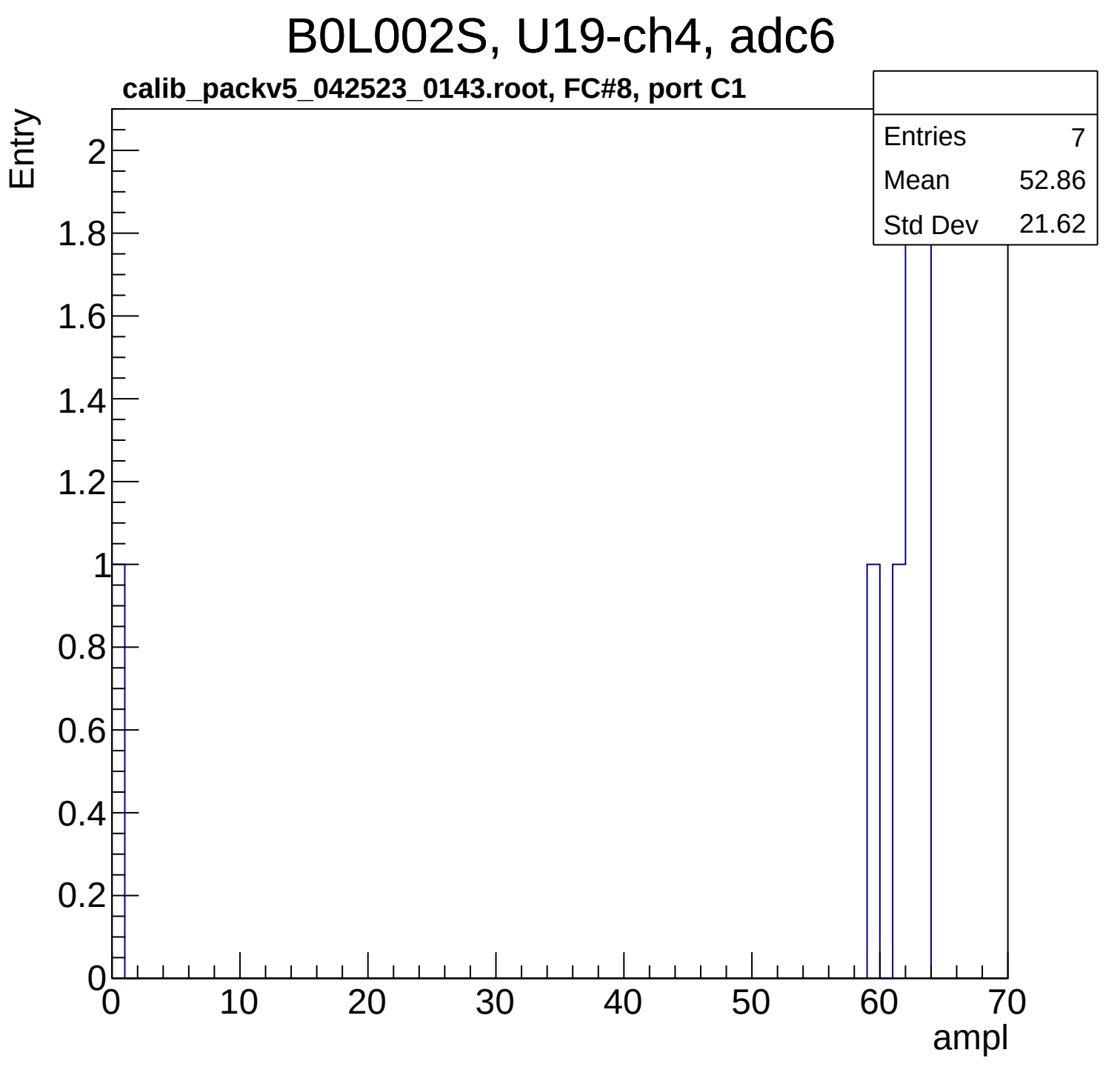

B0L002S, U19-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 36.98
Std Dev 3.261
Gaus mean : 37.4075
Gaus Width: 3.1621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

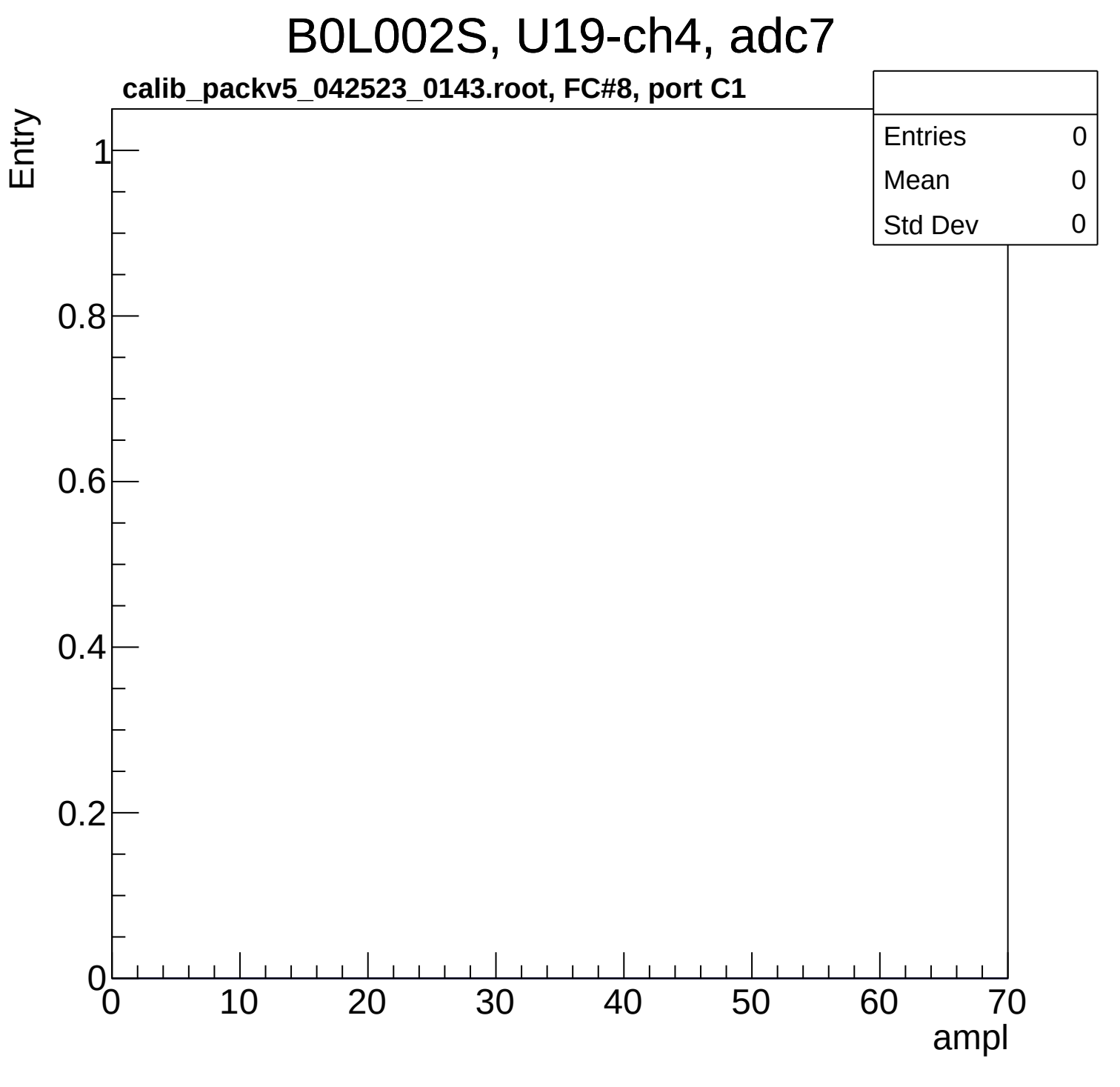

B0L002S, U19-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 42.81
Std Dev 3.748
Gaus mean : 42.8926
Gaus Width: 4.0052
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

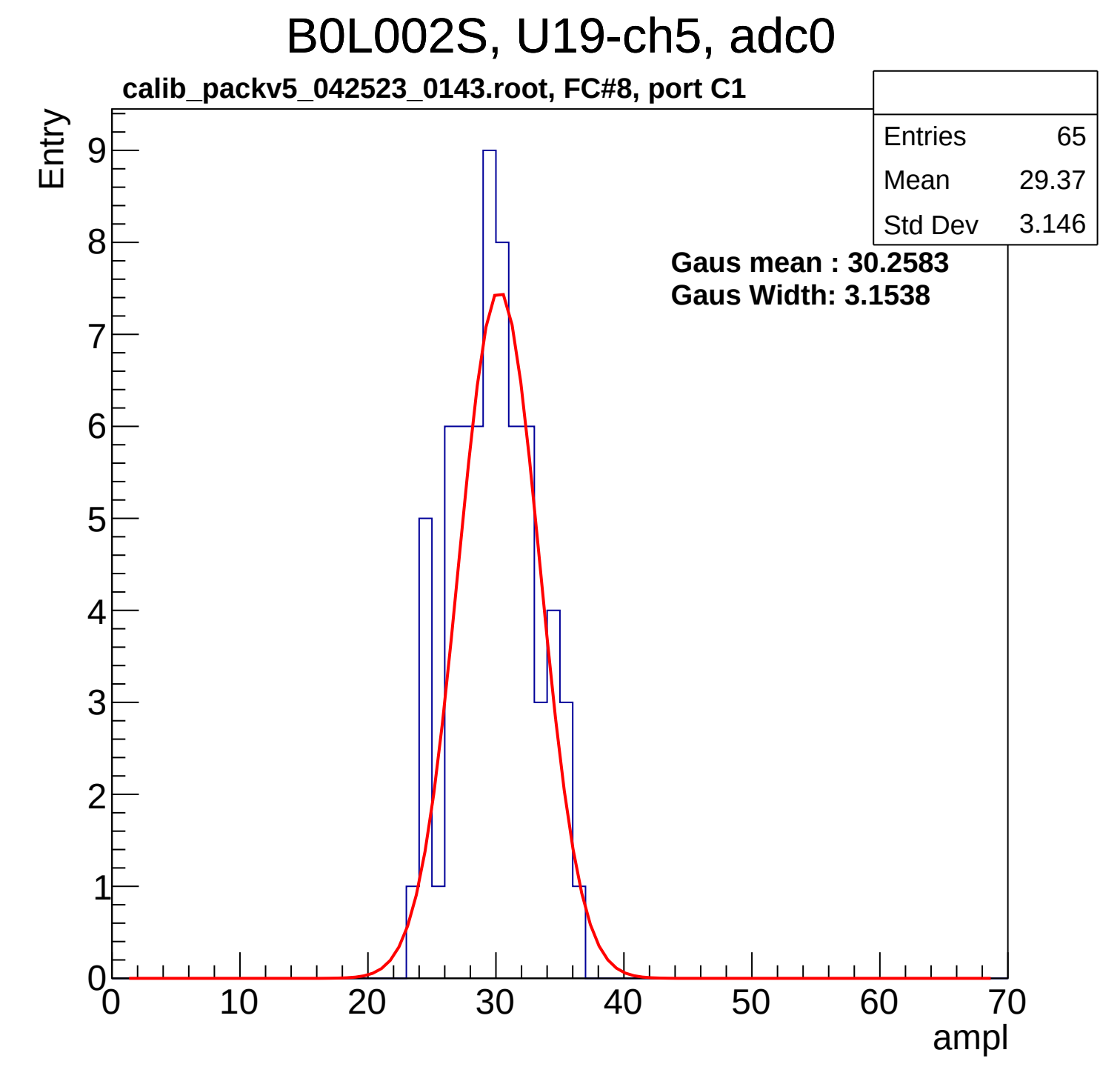

B0L002S, U19-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 48.98
Std Dev 3.064
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

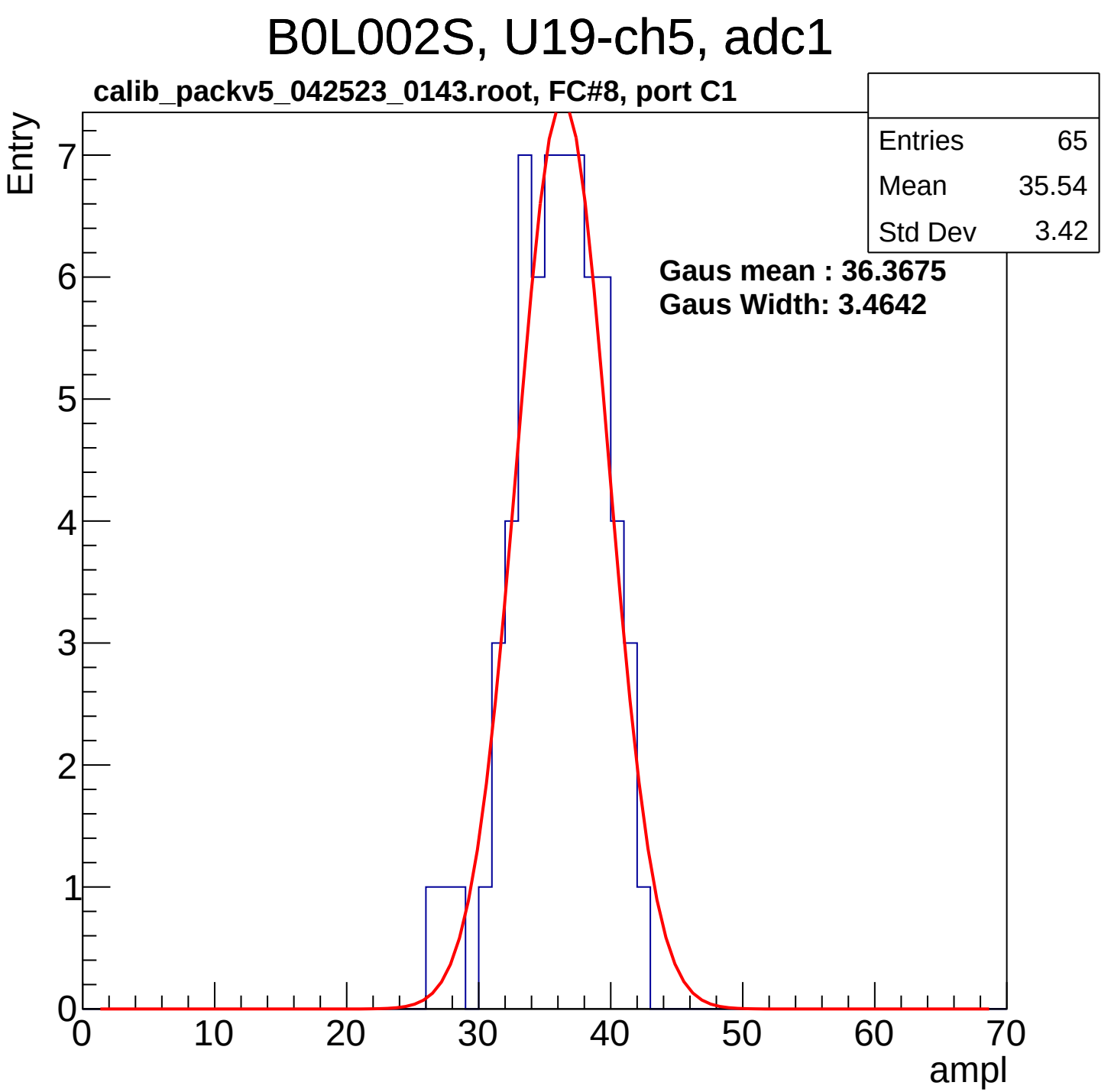

B0L002S, U19-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.41
Std Dev 2.738
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

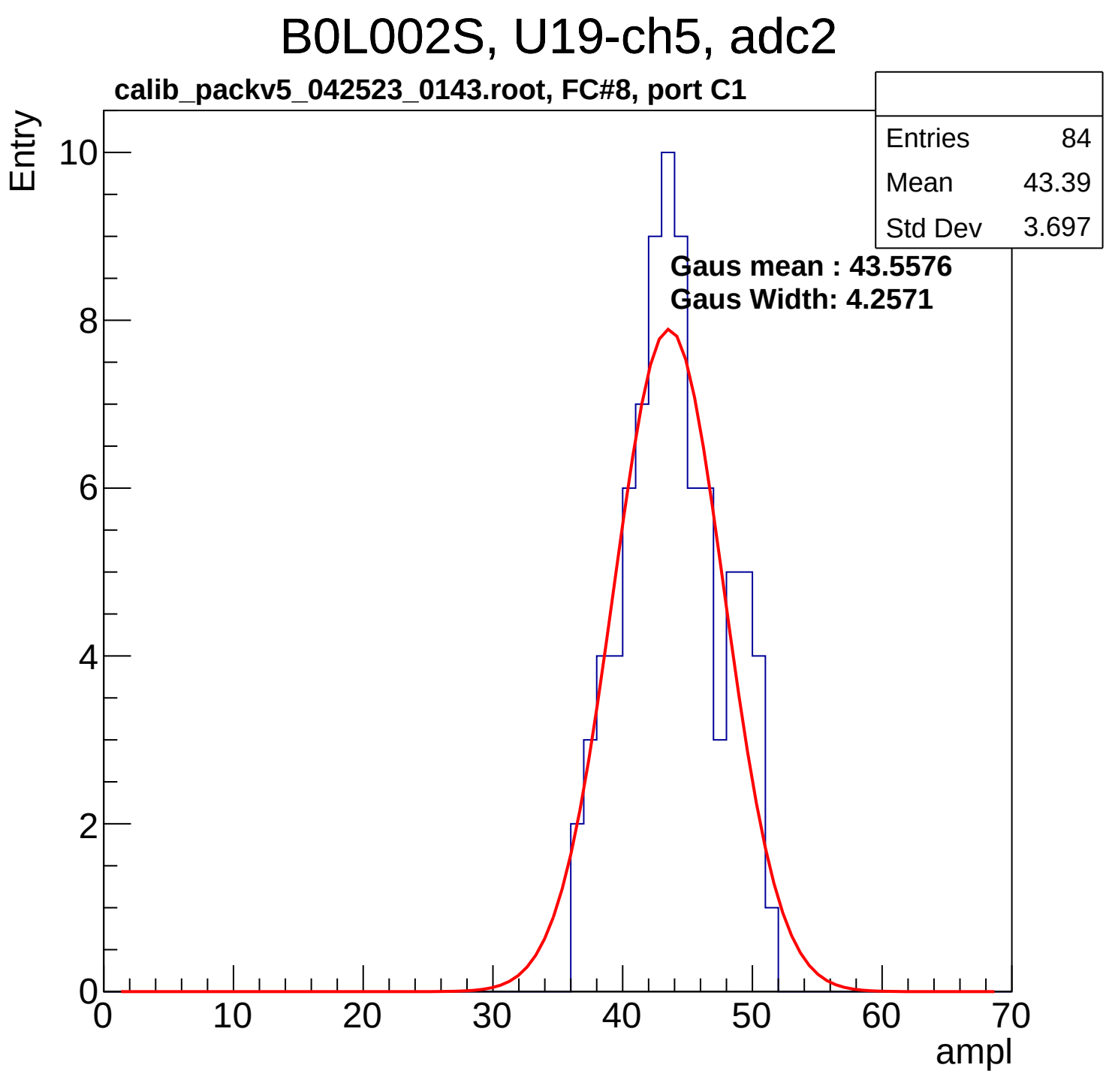

B0L002S, U19-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 60.11
Std Dev 2.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

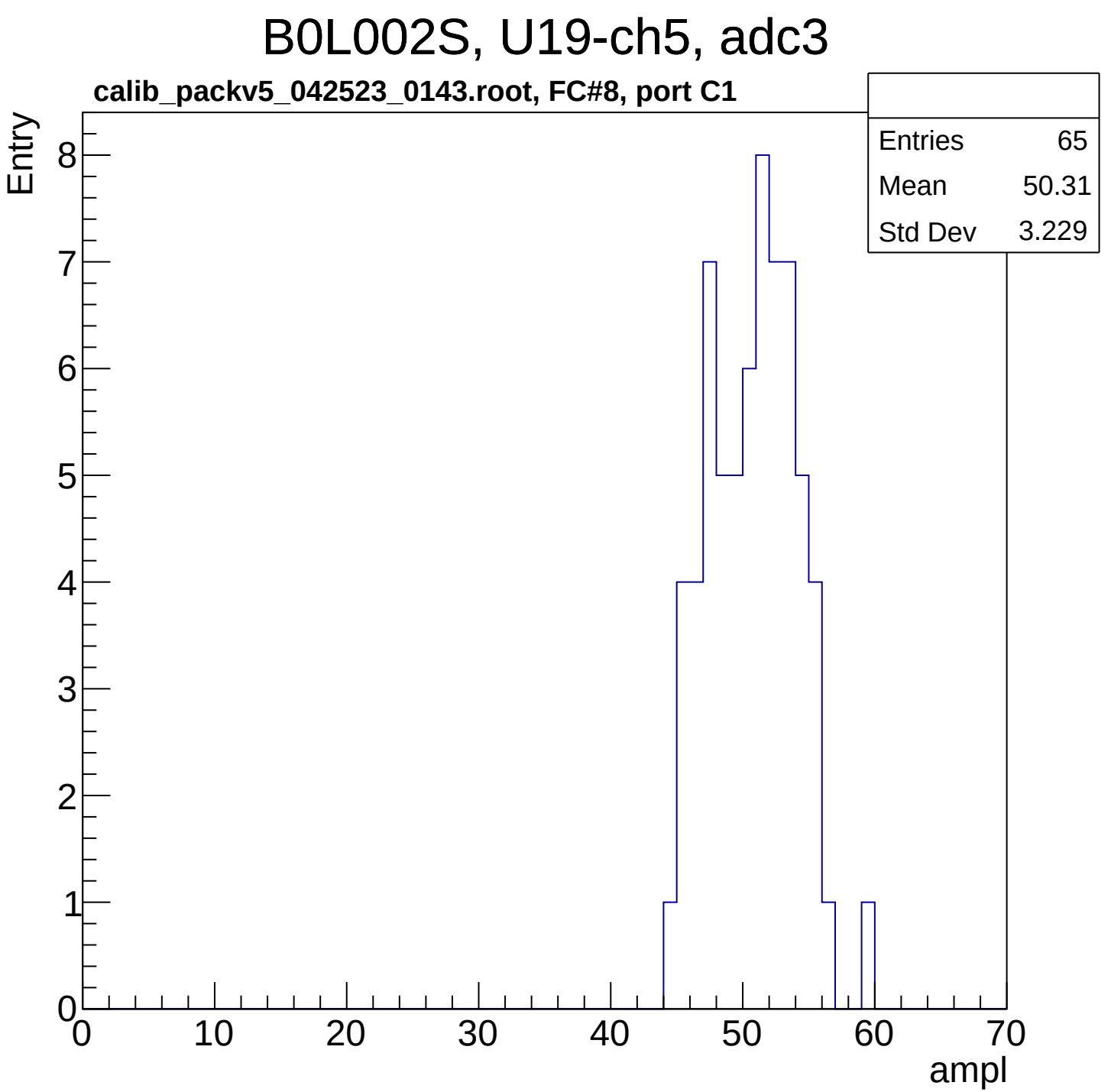

B0L002S, U19-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 52.86
Std Dev 21.62
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

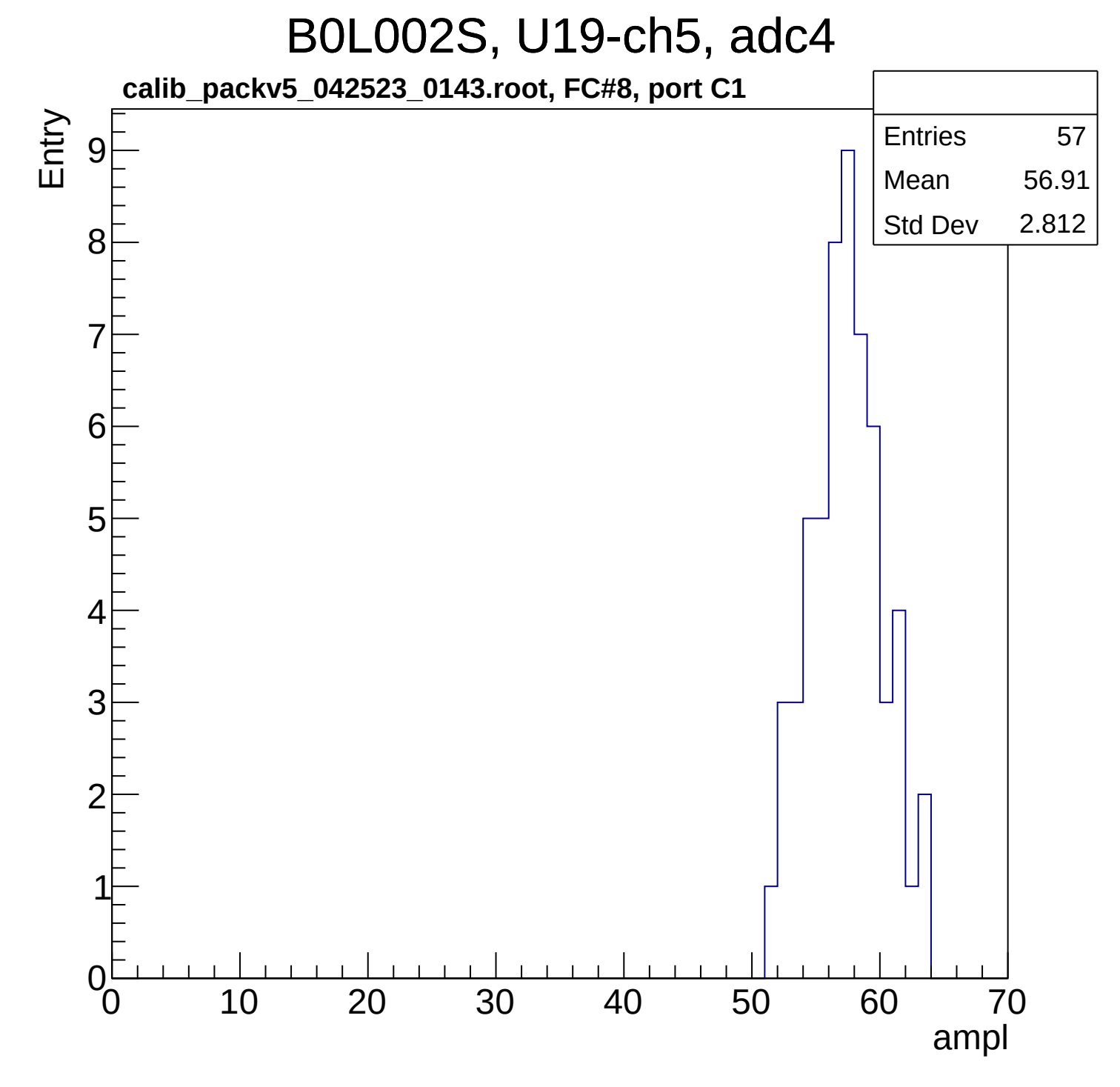

B0L002S, U19-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

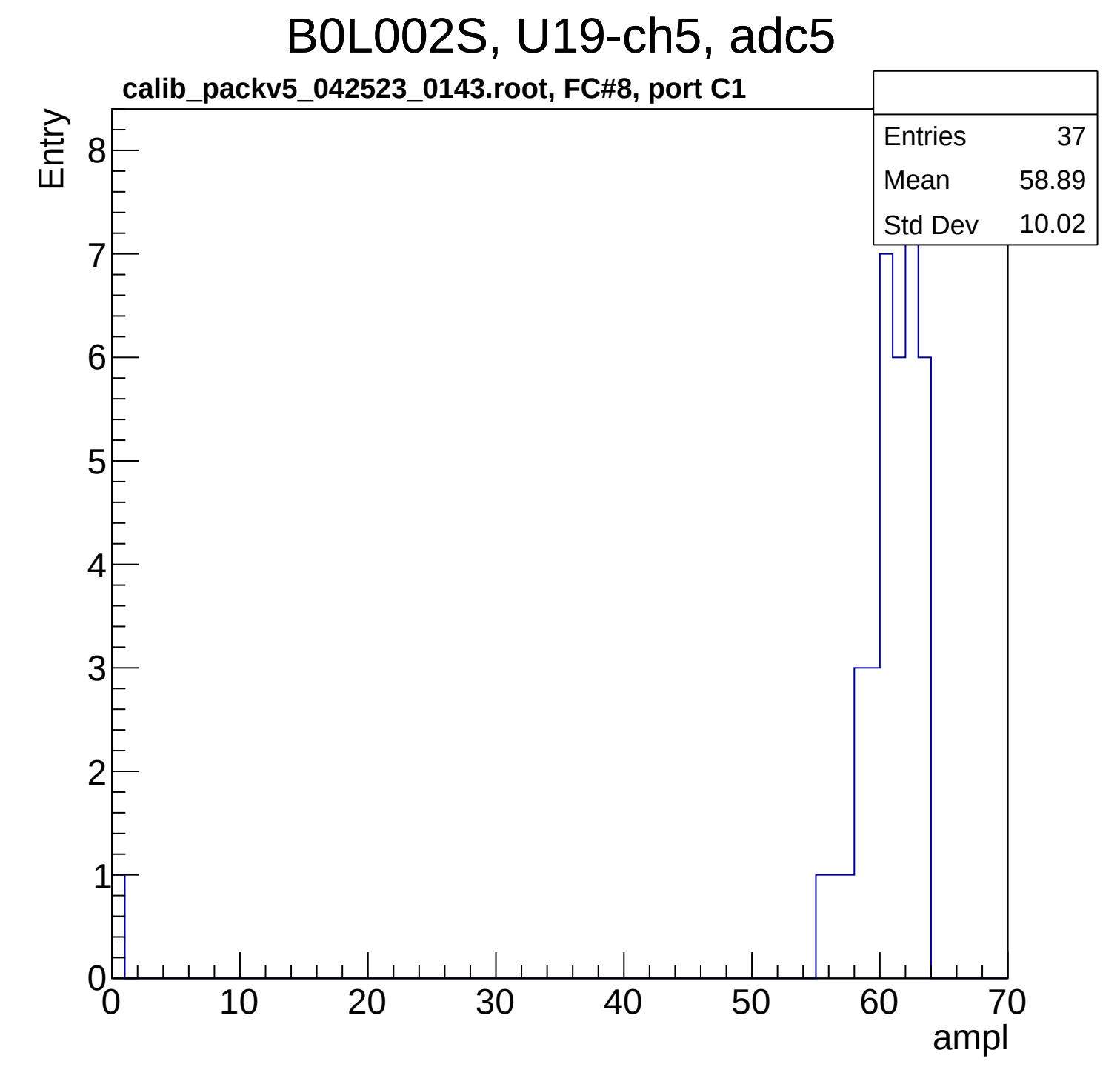

B0L002S, U19-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.37
Std Dev 3.146
Gaus mean : 30.2583
Gaus Width: 3.1538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

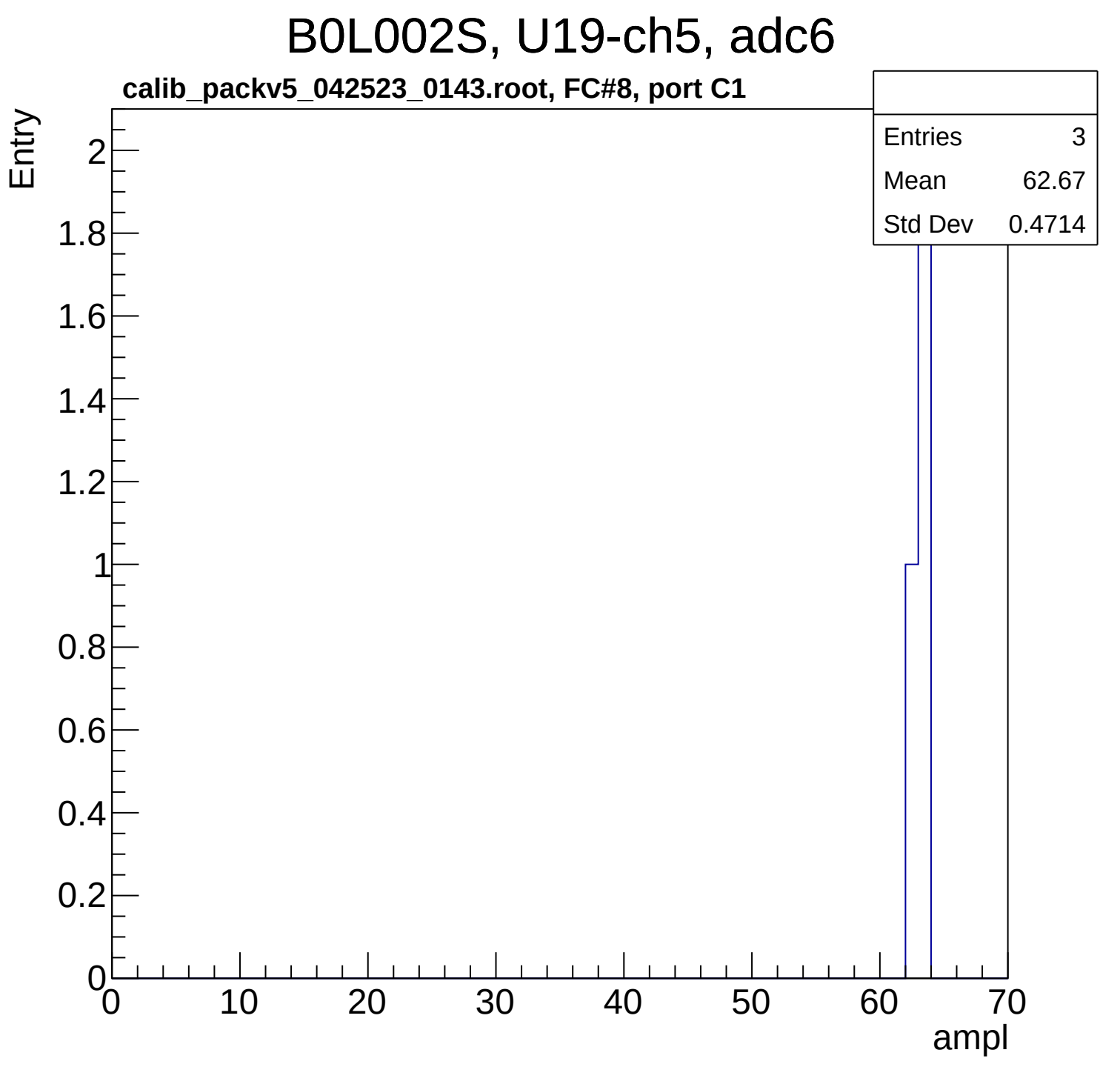

B0L002S, U19-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 35.54
Std Dev 3.42
Gaus mean : 36.3675
Gaus Width: 3.4642
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

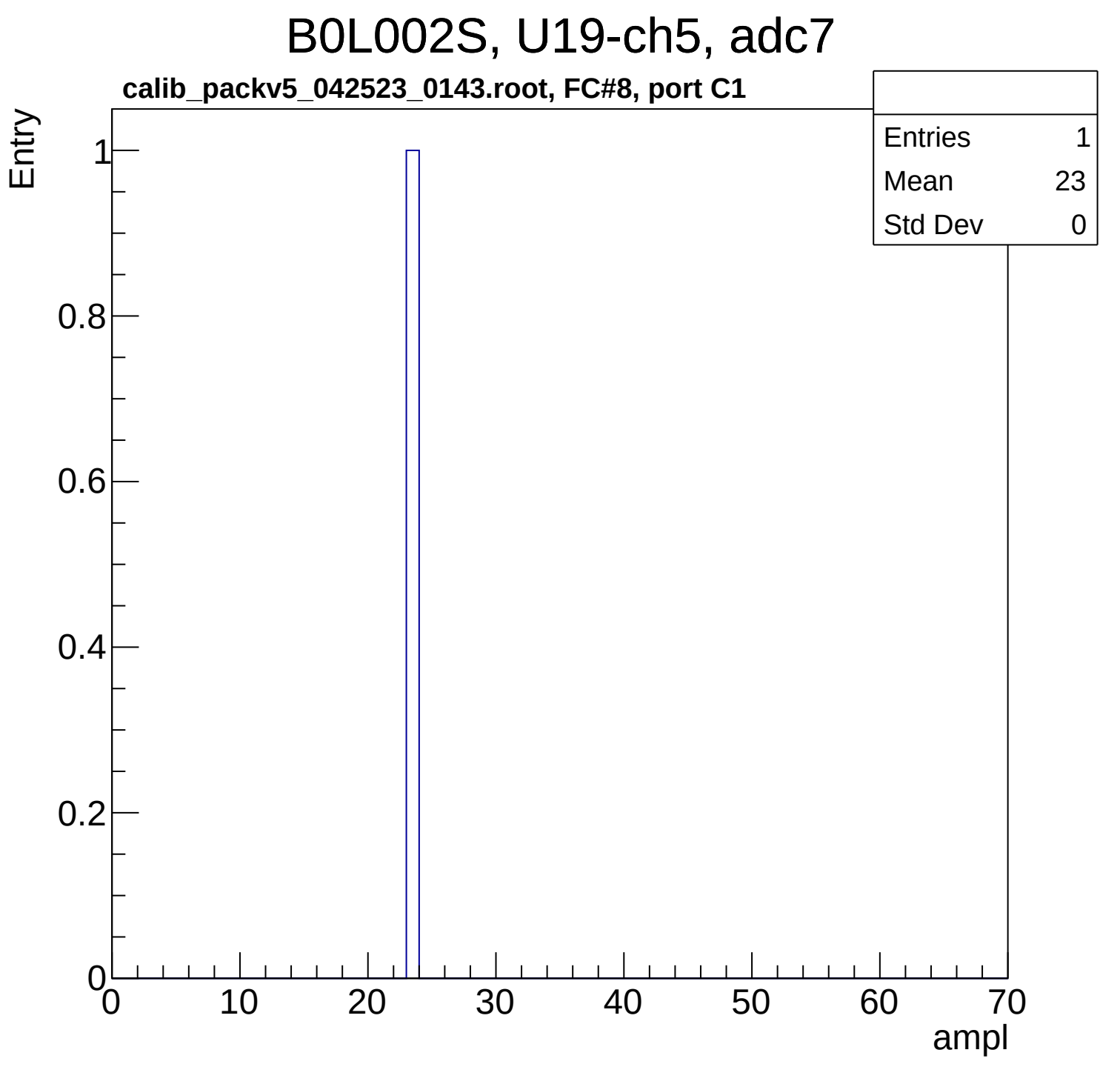

B0L002S, U19-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 43.39
Std Dev 3.697
Gaus mean : 43.5576
Gaus Width: 4.2571
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

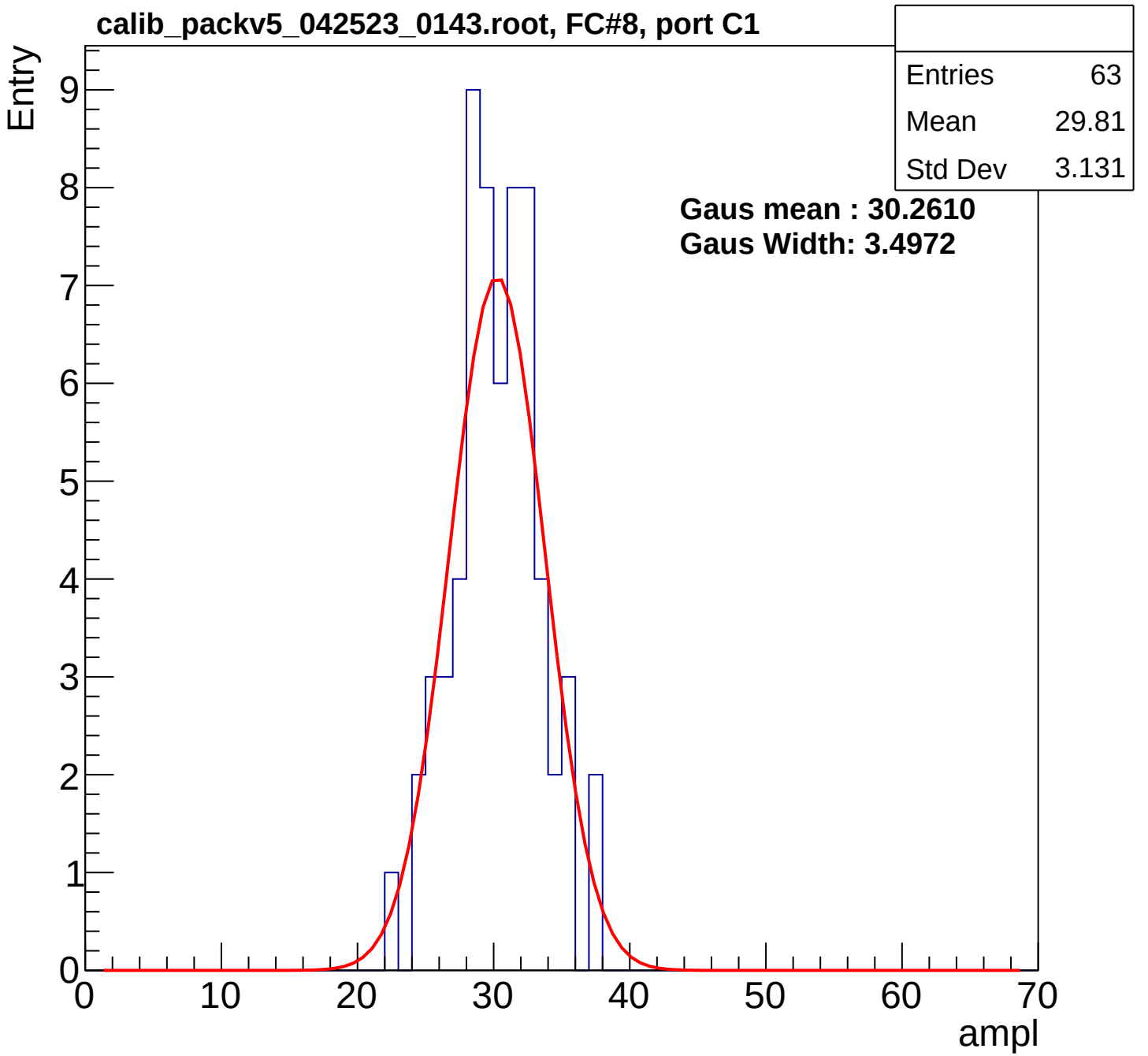

B0L002S, U19-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.31
Std Dev 3.229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

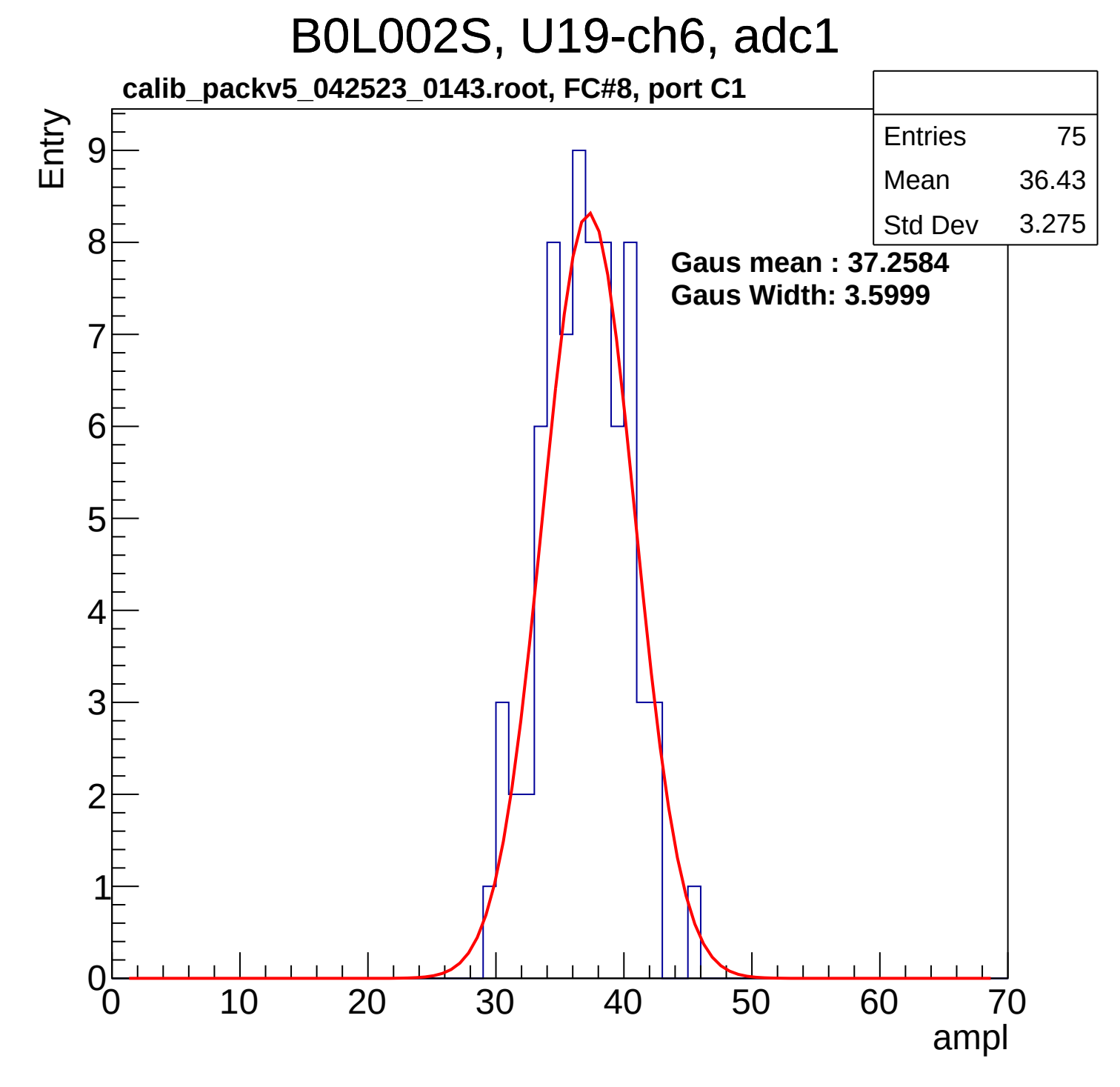

B0L002S, U19-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.91
Std Dev 2.812
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

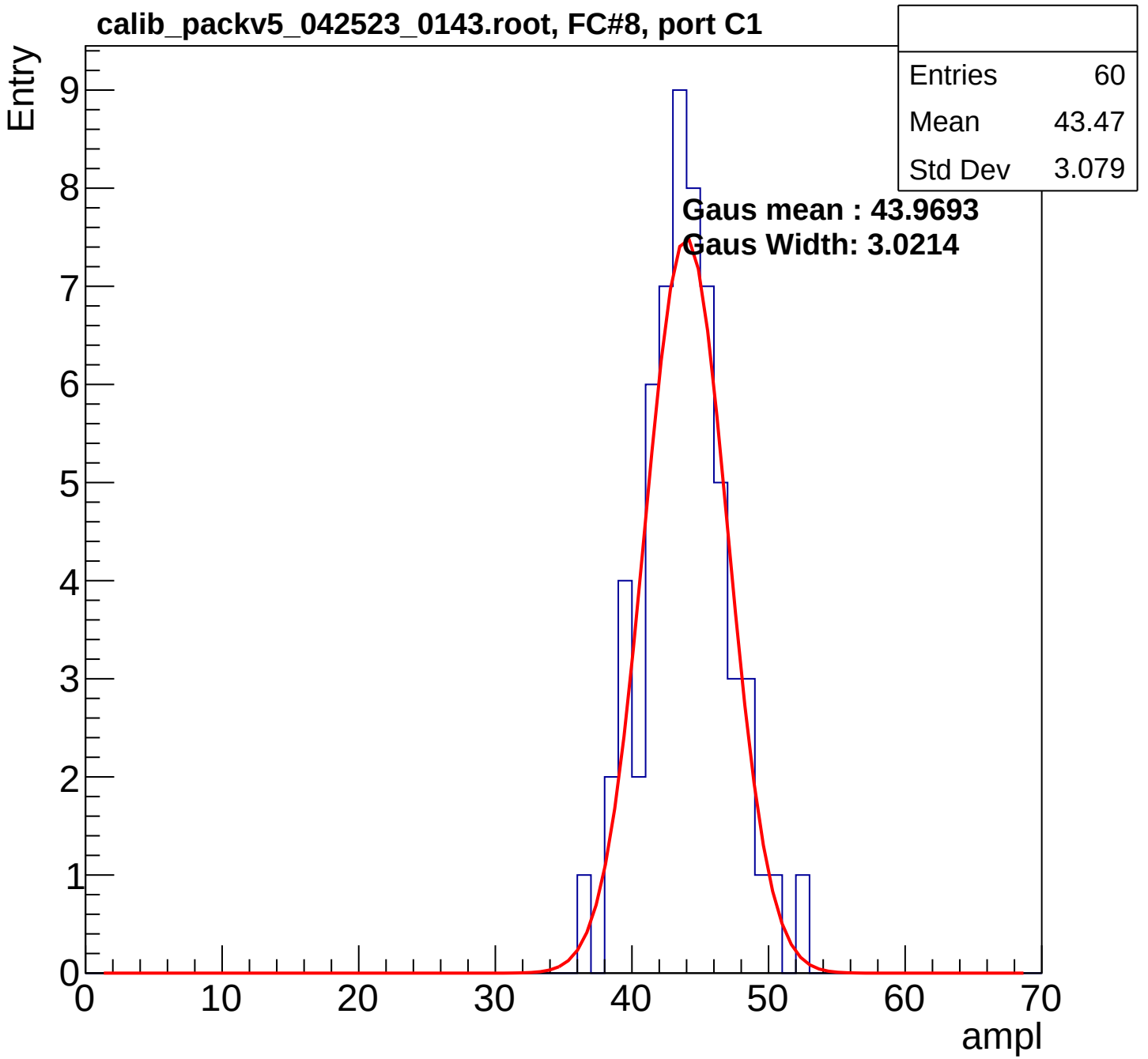

B0L002S, U19-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.89
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

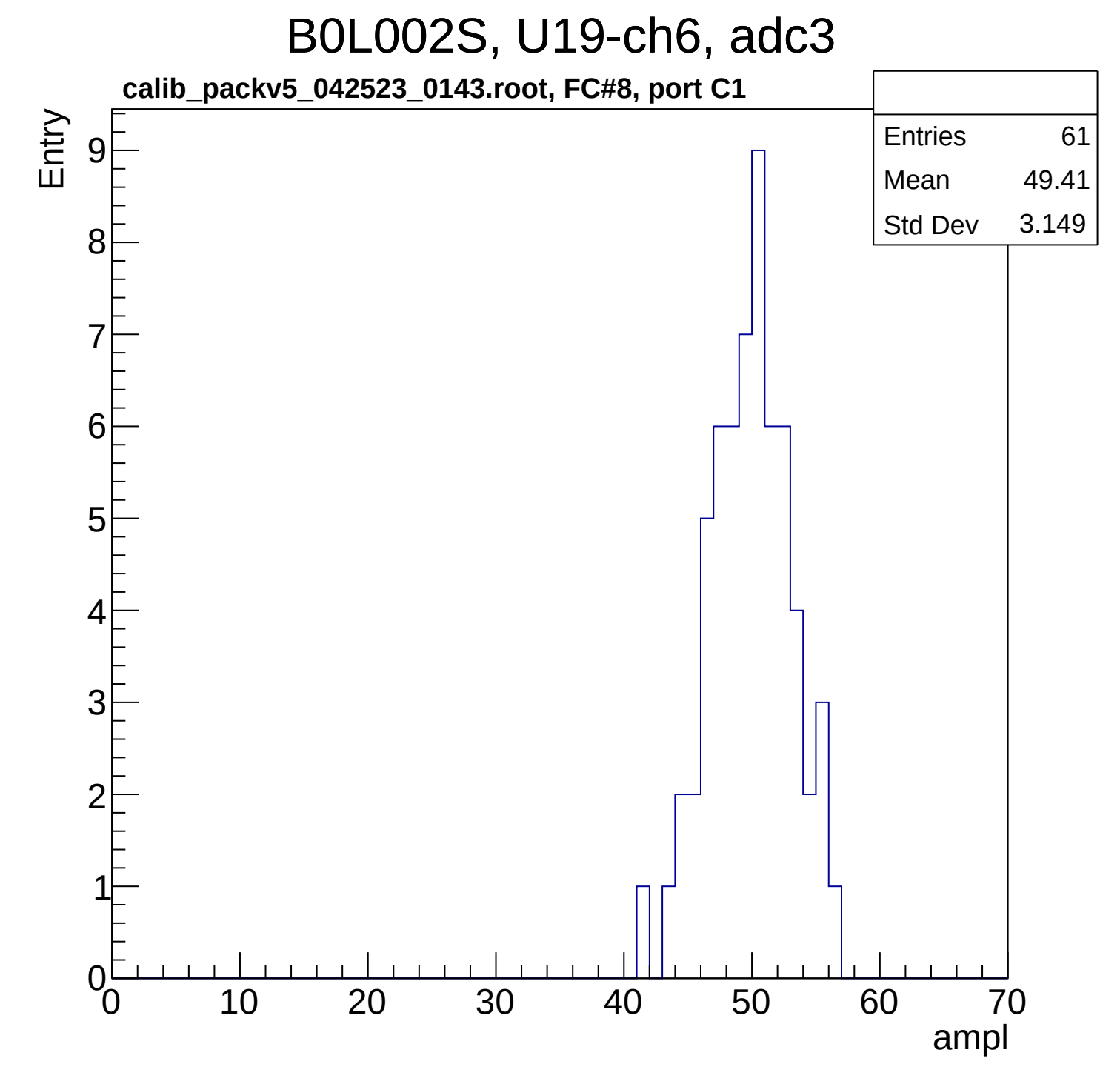

B0L002S, U19-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

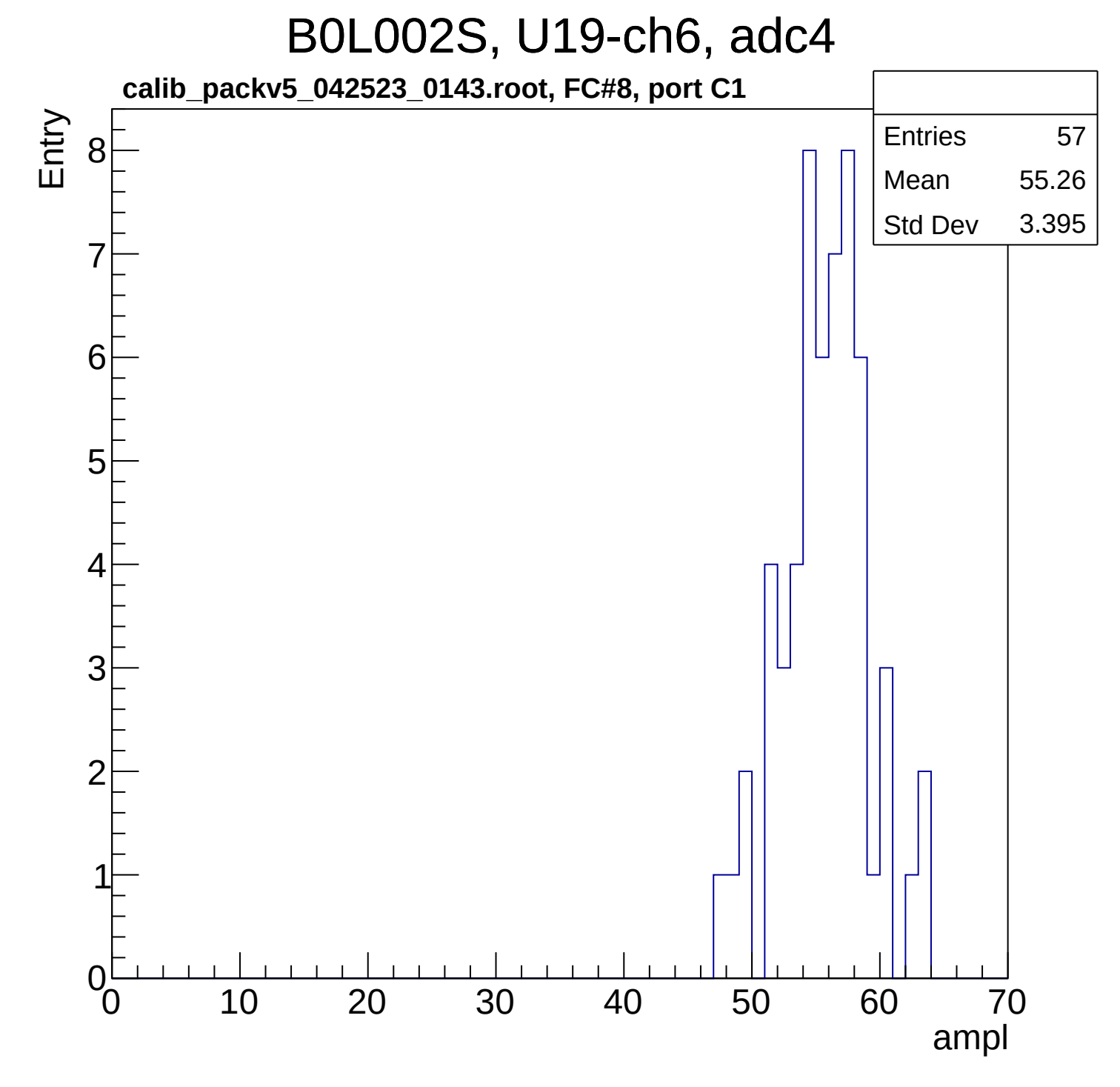

B0L002S, U19-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

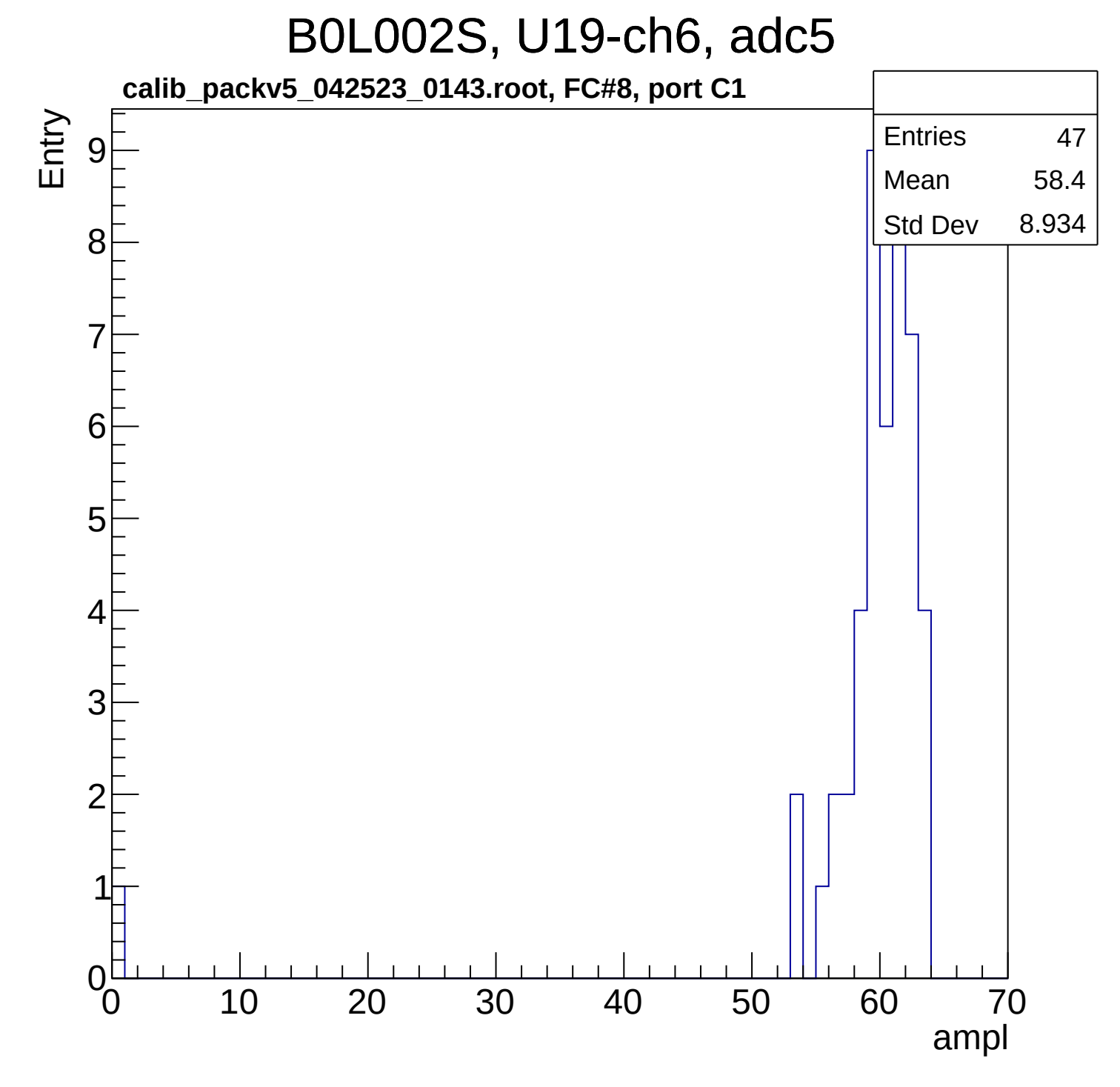

B0L002S, U19-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.81
Std Dev 3.131
Gaus mean : 30.2610
Gaus Width: 3.4972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

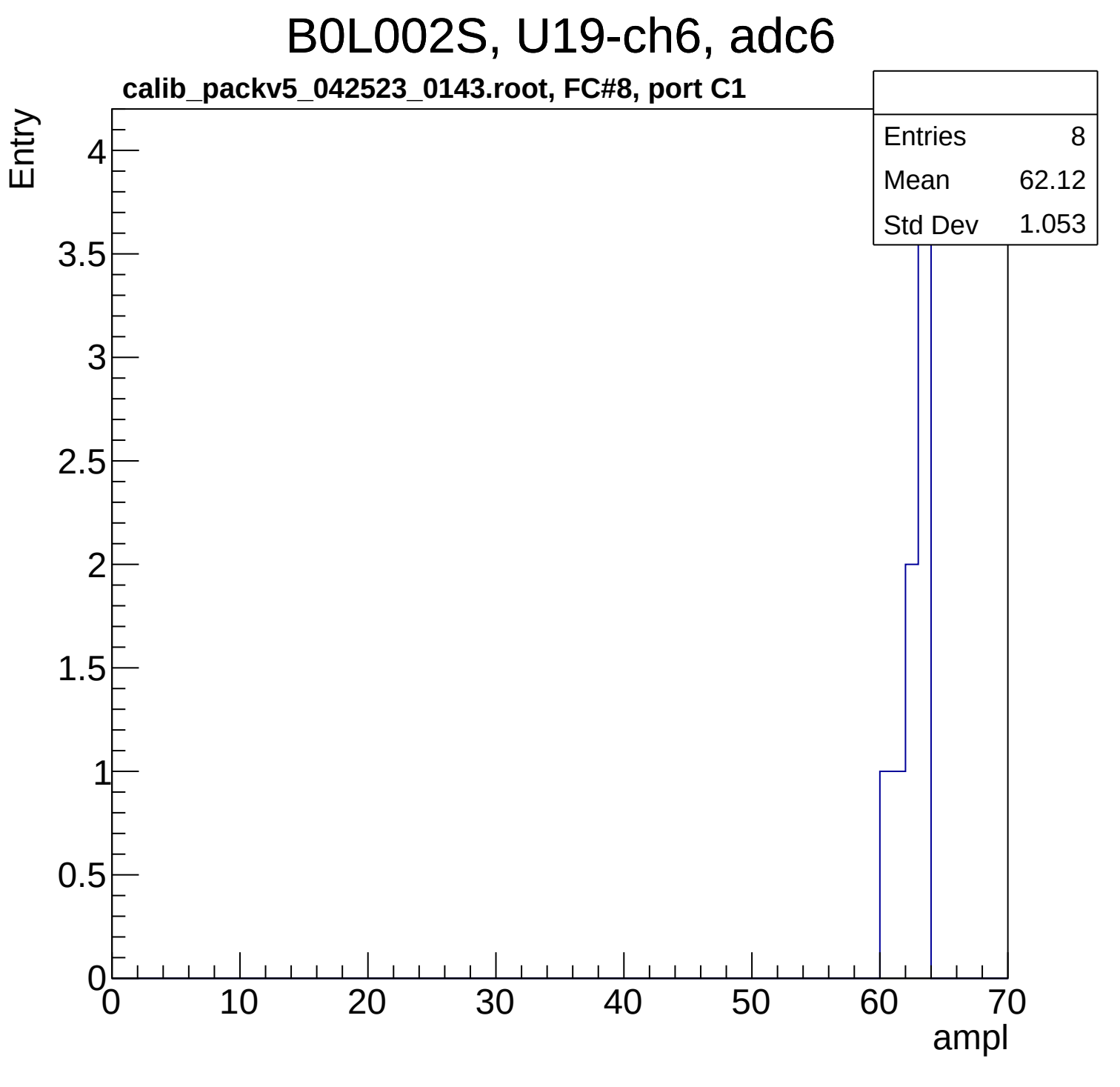

B0L002S, U19-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 36.43
Std Dev 3.275
Gaus mean : 37.2584
Gaus Width: 3.5999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

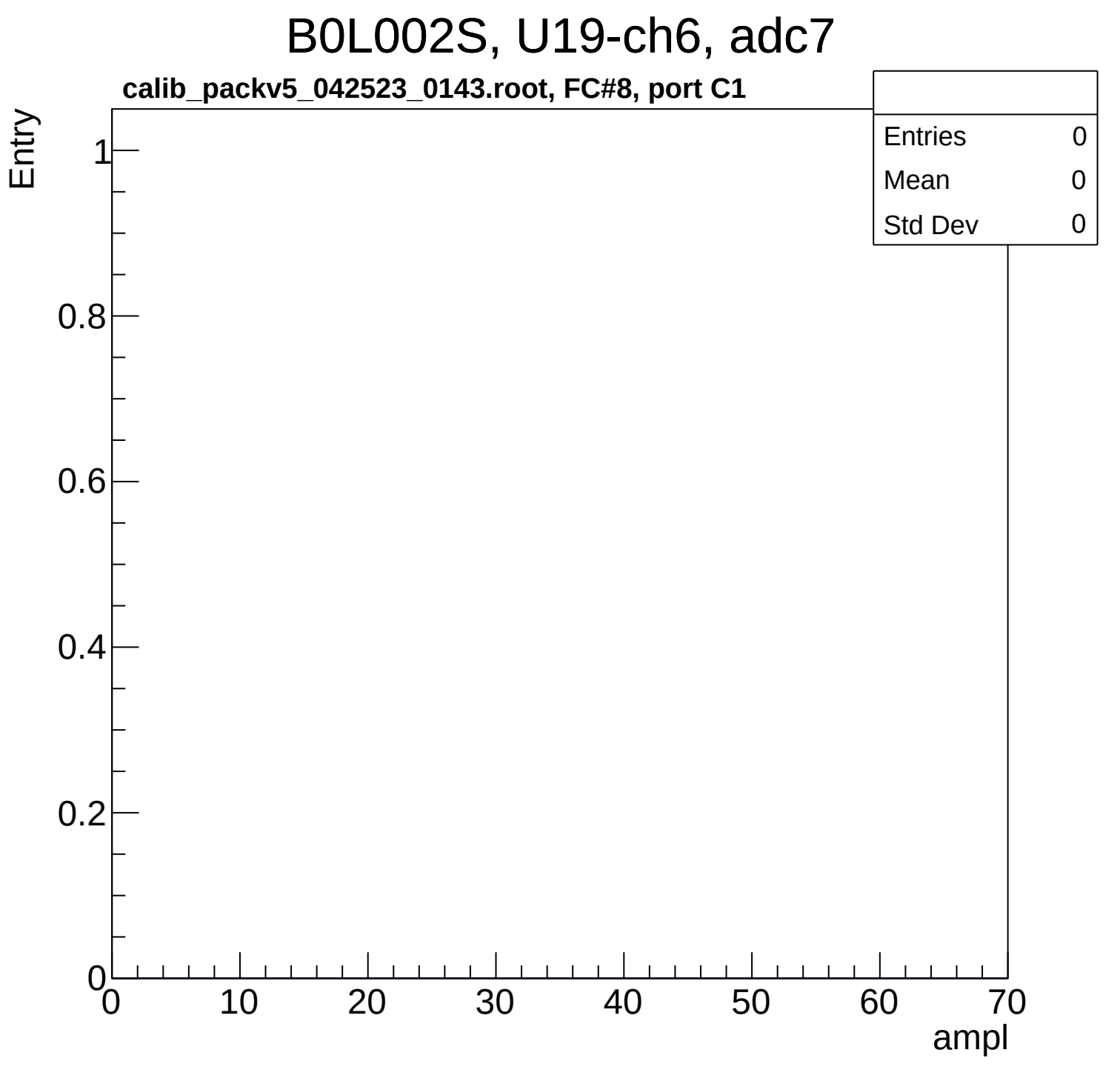

B0L002S, U19-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.47
Std Dev 3.079
Gaus mean : 43.9693
Gaus Width: 3.0214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

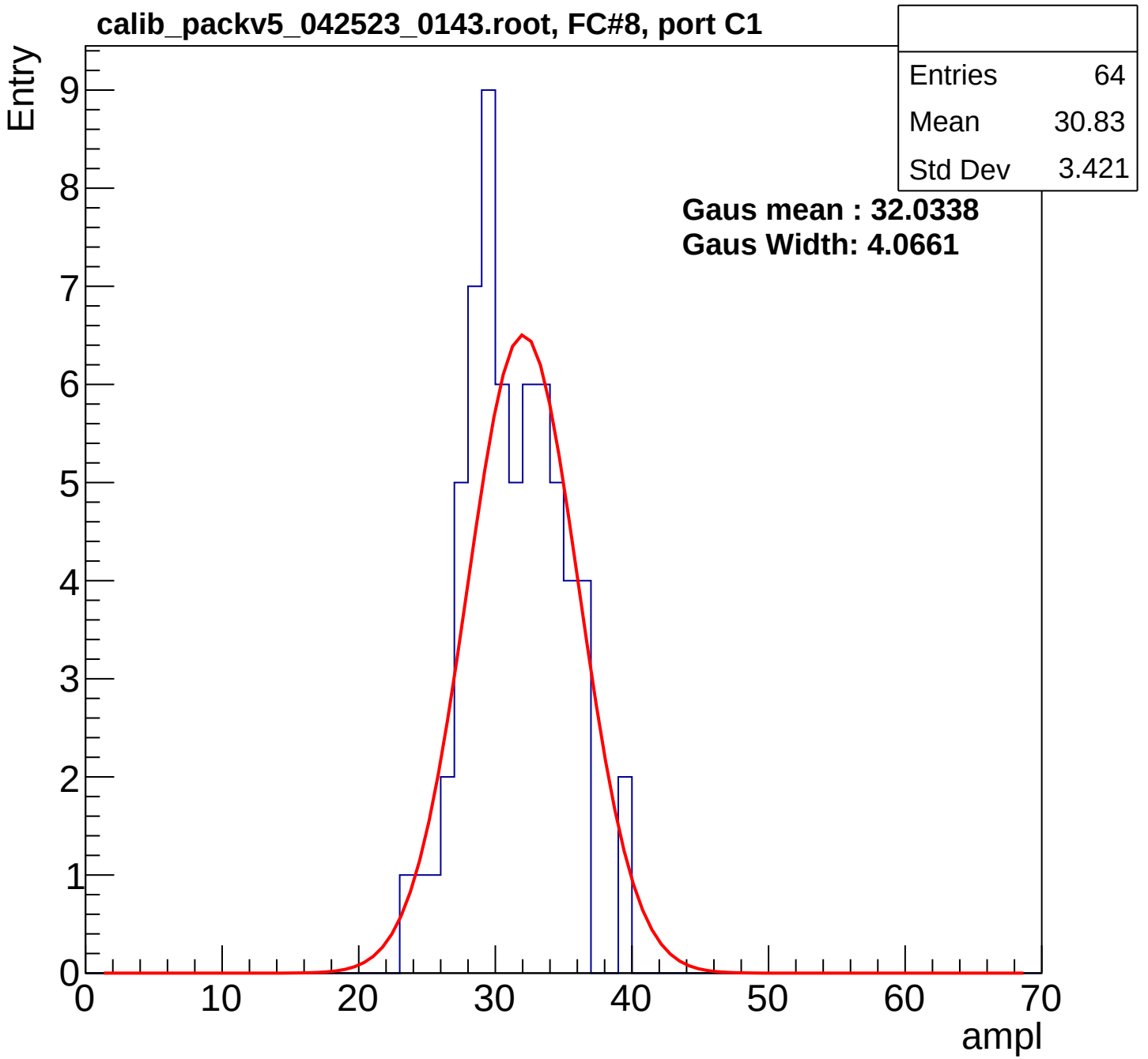

B0L002S, U19-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 49.41
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

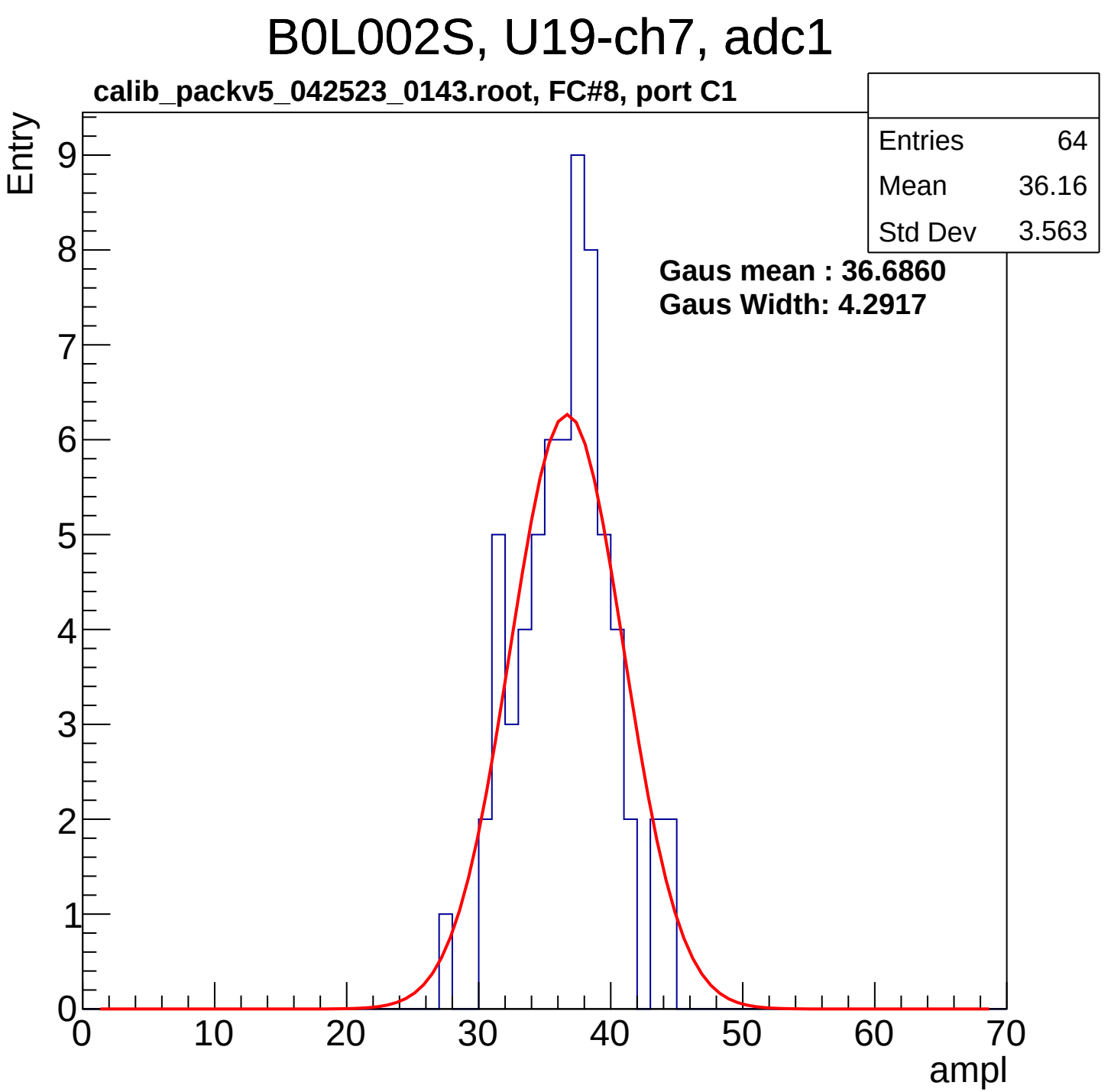

B0L002S, U19-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.26
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

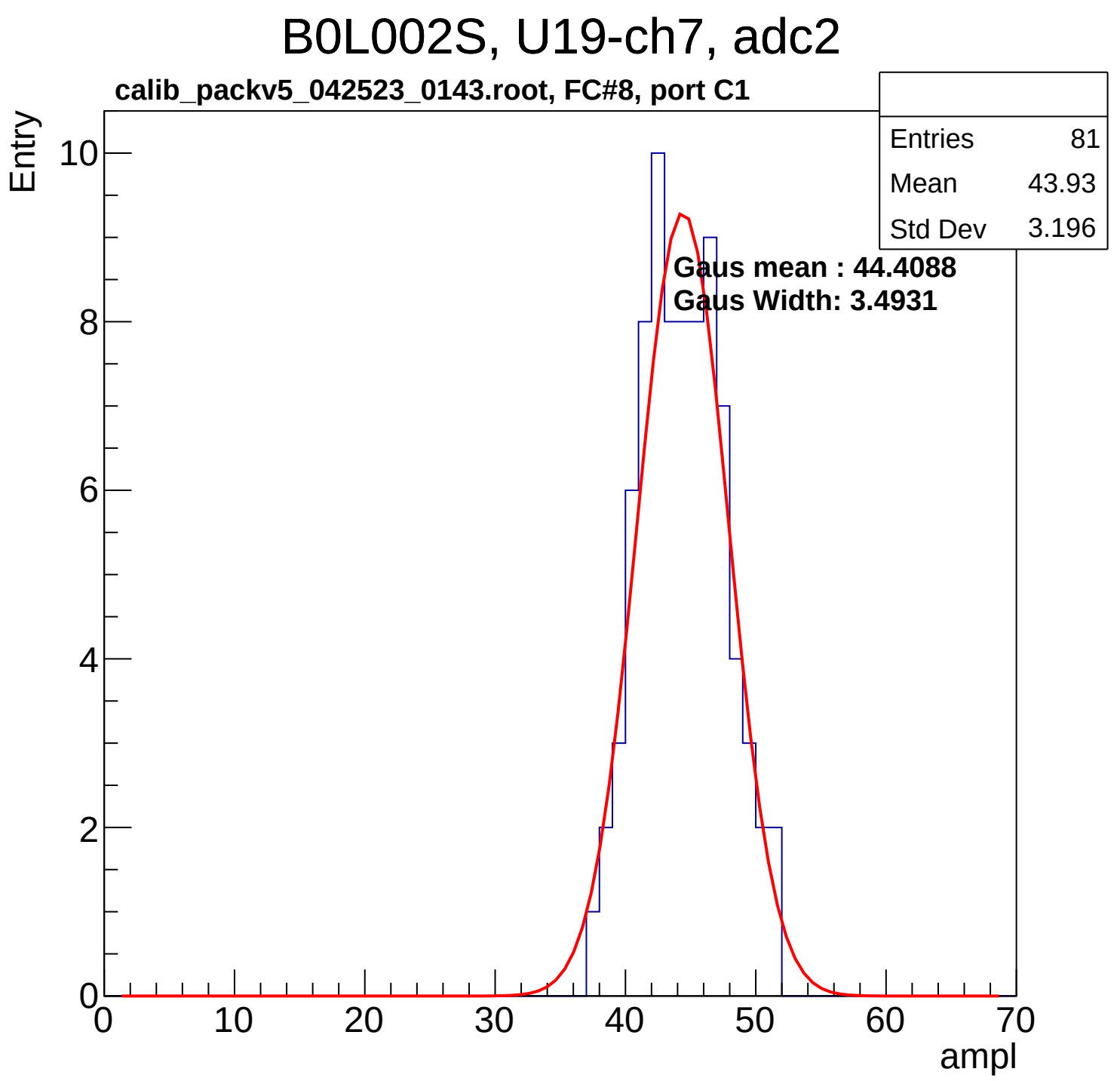

B0L002S, U19-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.4
Std Dev 8.934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

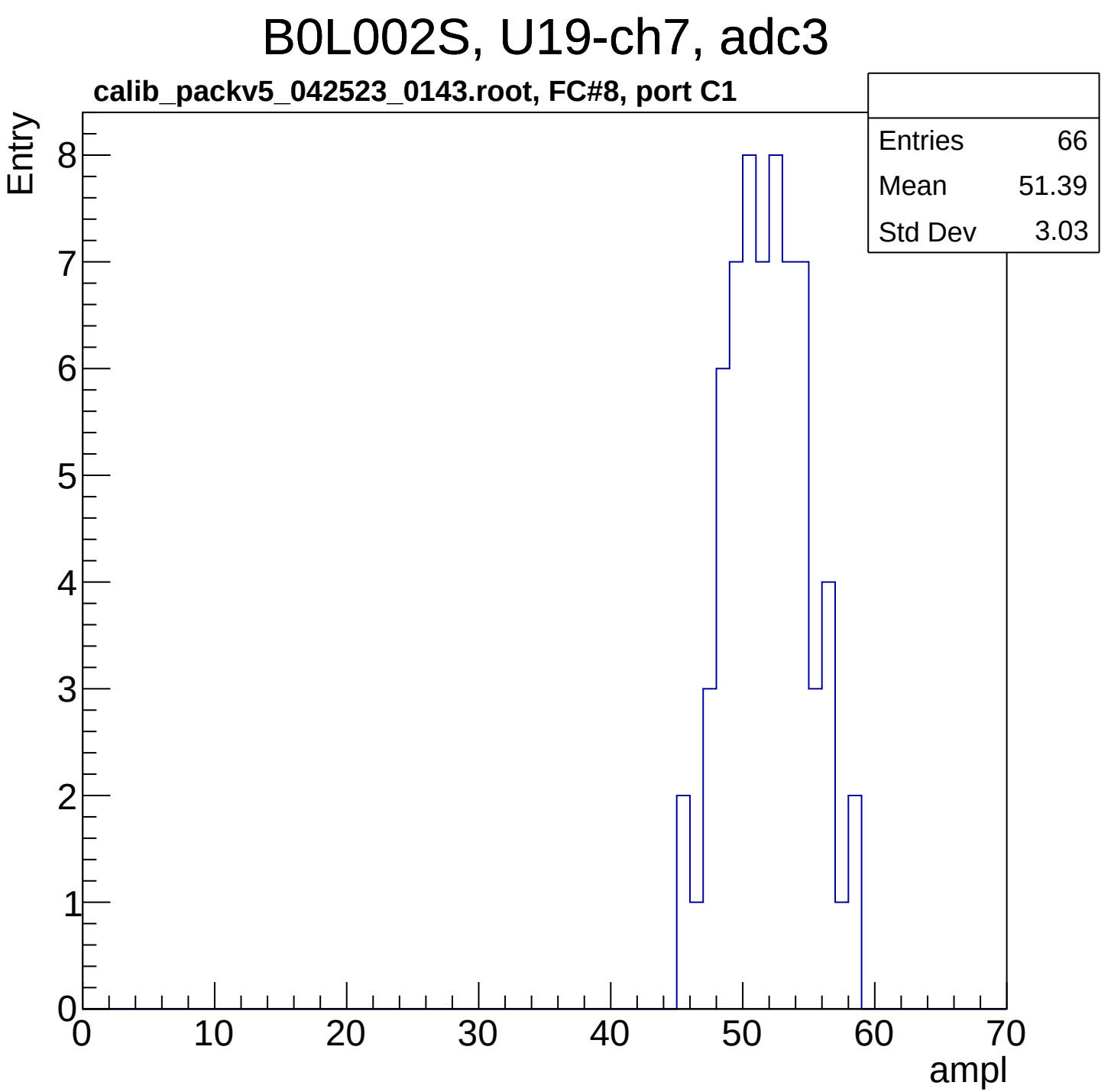

B0L002S, U19-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

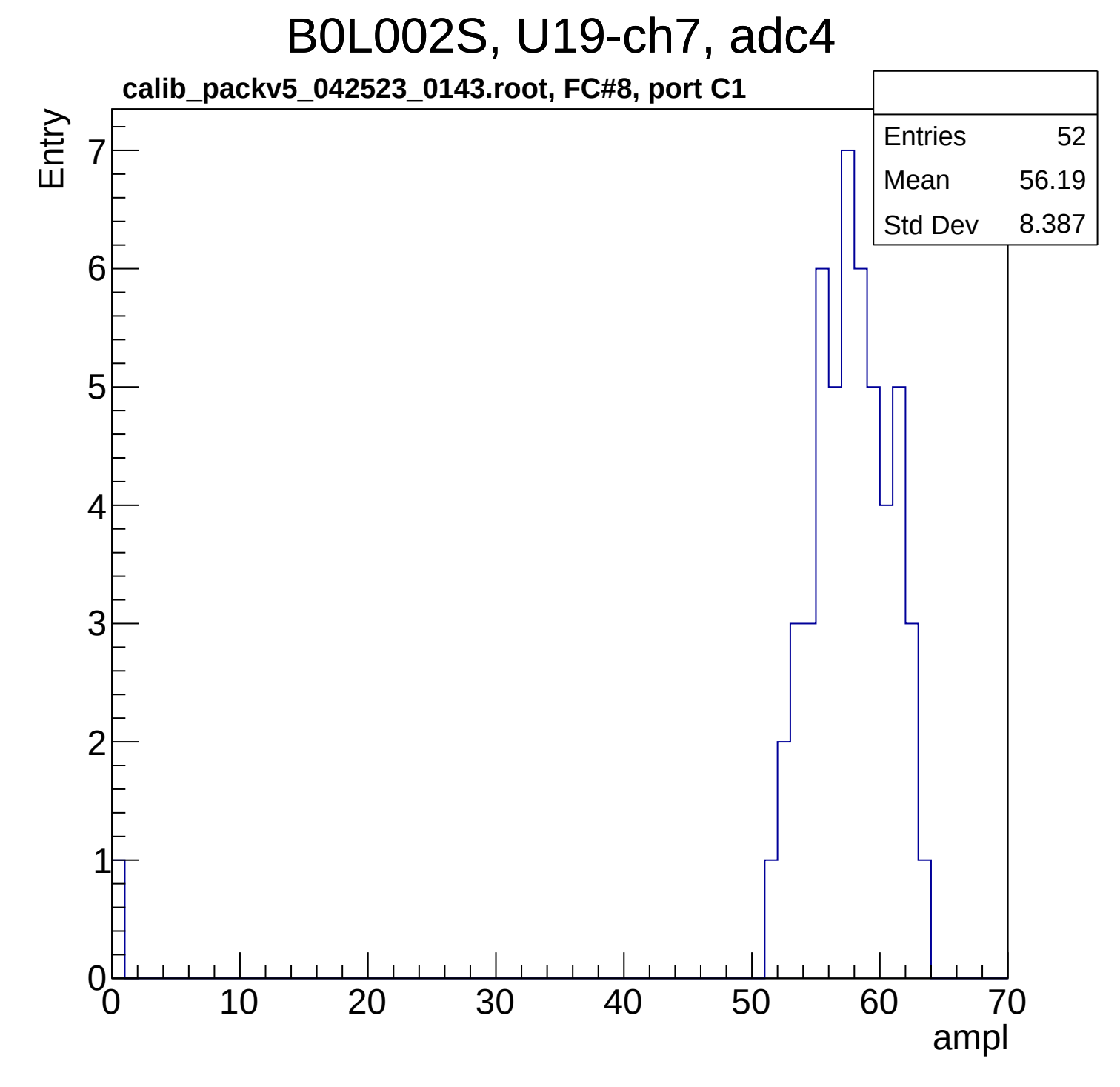

B0L002S, U19-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

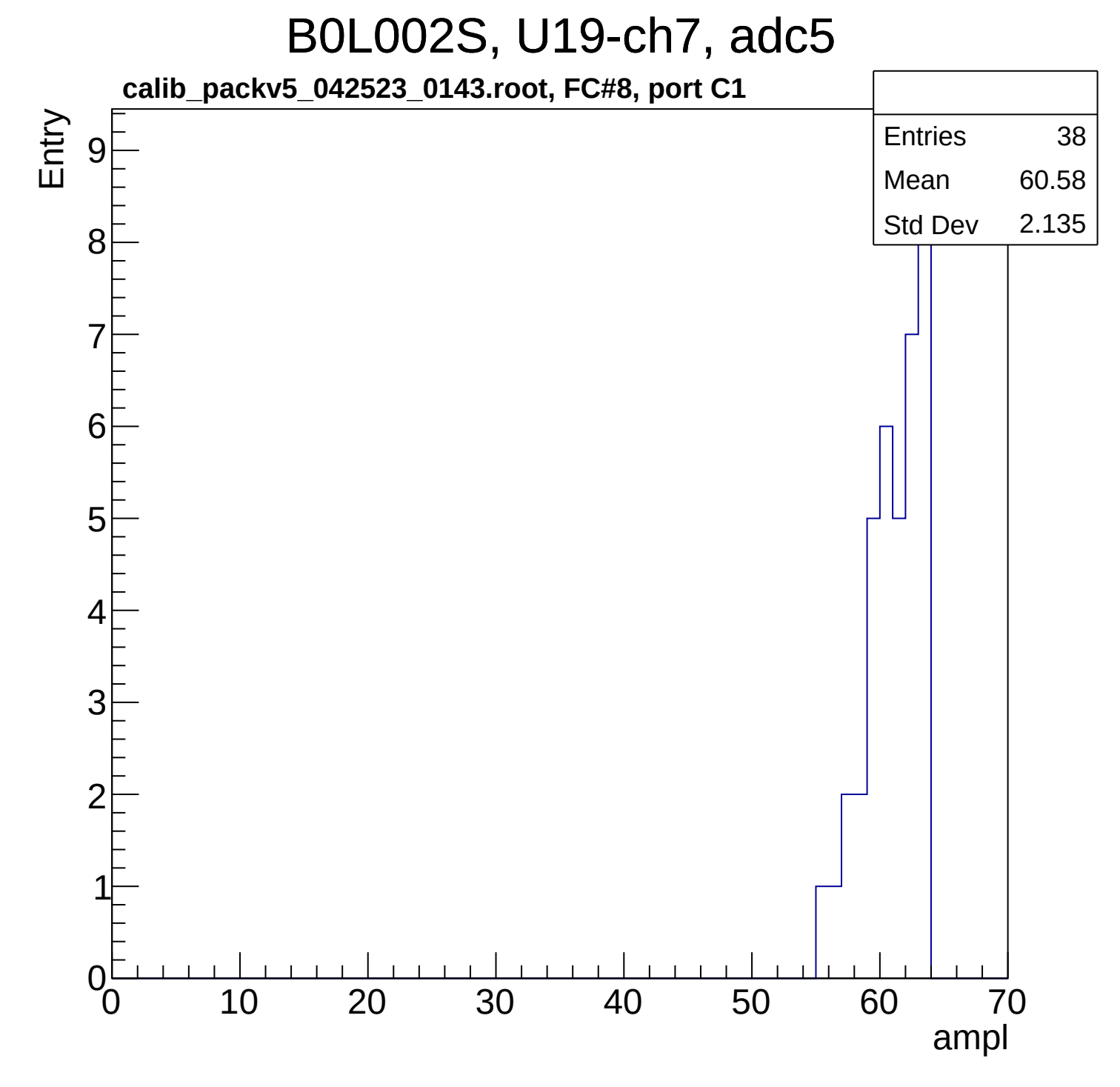

B0L002S, U19-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.83
Std Dev 3.421
Gaus mean : 32.0338
Gaus Width: 4.0661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

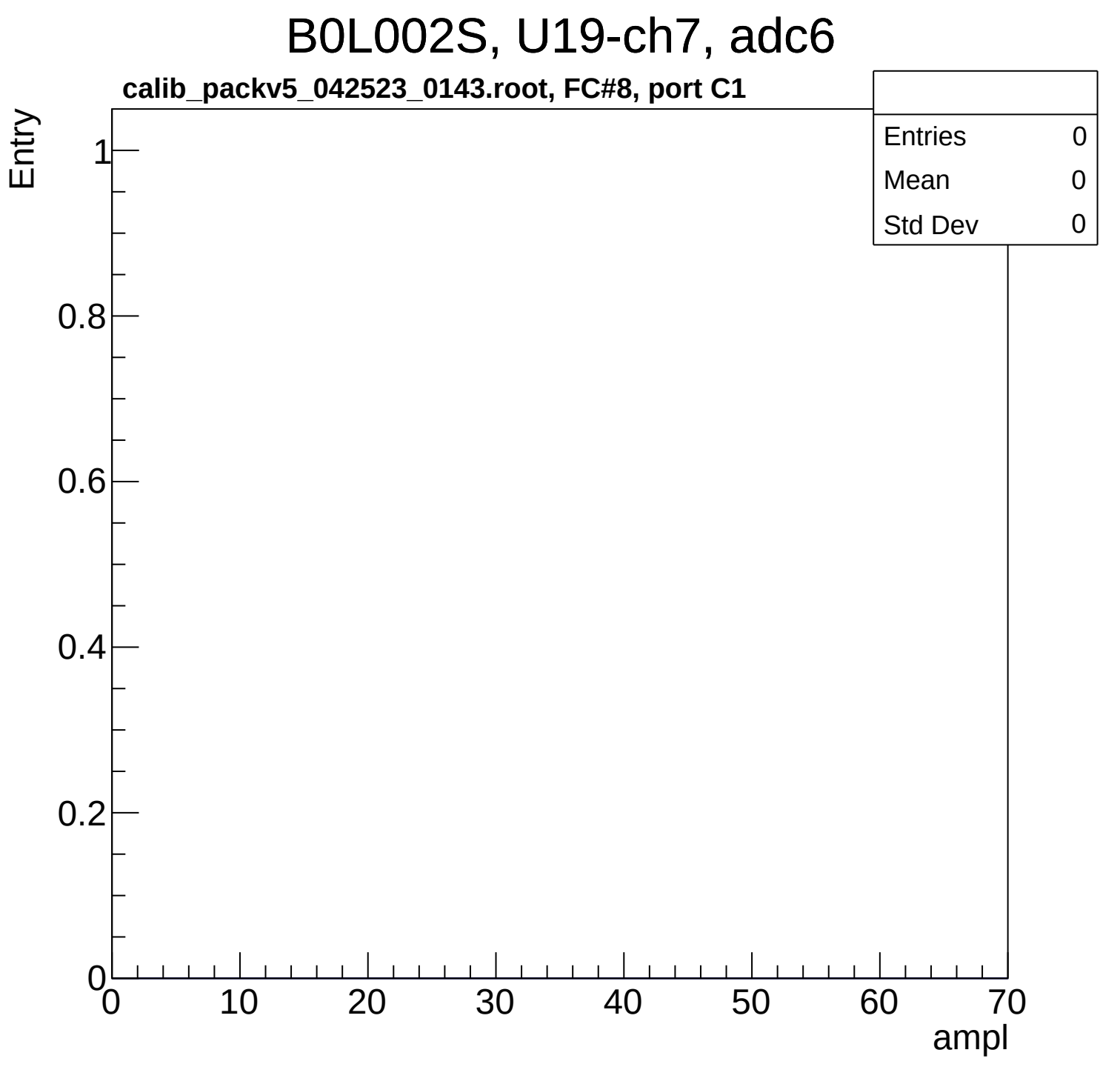

B0L002S, U19-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 36.16
Std Dev 3.563
Gaus mean : 36.6860
Gaus Width: 4.2917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

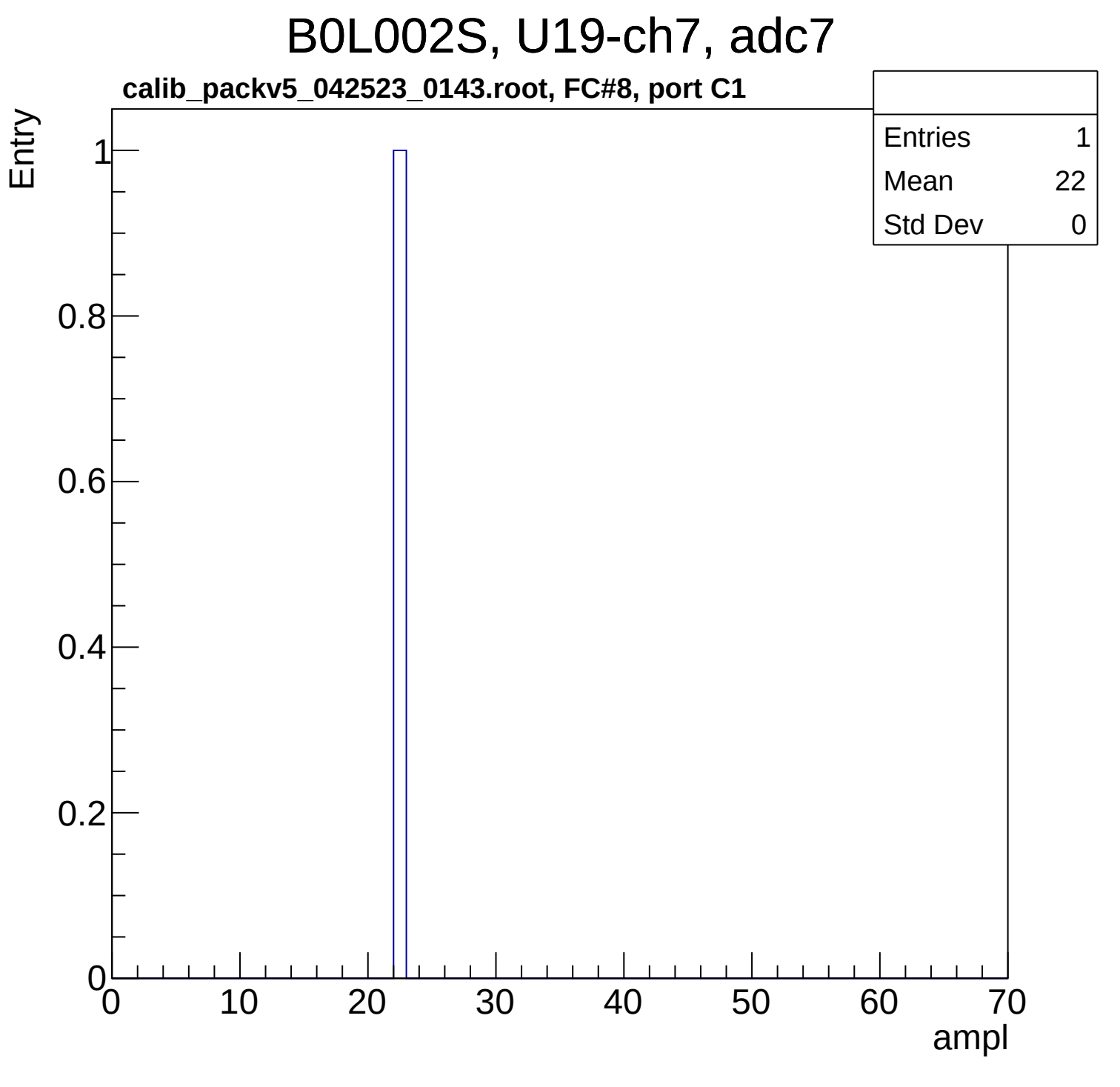

B0L002S, U19-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 43.93
Std Dev 3.196
Gaus mean : 44.4088
Gaus Width: 3.4931
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

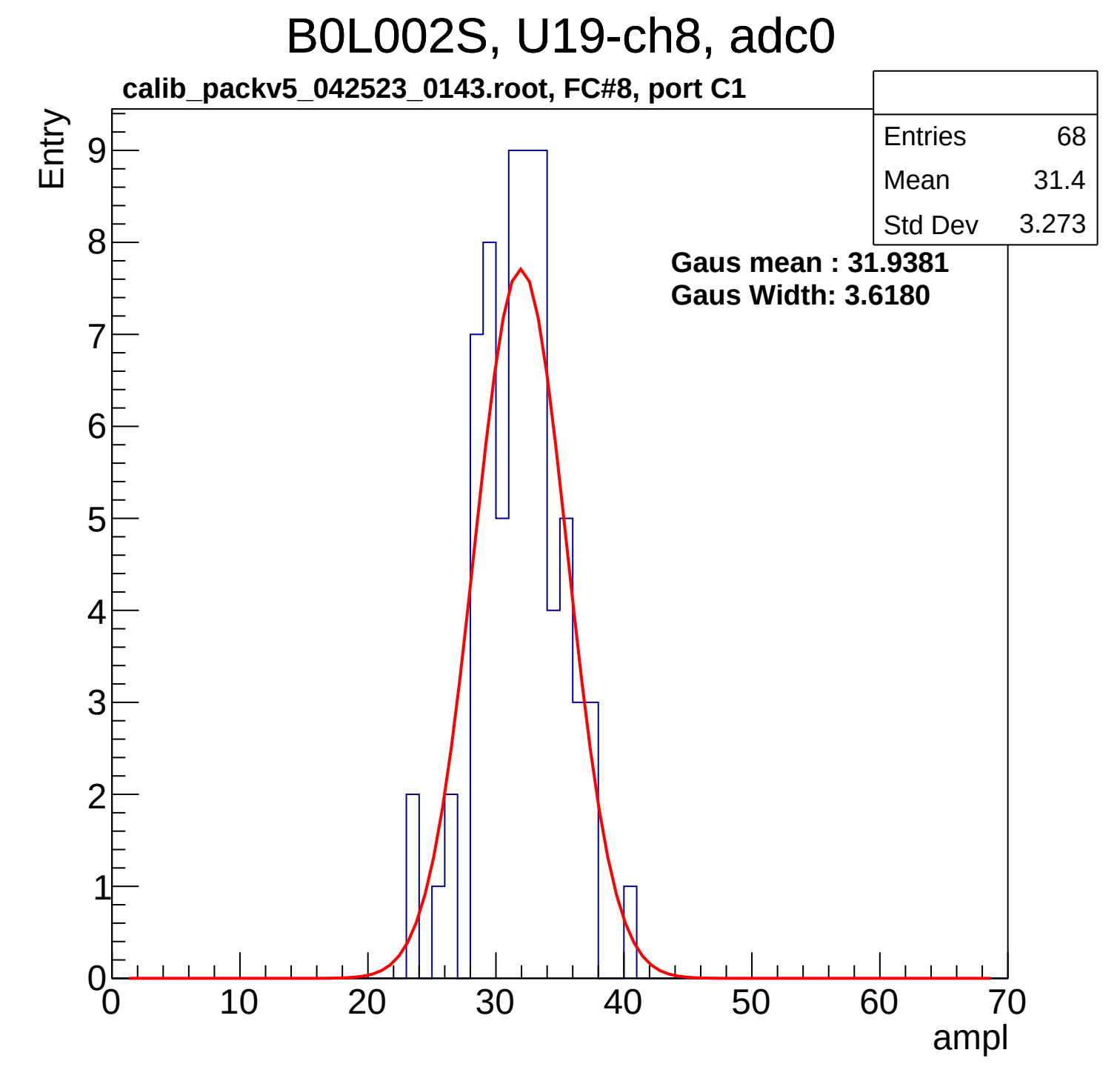

B0L002S, U19-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.39
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

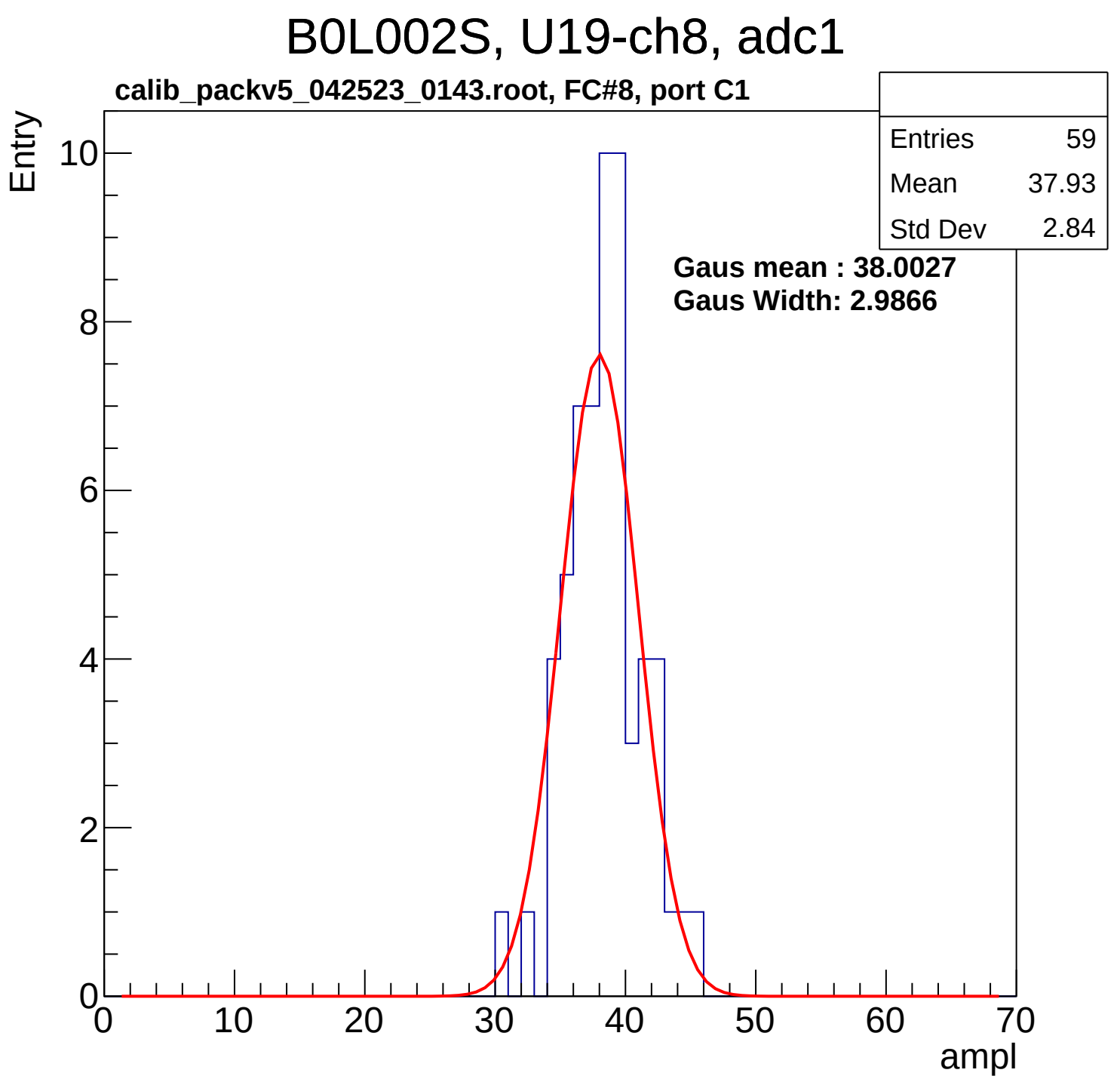

B0L002S, U19-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.19
Std Dev 8.387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

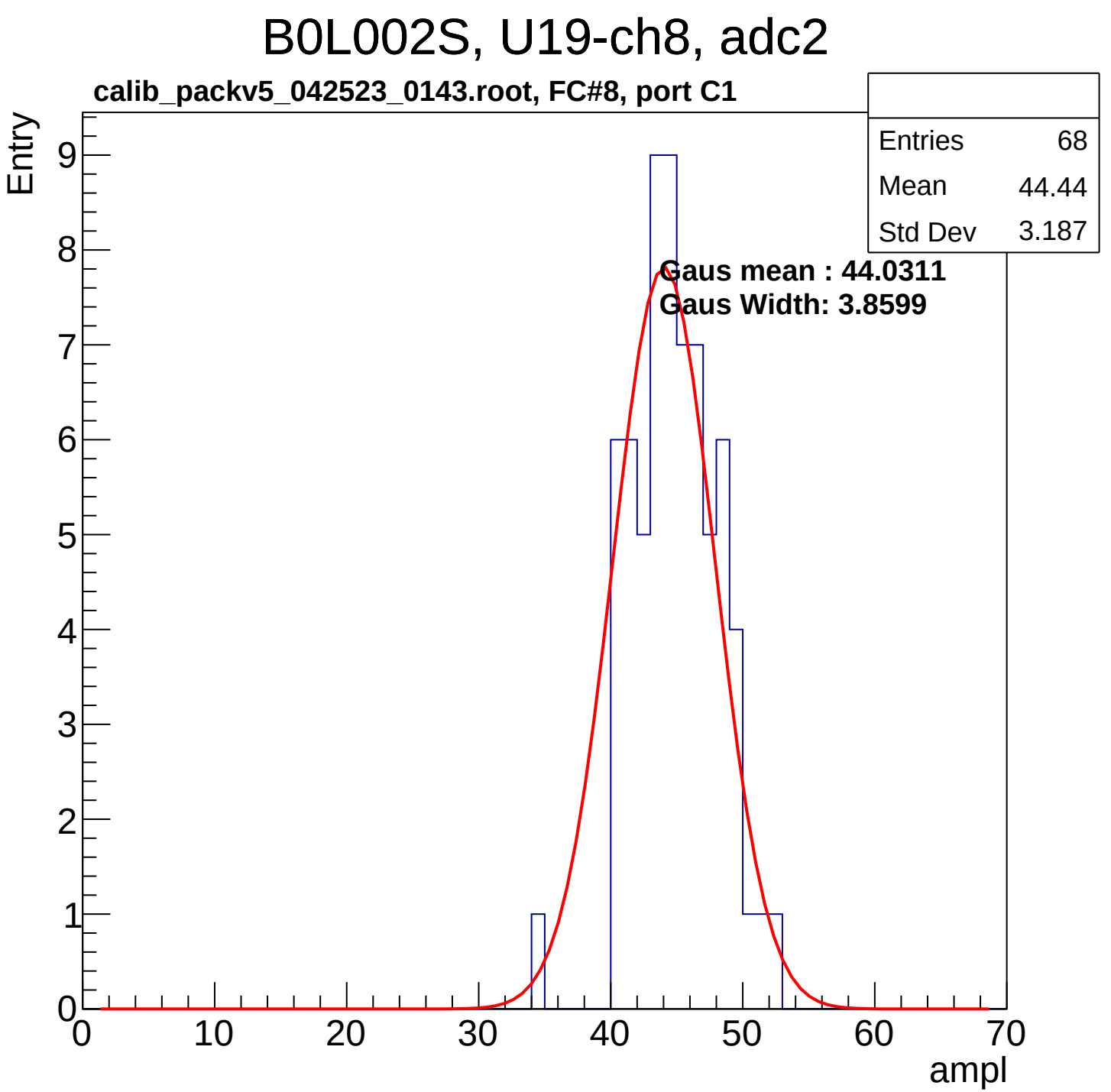

B0L002S, U19-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.58
Std Dev 2.135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

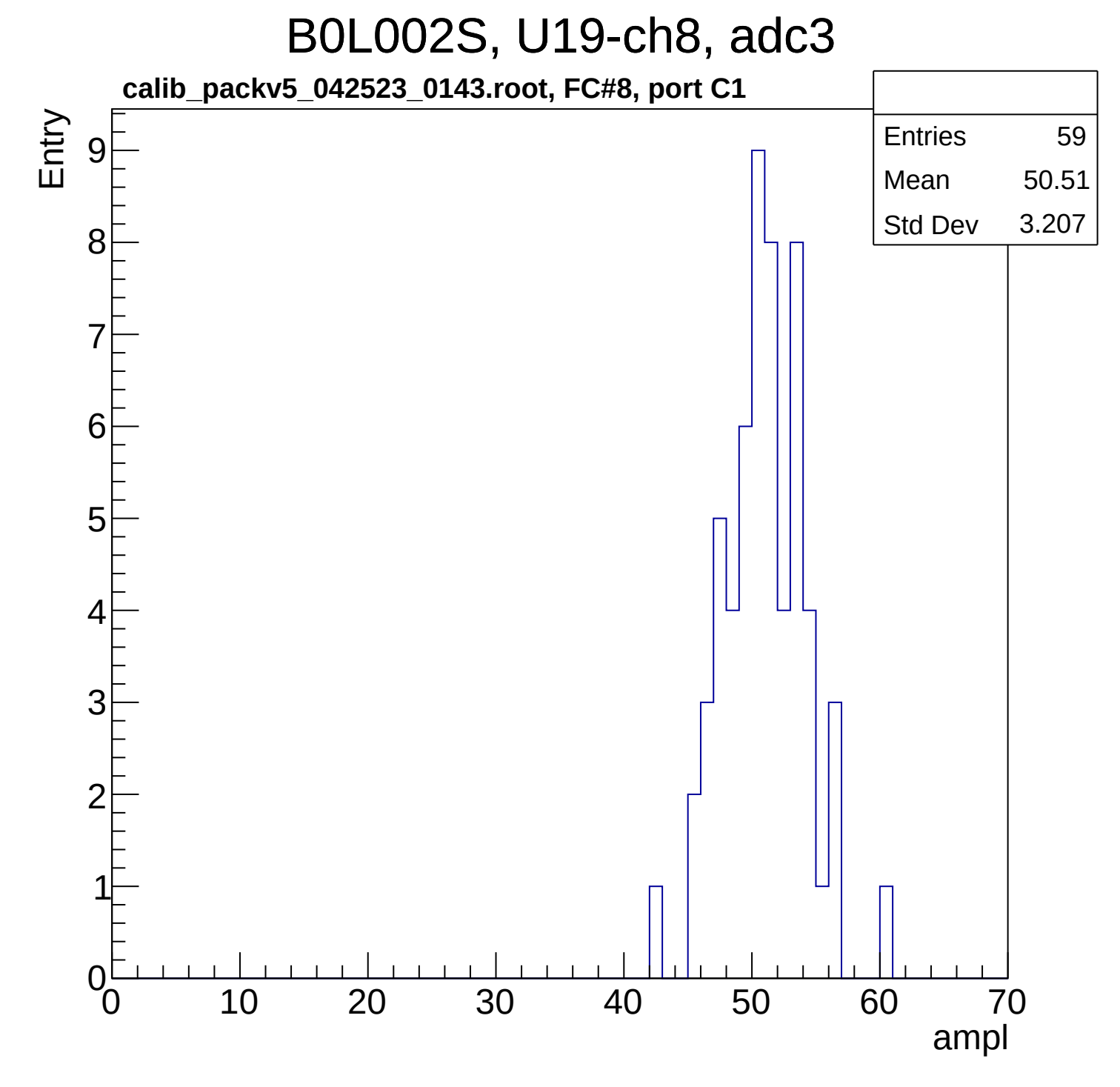

B0L002S, U19-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

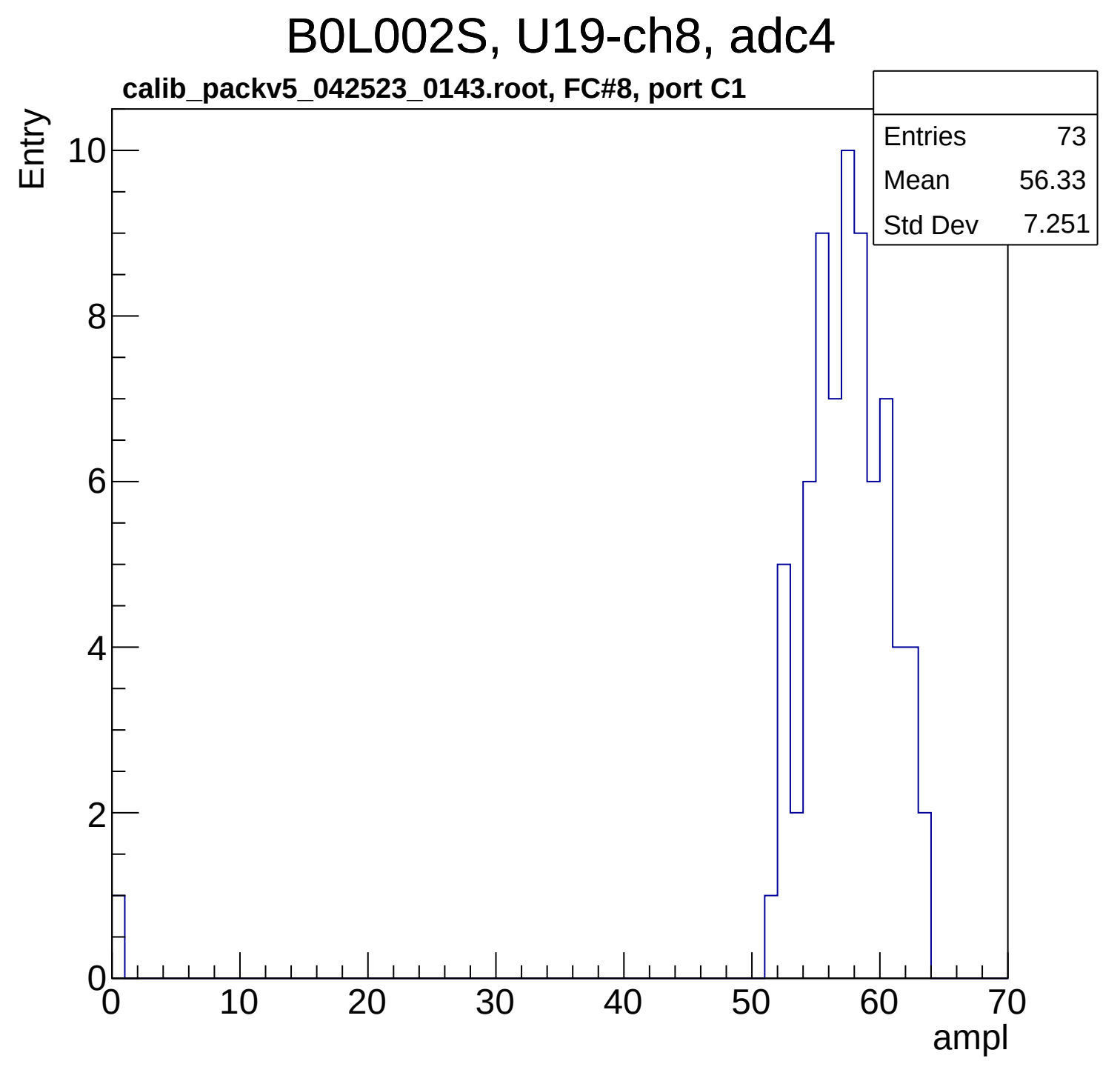

B0L002S, U19-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

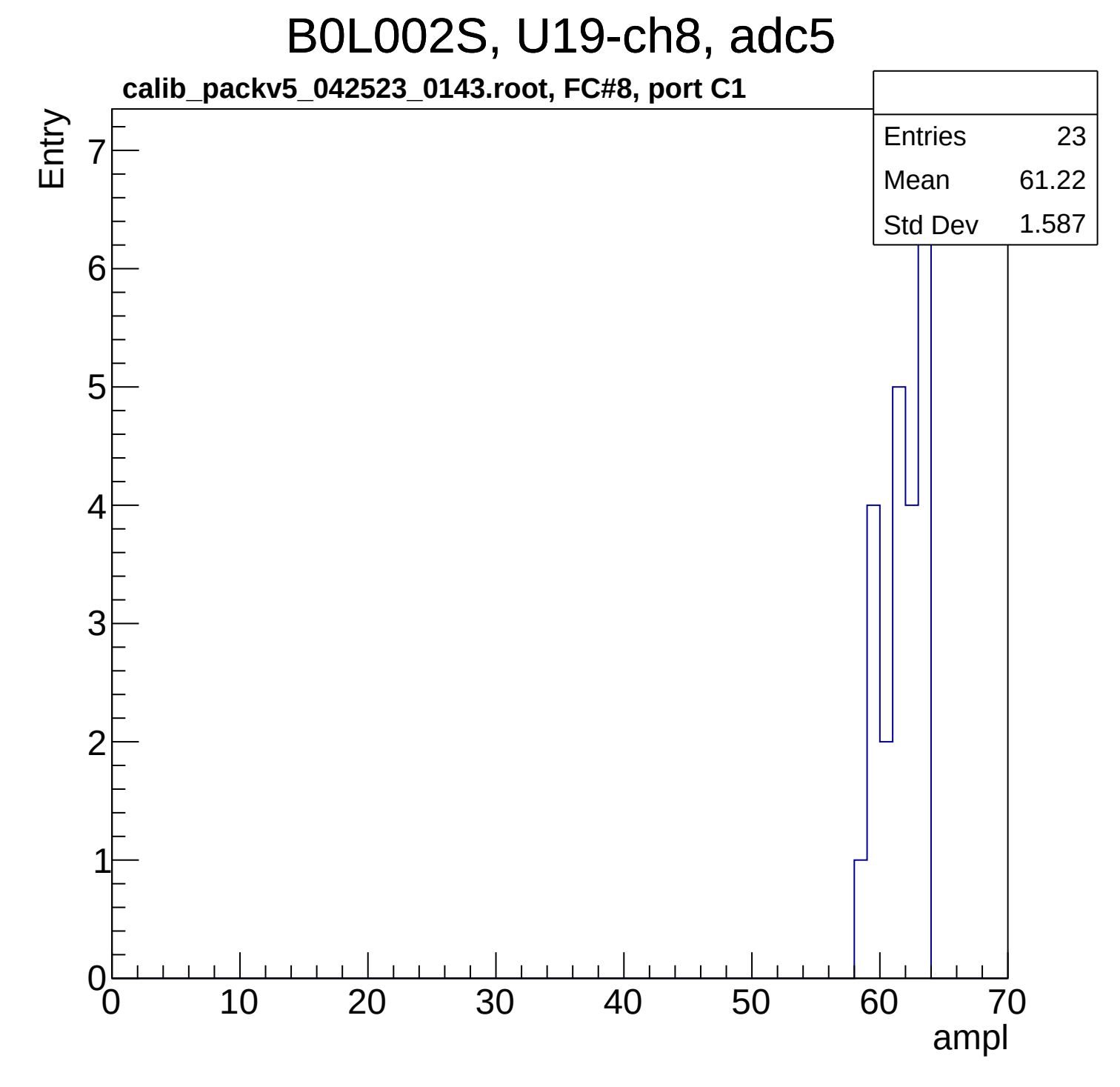

B0L002S, U19-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.4
Std Dev 3.273
Gaus mean : 31.9381
Gaus Width: 3.6180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

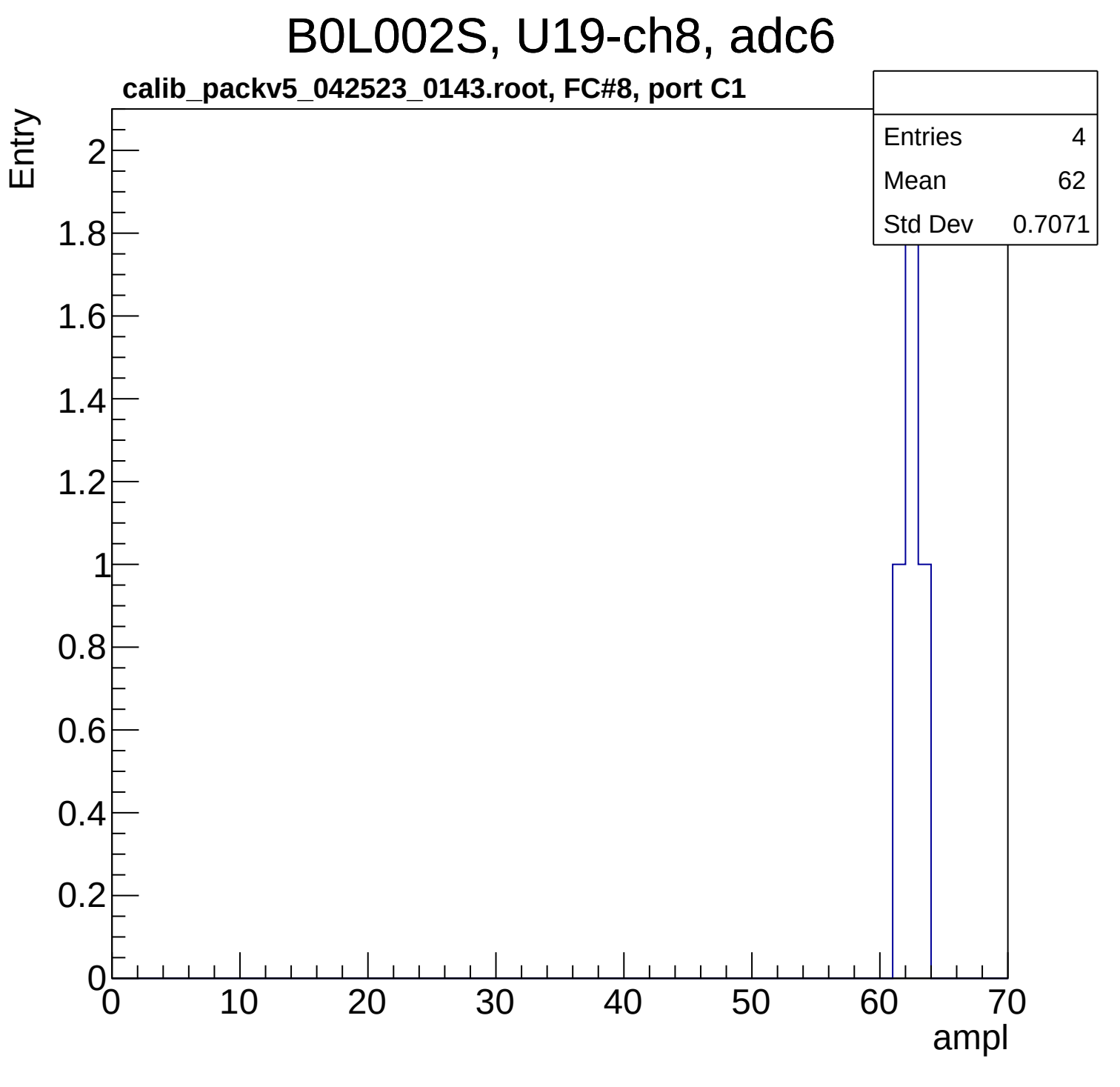

B0L002S, U19-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.93
Std Dev 2.84
Gaus mean : 38.0027
Gaus Width: 2.9866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

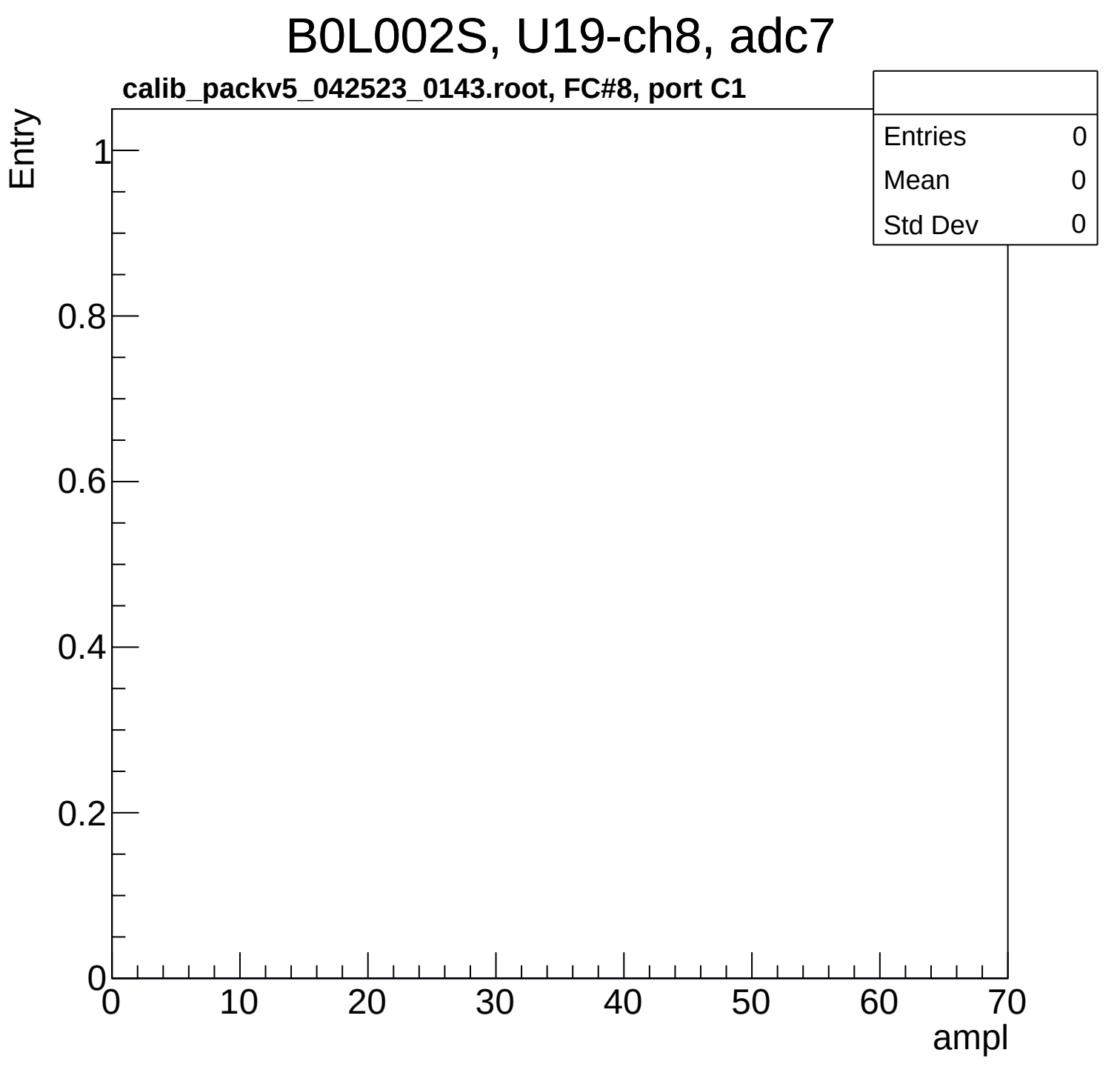

B0L002S, U19-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.44
Std Dev 3.187
Gaus mean : 44.0311
Gaus Width: 3.8599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

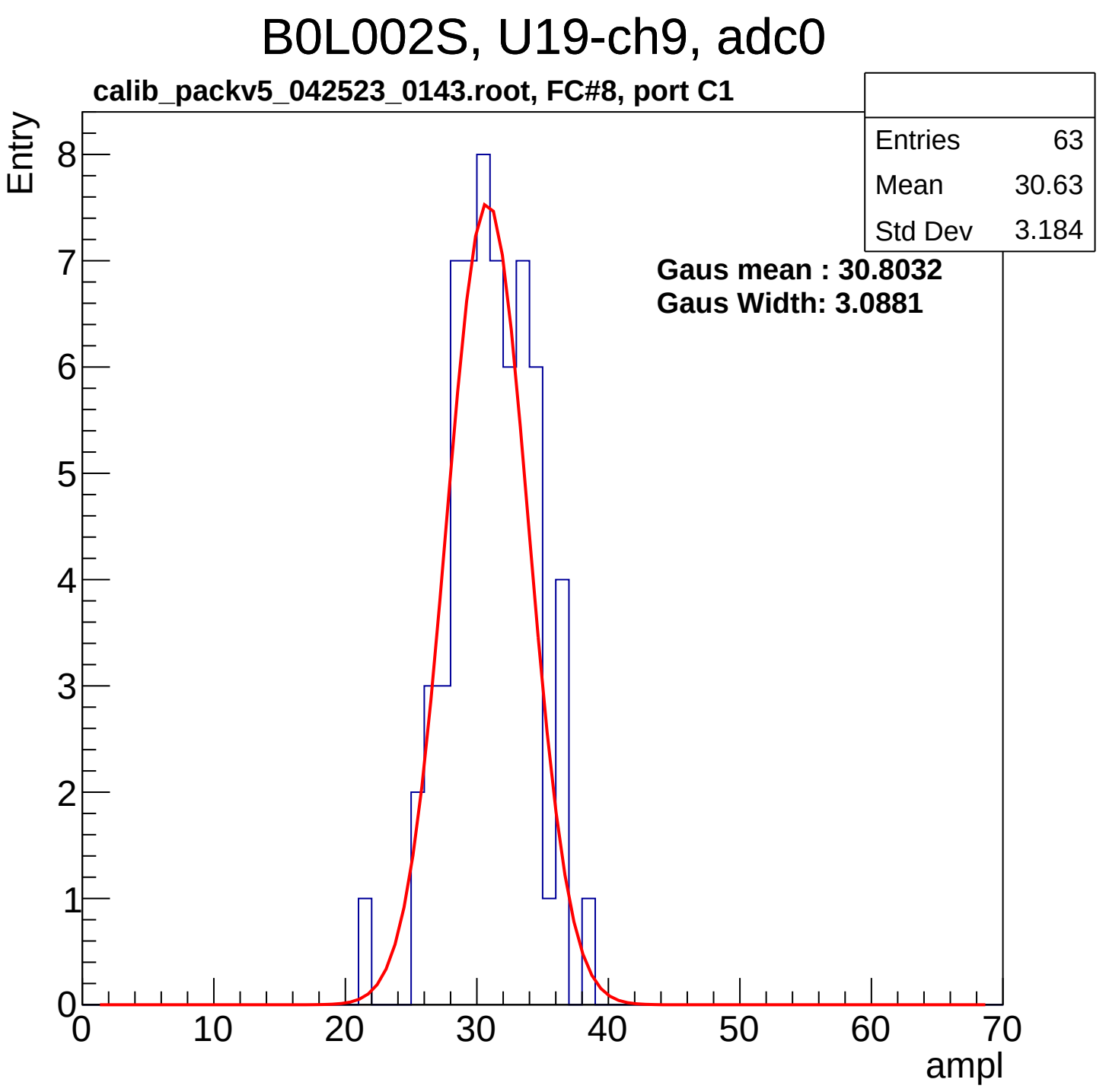

B0L002S, U19-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.51
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

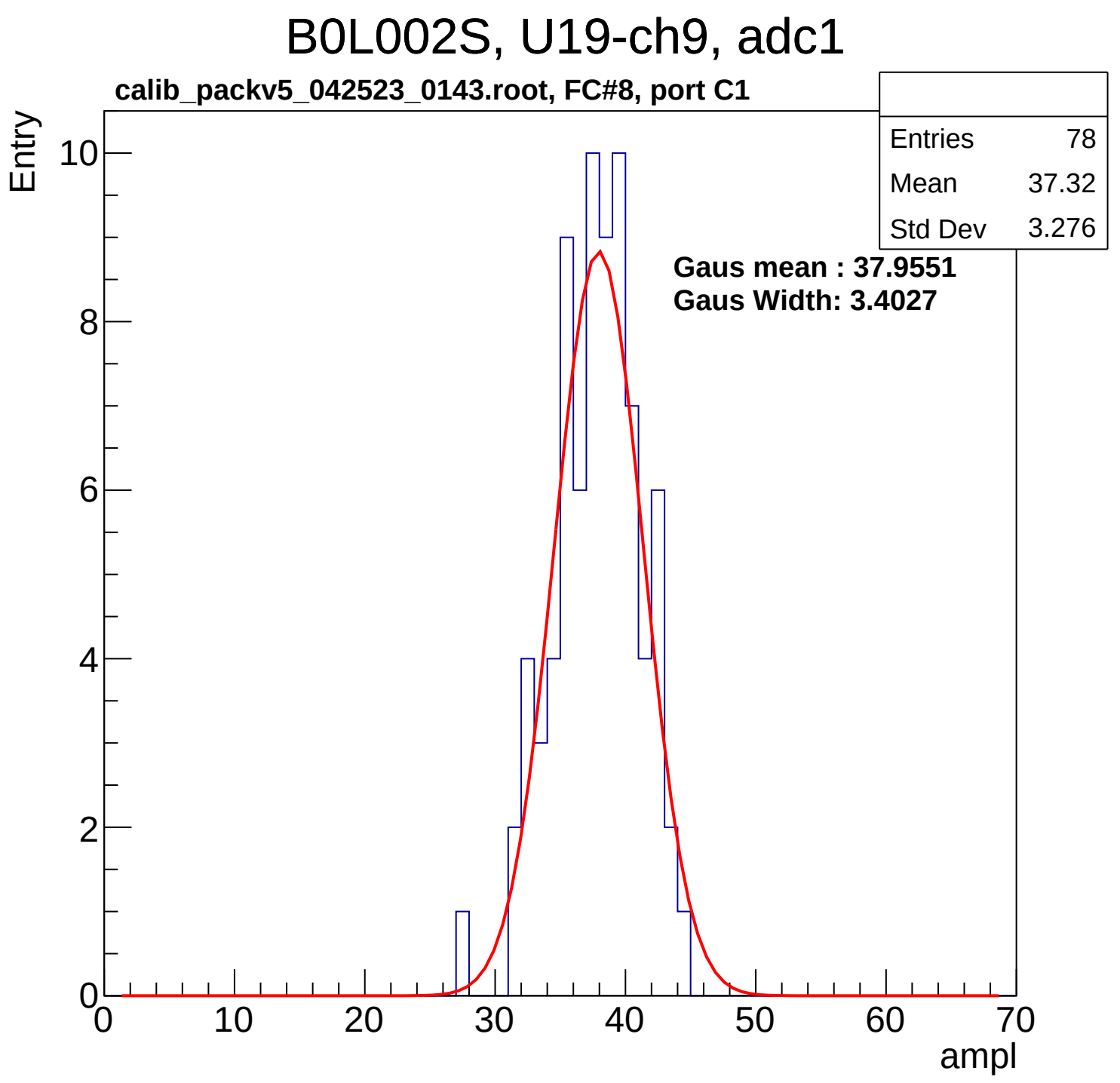

B0L002S, U19-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 56.33
Std Dev 7.251
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

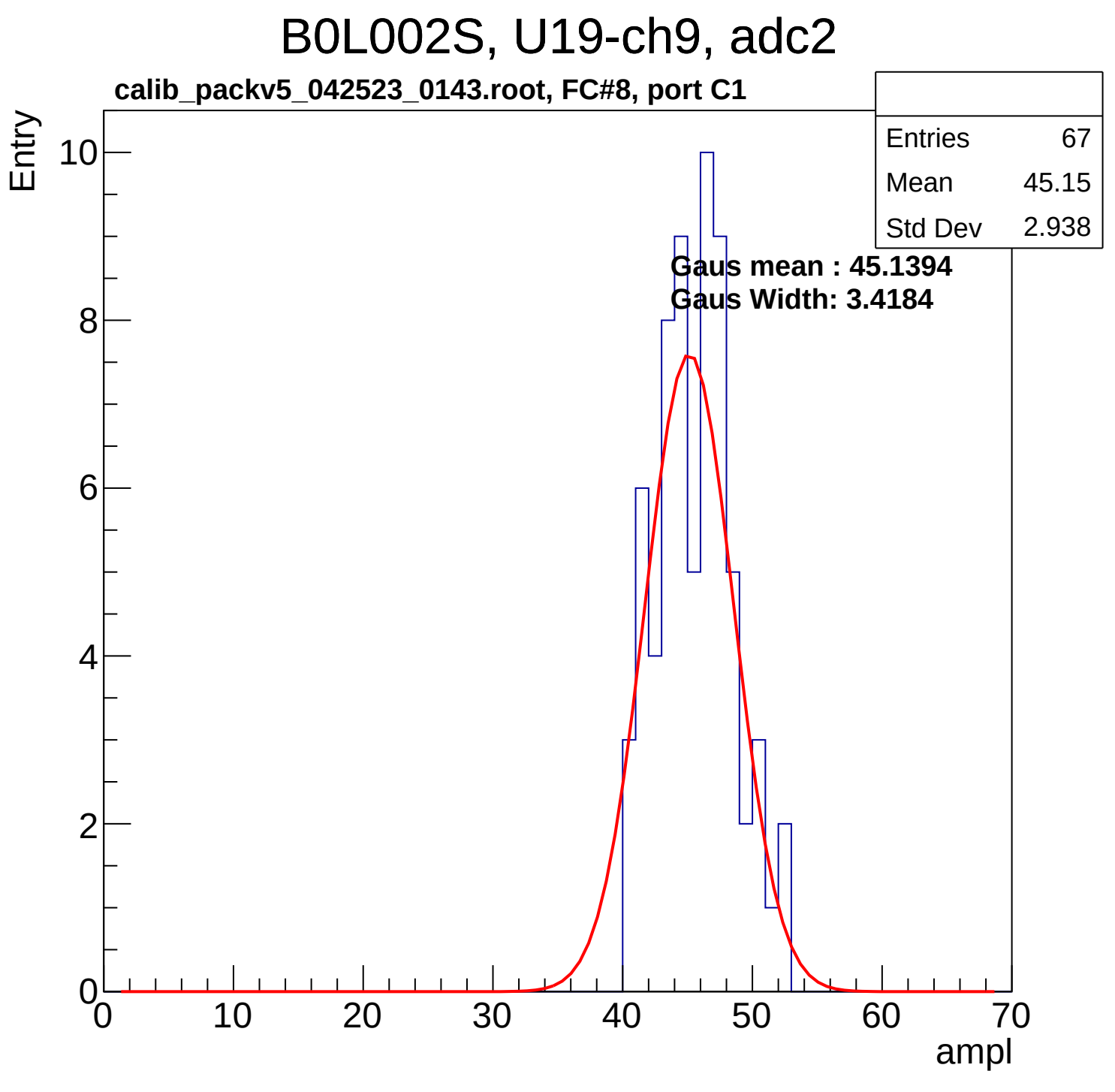

B0L002S, U19-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 61.22
Std Dev 1.587
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

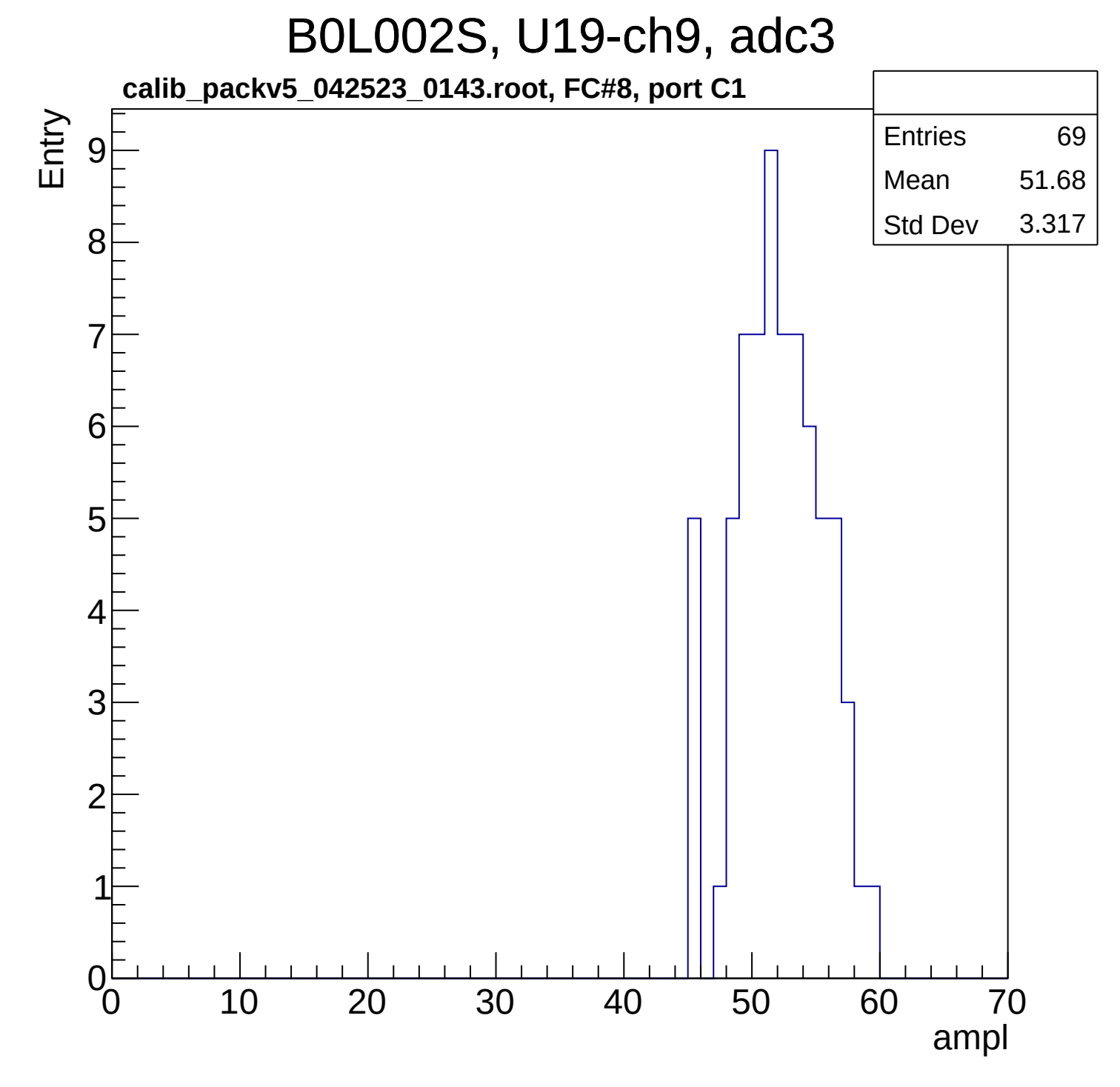

B0L002S, U19-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

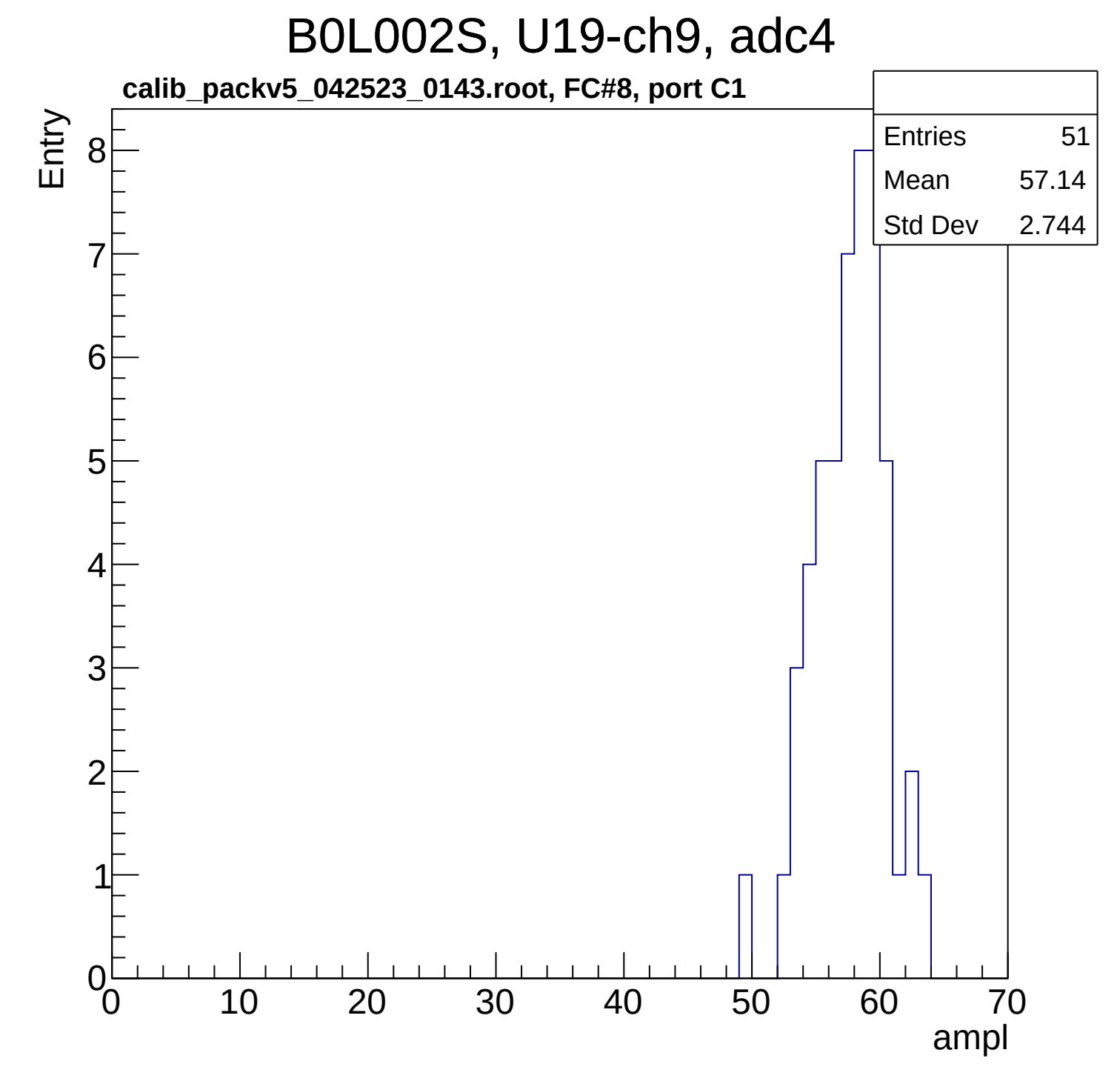

B0L002S, U19-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

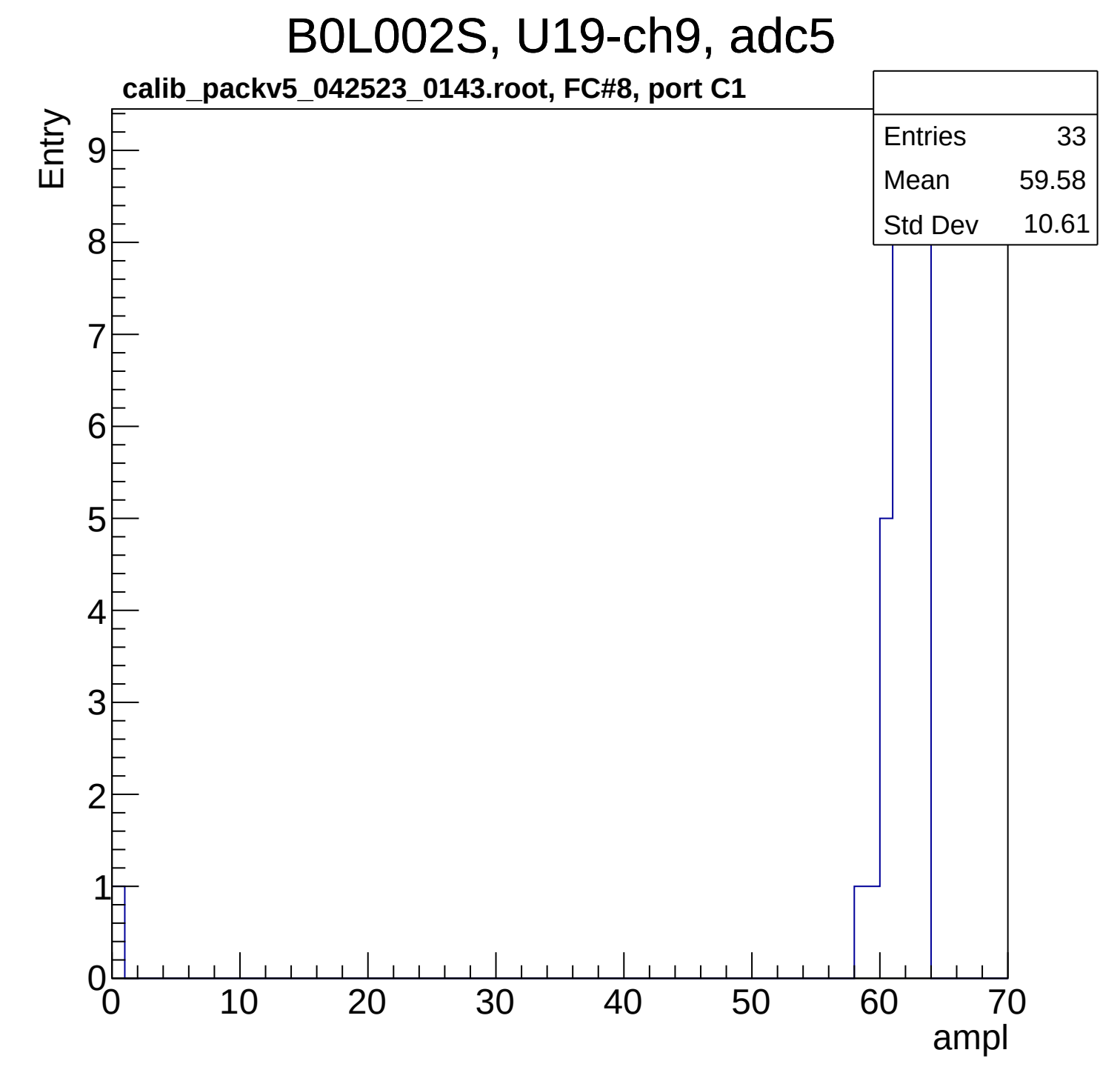

B0L002S, U19-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.63
Std Dev 3.184
Gaus mean : 30.8032
Gaus Width: 3.0881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

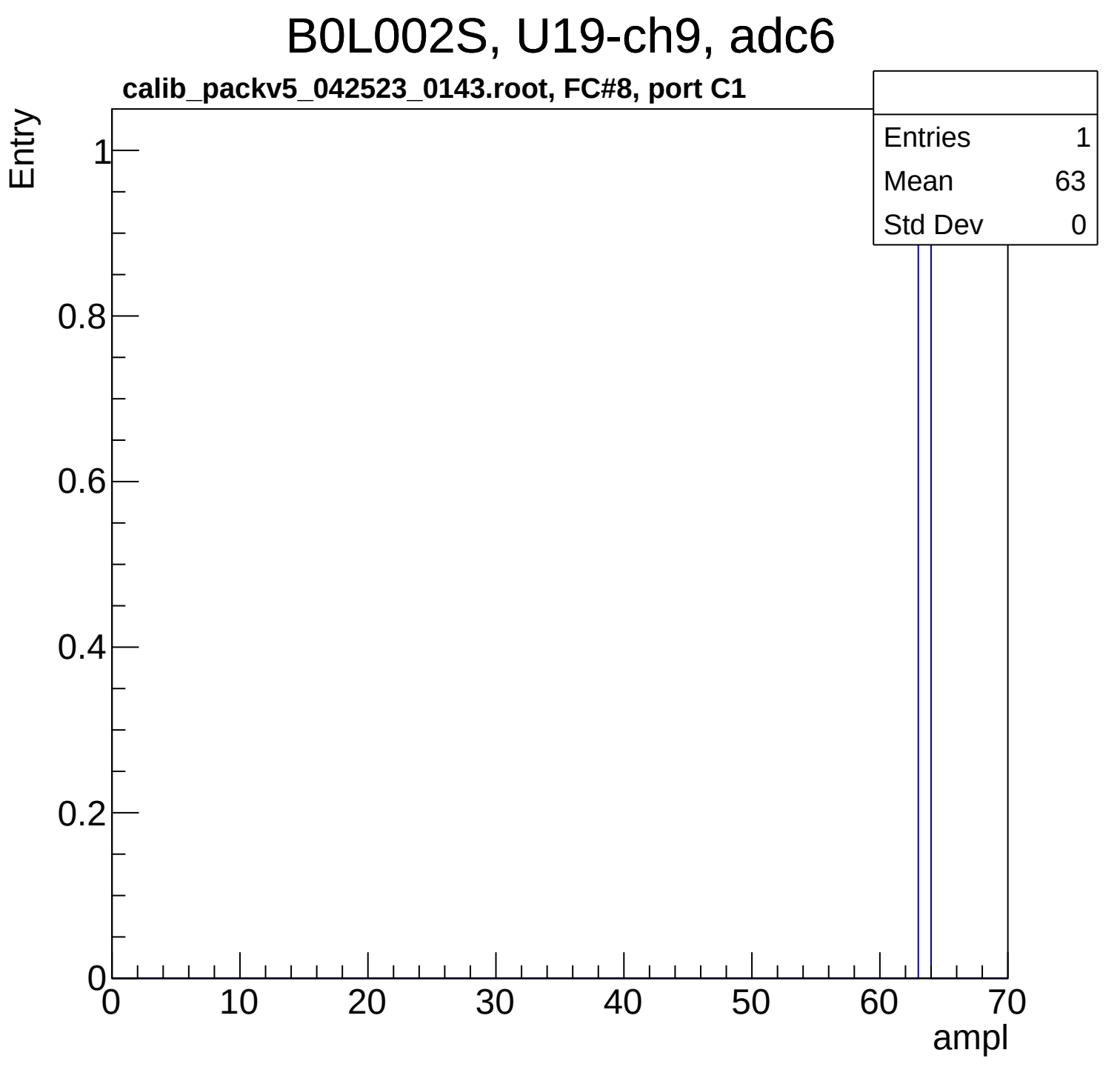

B0L002S, U19-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.32
Std Dev 3.276
Gaus mean : 37.9551
Gaus Width: 3.4027
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

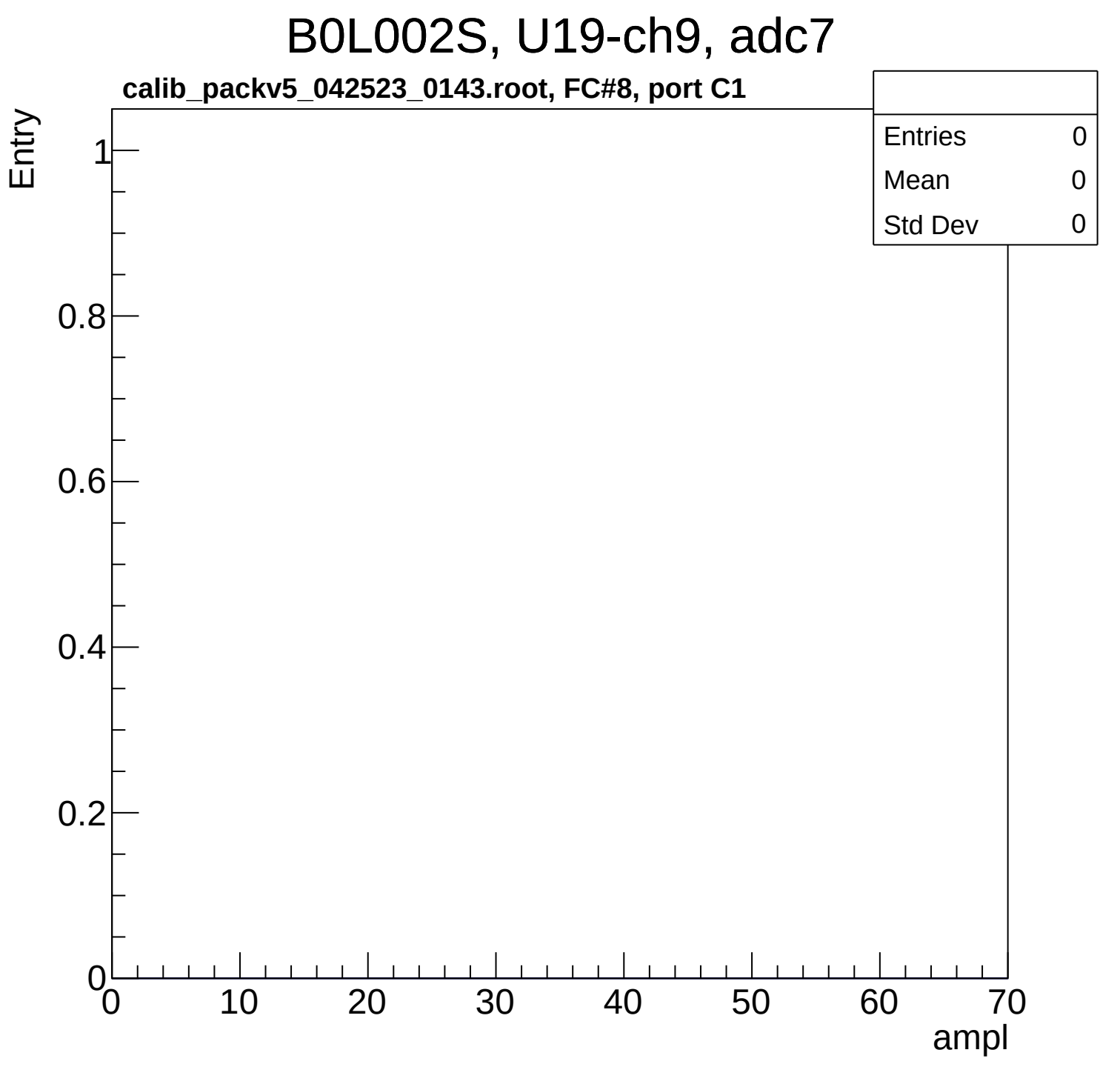

B0L002S, U19-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.15
Std Dev 2.938
Gaus mean : 45.1394
Gaus Width: 3.4184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

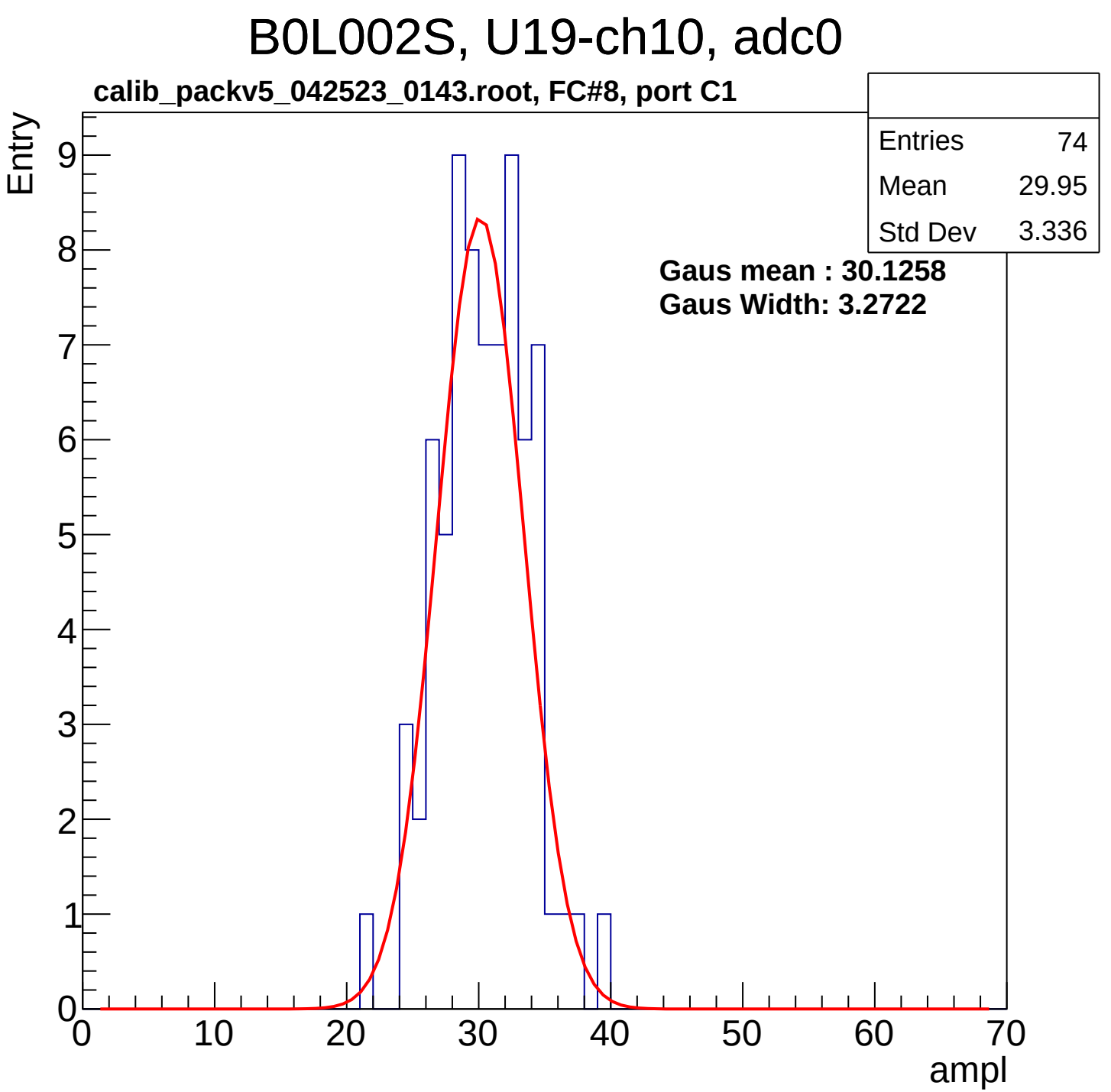

B0L002S, U19-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.68
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

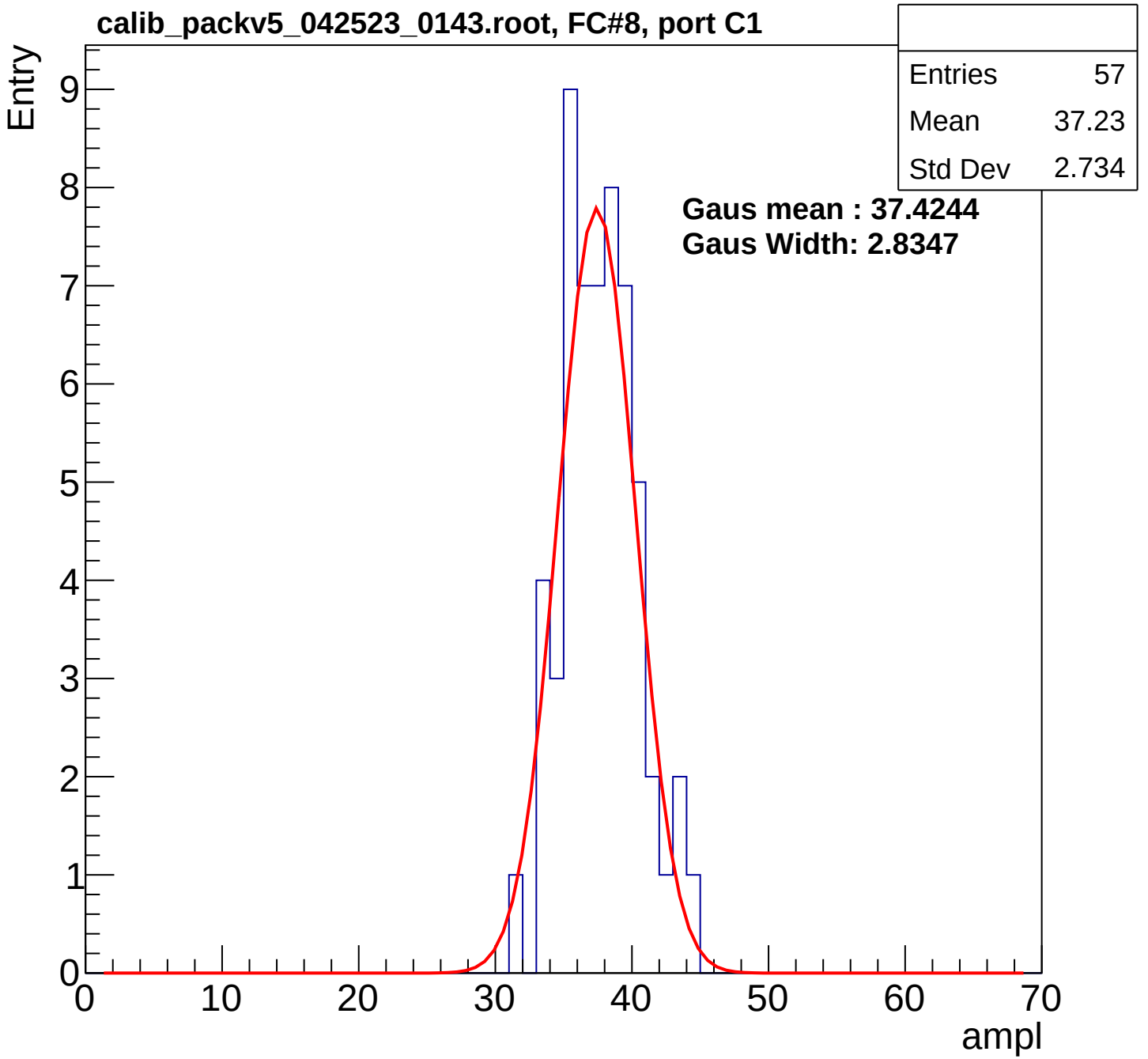

B0L002S, U19-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.14
Std Dev 2.744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

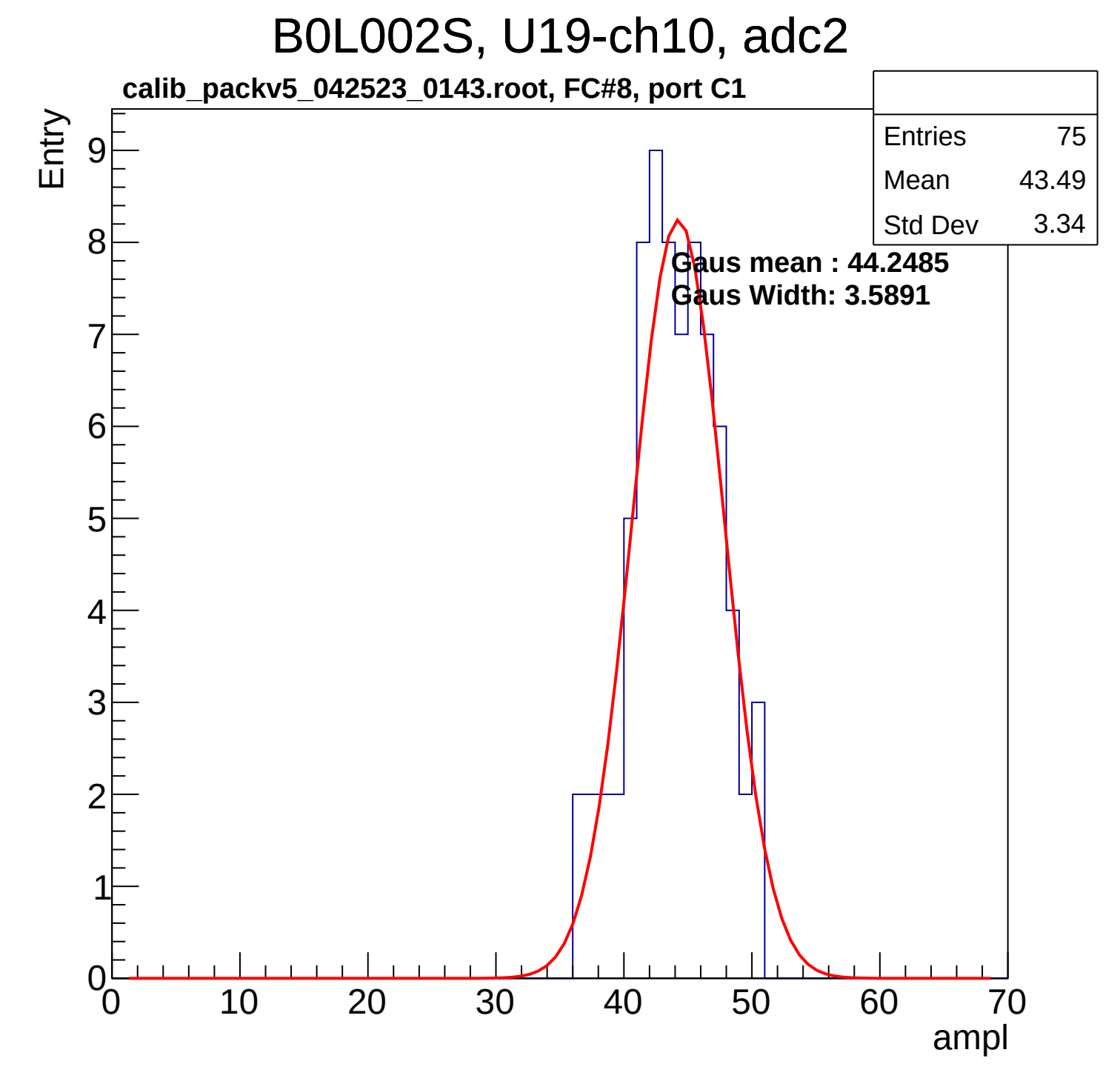

B0L002S, U19-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.58
Std Dev 10.61
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

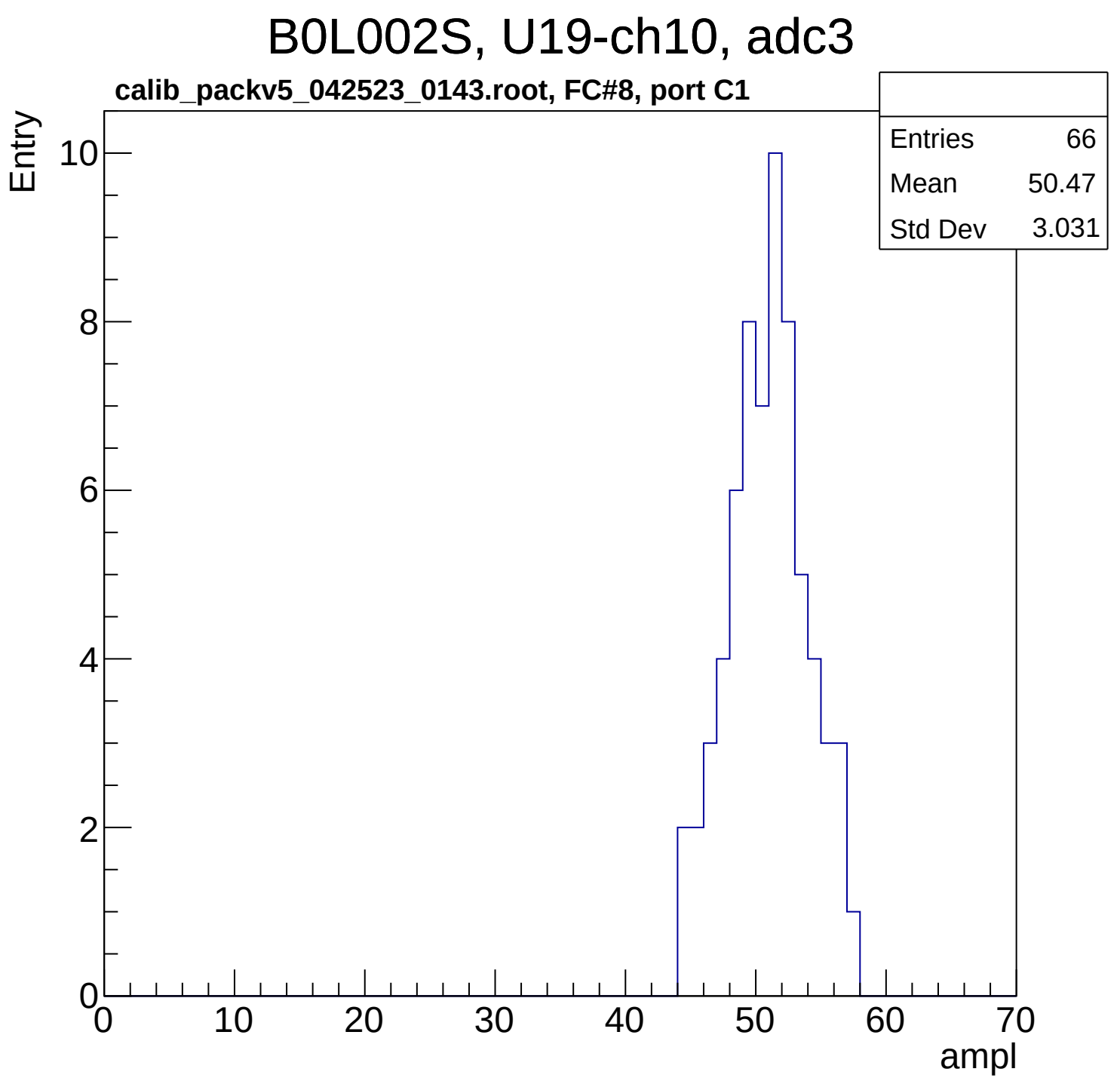

B0L002S, U19-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

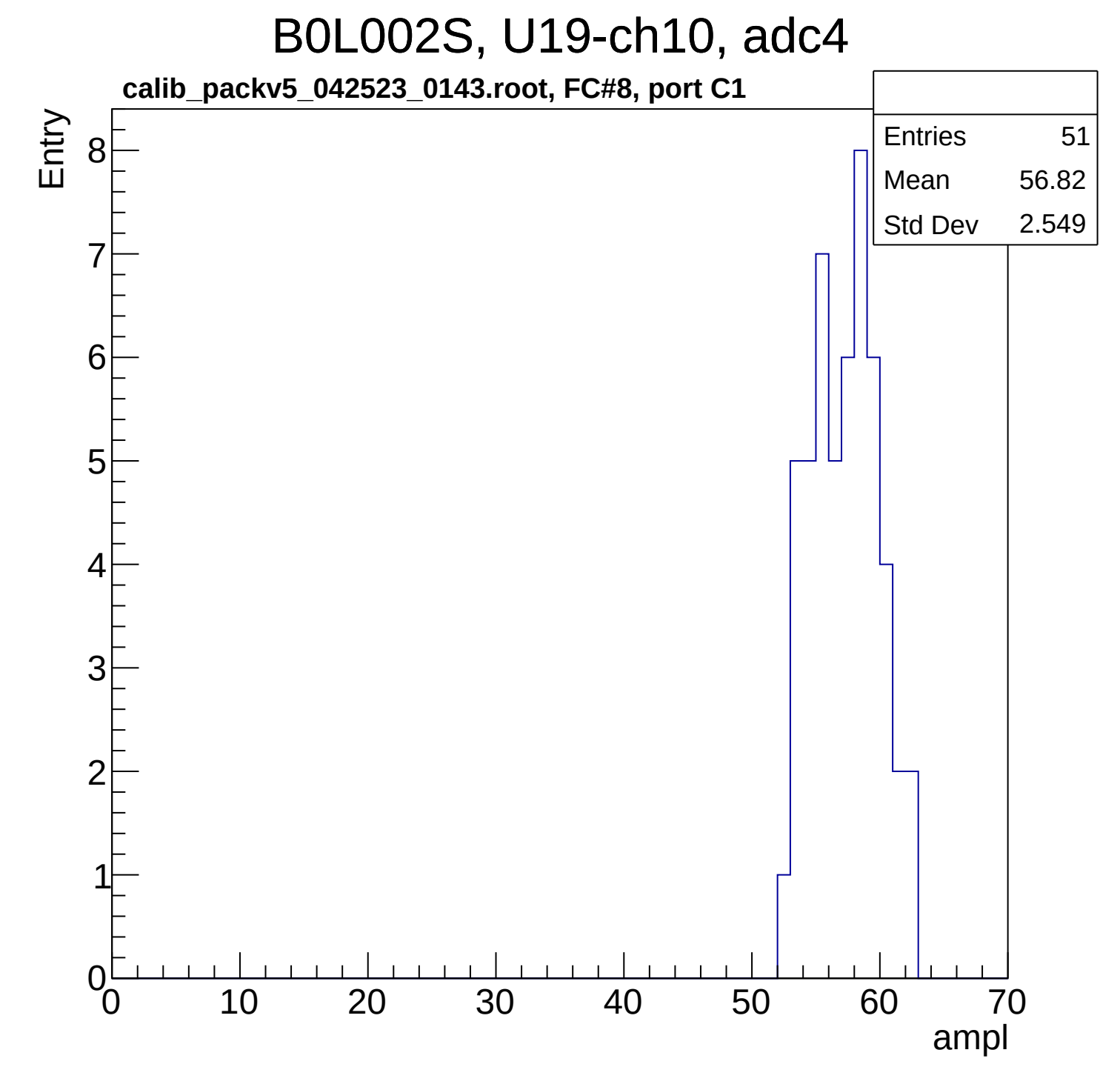

B0L002S, U19-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

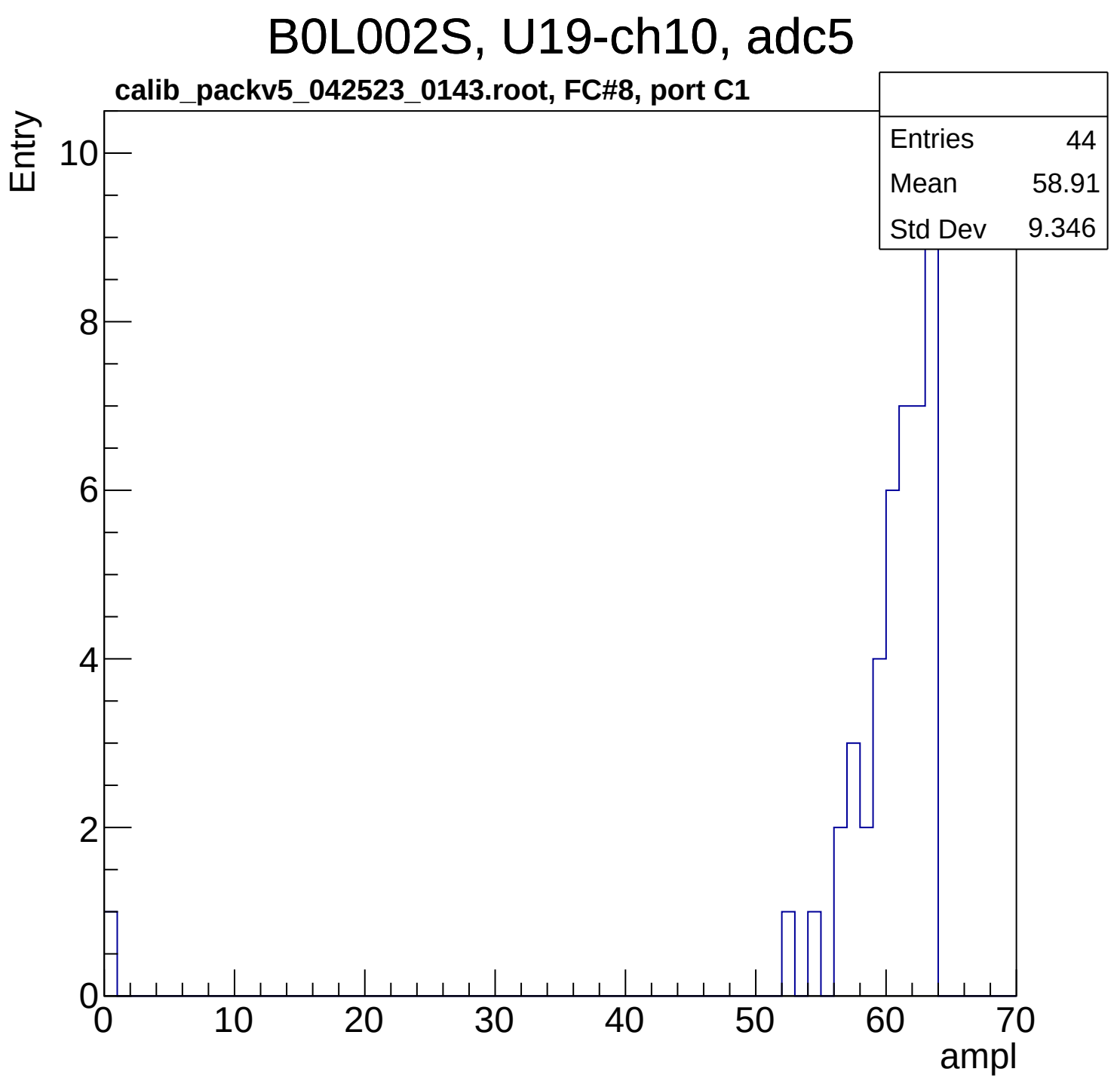

B0L002S, U19-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.95
Std Dev 3.336
Gaus mean : 30.1258
Gaus Width: 3.2722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

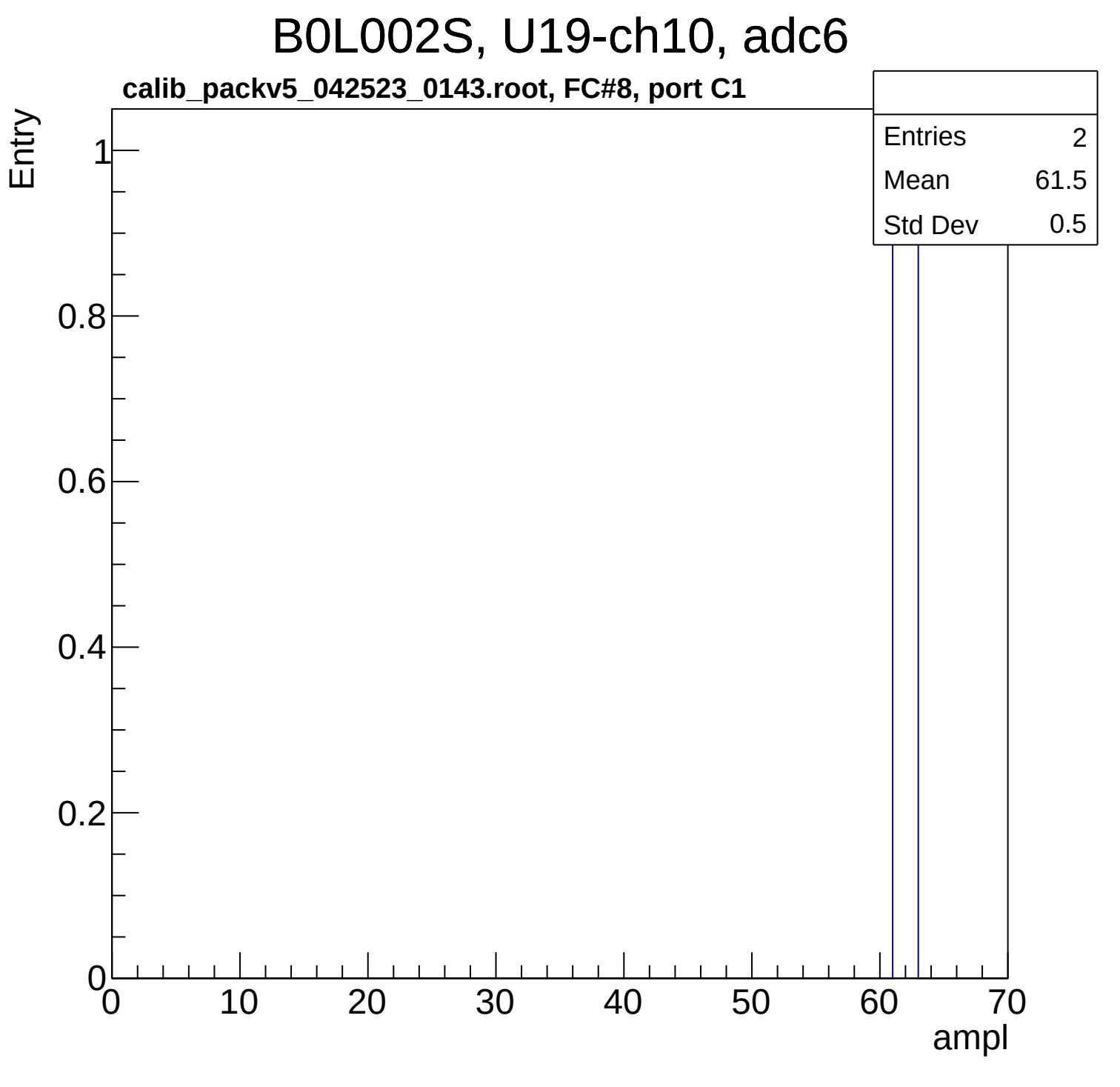

B0L002S, U19-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.23
Std Dev 2.734
Gaus mean : 37.4244
Gaus Width: 2.8347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

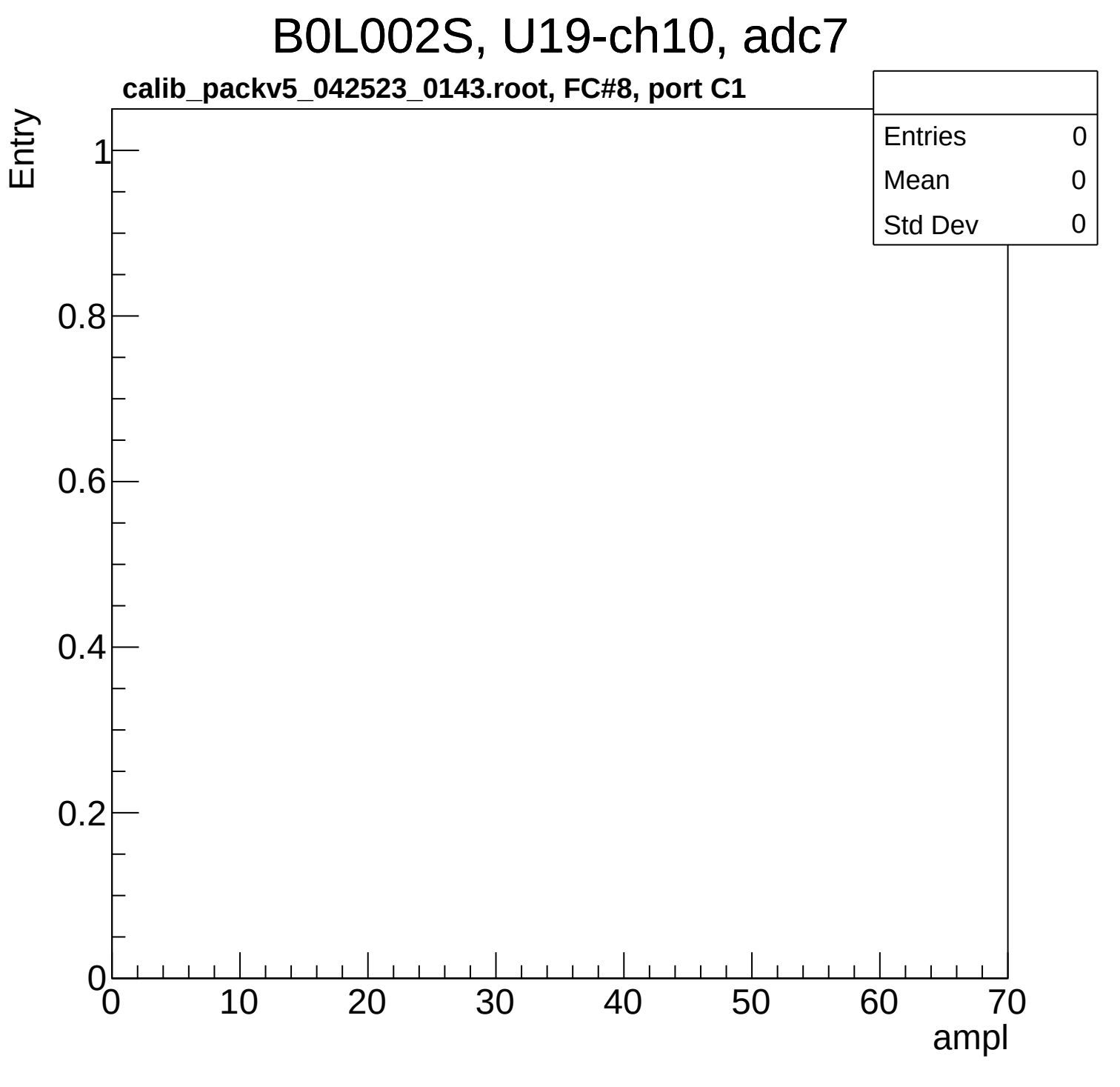

B0L002S, U19-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 43.49
Std Dev 3.34
Gaus mean : 44.2485
Gaus Width: 3.5891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

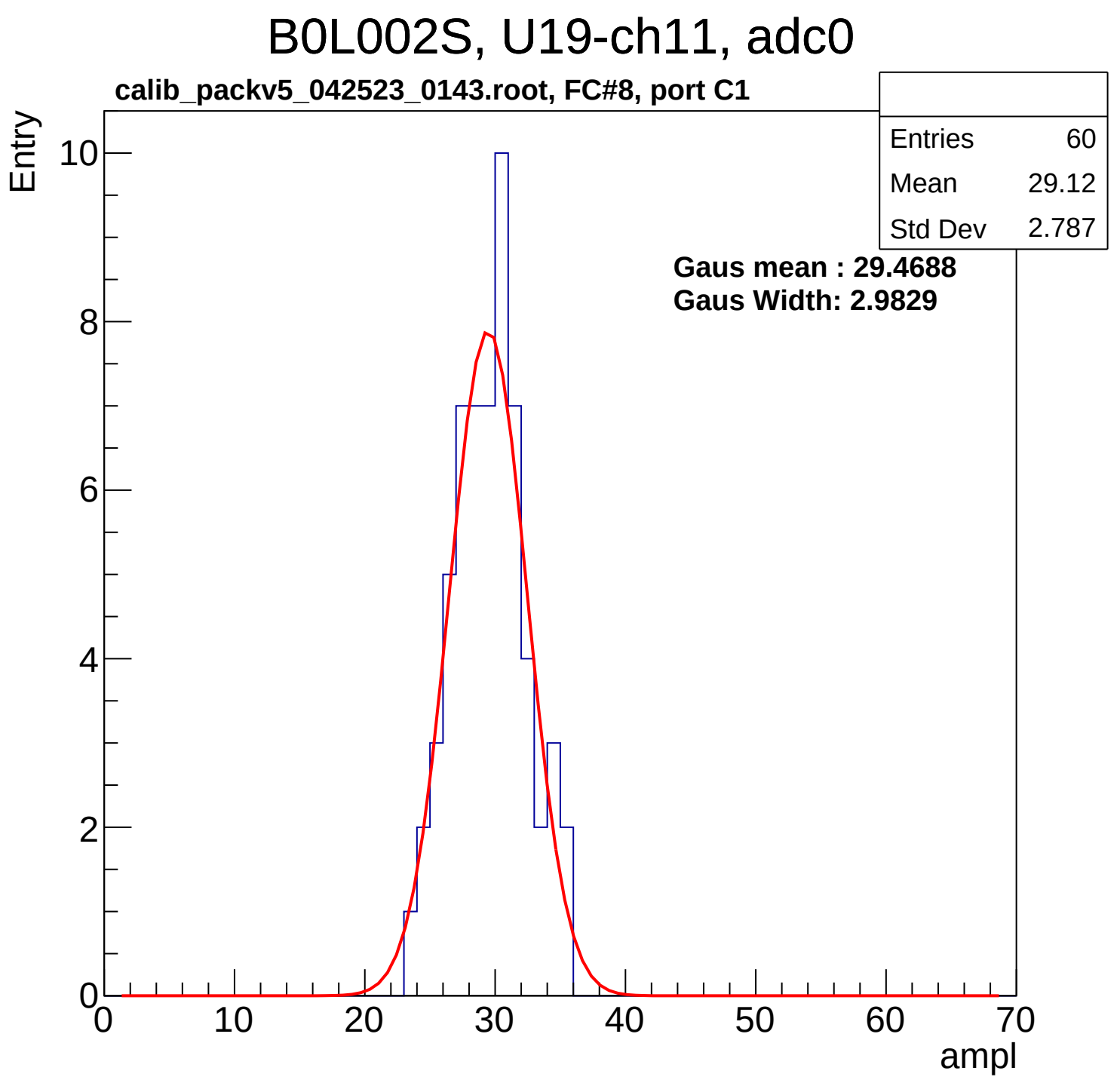

B0L002S, U19-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.47
Std Dev 3.031
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

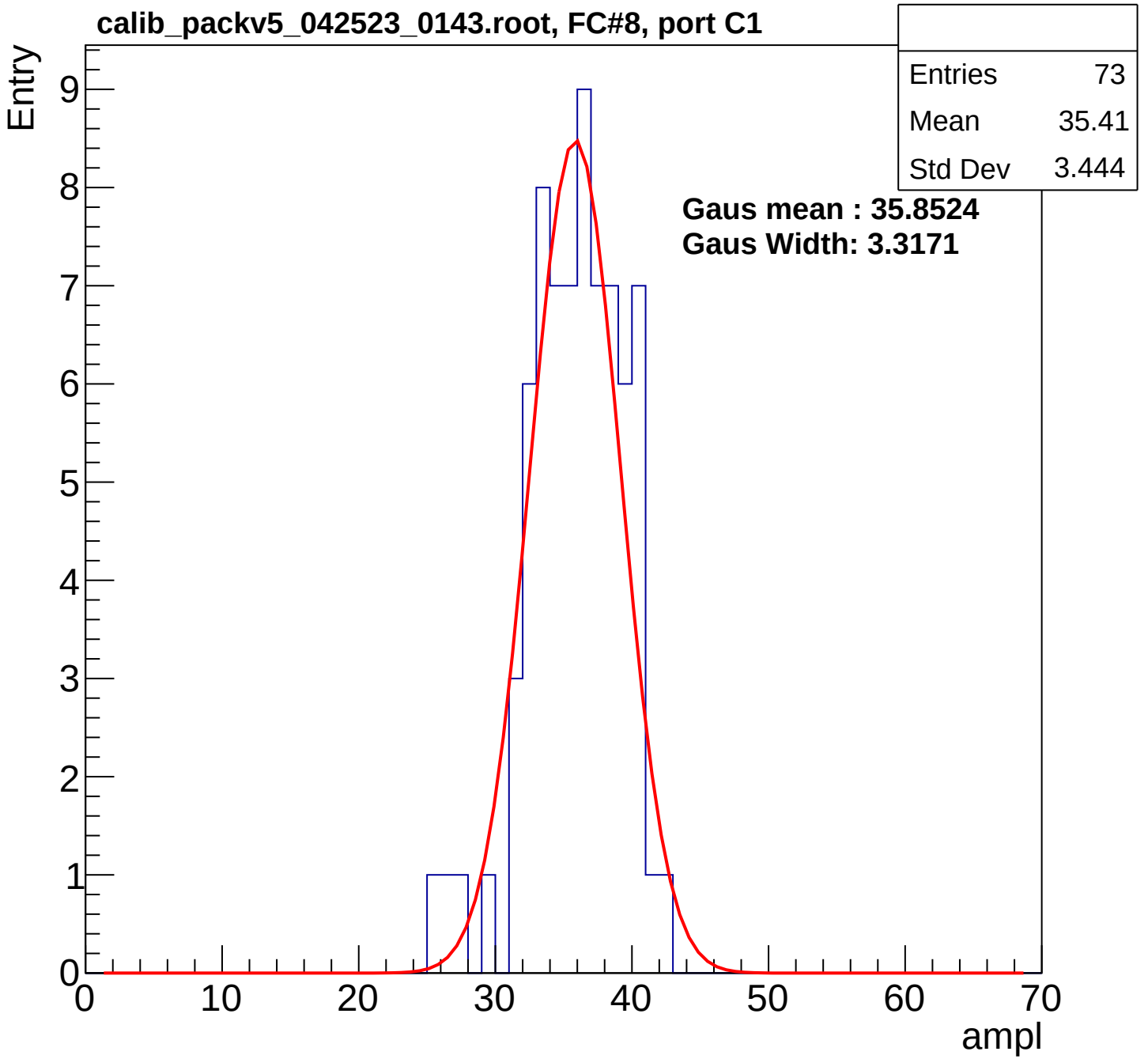

B0L002S, U19-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.82
Std Dev 2.549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

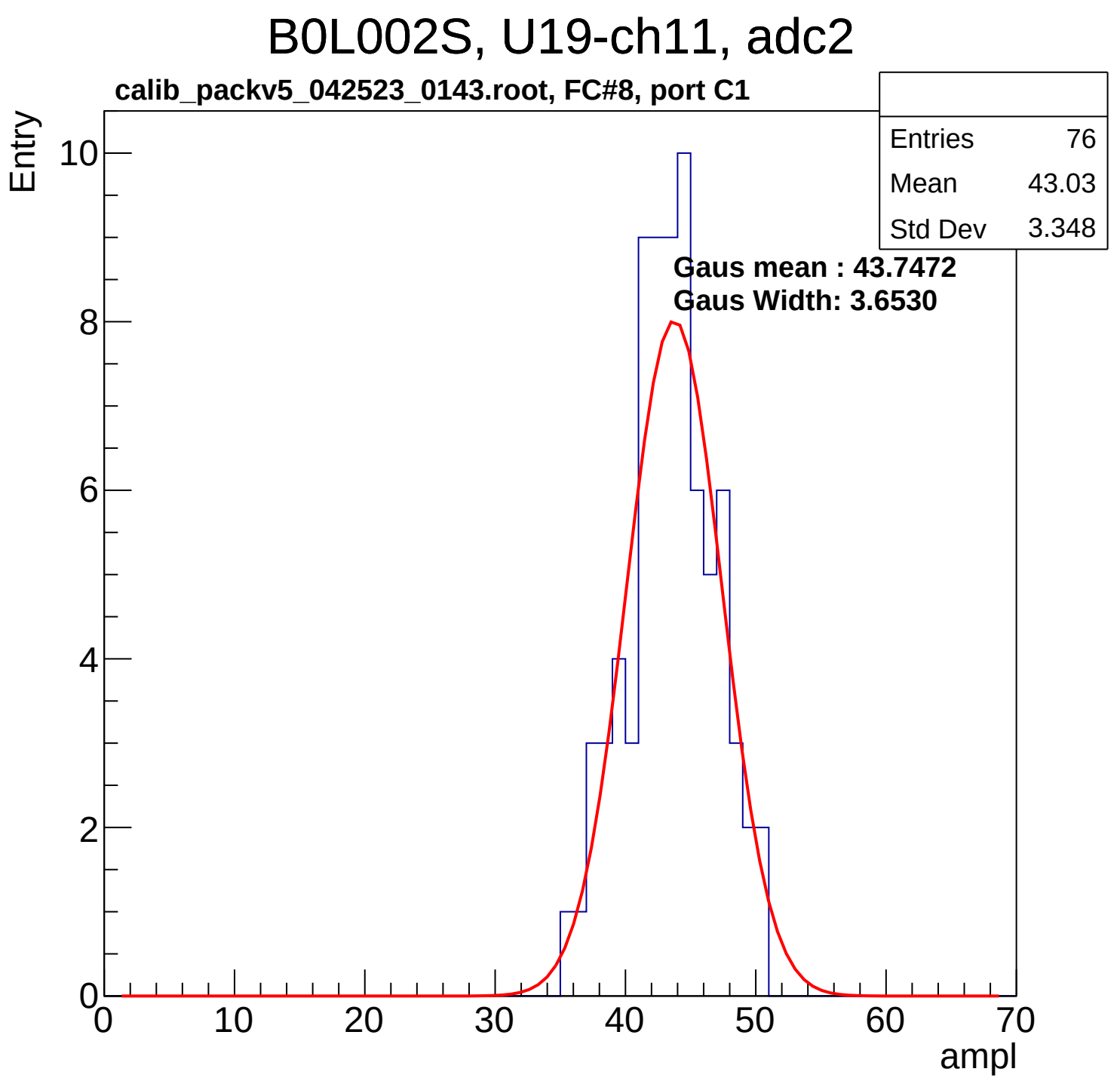

B0L002S, U19-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.91
Std Dev 9.346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

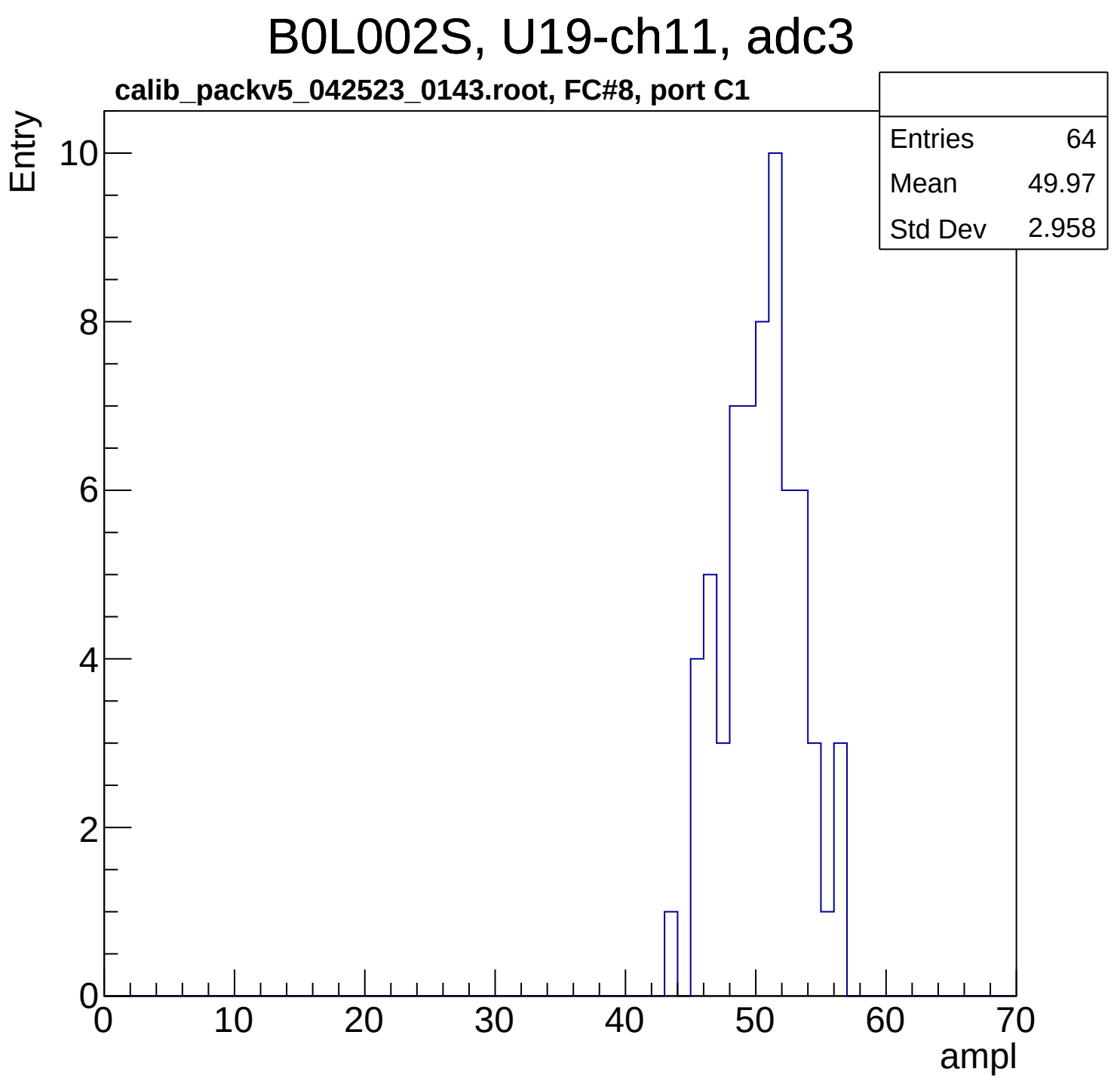

B0L002S, U19-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

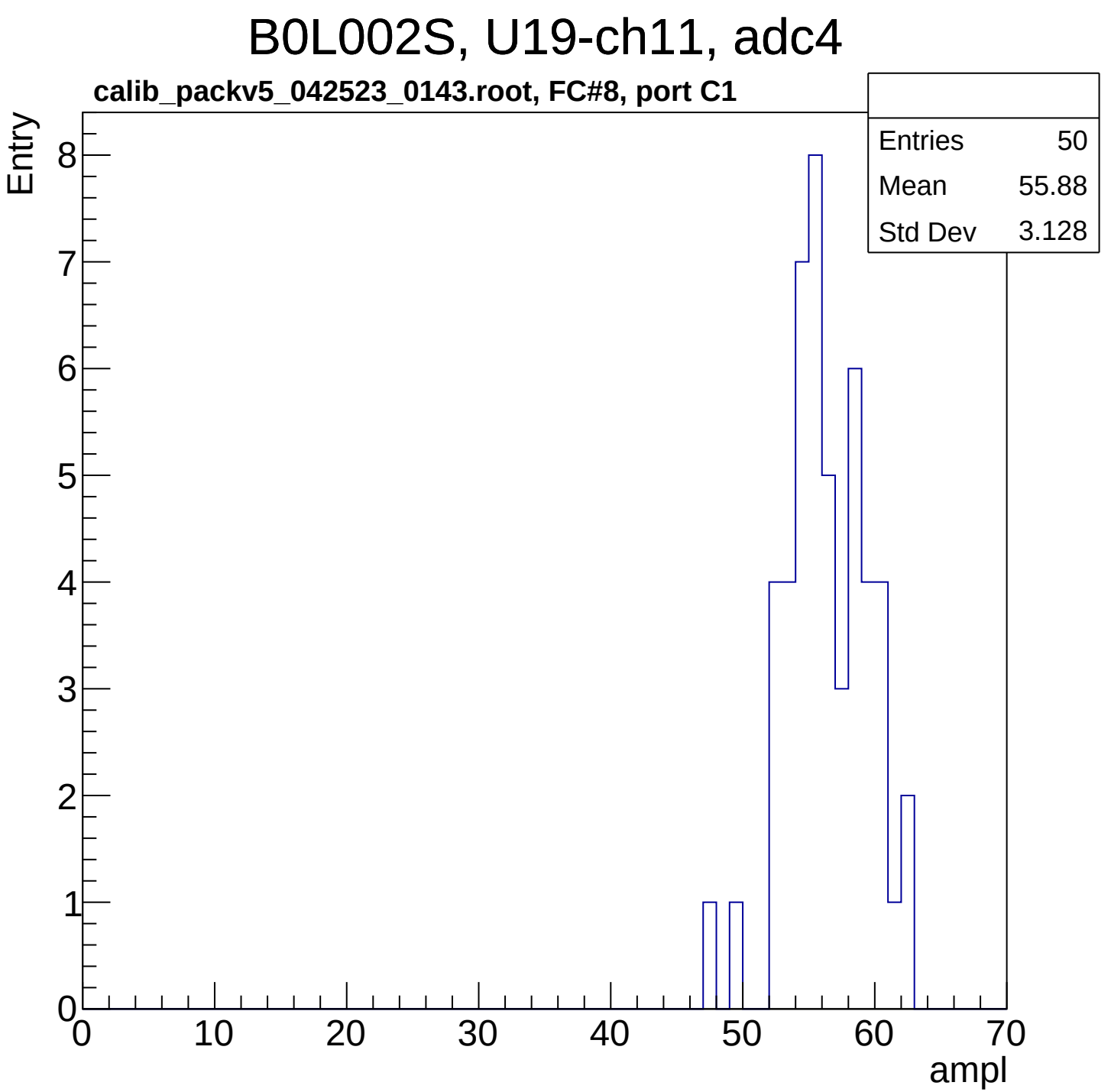

B0L002S, U19-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

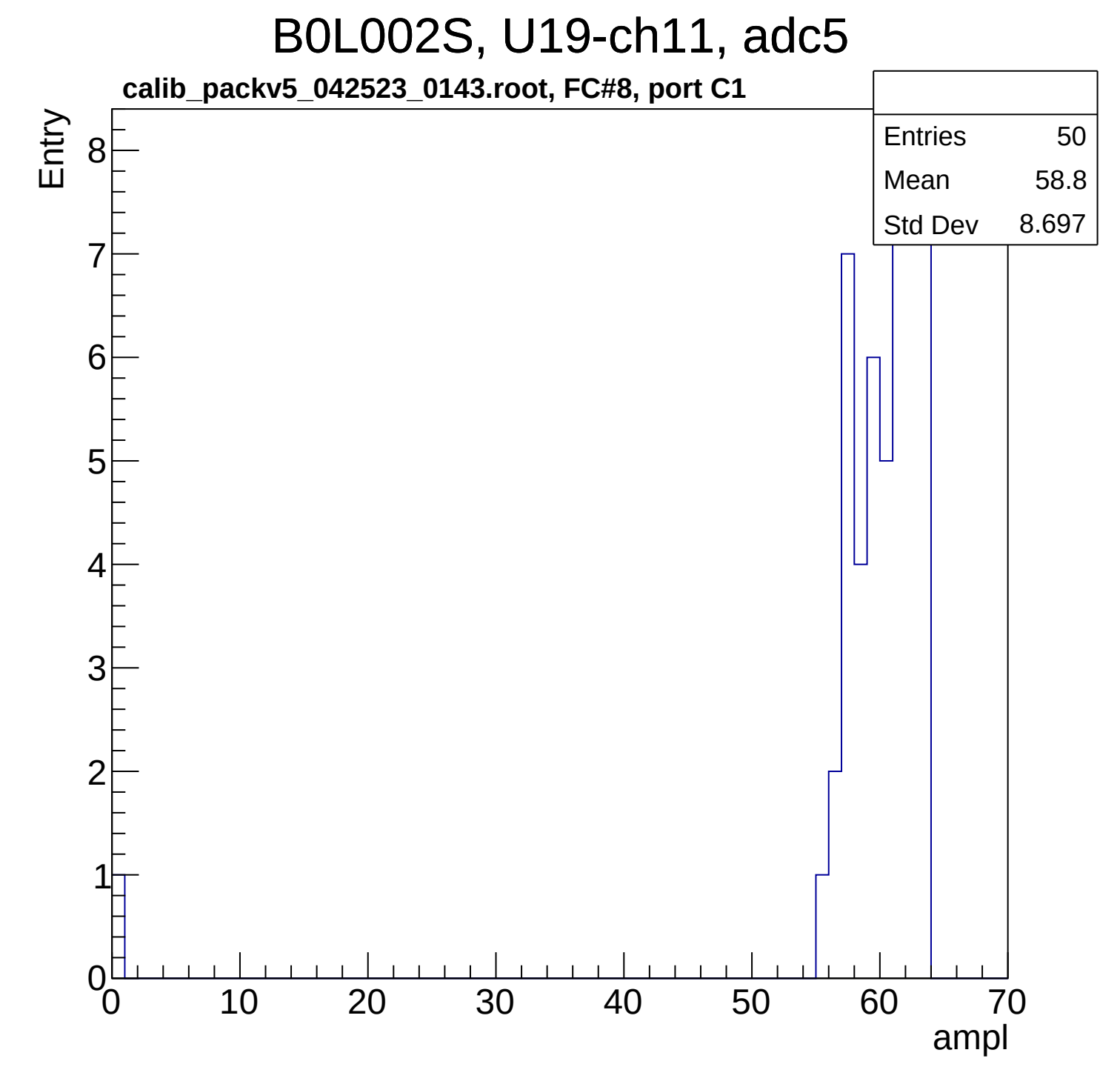

B0L002S, U19-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 29.12
Std Dev 2.787
Gaus mean : 29.4688
Gaus Width: 2.9829
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

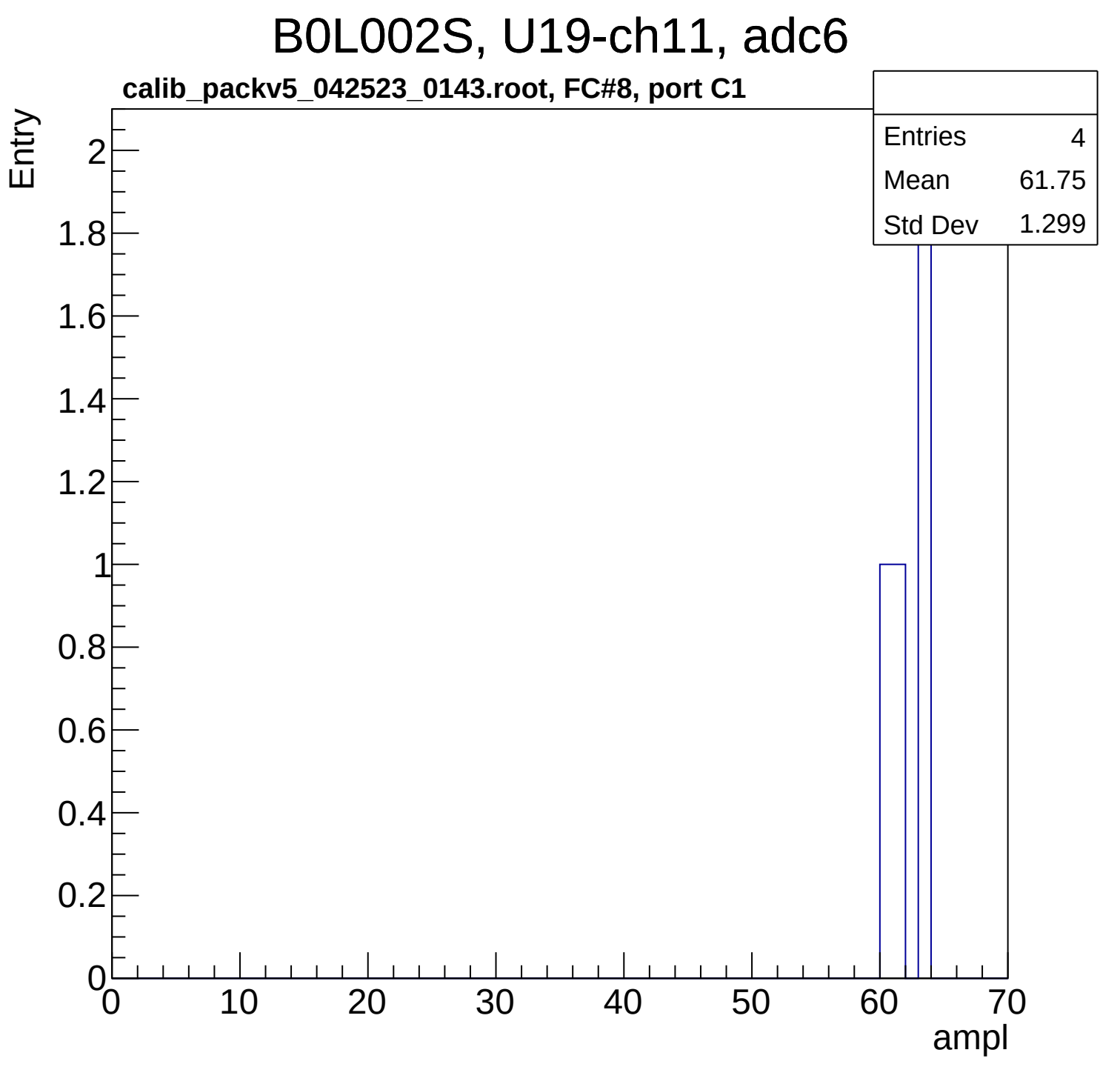

B0L002S, U19-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 35.41
Std Dev 3.444
Gaus mean : 35.8524
Gaus Width: 3.3171
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

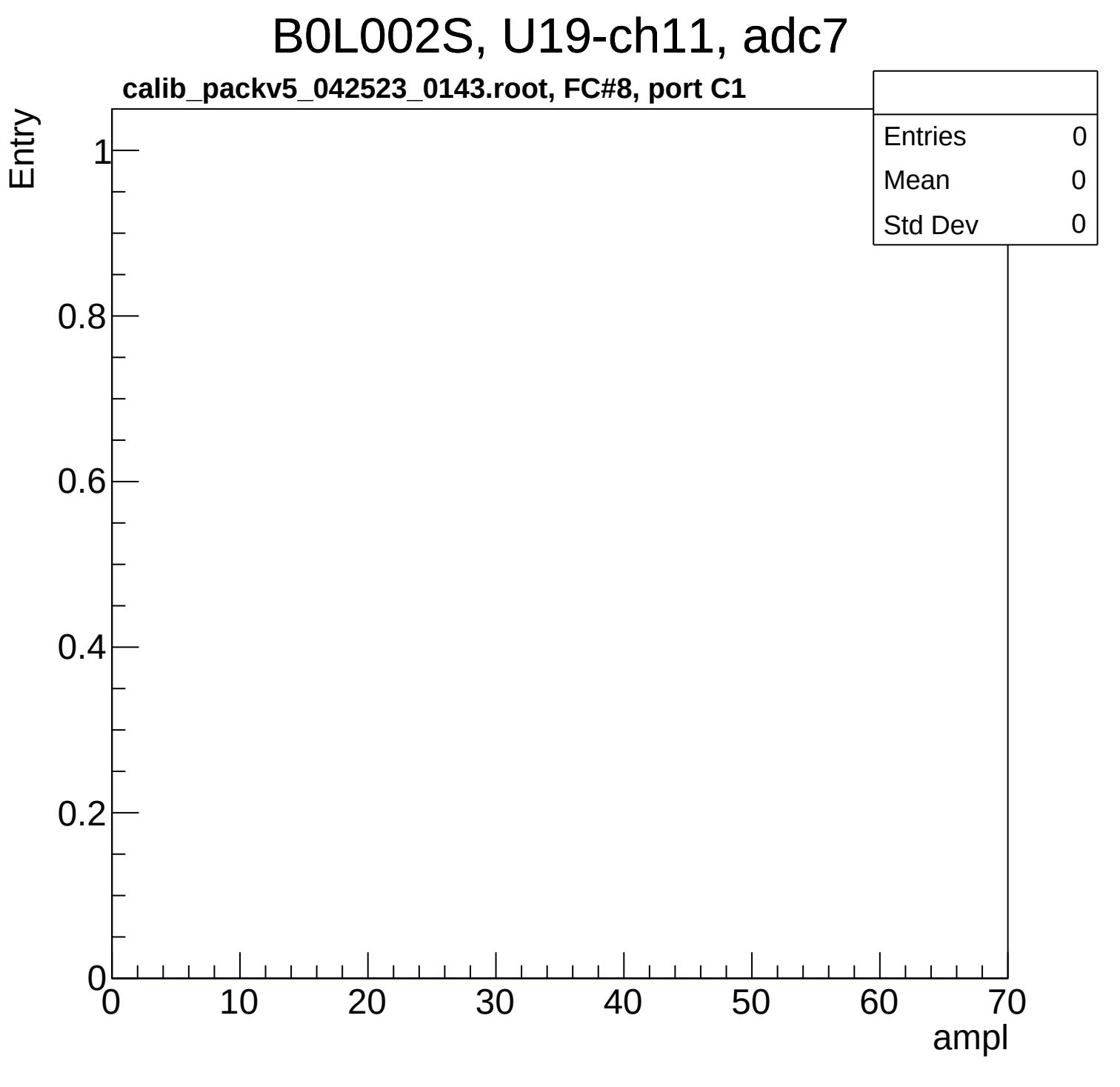

B0L002S, U19-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 43.03
Std Dev 3.348
Gaus mean : 43.7472
Gaus Width: 3.6530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

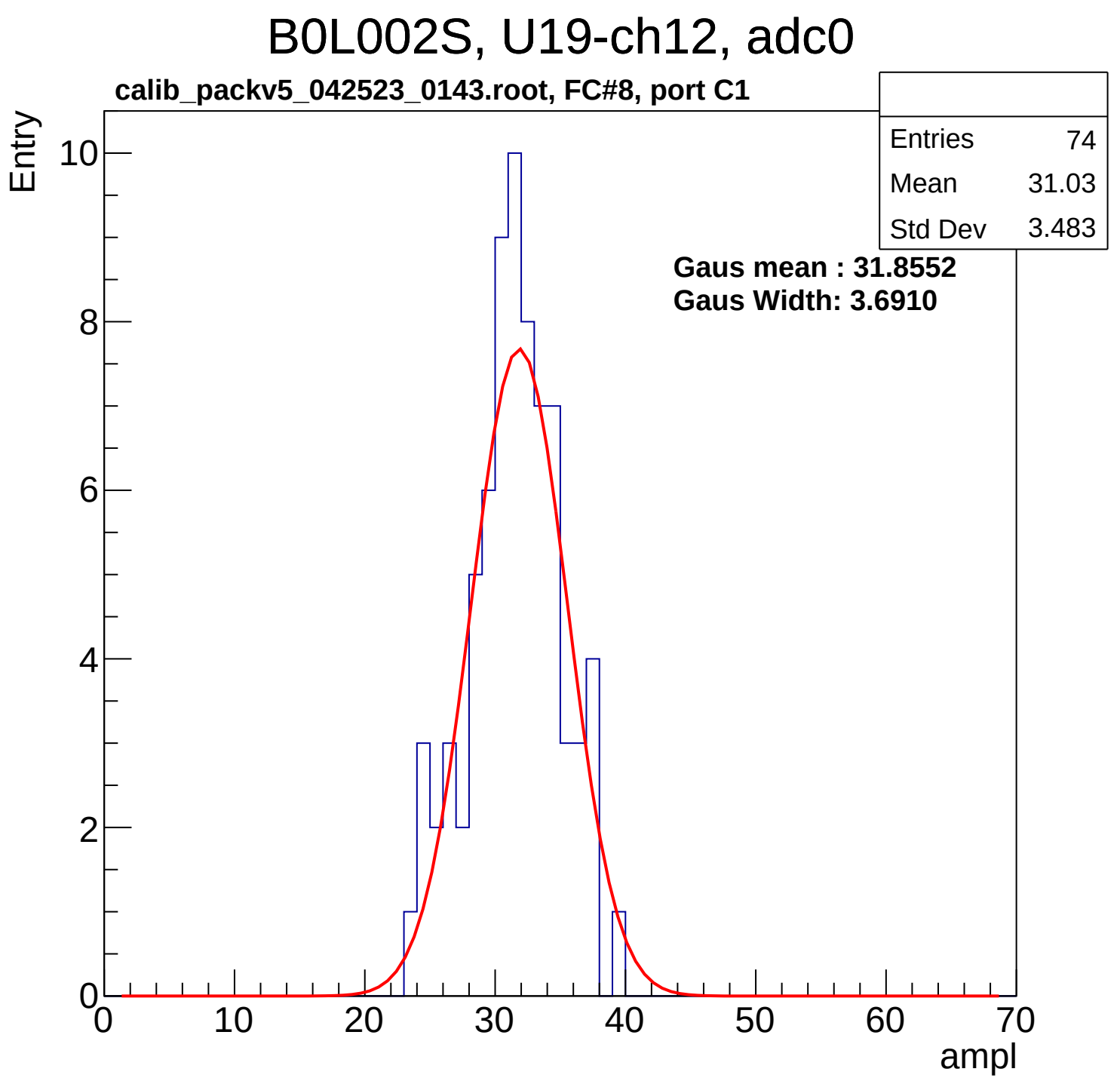

B0L002S, U19-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.97
Std Dev 2.958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

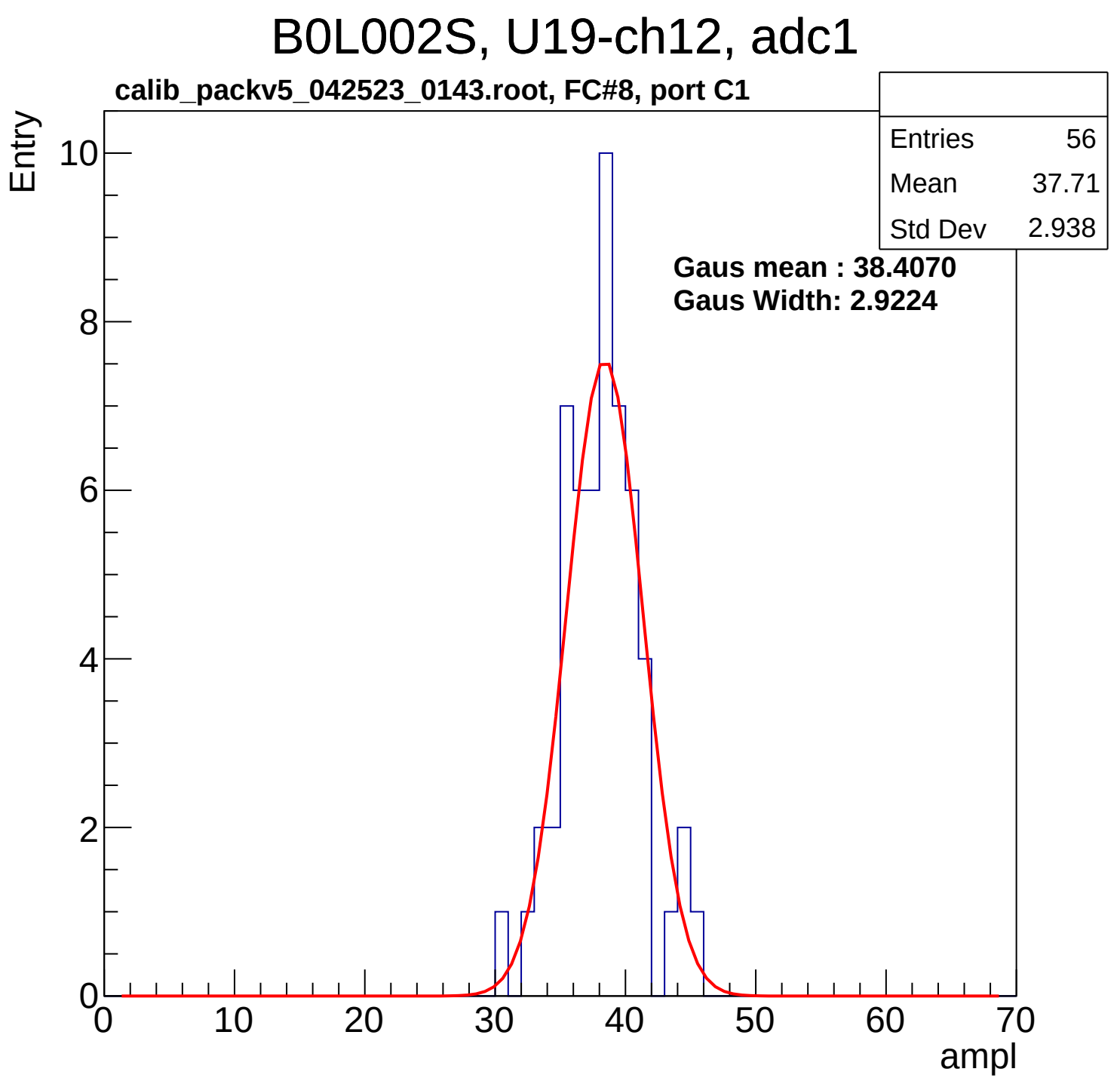

B0L002S, U19-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 55.88
Std Dev 3.128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

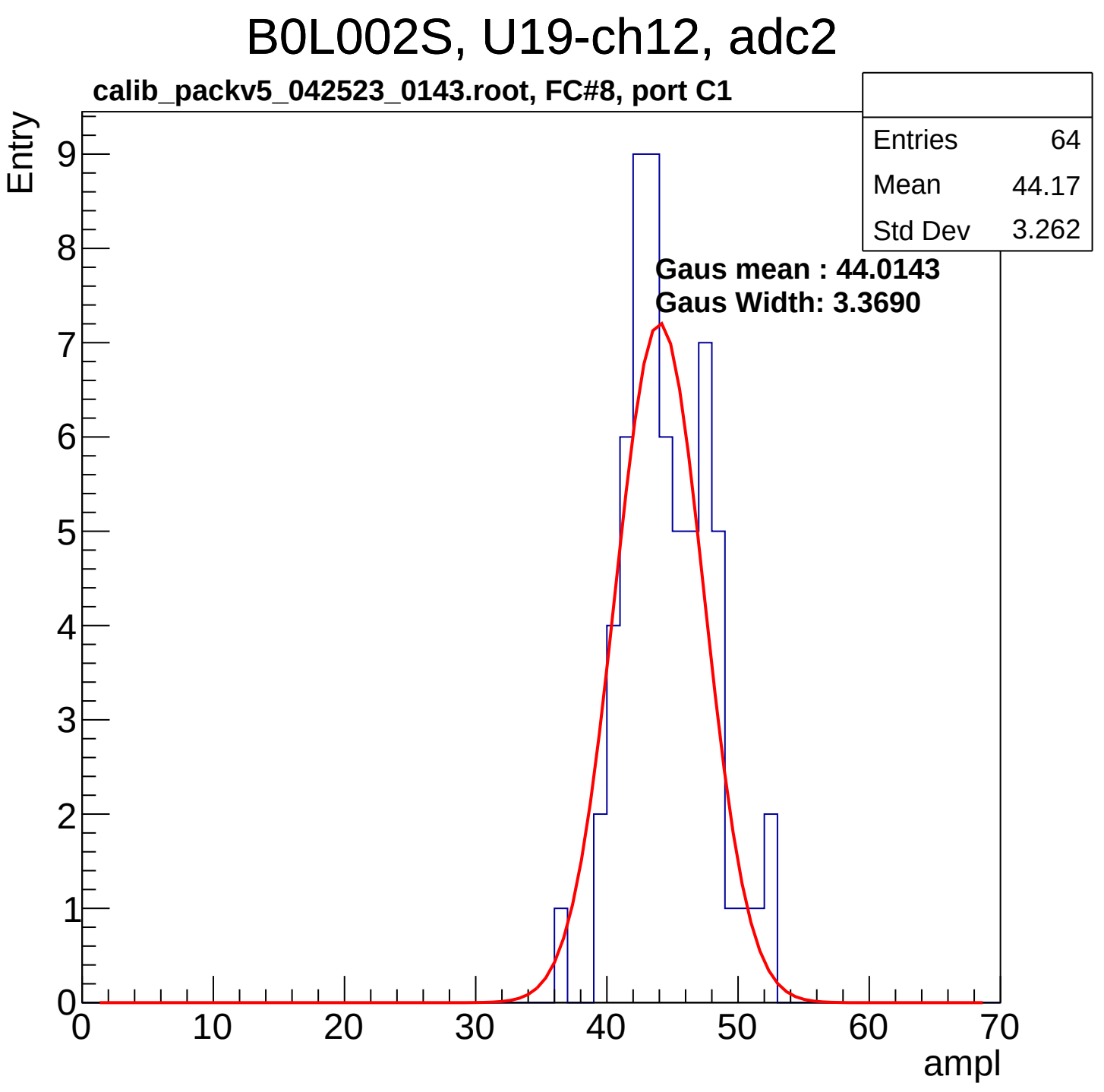

B0L002S, U19-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.8
Std Dev 8.697
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

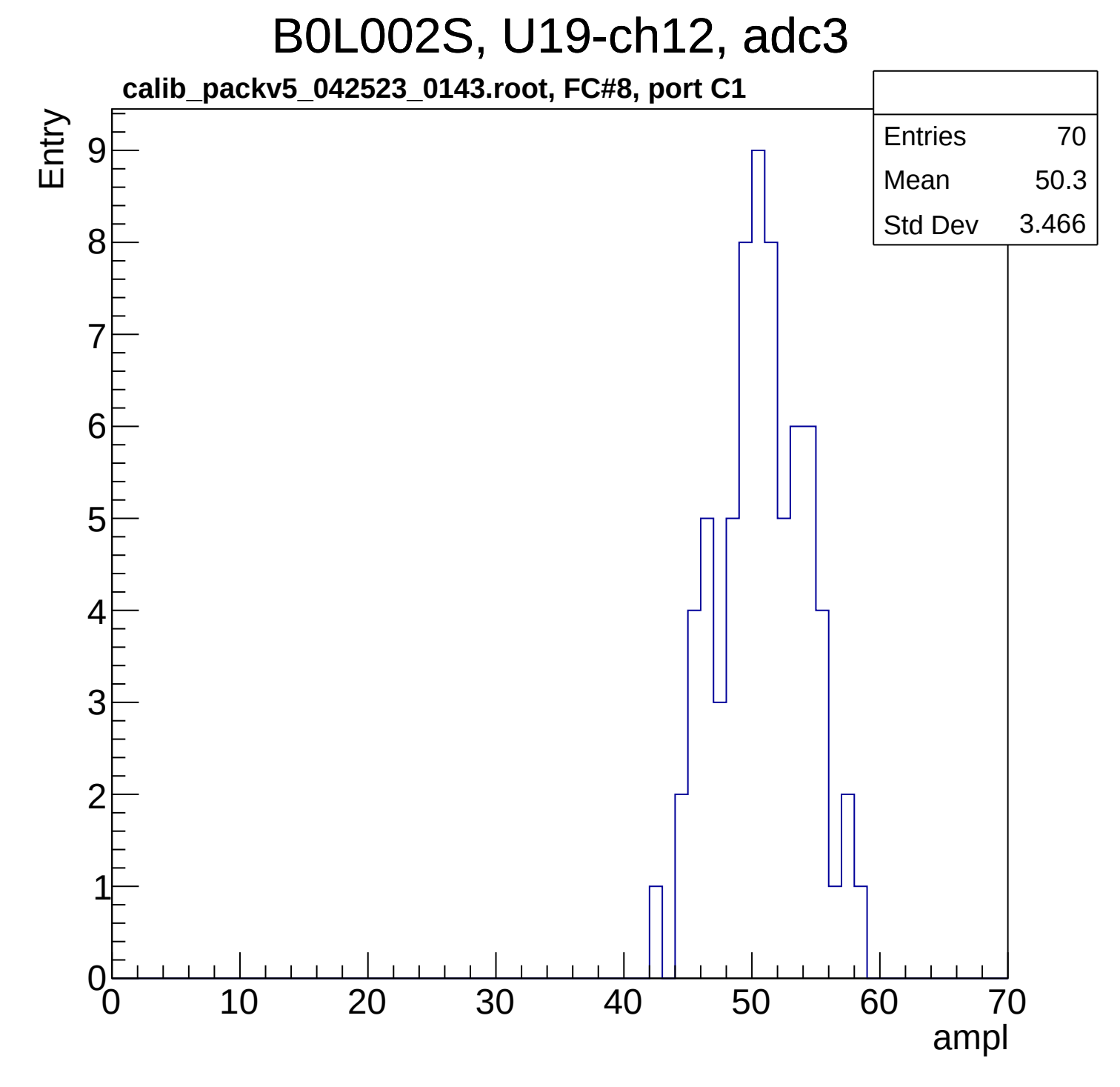

B0L002S, U19-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

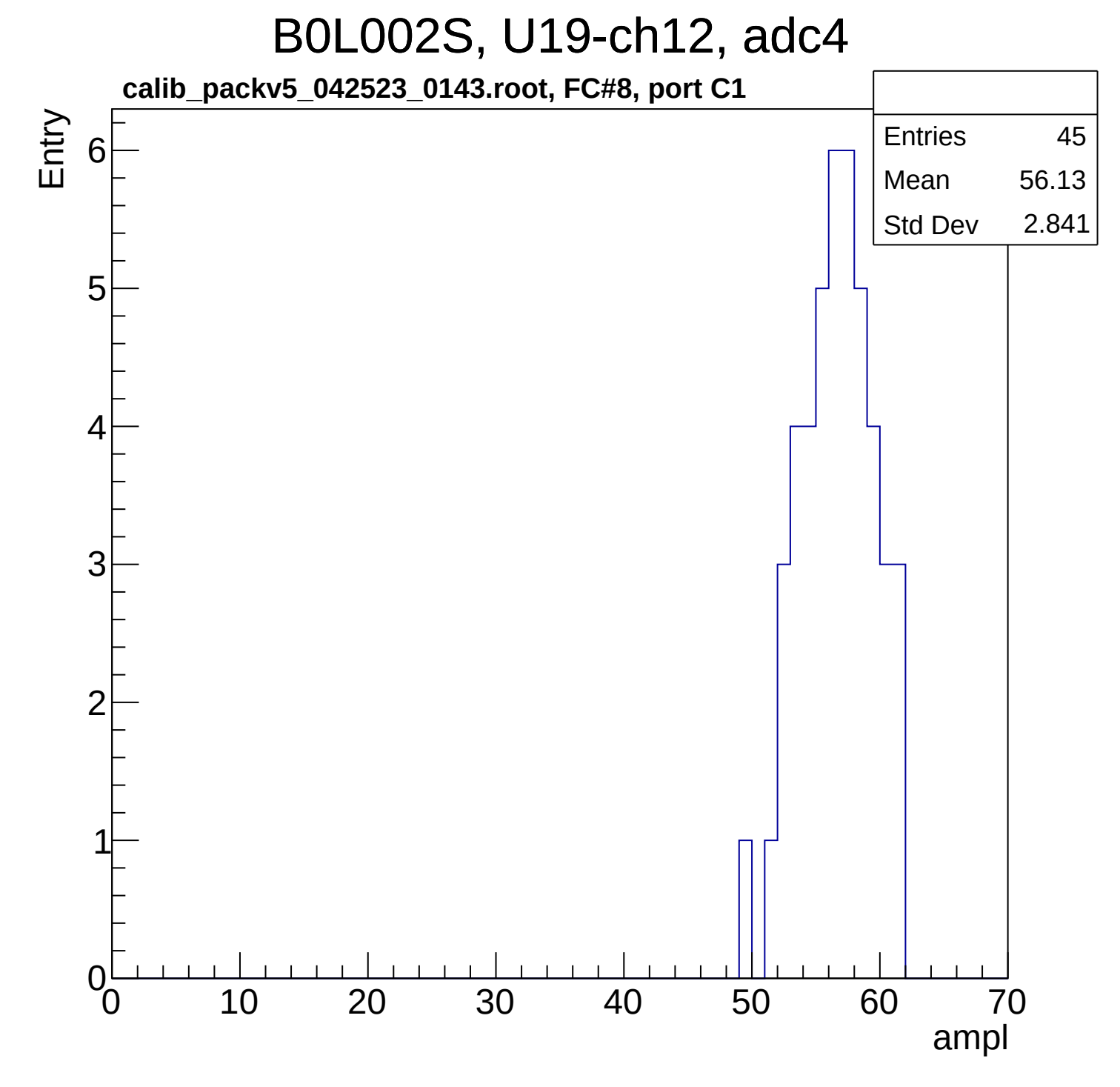

B0L002S, U19-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

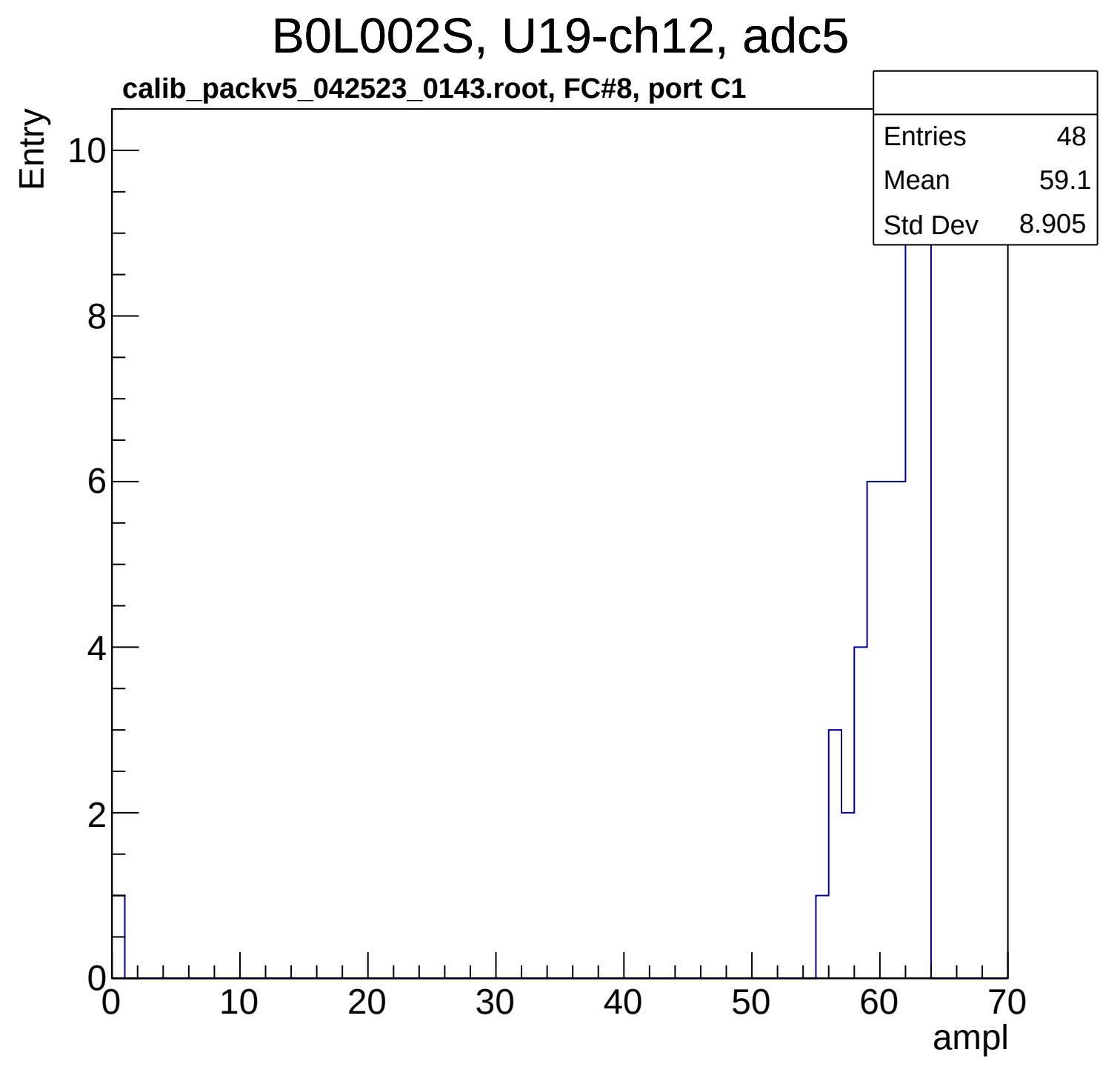

B0L002S, U19-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.03
Std Dev 3.483
Gaus mean : 31.8552
Gaus Width: 3.6910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

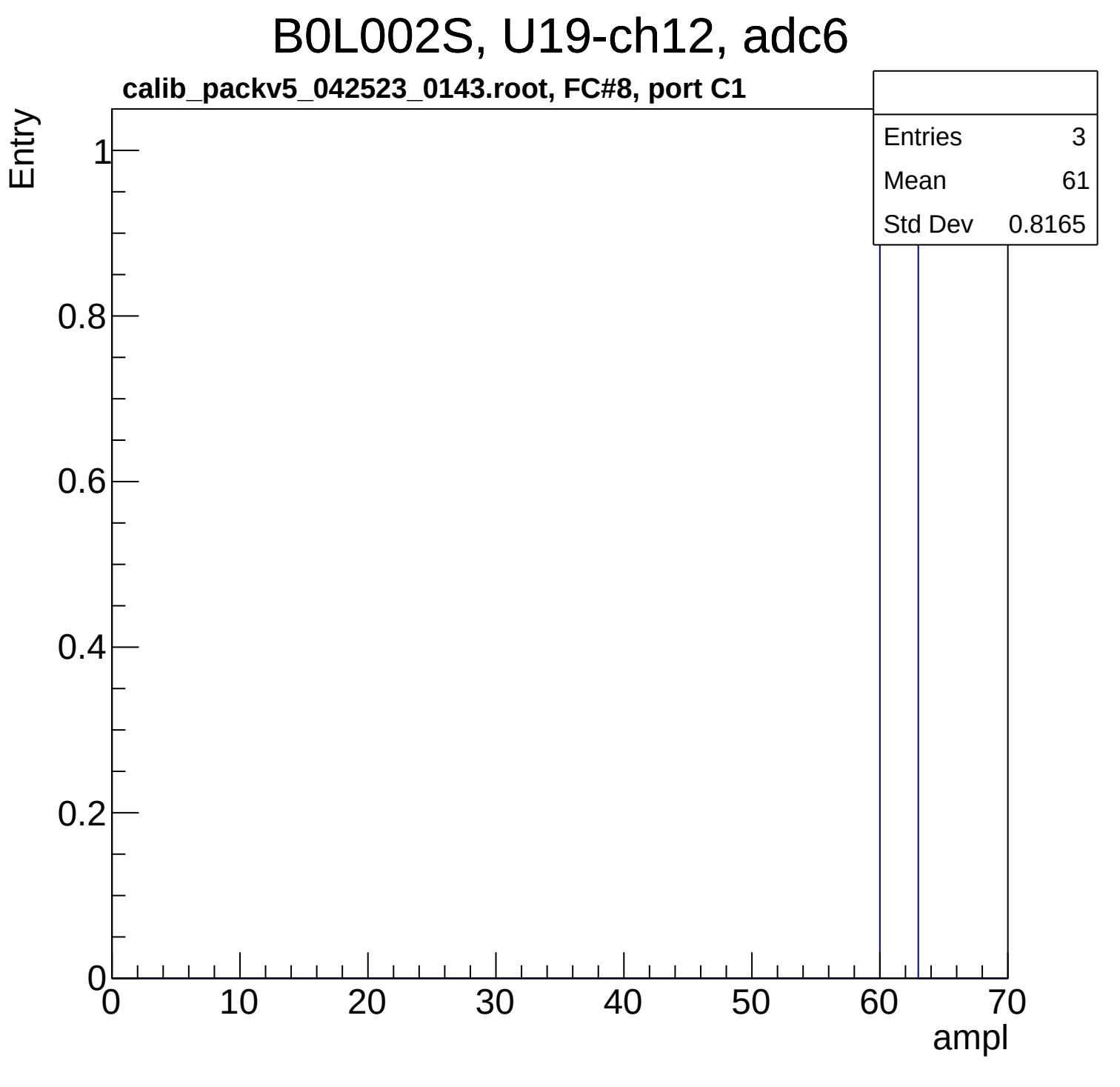

B0L002S, U19-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 37.71
Std Dev 2.938
Gaus mean : 38.4070
Gaus Width: 2.9224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

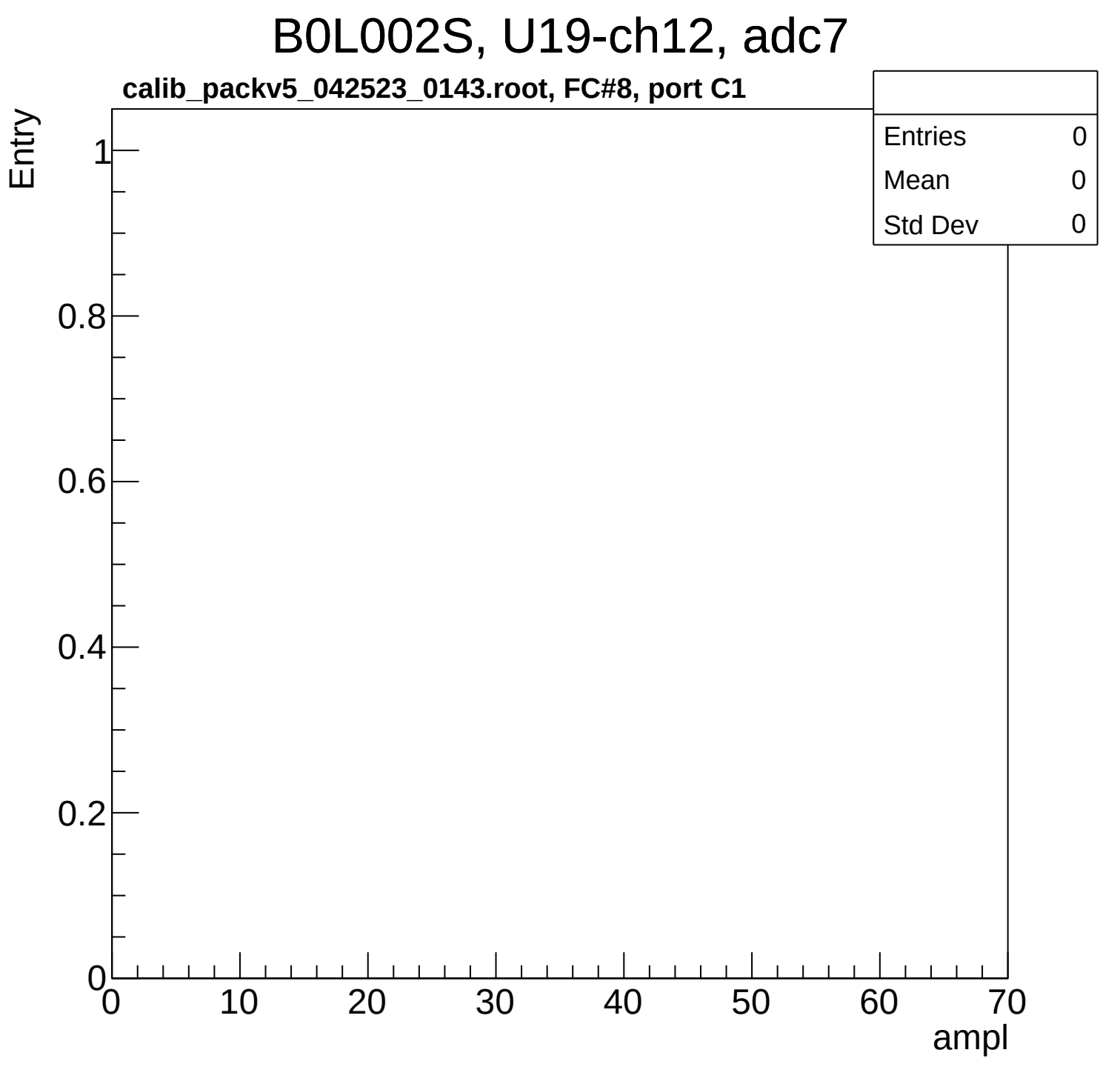

B0L002S, U19-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.17
Std Dev 3.262
Gaus mean : 44.0143
Gaus Width: 3.3690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

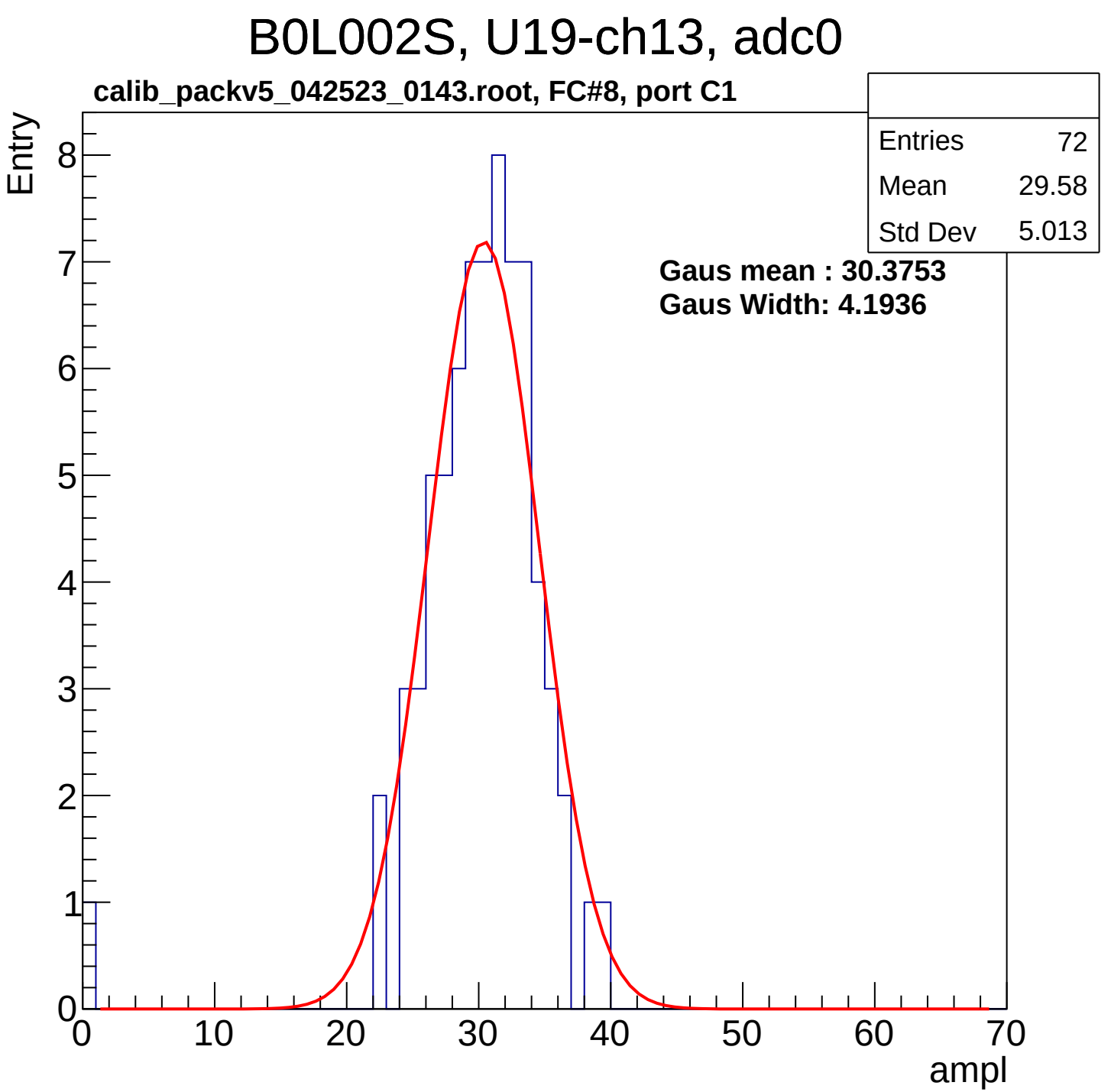

B0L002S, U19-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 50.3
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

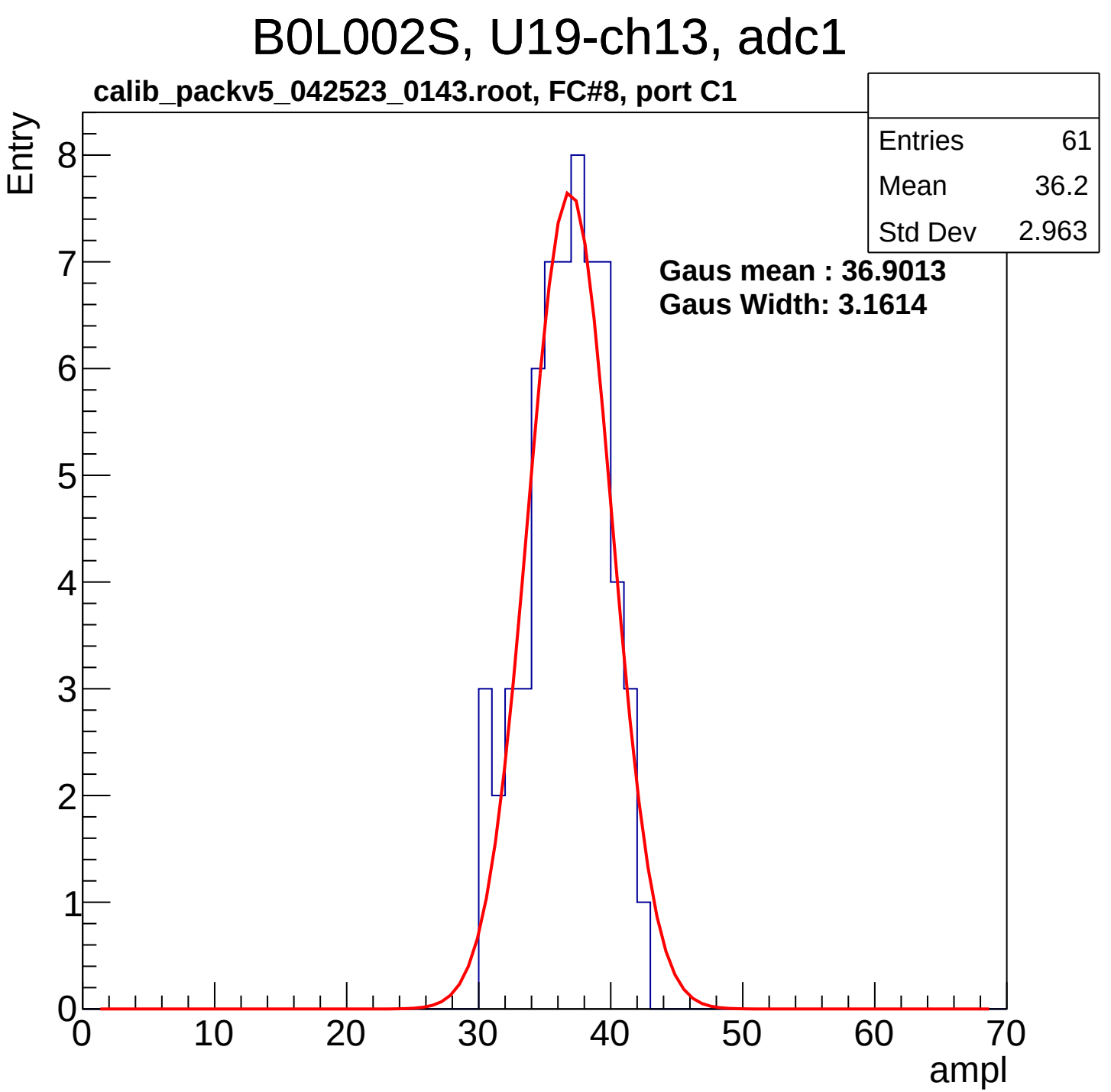

B0L002S, U19-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.13
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

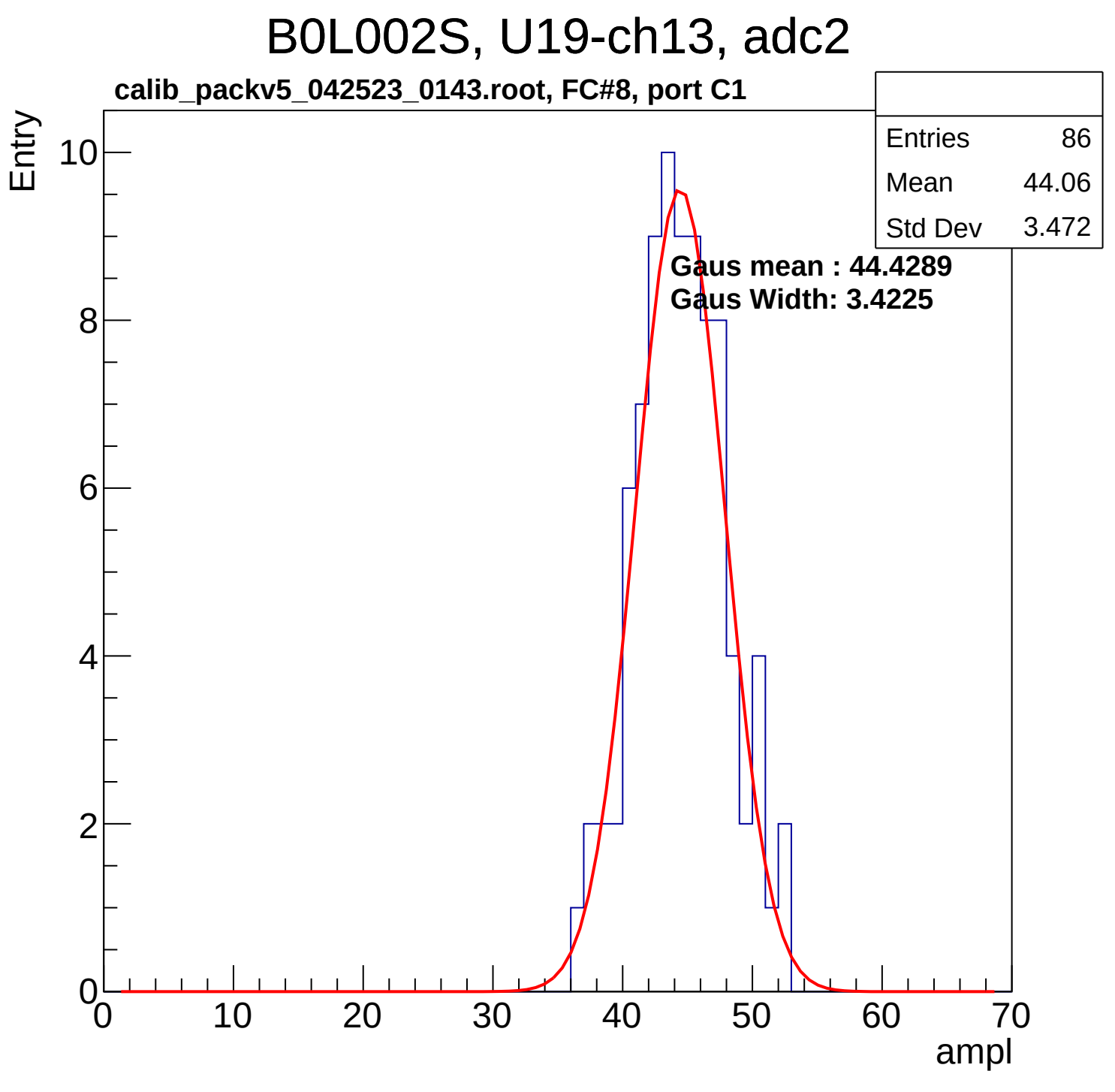

B0L002S, U19-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 59.1
Std Dev 8.905
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

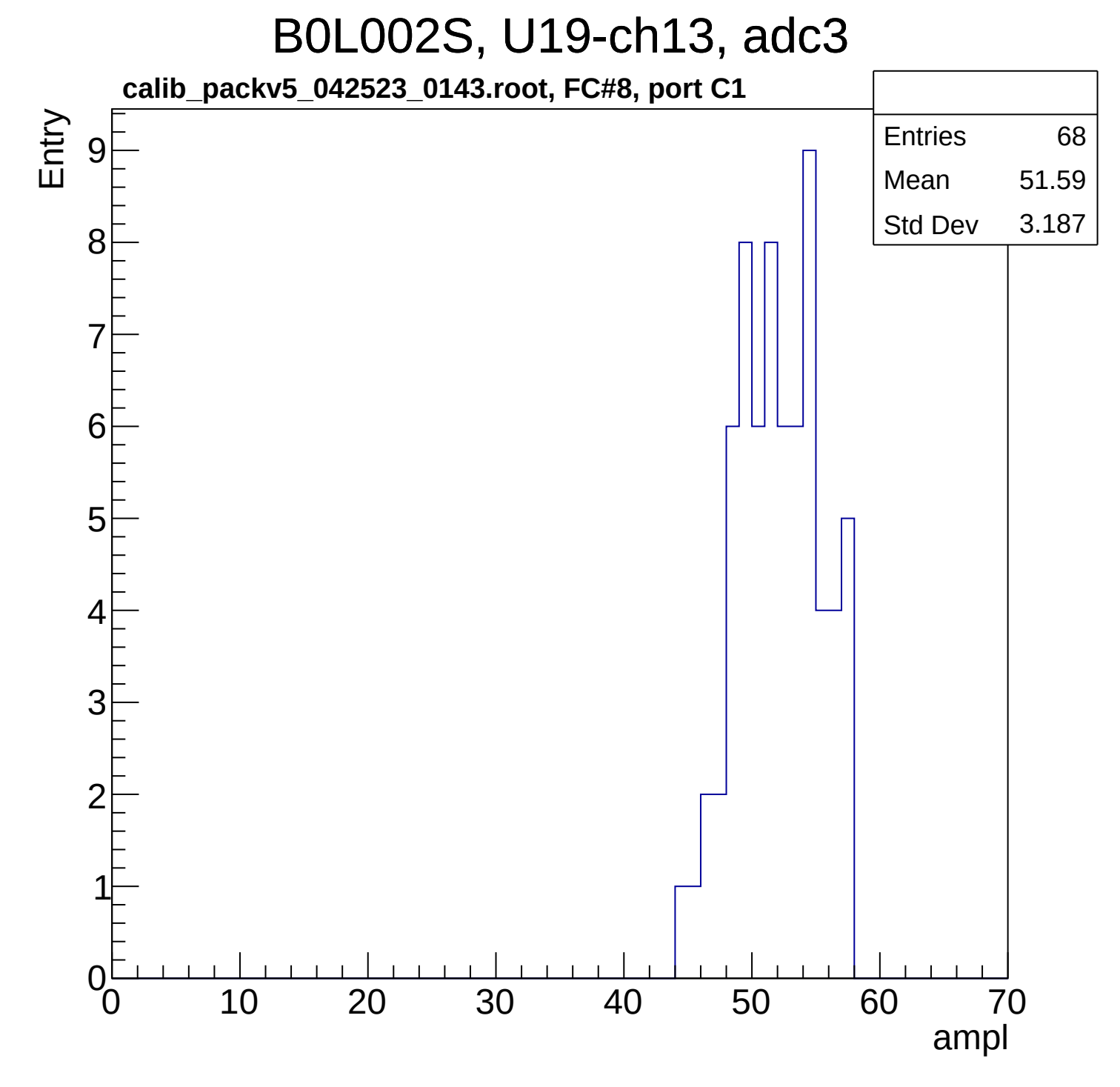

B0L002S, U19-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

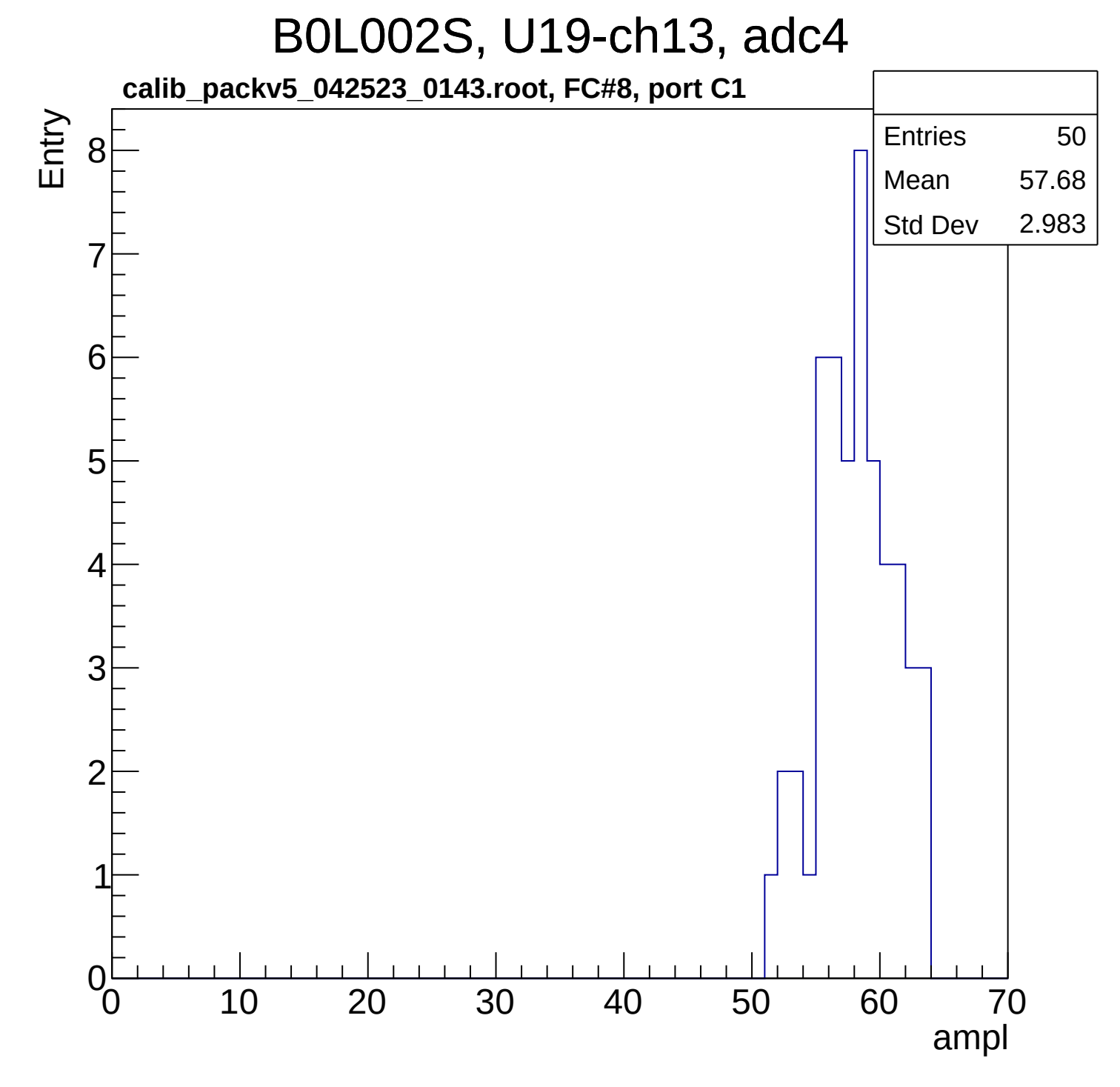

B0L002S, U19-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

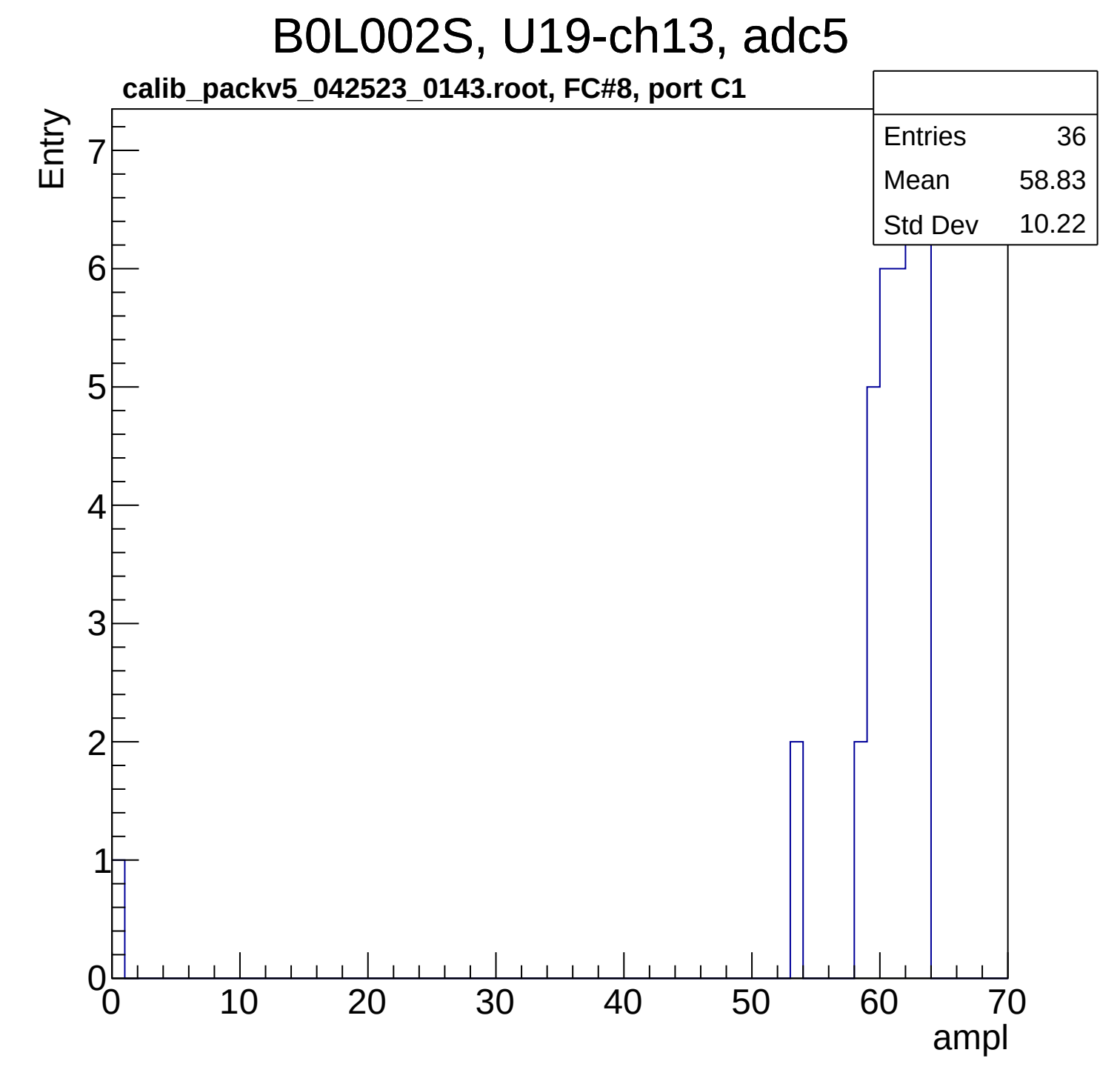

B0L002S, U19-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.58
Std Dev 5.013
Gaus mean : 30.3753
Gaus Width: 4.1936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

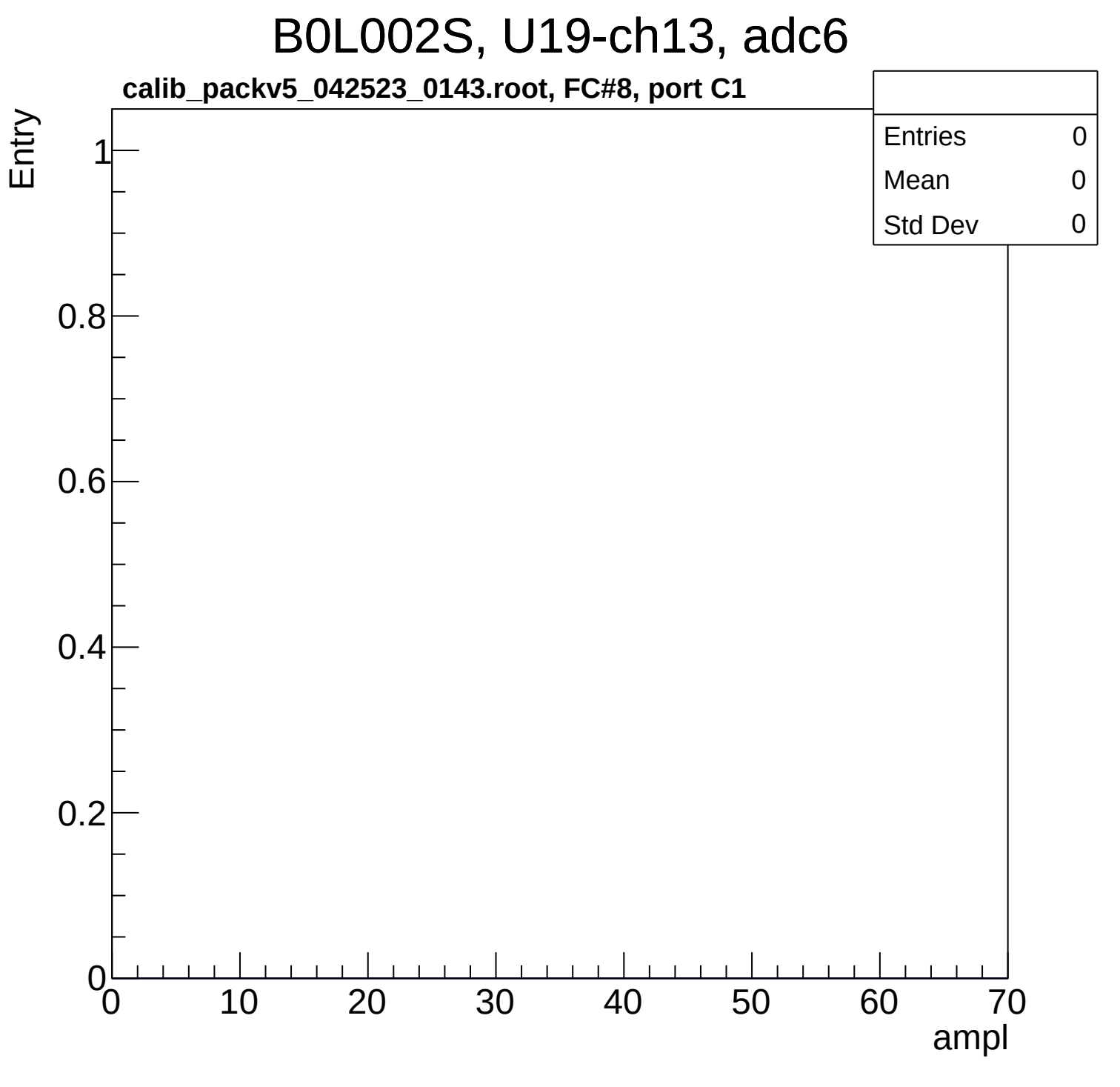

B0L002S, U19-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.2
Std Dev 2.963
Gaus mean : 36.9013
Gaus Width: 3.1614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

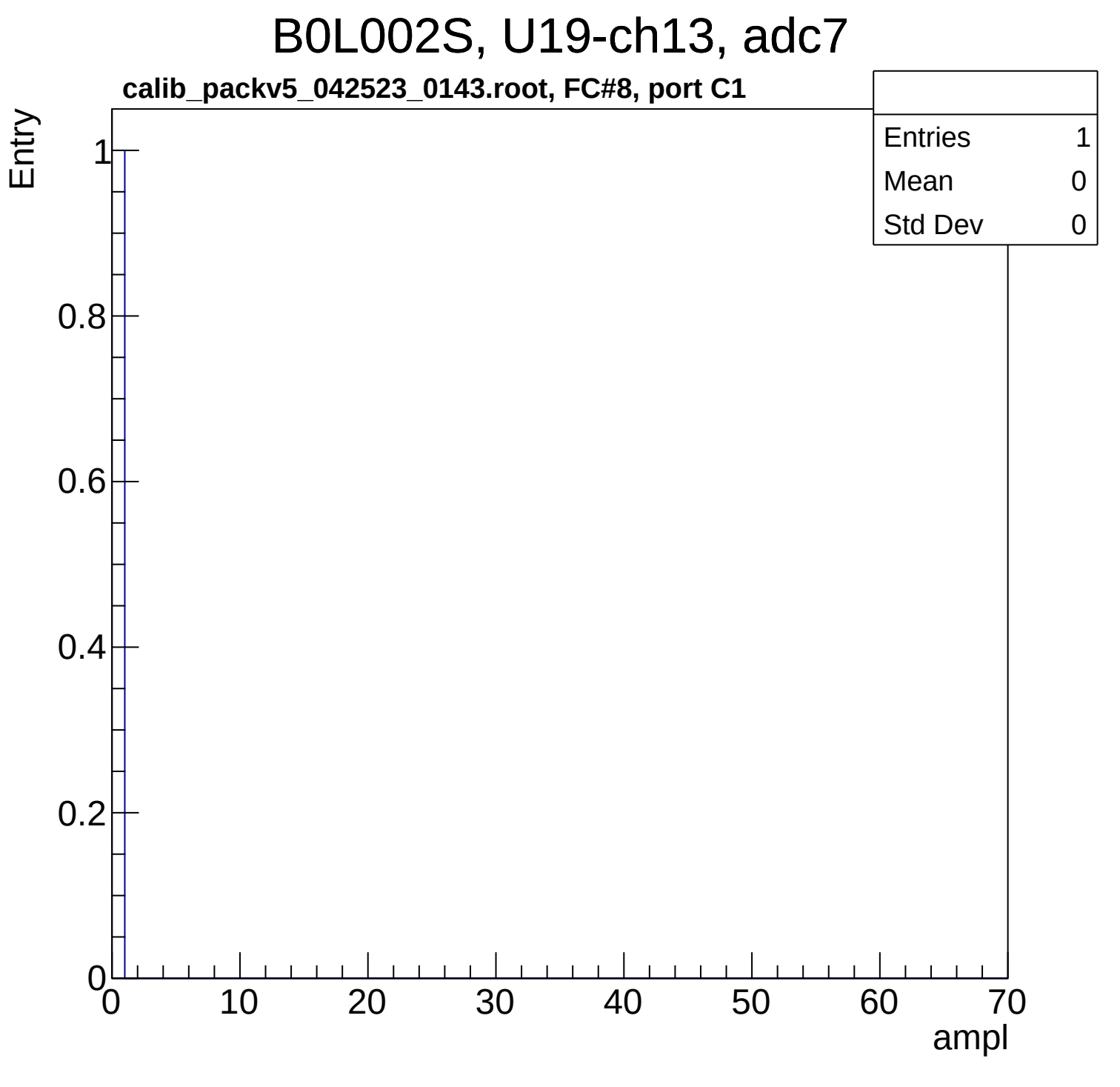

B0L002S, U19-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 44.06
Std Dev 3.472
Gaus mean : 44.4289
Gaus Width: 3.4225
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

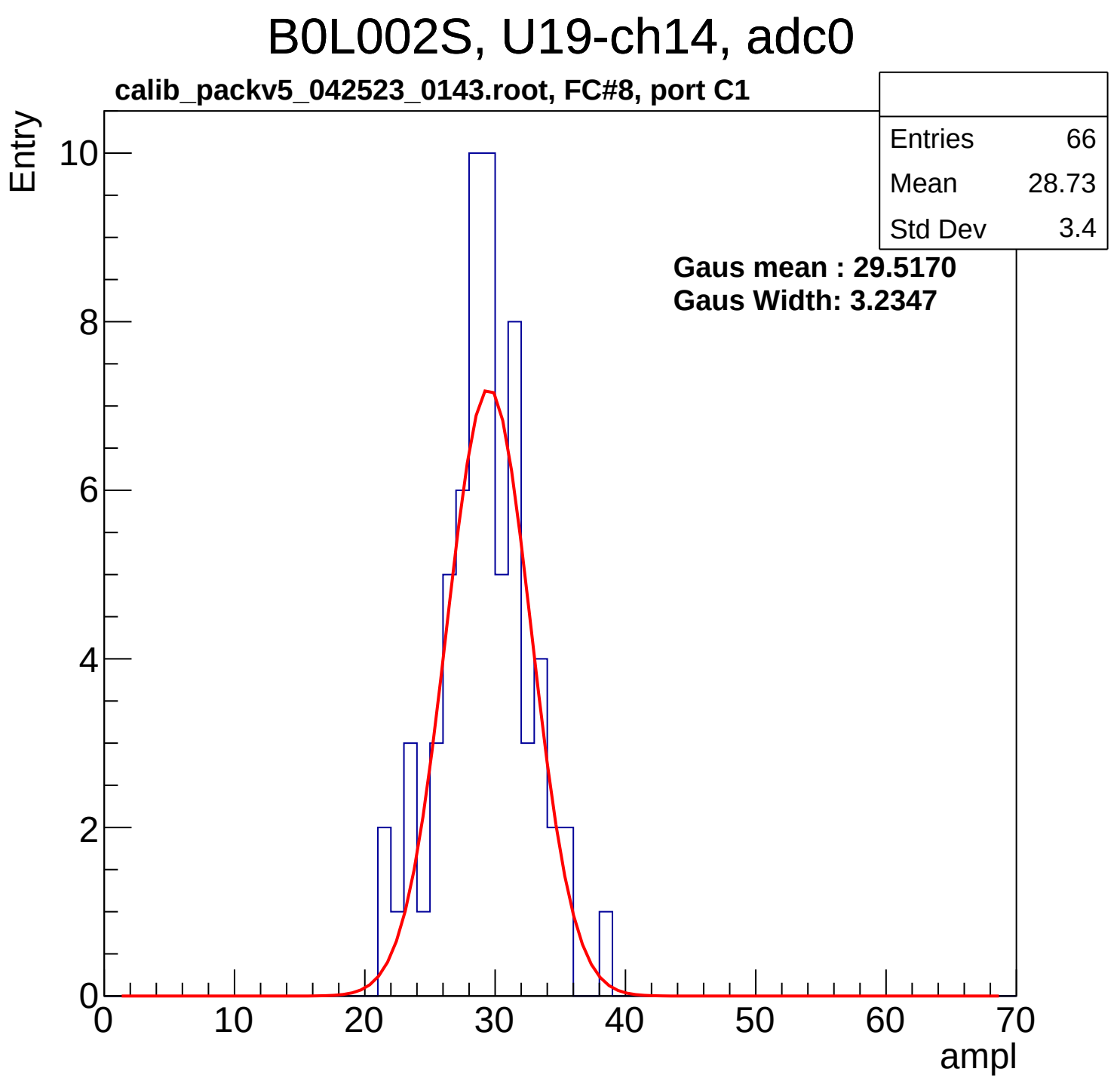

B0L002S, U19-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.59
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

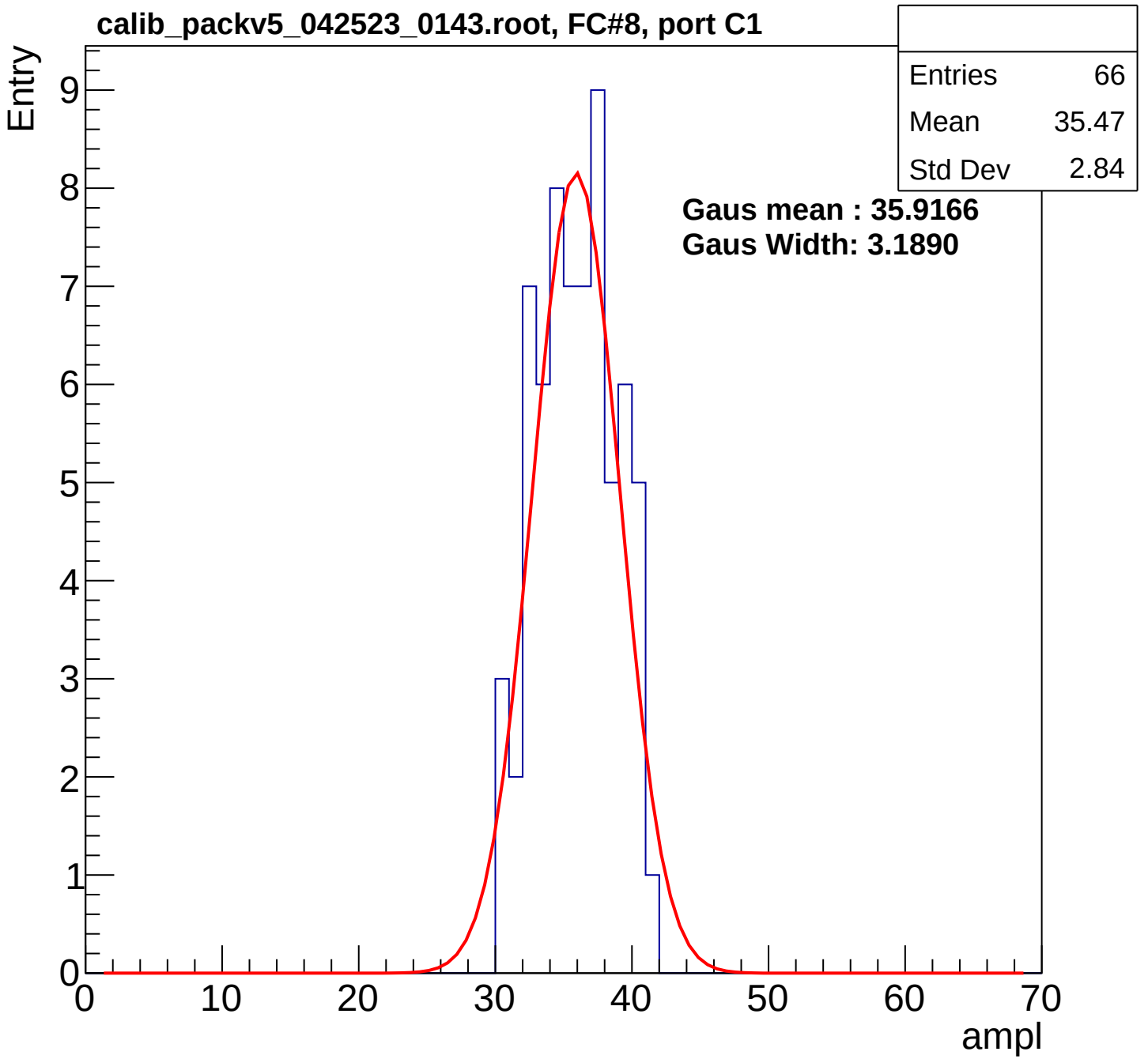

B0L002S, U19-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.68
Std Dev 2.983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

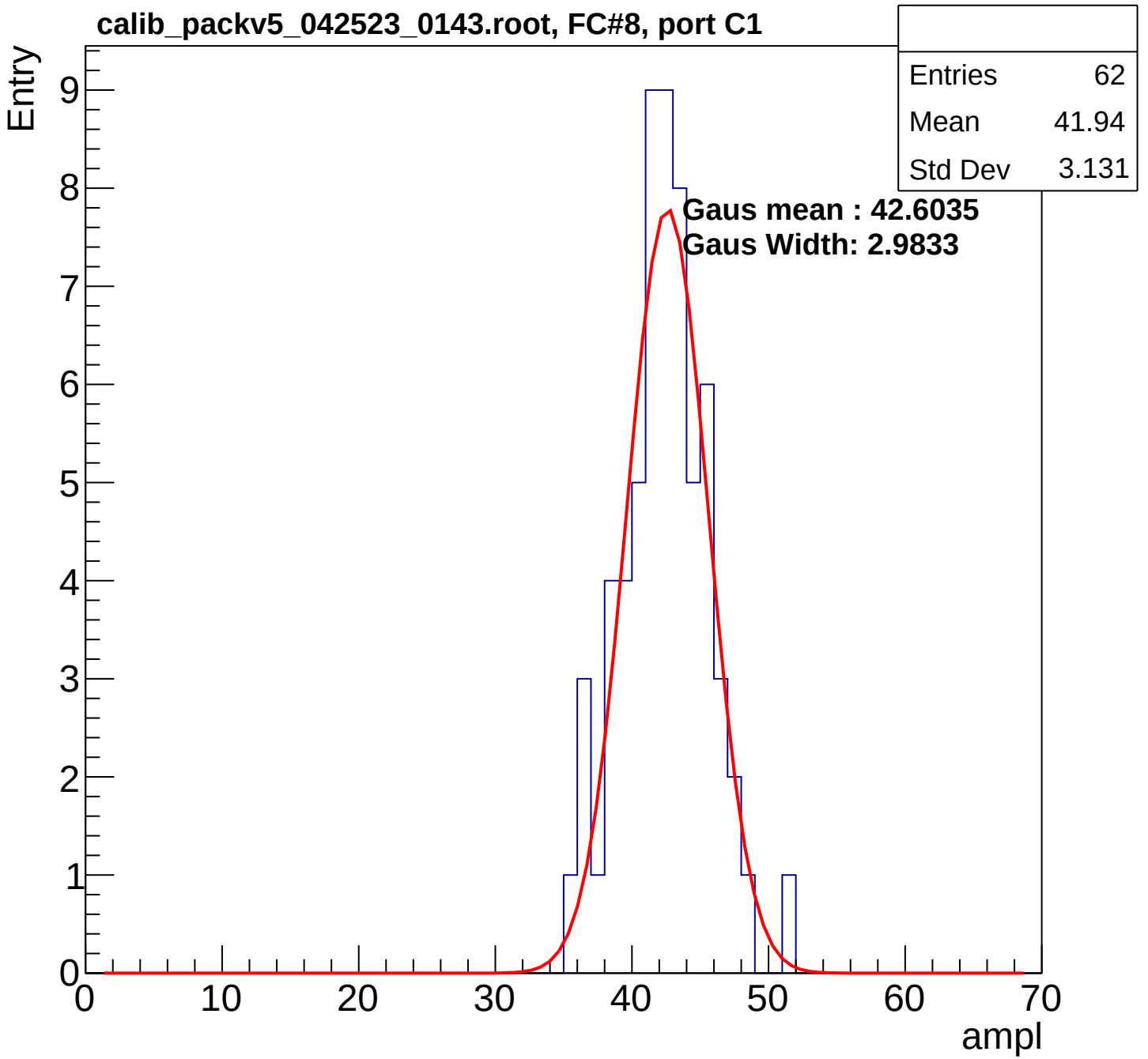

B0L002S, U19-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.83
Std Dev 10.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

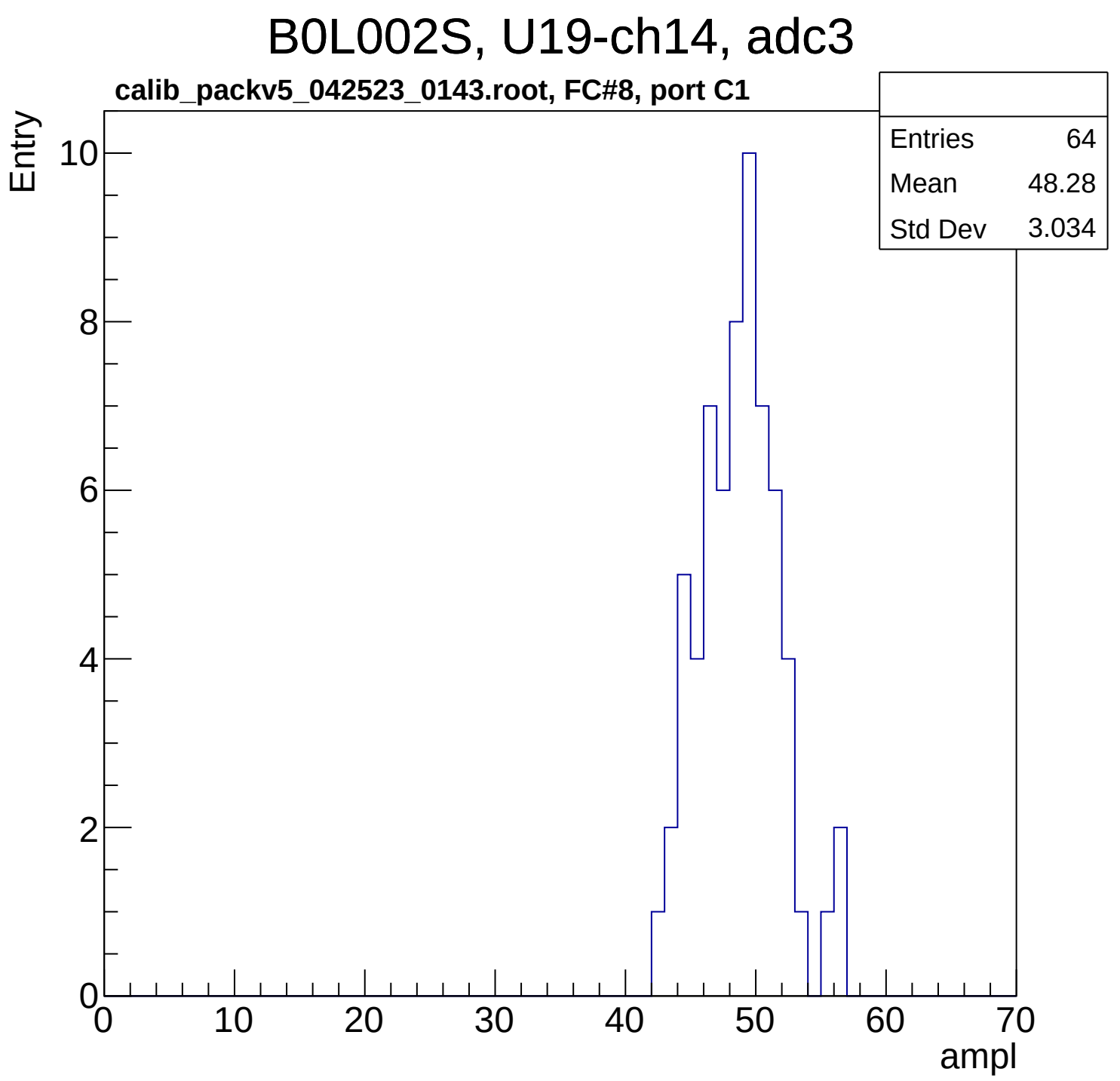

B0L002S, U19-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

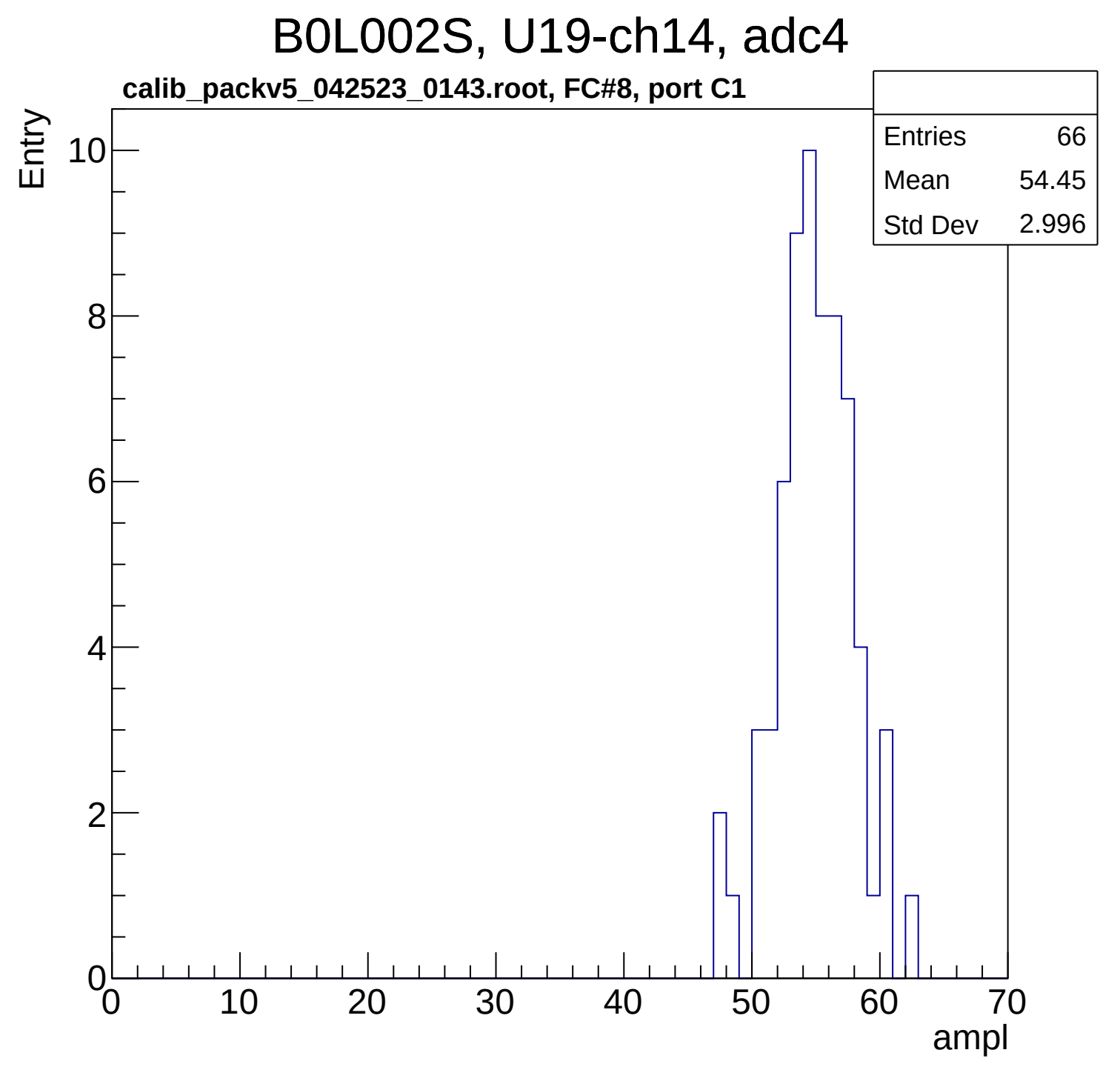

B0L002S, U19-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

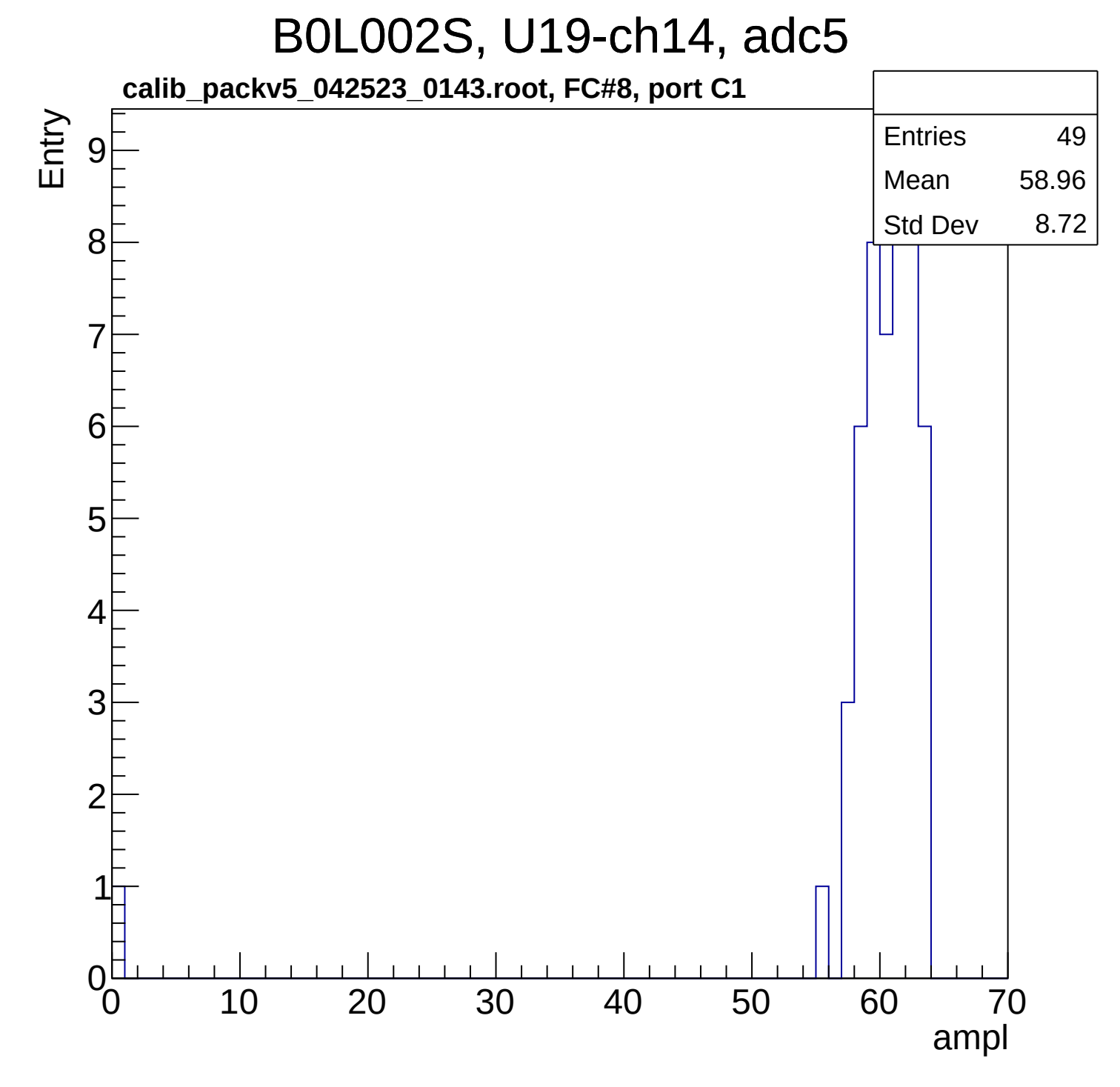

B0L002S, U19-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 28.73
Std Dev 3.4
Gaus mean : 29.5170
Gaus Width: 3.2347
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

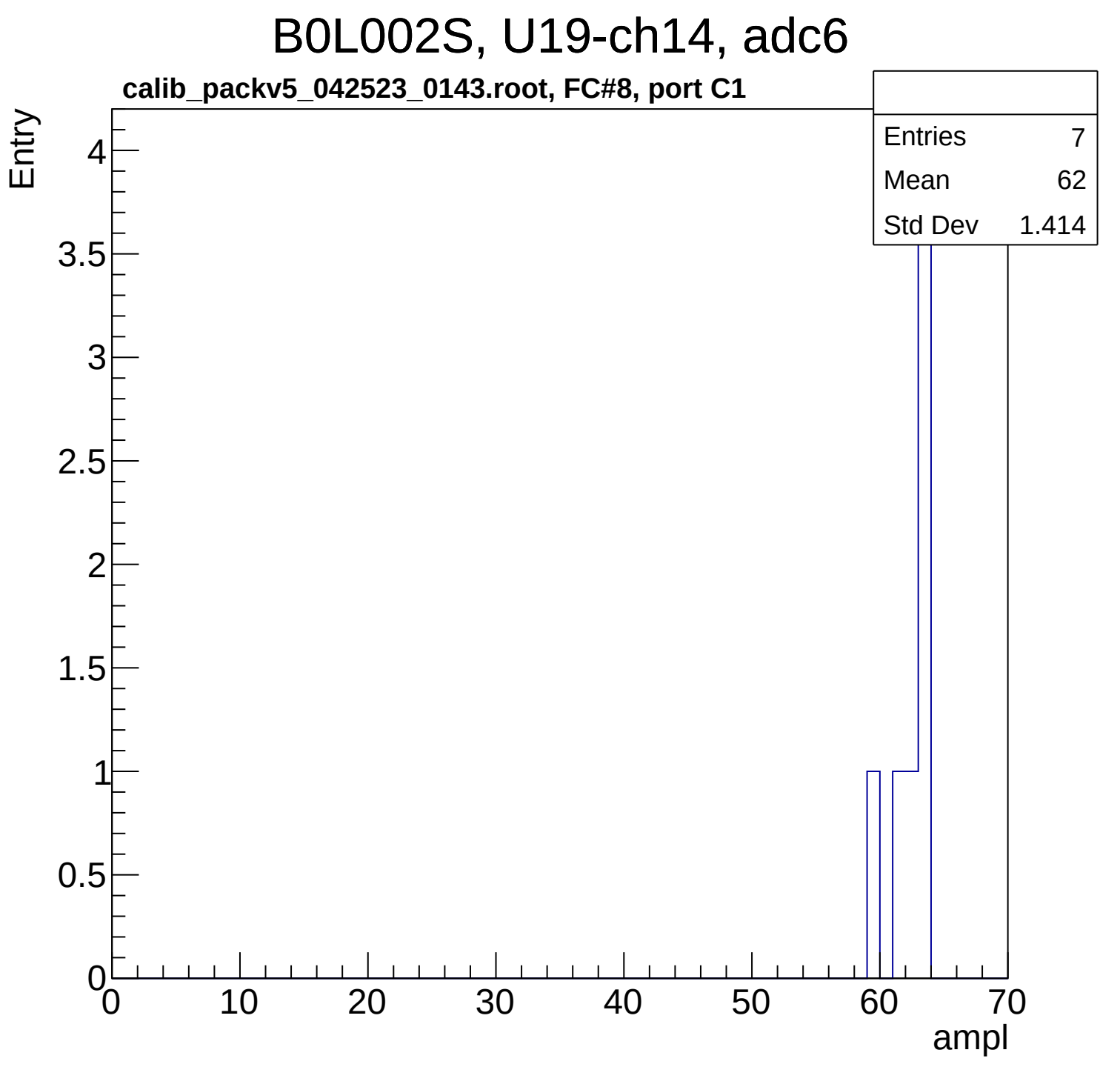

B0L002S, U19-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 35.47
Std Dev 2.84
Gaus mean : 35.9166
Gaus Width: 3.1890
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

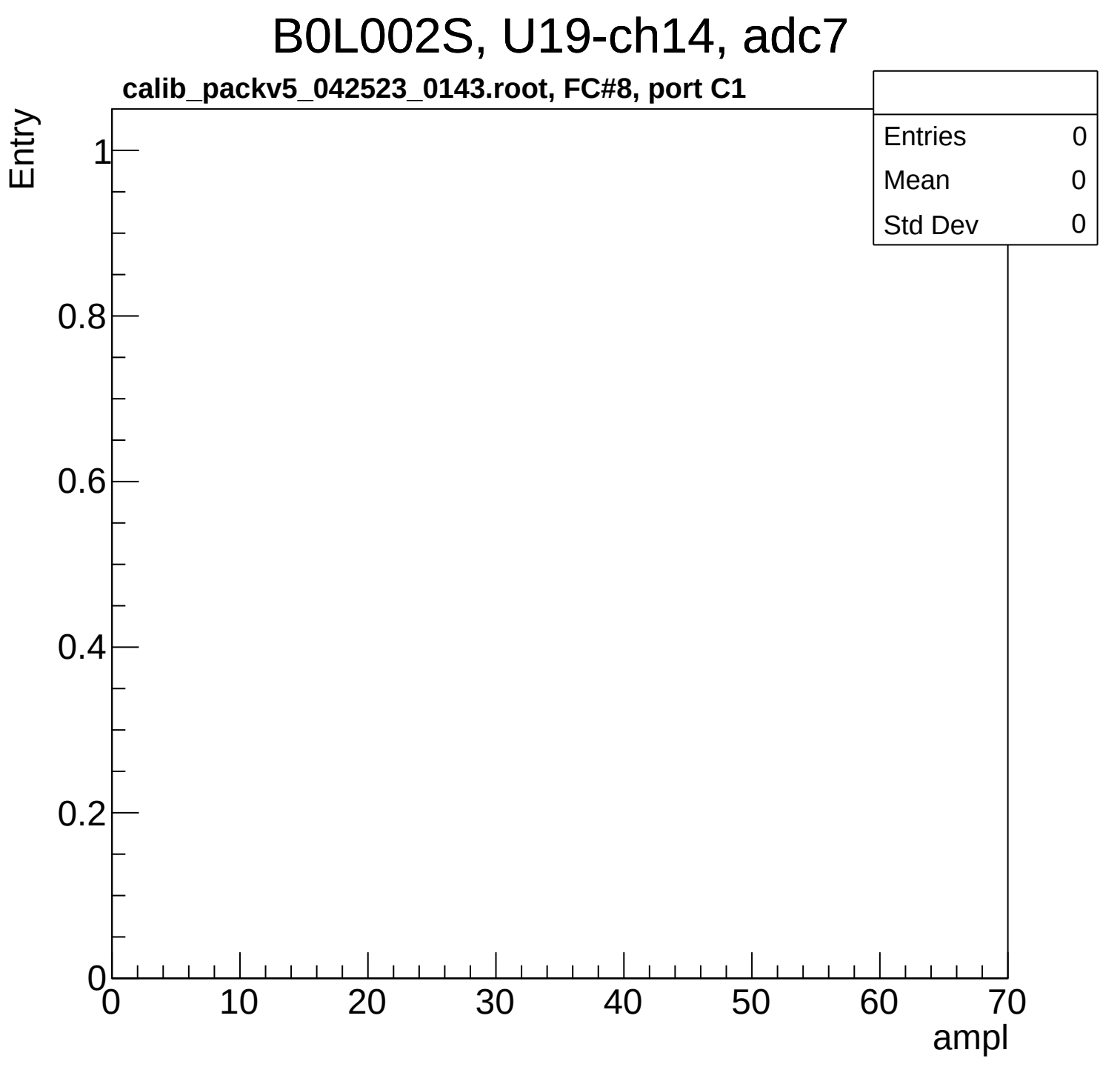

B0L002S, U19-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 41.94
Std Dev 3.131
Gaus mean : 42.6035
Gaus Width: 2.9833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

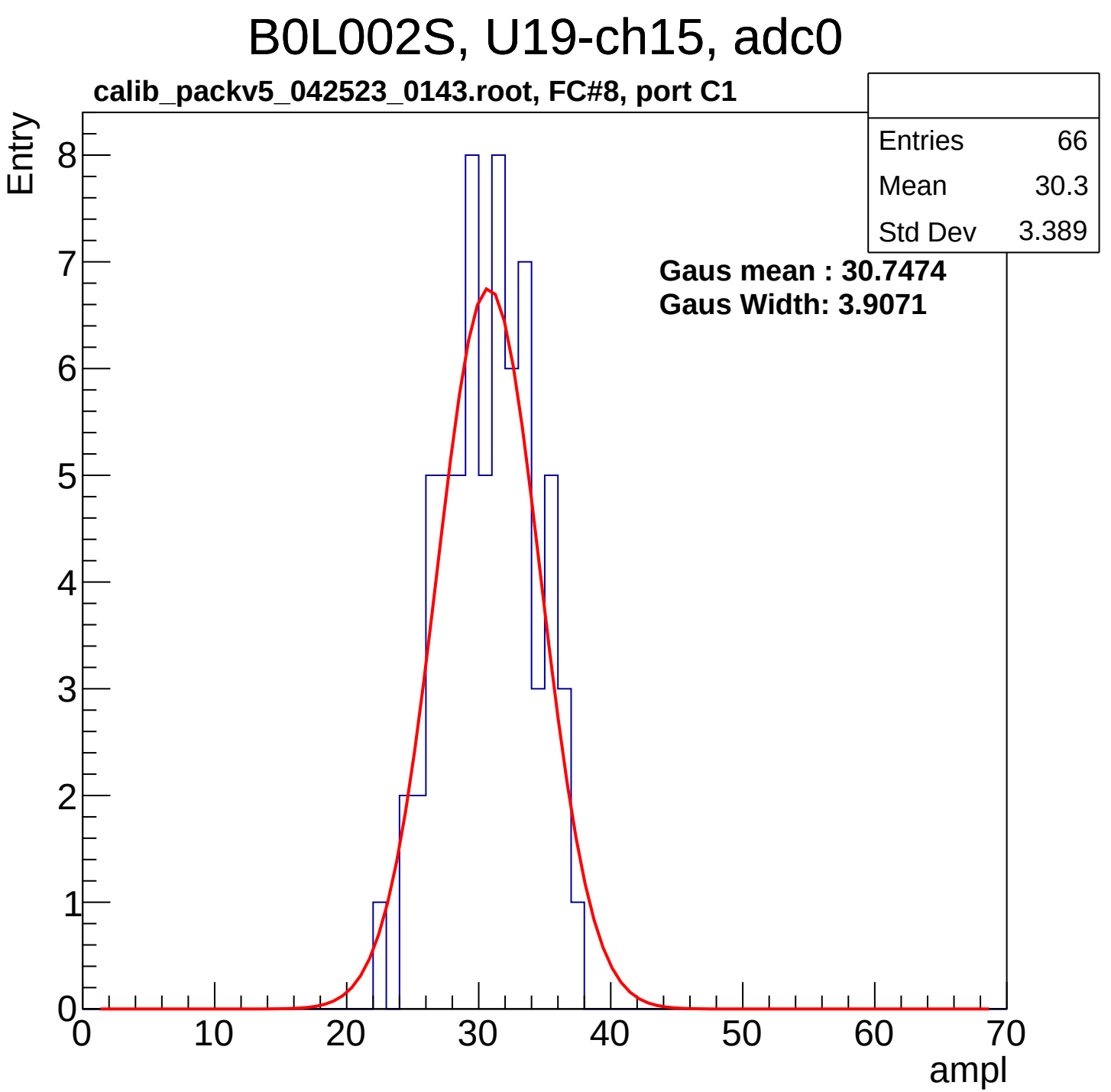

B0L002S, U19-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 48.28
Std Dev 3.034
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

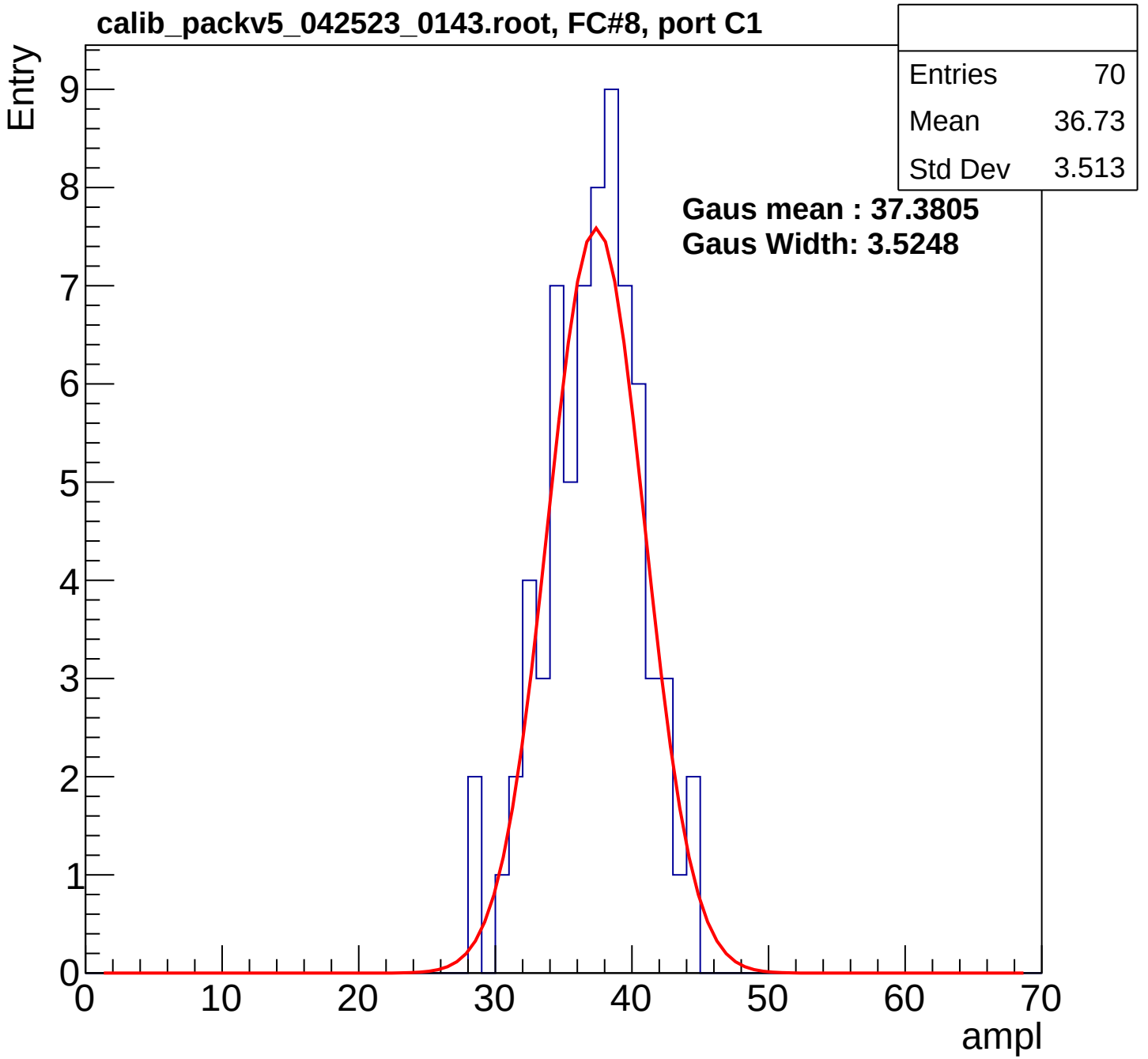

B0L002S, U19-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 54.45
Std Dev 2.996
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

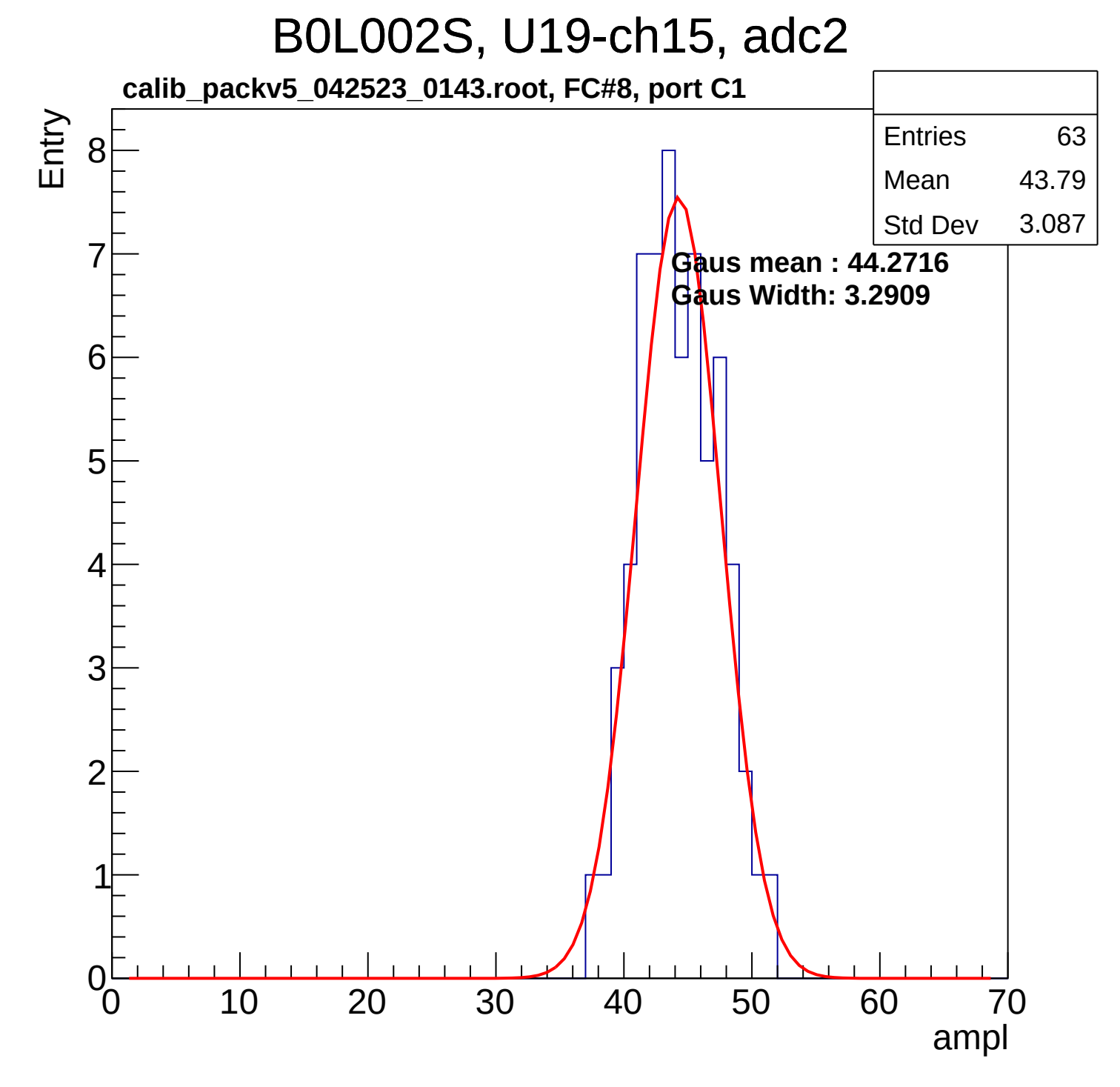

B0L002S, U19-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.96
Std Dev 8.72
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

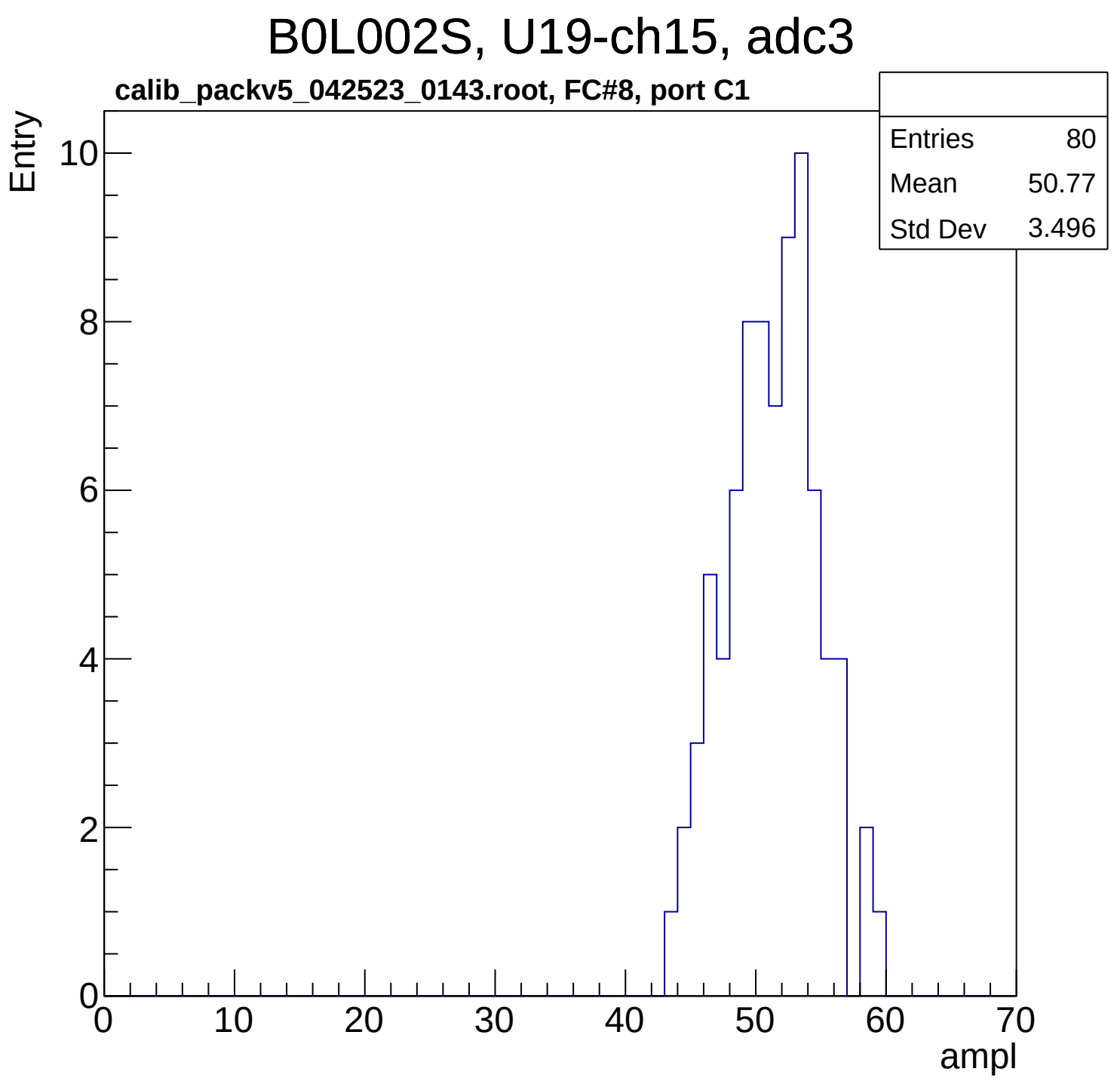

B0L002S, U19-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

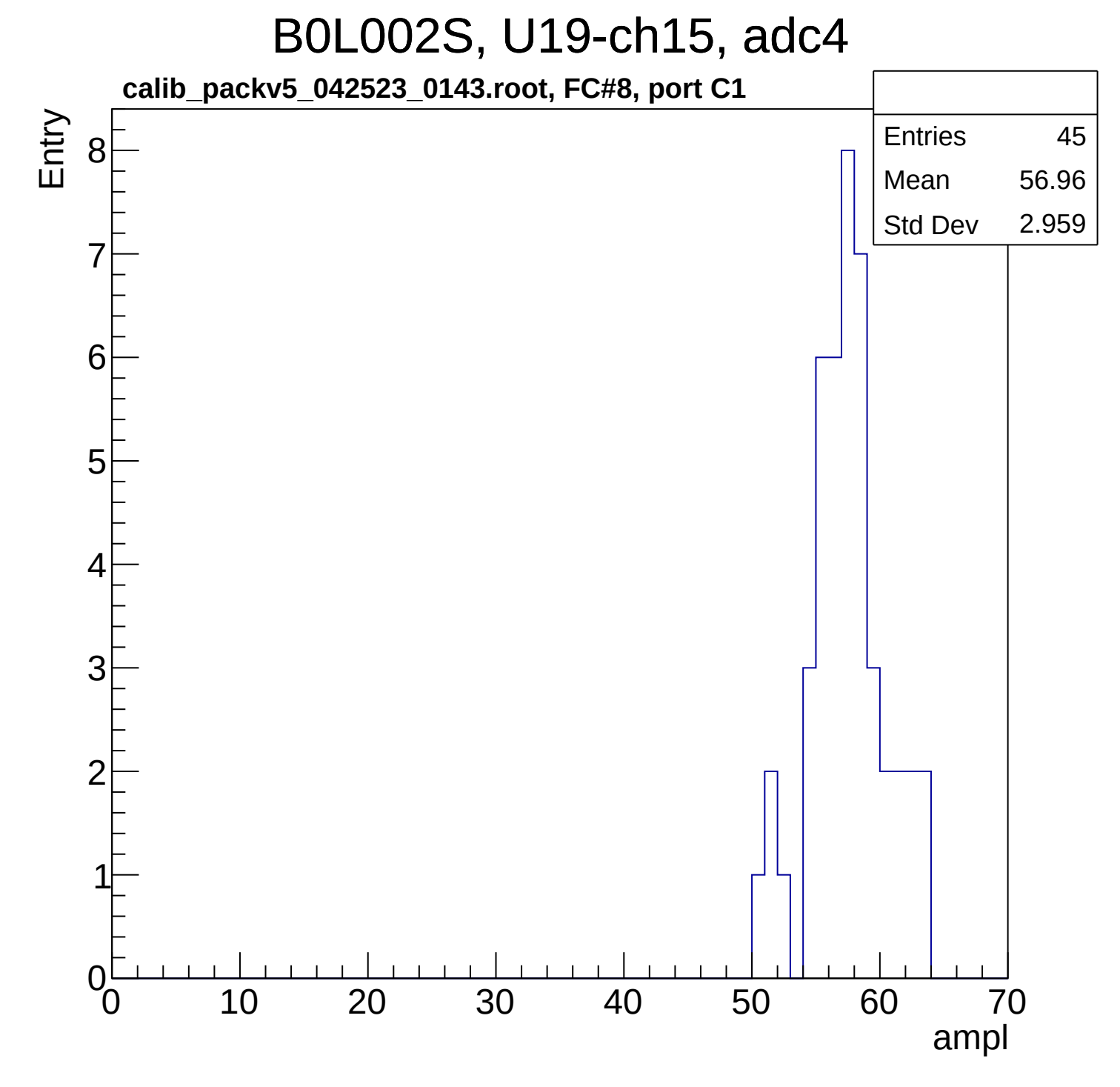

B0L002S, U19-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

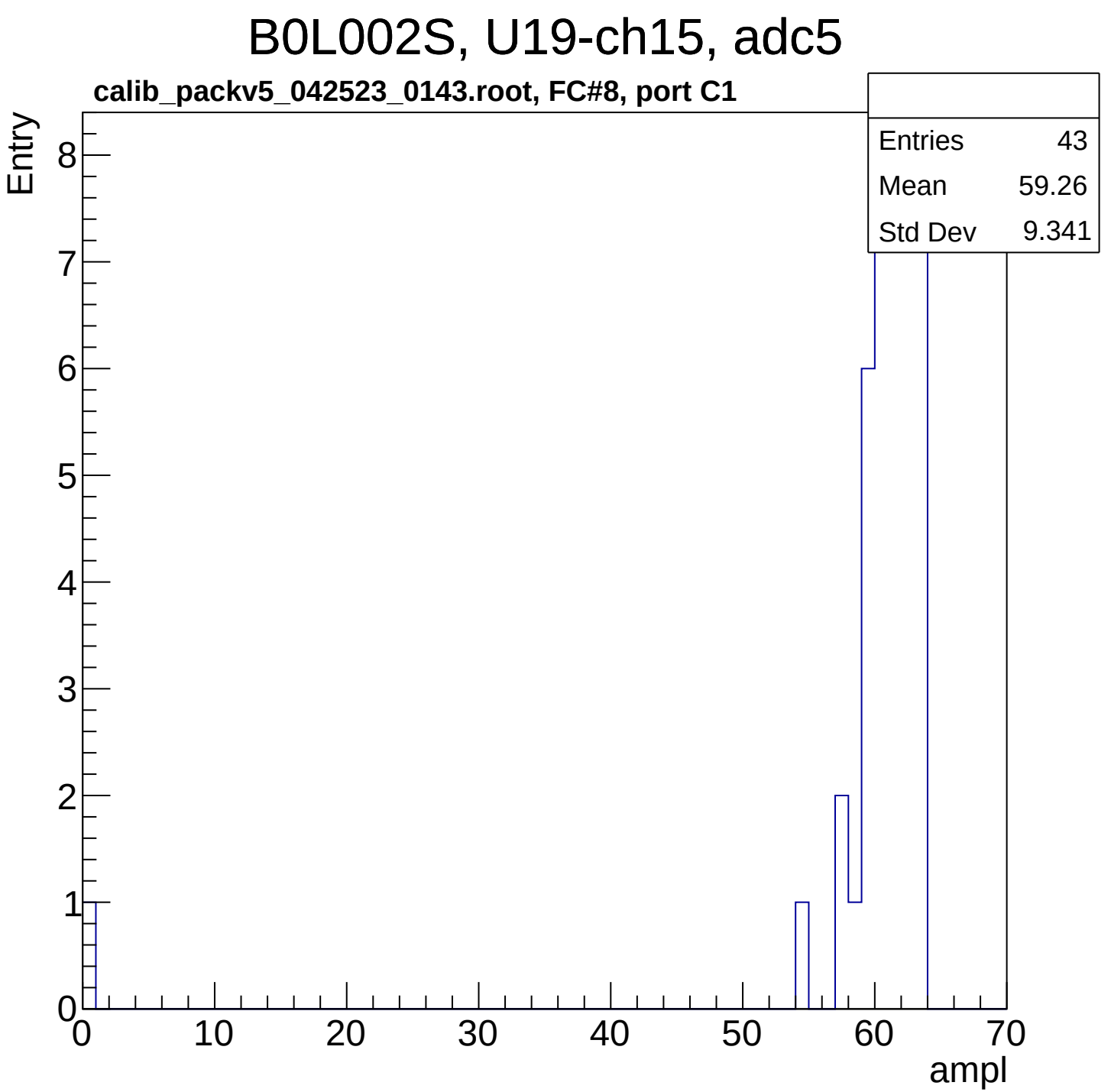

B0L002S, U19-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.3
Std Dev 3.389
Gaus mean : 30.7474
Gaus Width: 3.9071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

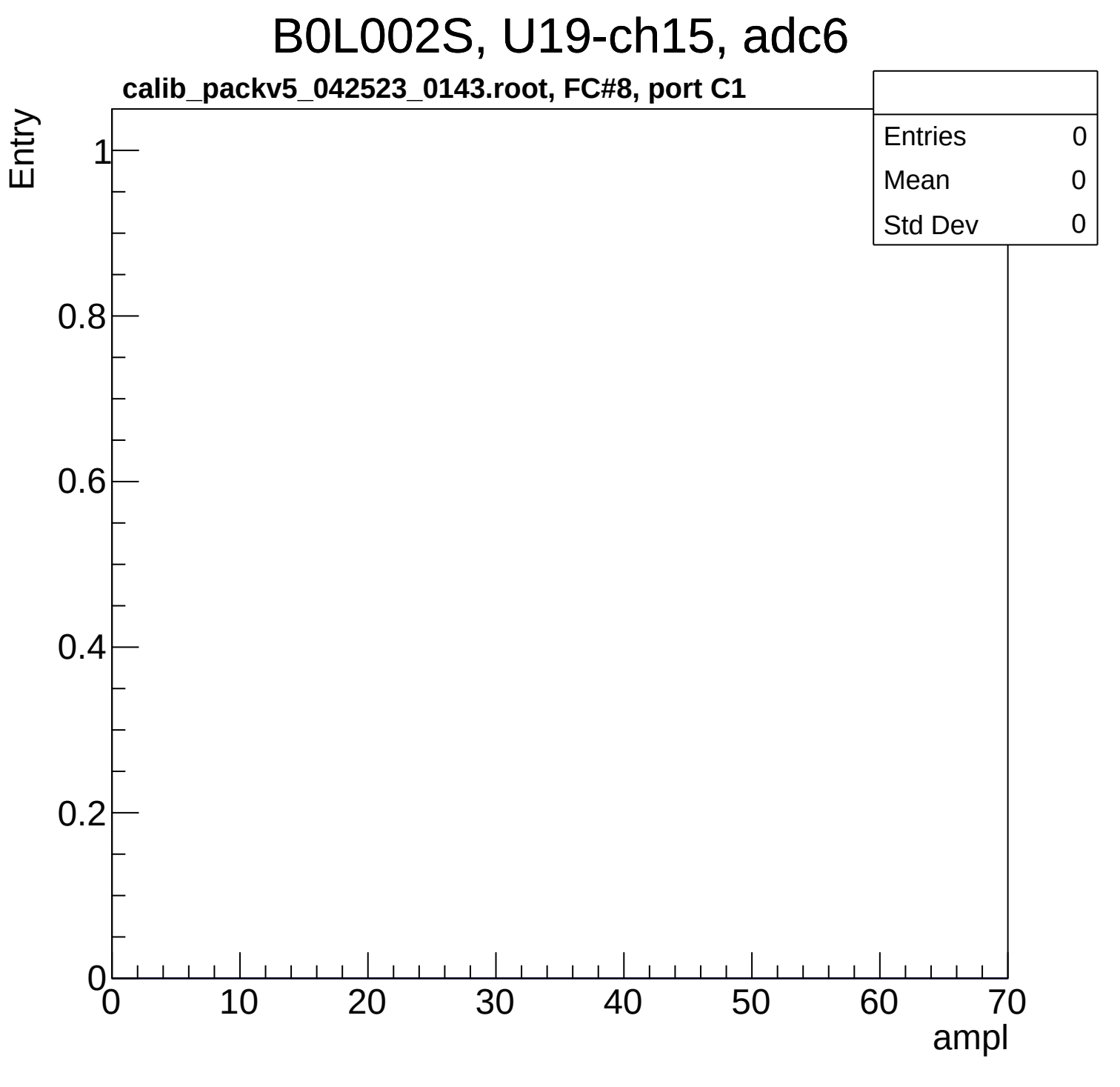

B0L002S, U19-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.73
Std Dev 3.513
Gaus mean : 37.3805
Gaus Width: 3.5248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

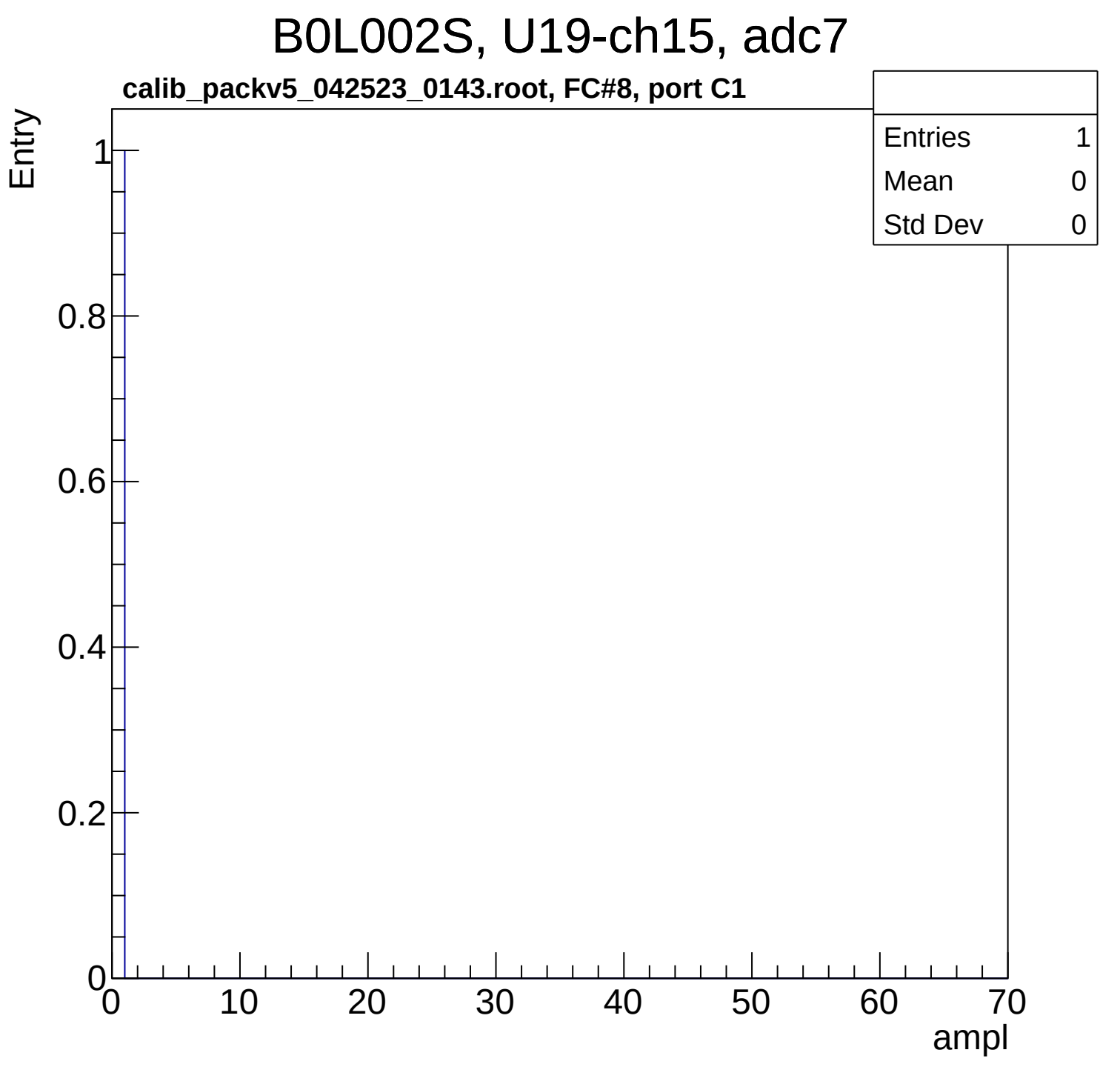

B0L002S, U19-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 43.79
Std Dev 3.087
Gaus mean : 44.2716
Gaus Width: 3.2909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

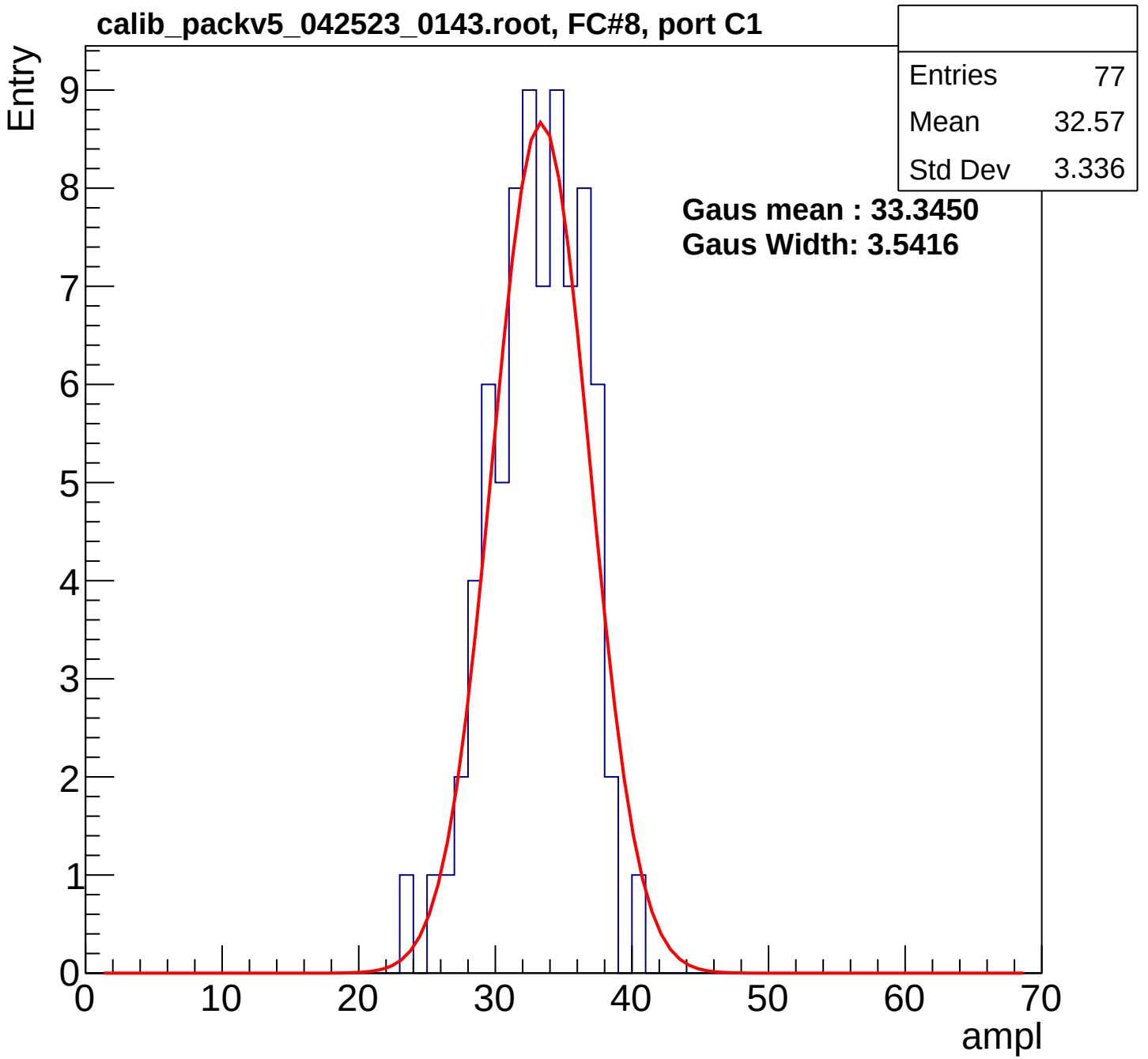

B0L002S, U19-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 50.77
Std Dev 3.496
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

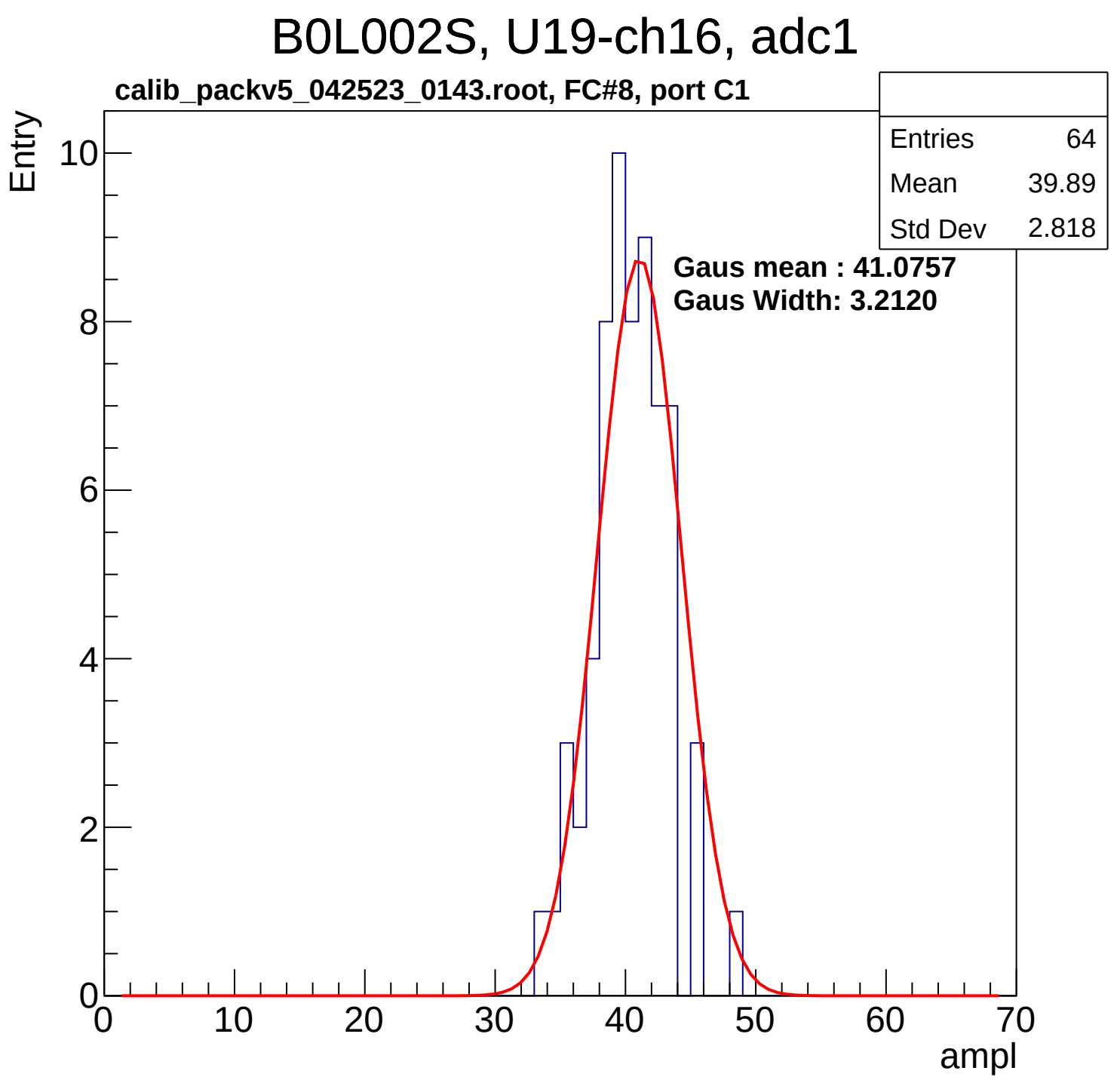

B0L002S, U19-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.96
Std Dev 2.959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

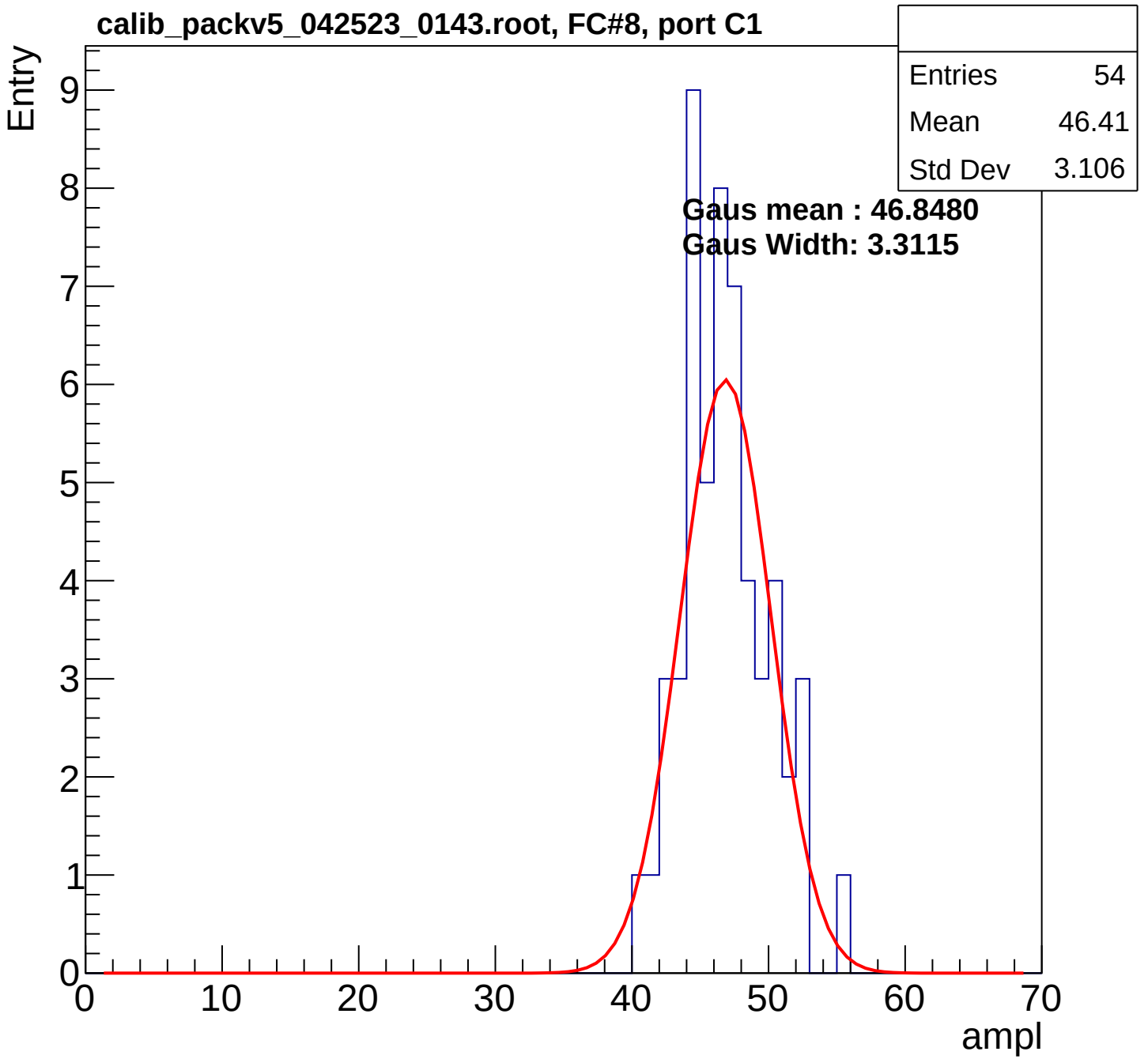

B0L002S, U19-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.26
Std Dev 9.341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

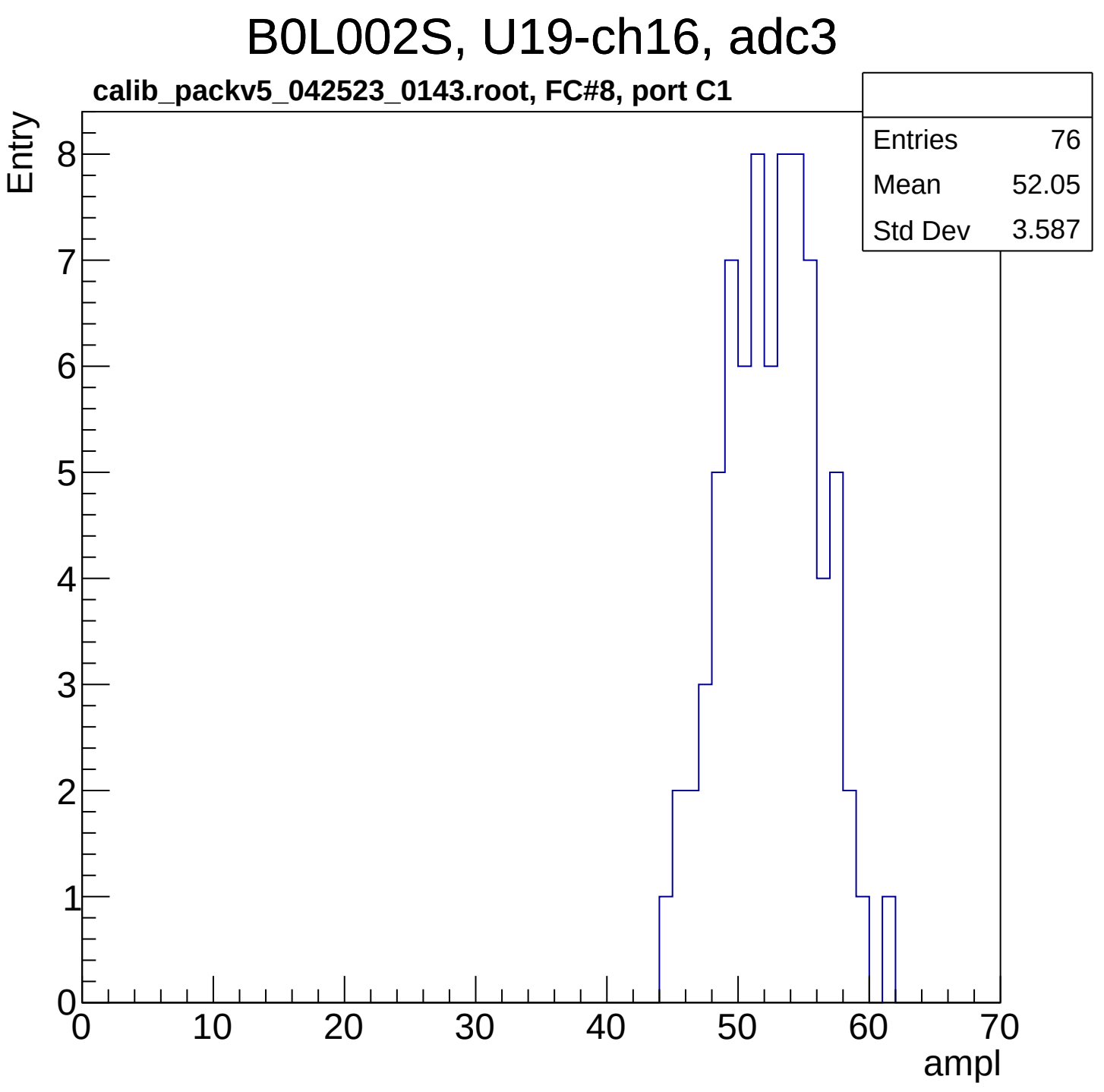

B0L002S, U19-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

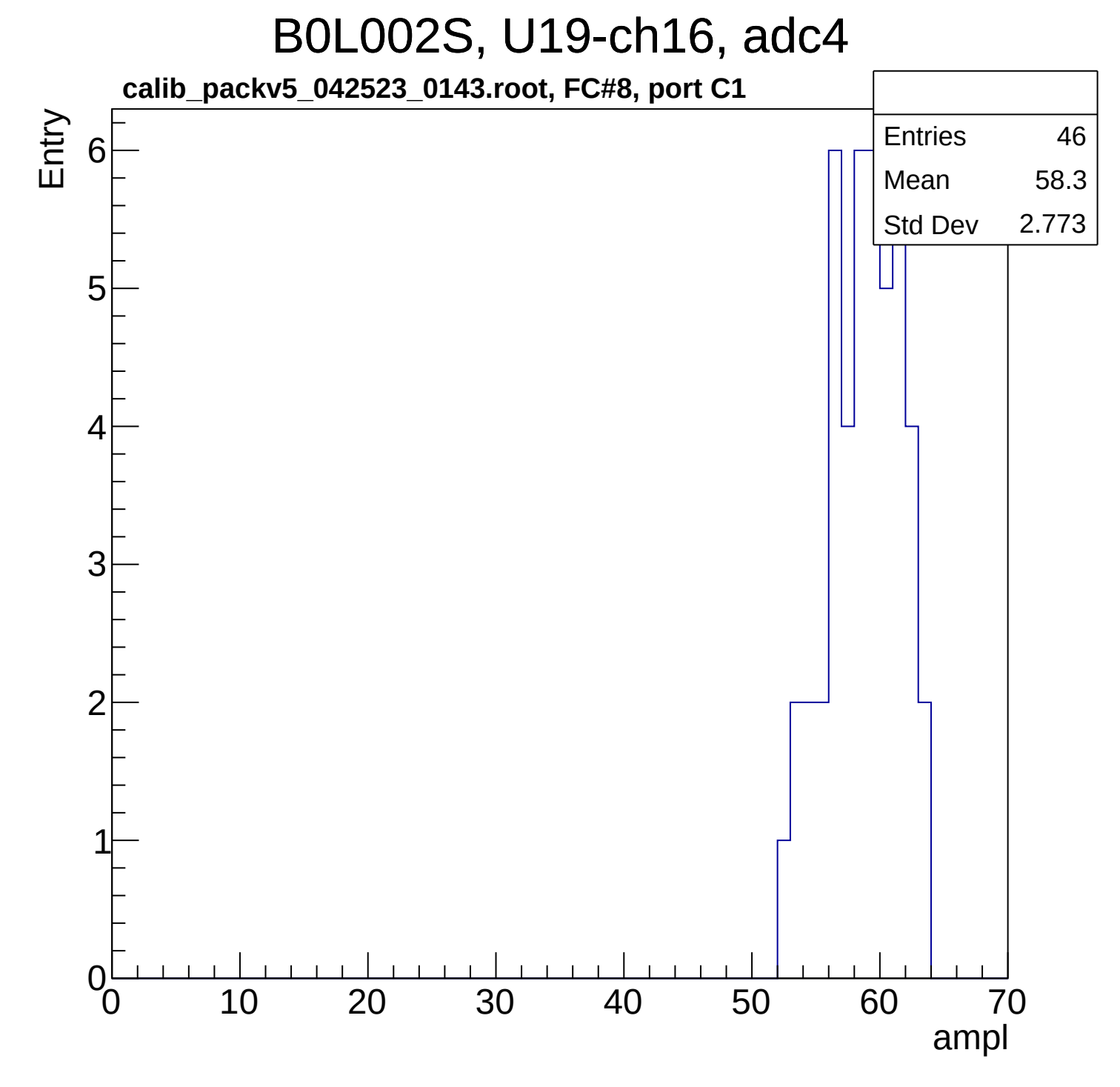

B0L002S, U19-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

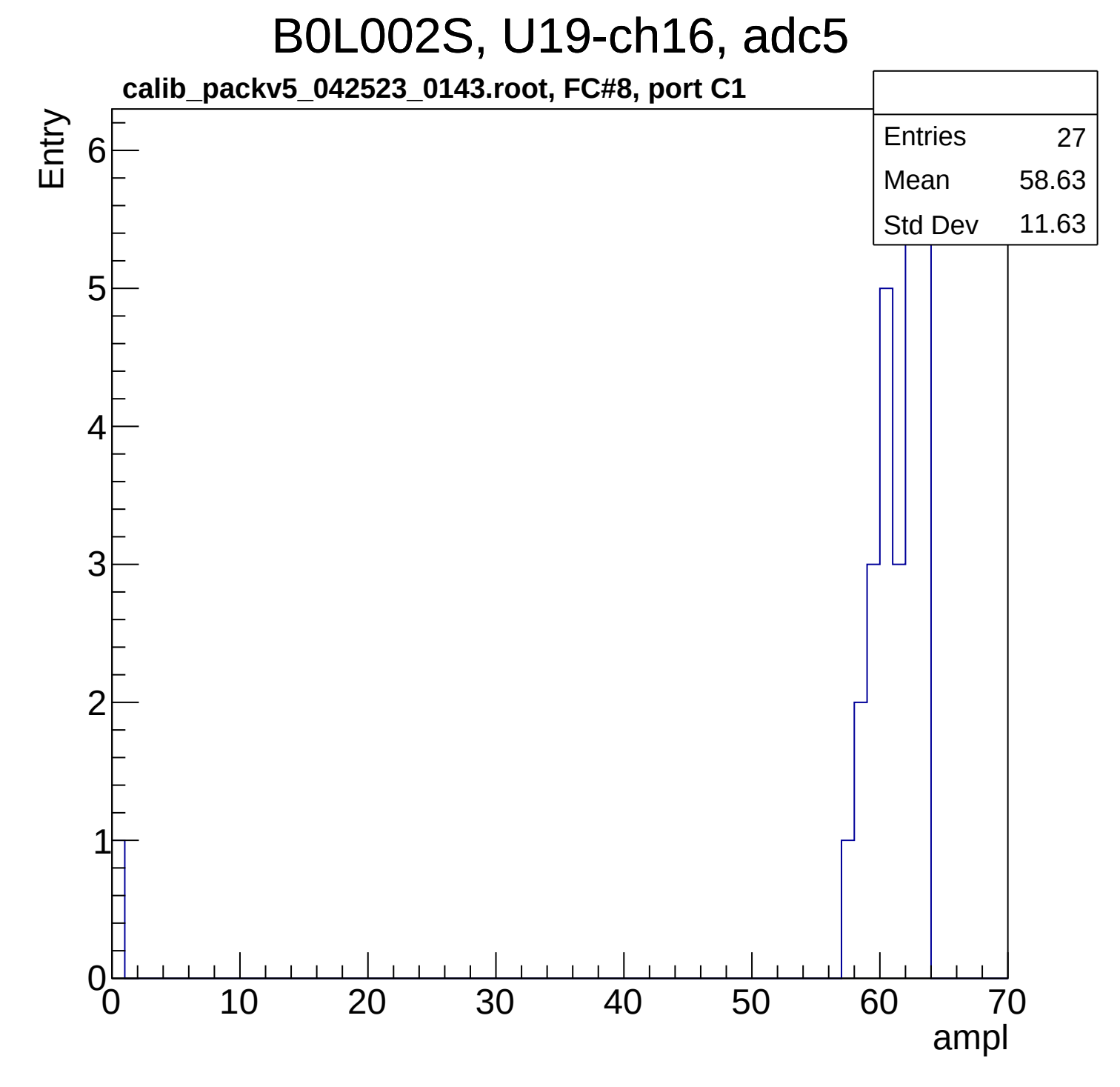

B0L002S, U19-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 32.57
Std Dev 3.336
Gaus mean : 33.3450
Gaus Width: 3.5416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

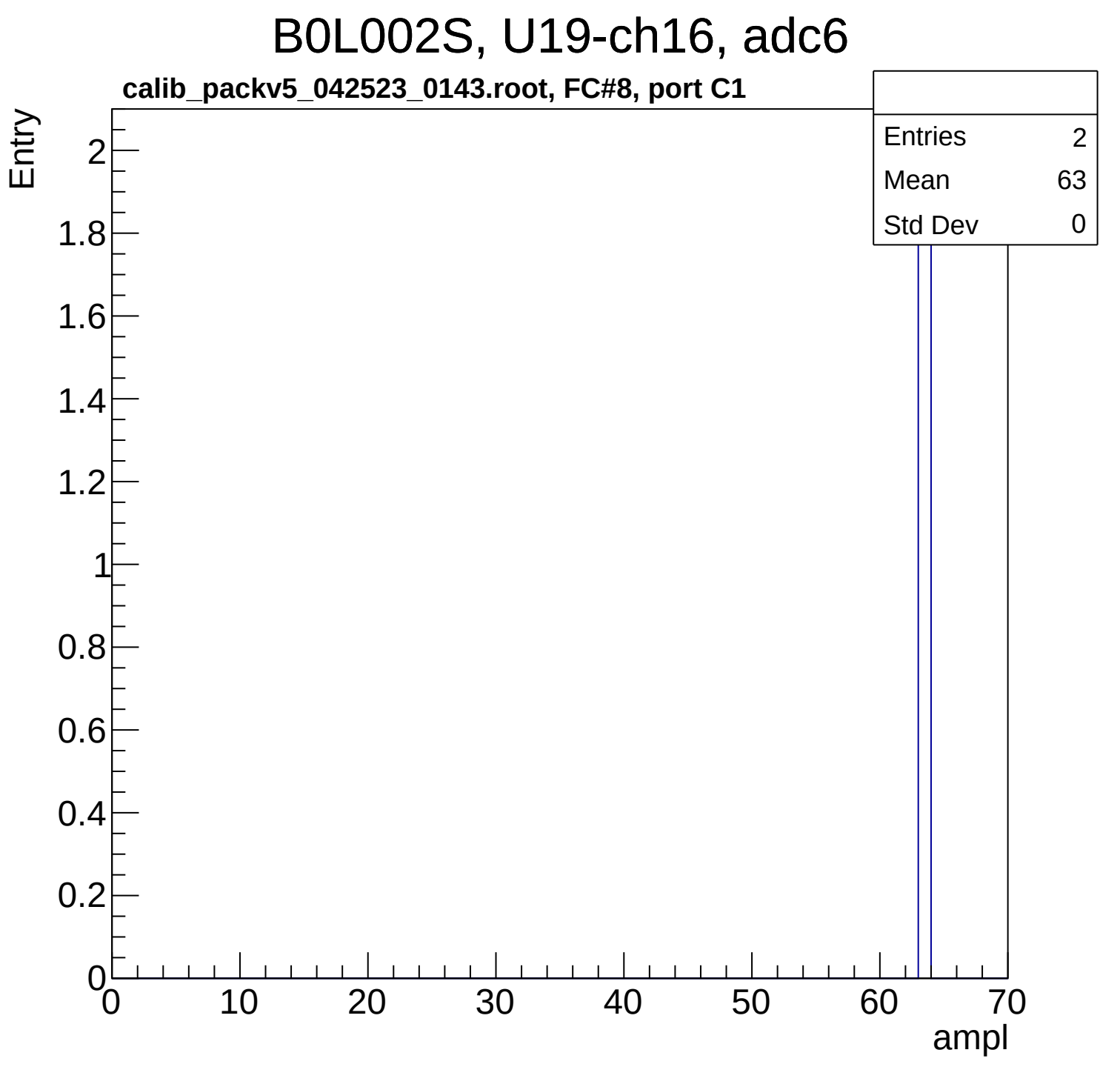

B0L002S, U19-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 39.89
Std Dev 2.818
Gaus mean : 41.0757
Gaus Width: 3.2120
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

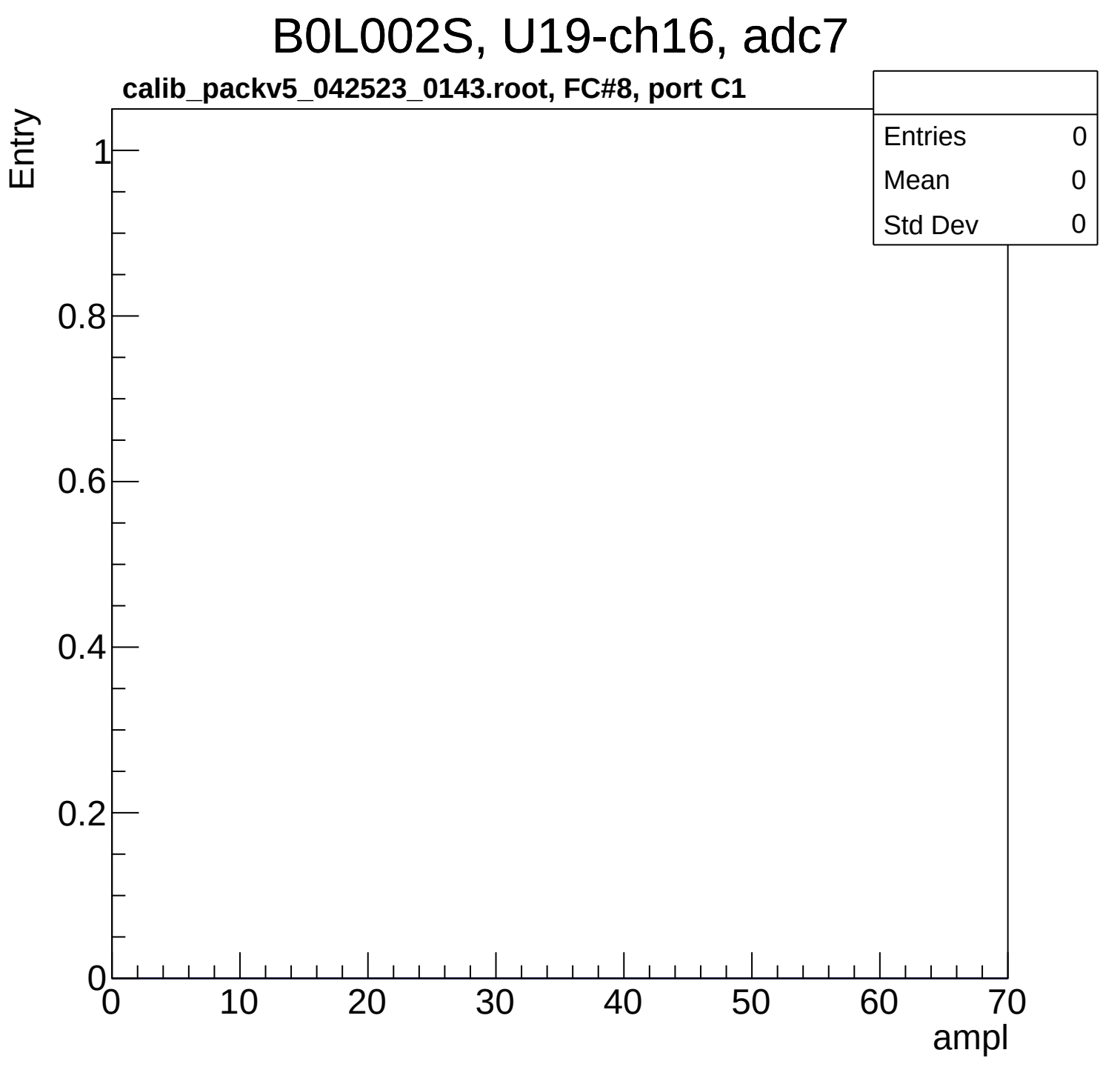

B0L002S, U19-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 46.41
Std Dev 3.106
Gaus mean : 46.8480
Gaus Width: 3.3115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

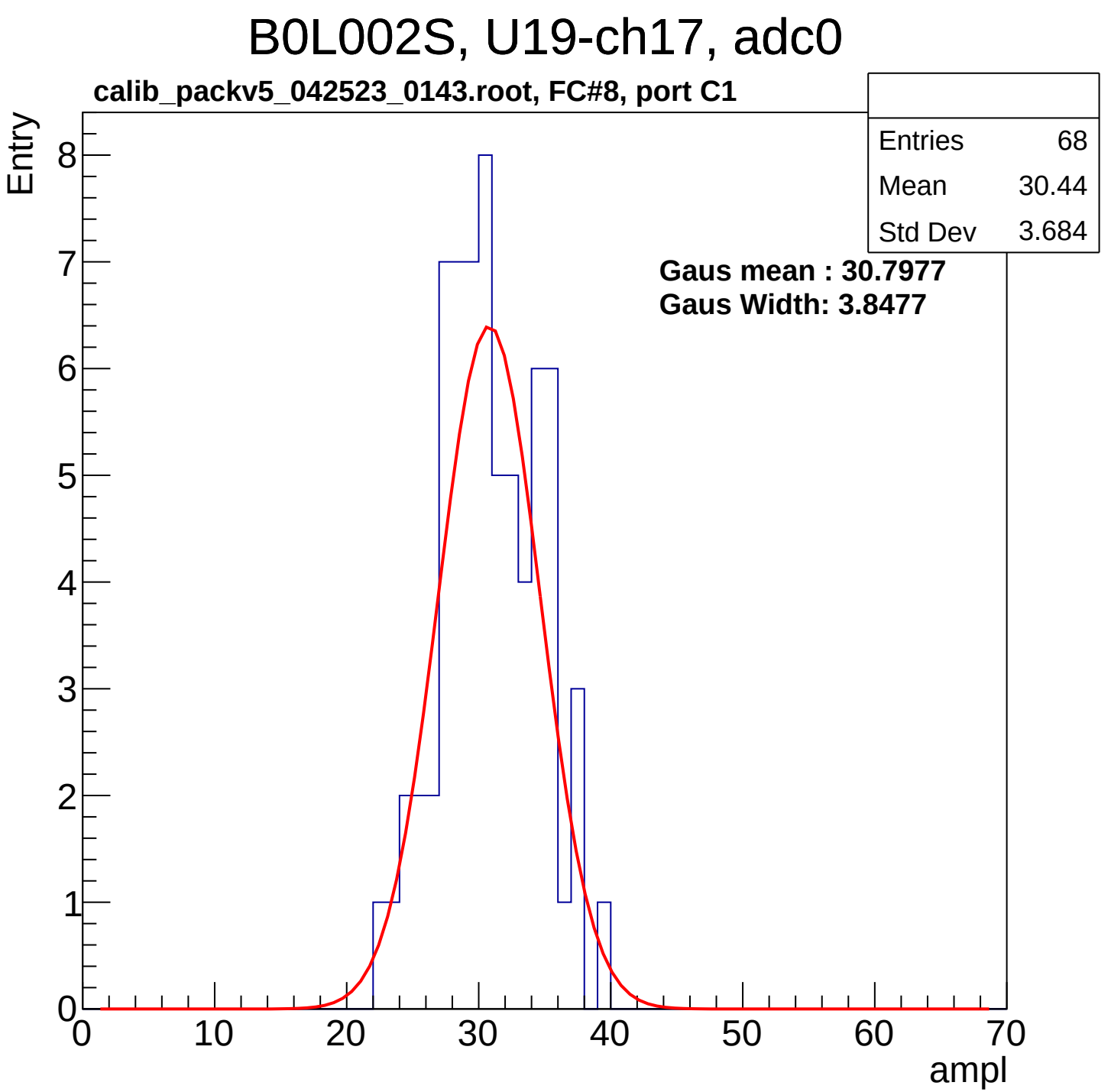

B0L002S, U19-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.05
Std Dev 3.587
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

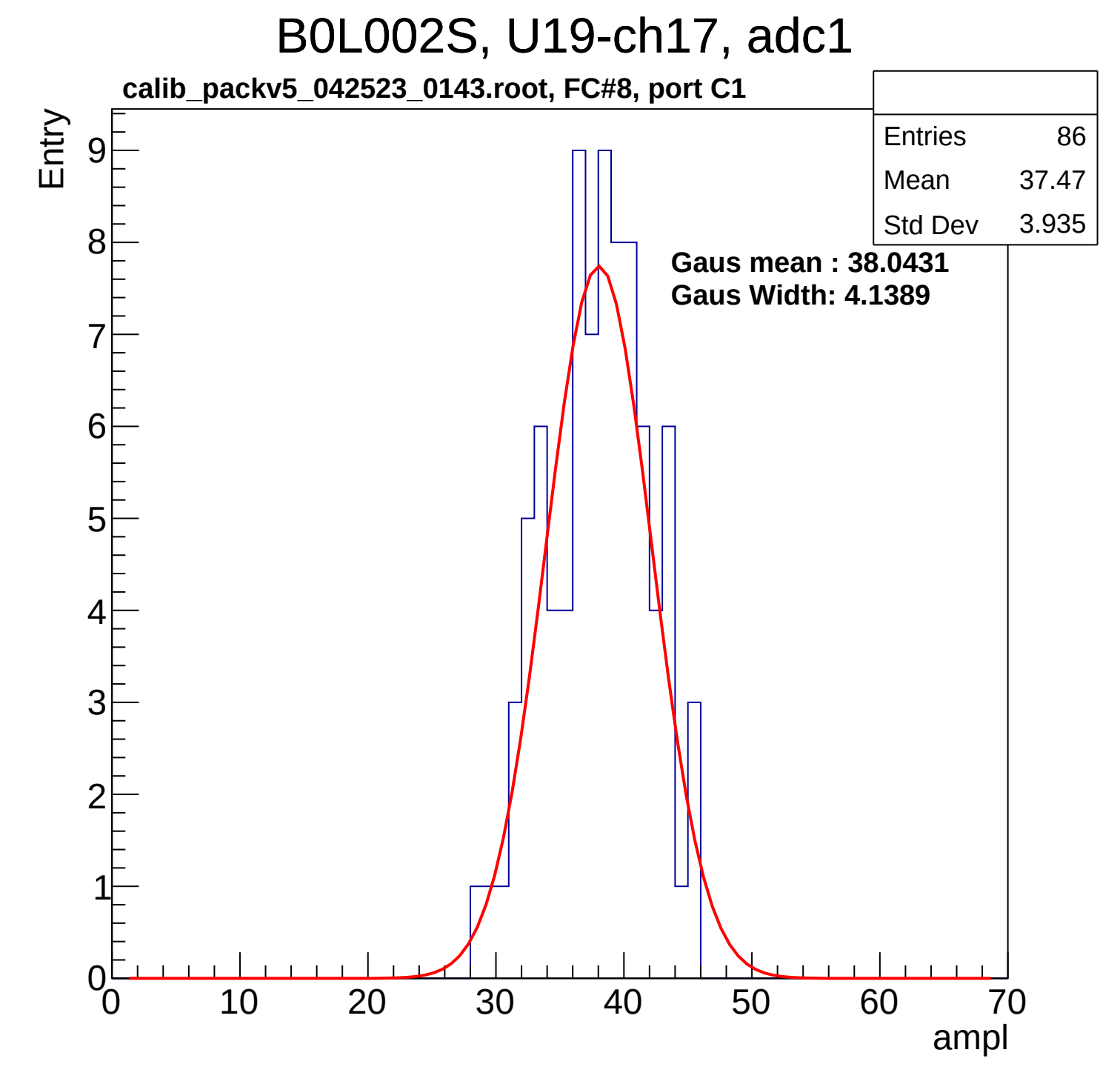

B0L002S, U19-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 58.3
Std Dev 2.773
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

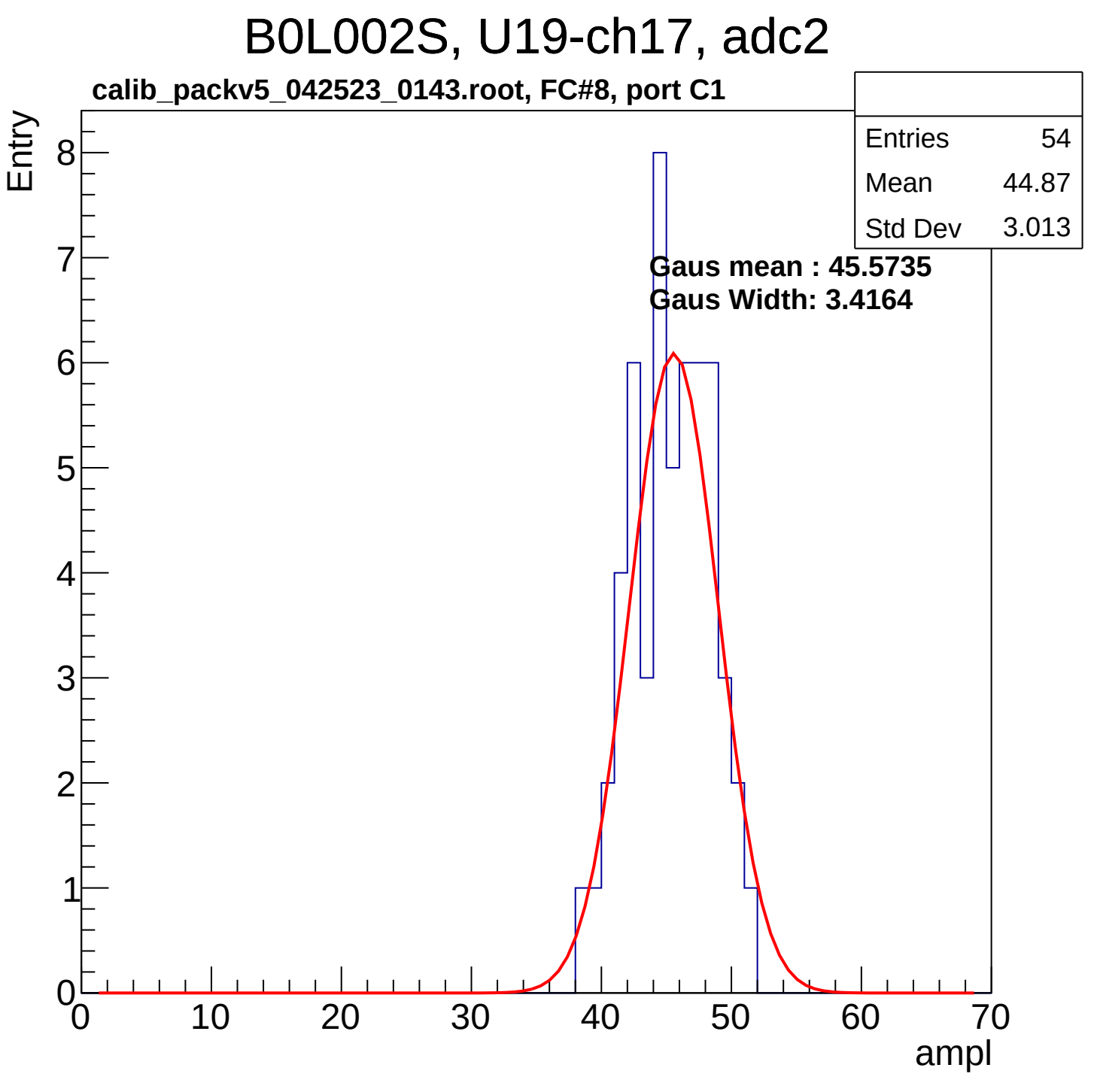

B0L002S, U19-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.63
Std Dev 11.63
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

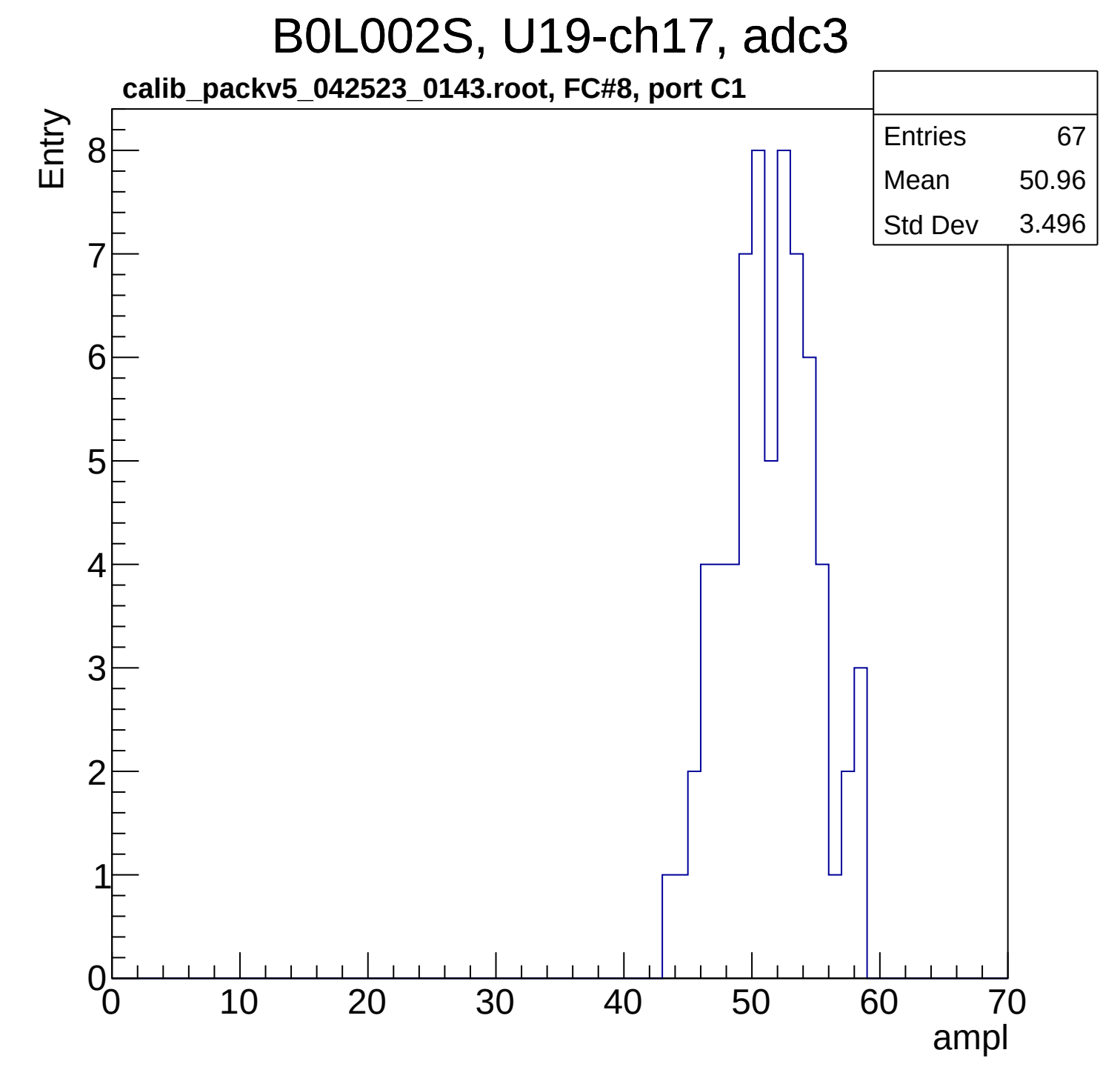

B0L002S, U19-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

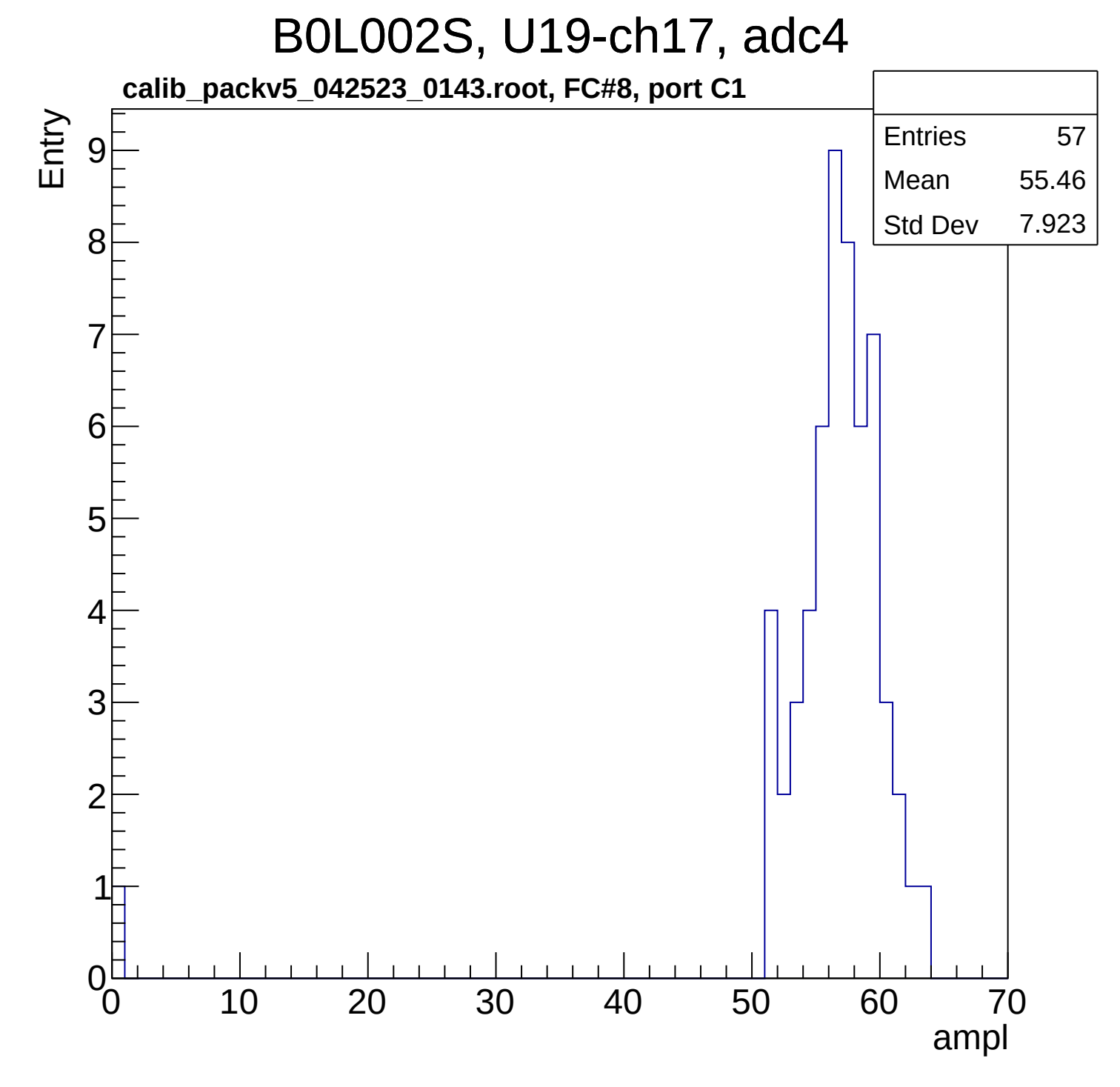

B0L002S, U19-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

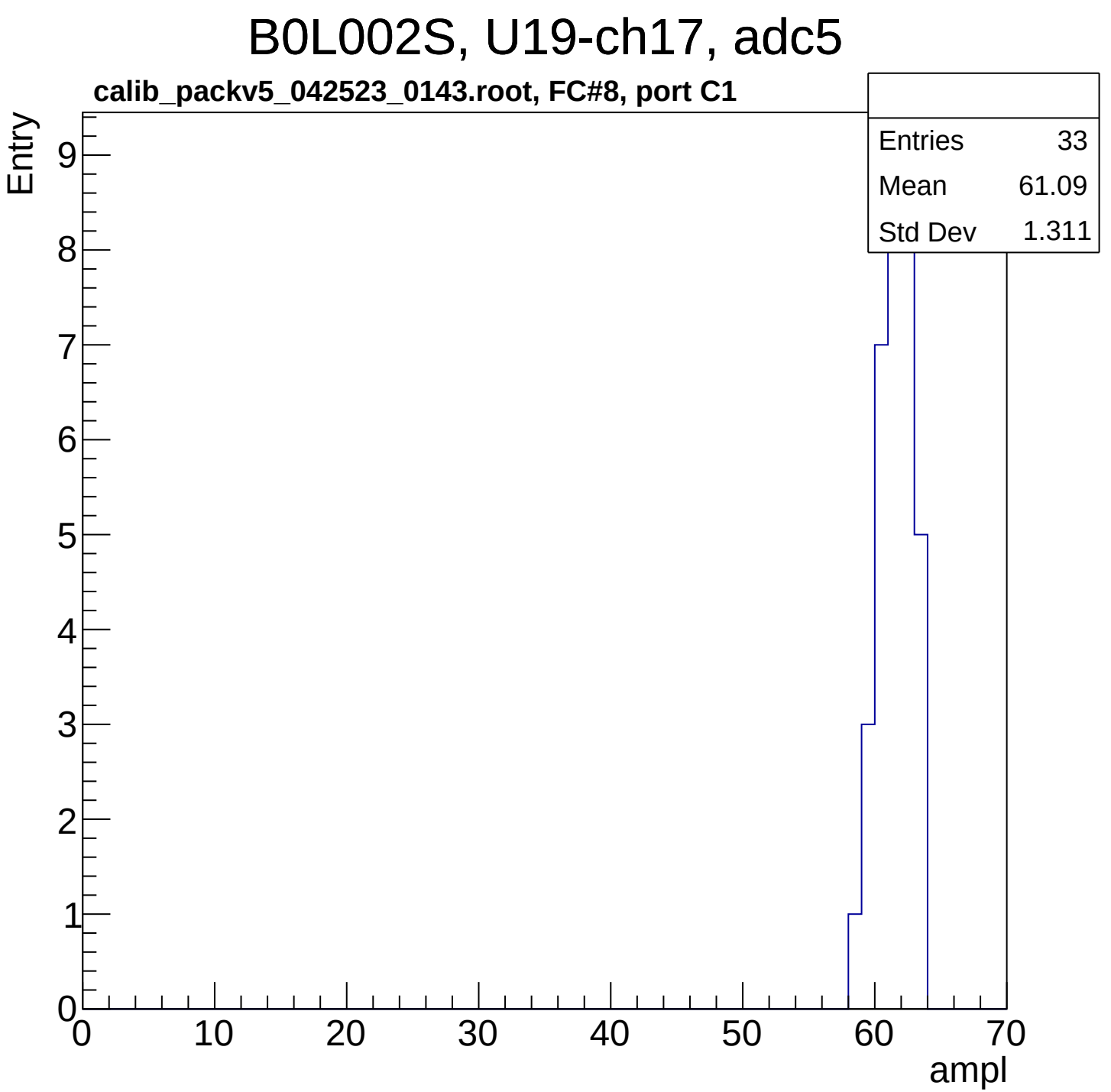

B0L002S, U19-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.44
Std Dev 3.684
Gaus mean : 30.7977
Gaus Width: 3.8477
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

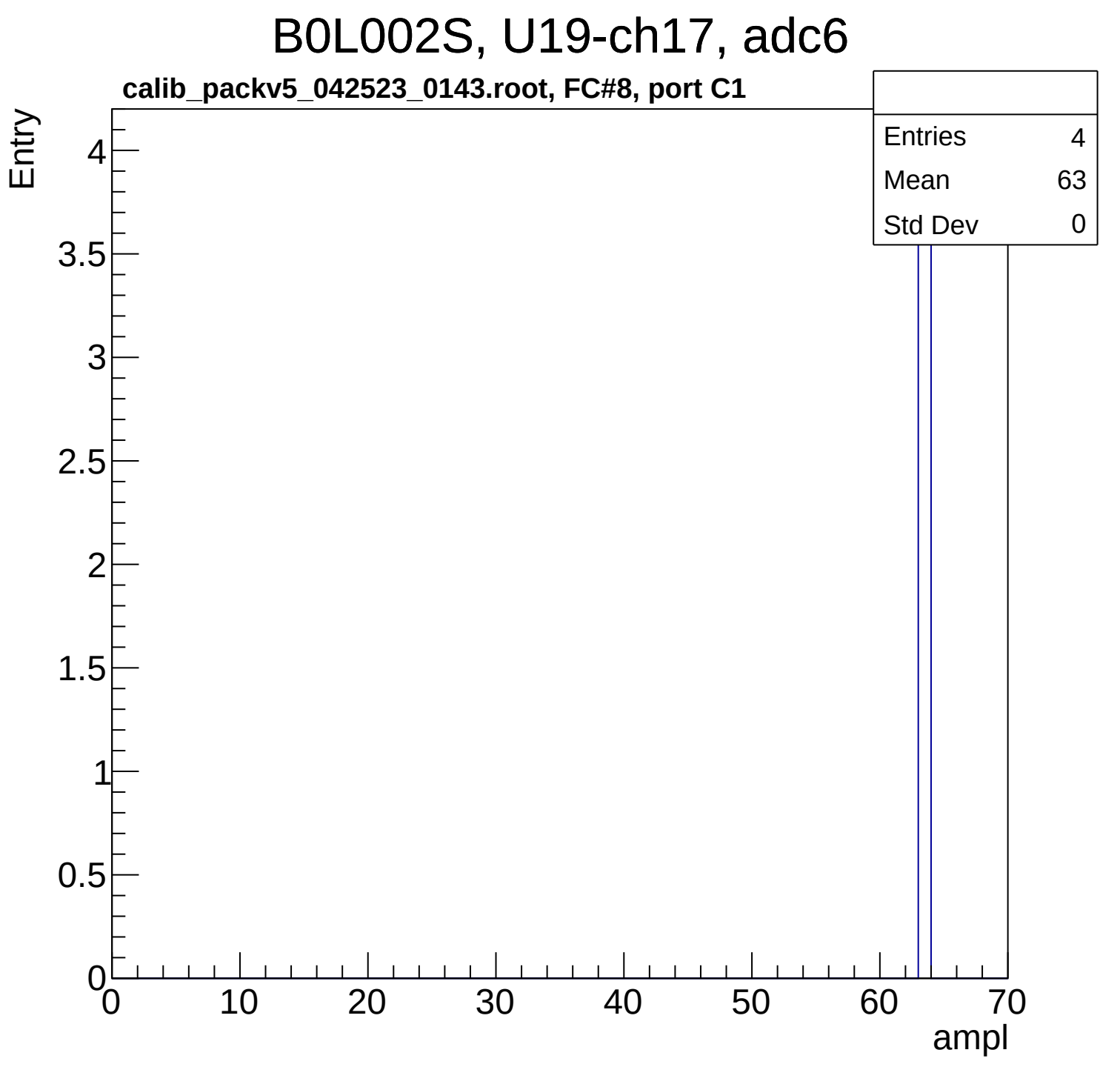

B0L002S, U19-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 37.47
Std Dev 3.935
Gaus mean : 38.0431
Gaus Width: 4.1389
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

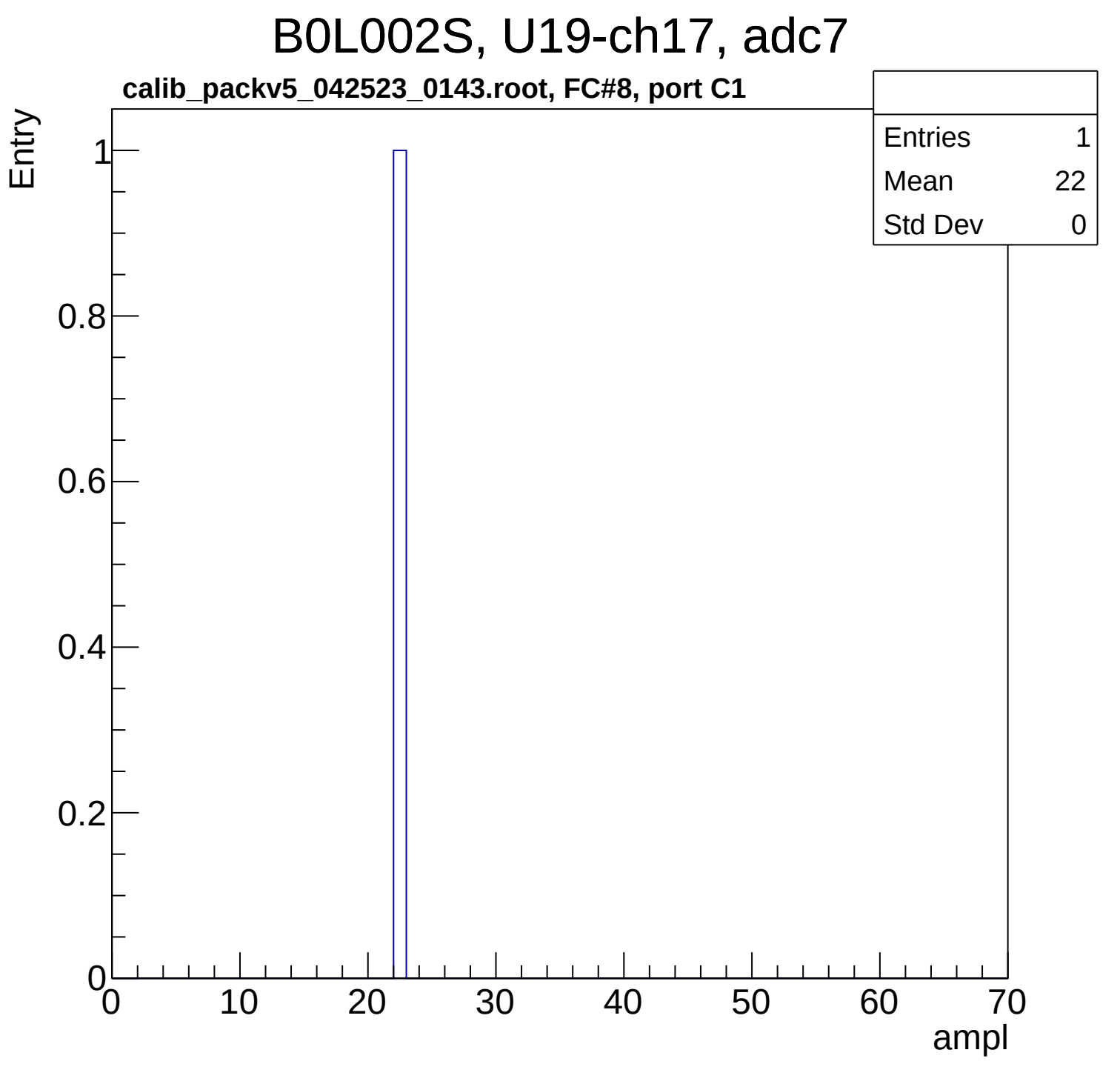

B0L002S, U19-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.87
Std Dev 3.013
Gaus mean : 45.5735
Gaus Width: 3.4164
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

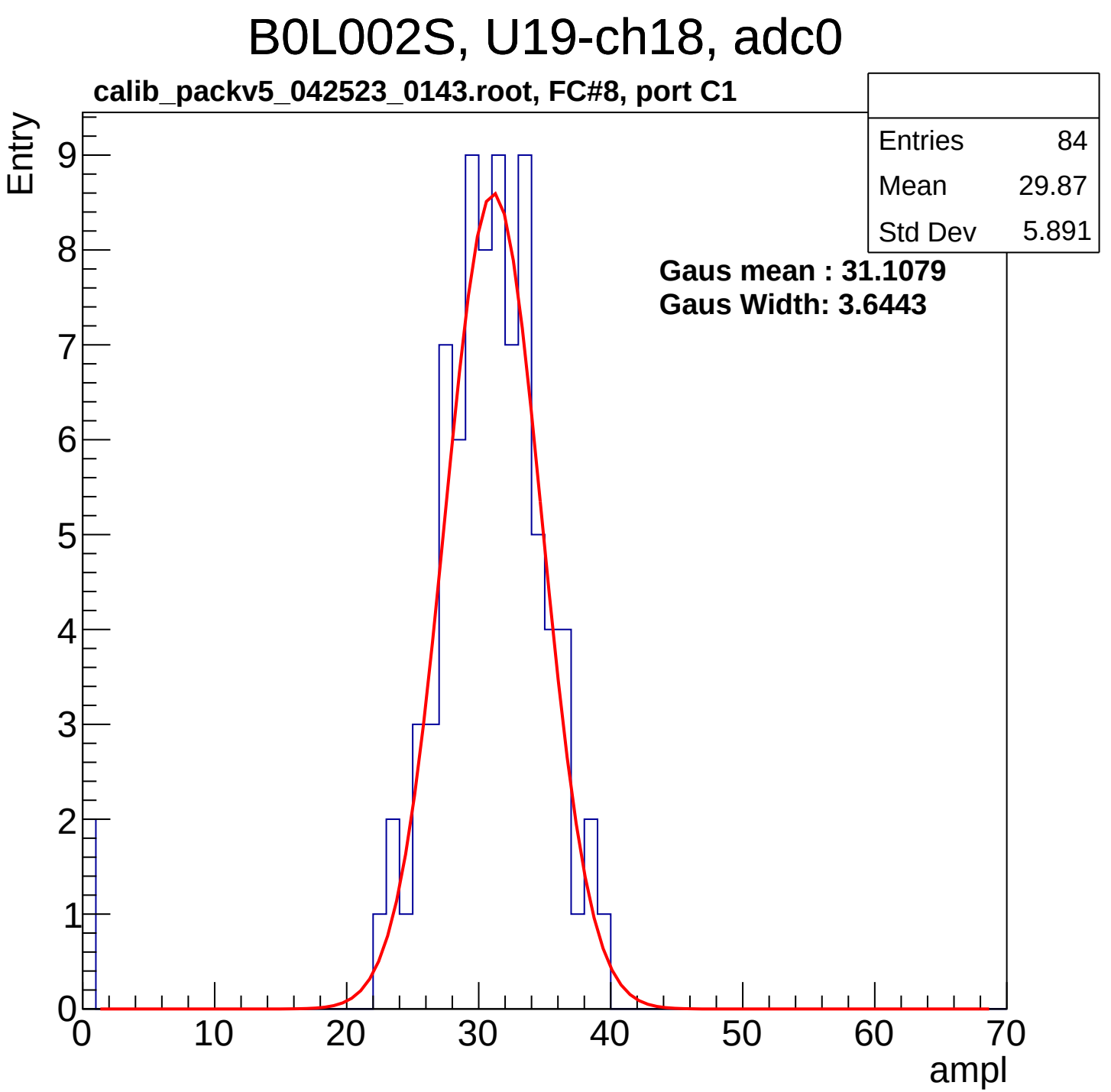

B0L002S, U19-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.96
Std Dev 3.496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

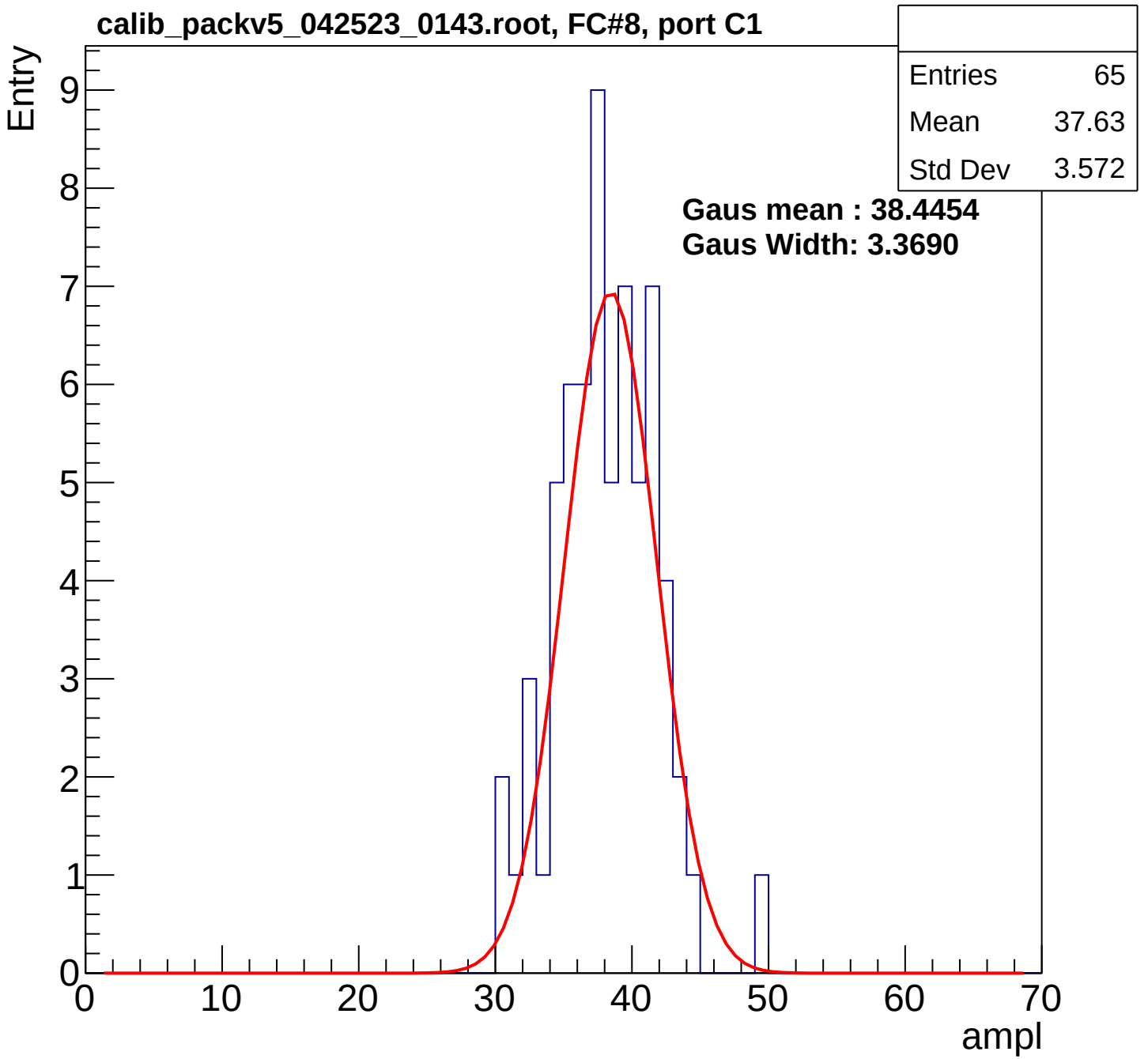

B0L002S, U19-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.46
Std Dev 7.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

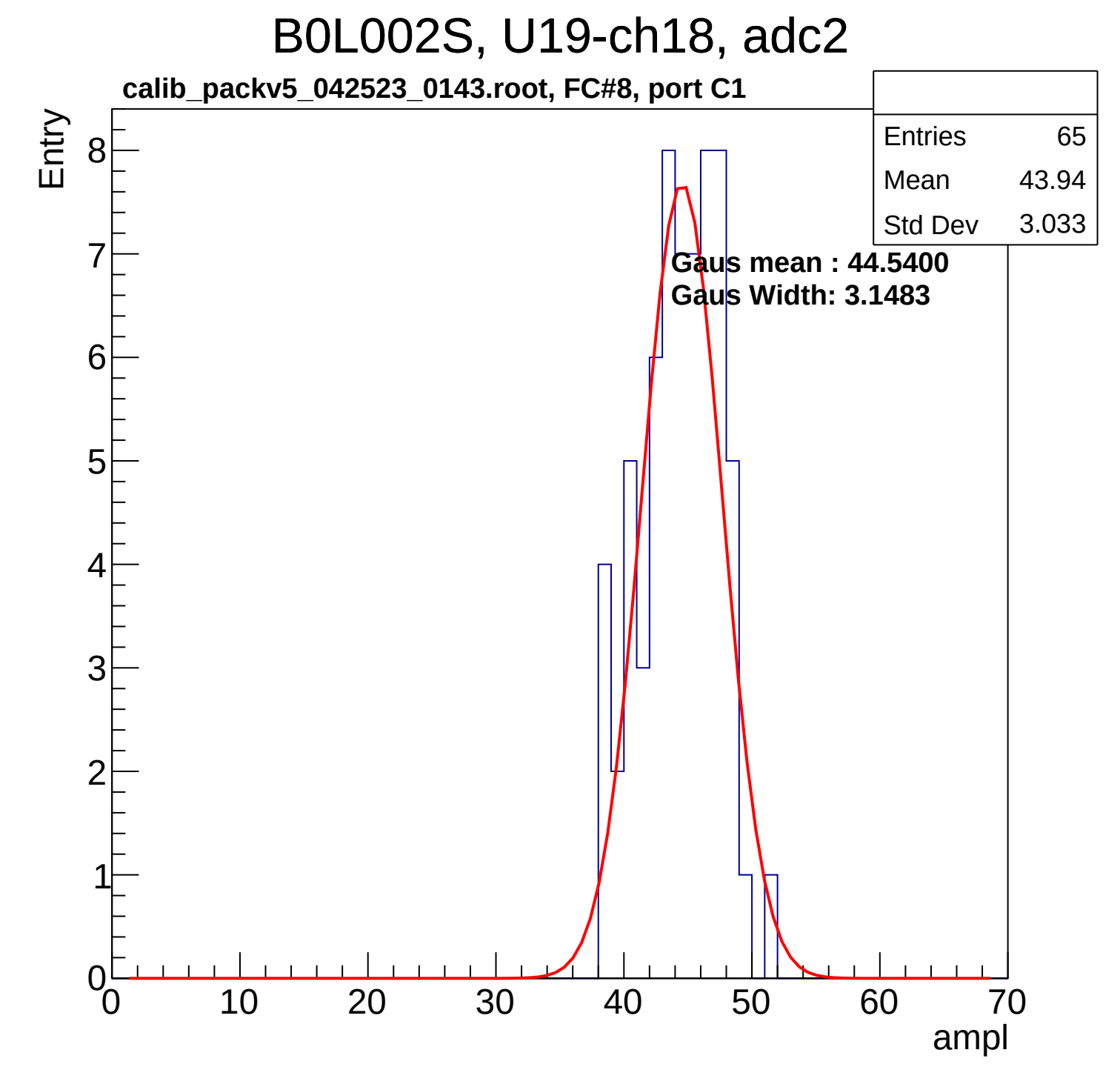

B0L002S, U19-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 61.09
Std Dev 1.311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

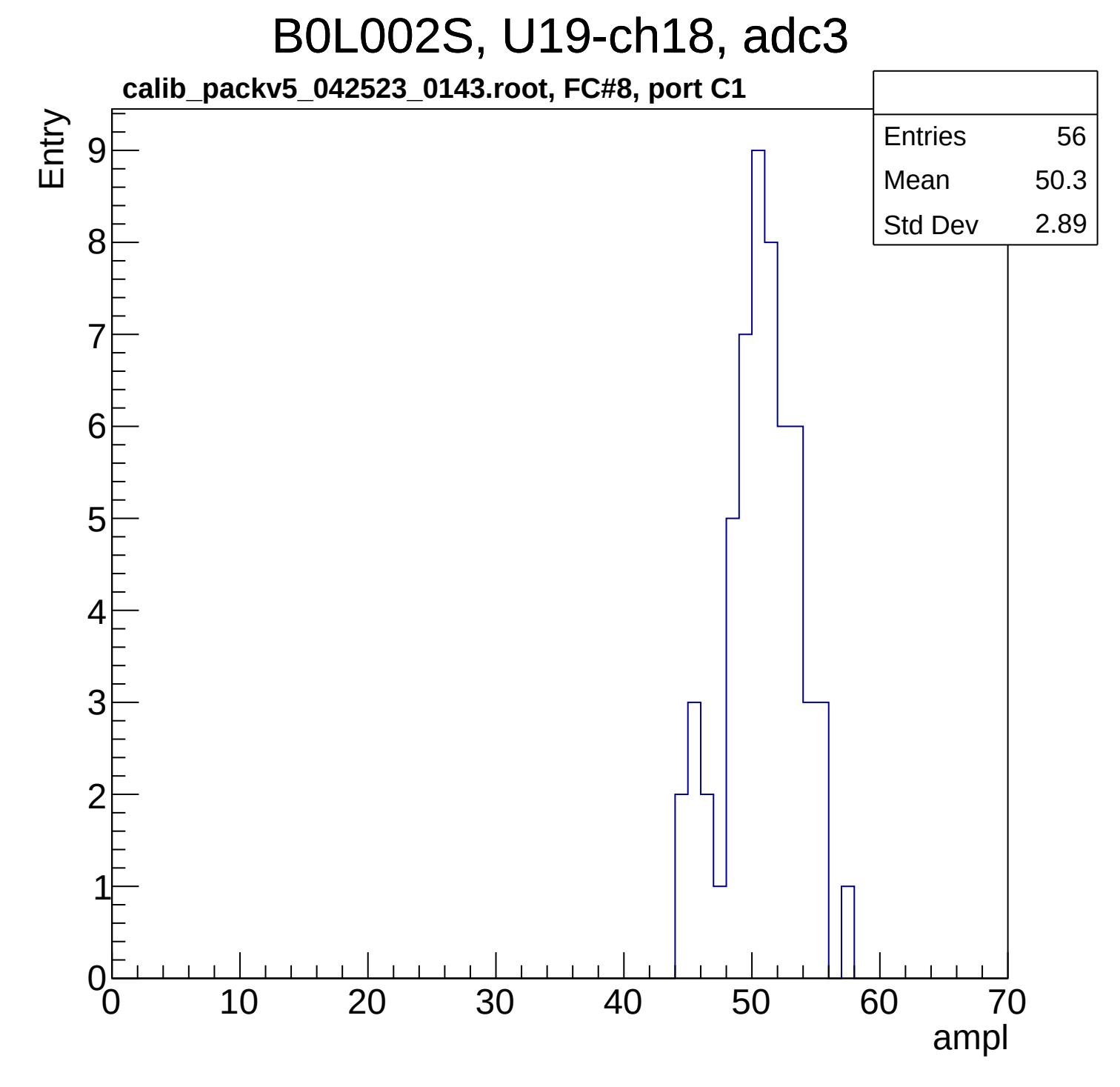

B0L002S, U19-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

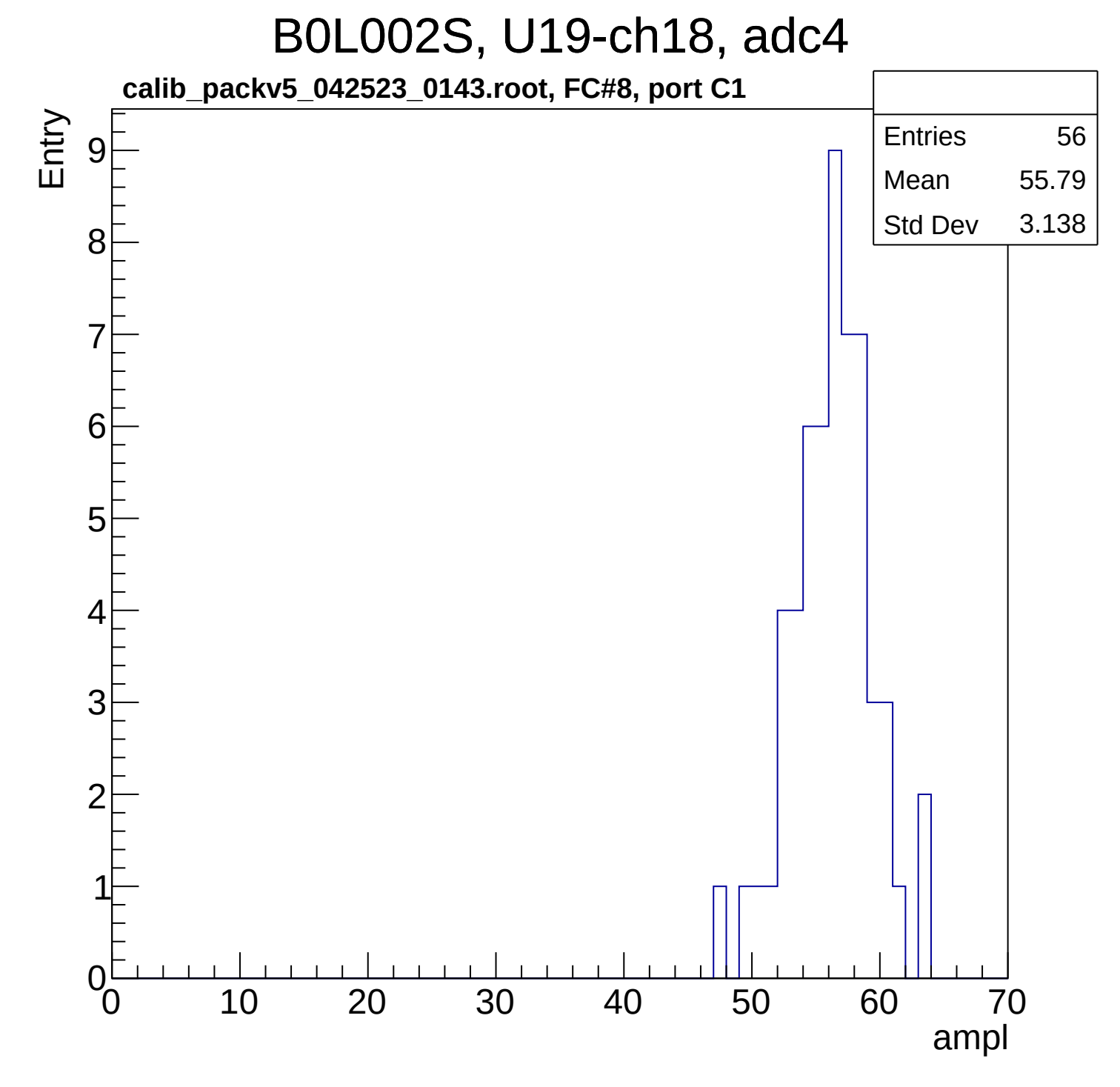

B0L002S, U19-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

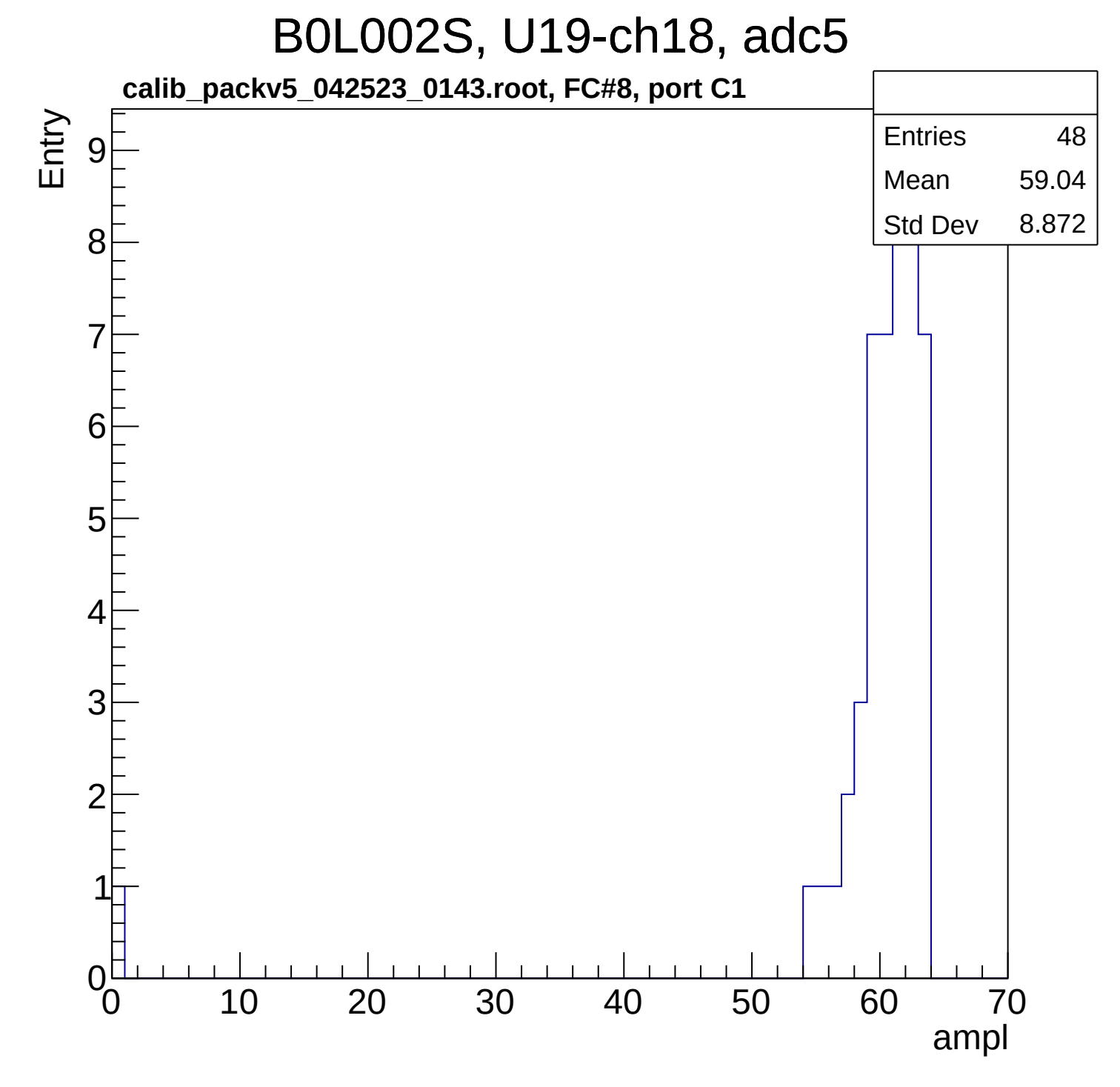

B0L002S, U19-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.87
Std Dev 5.891
Gaus mean : 31.1079
Gaus Width: 3.6443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

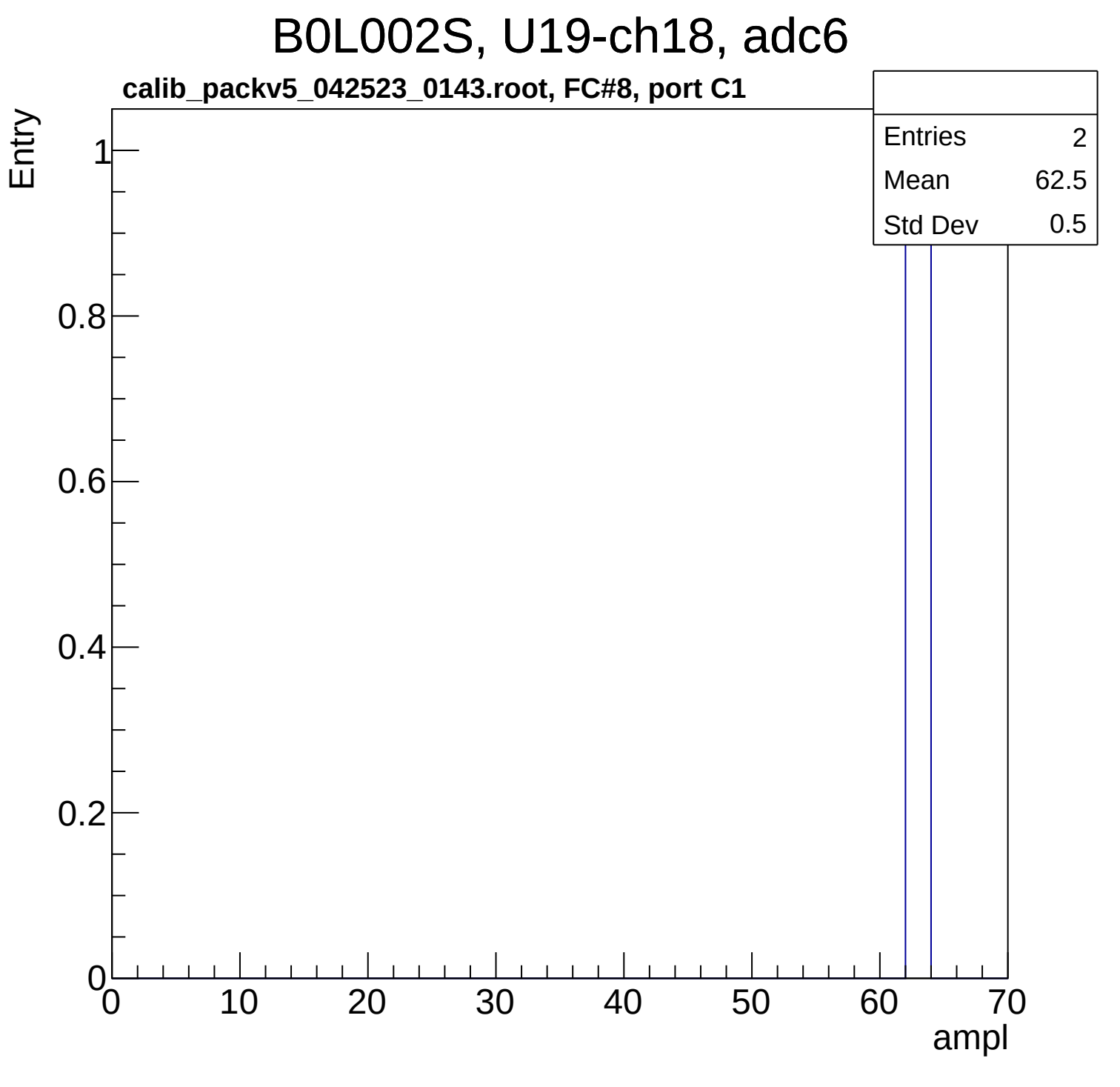

B0L002S, U19-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.63
Std Dev 3.572
Gaus mean : 38.4454
Gaus Width: 3.3690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

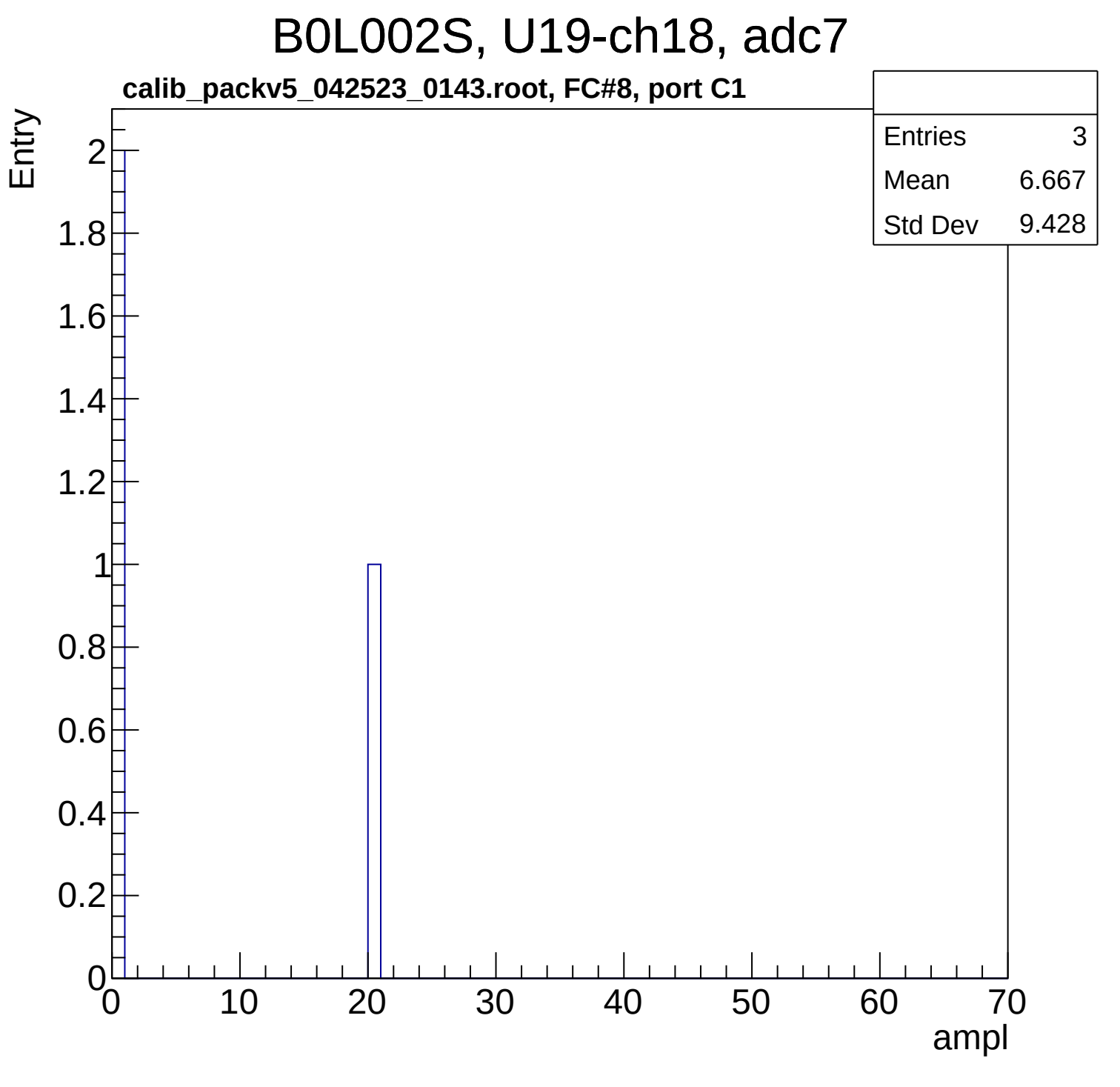

B0L002S, U19-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.94
Std Dev 3.033
Gaus mean : 44.5400
Gaus Width: 3.1483
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

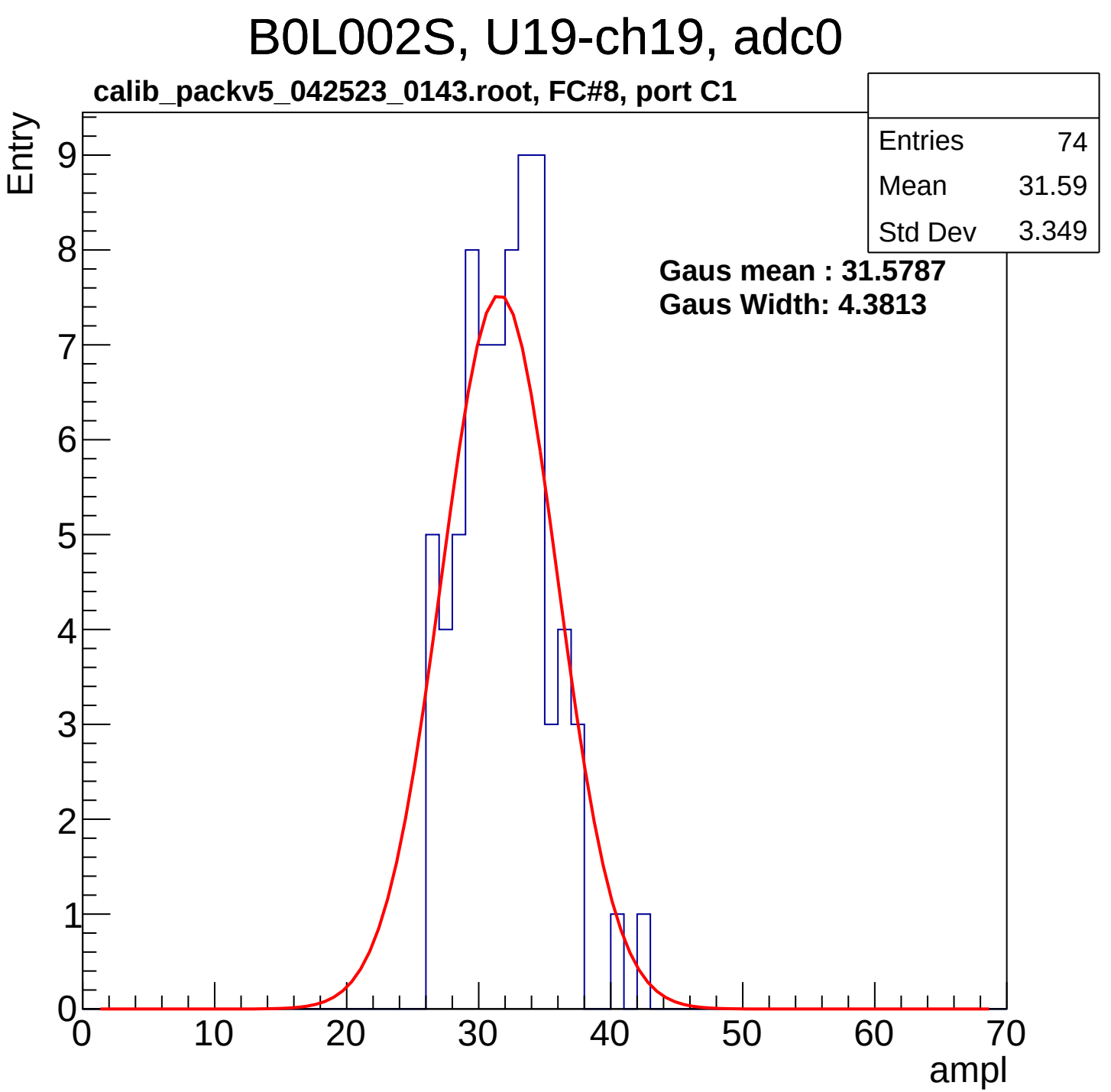

B0L002S, U19-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 50.3
Std Dev 2.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

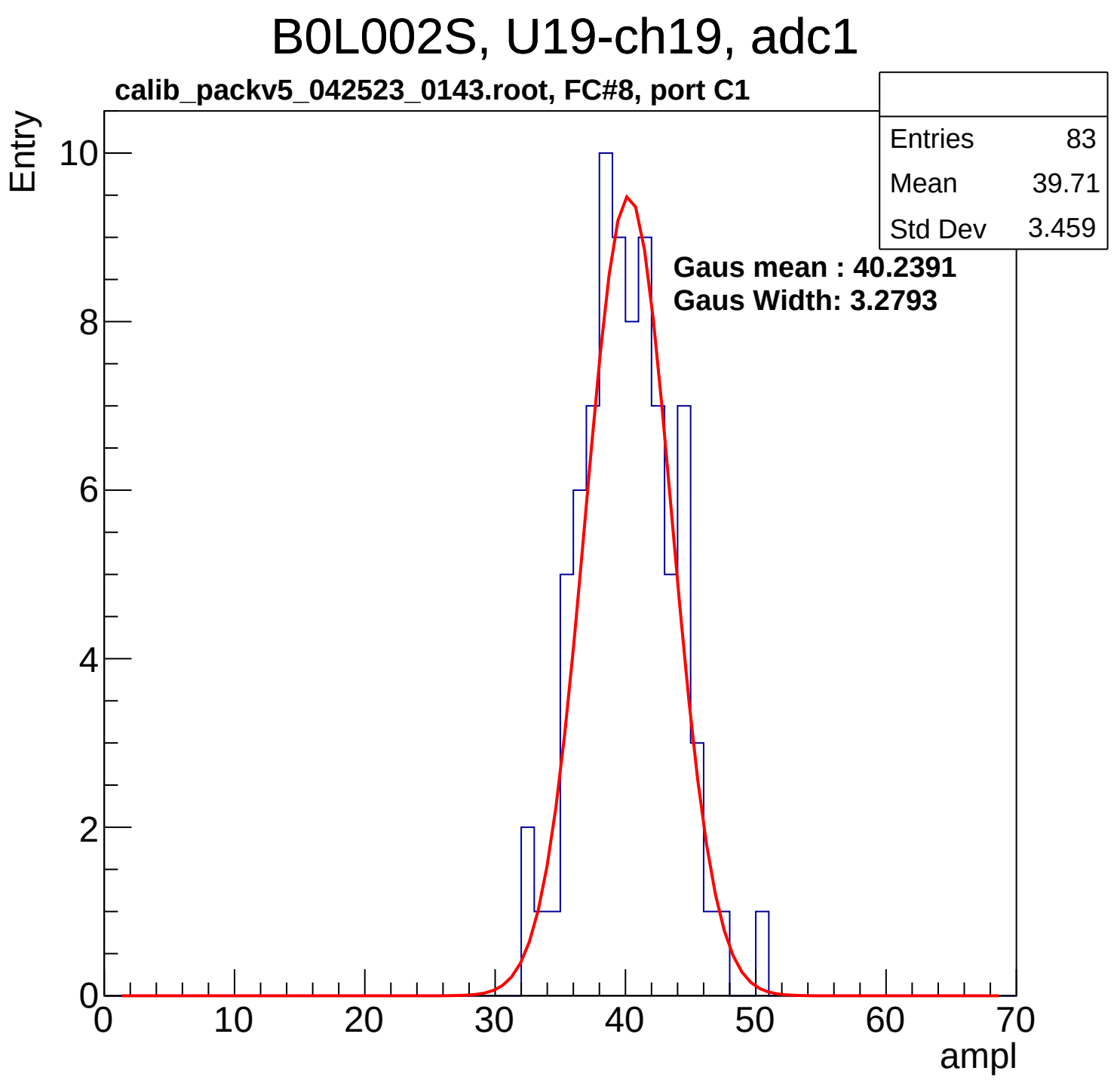

B0L002S, U19-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.79
Std Dev 3.138
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

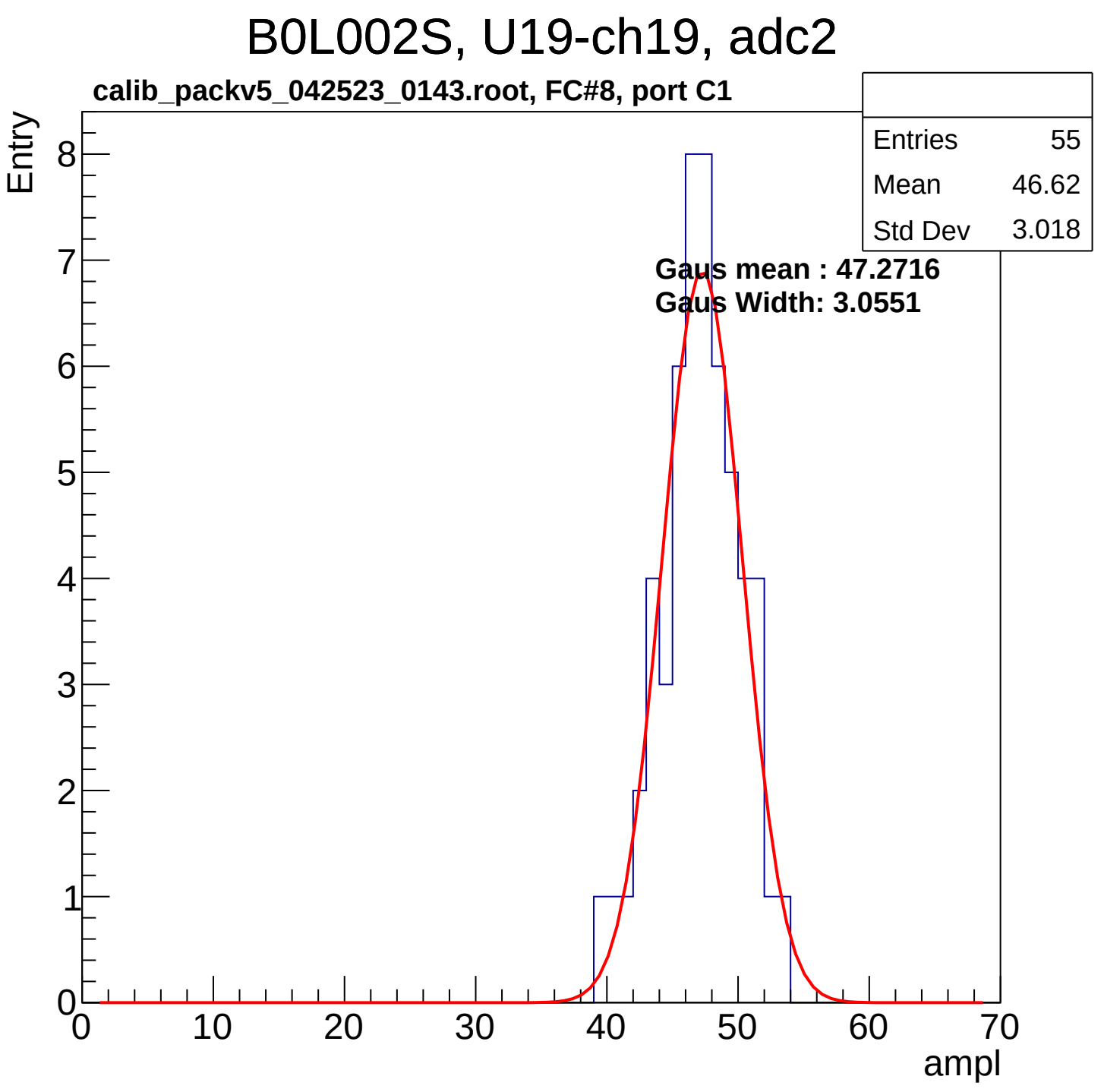

B0L002S, U19-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 59.04
Std Dev 8.872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

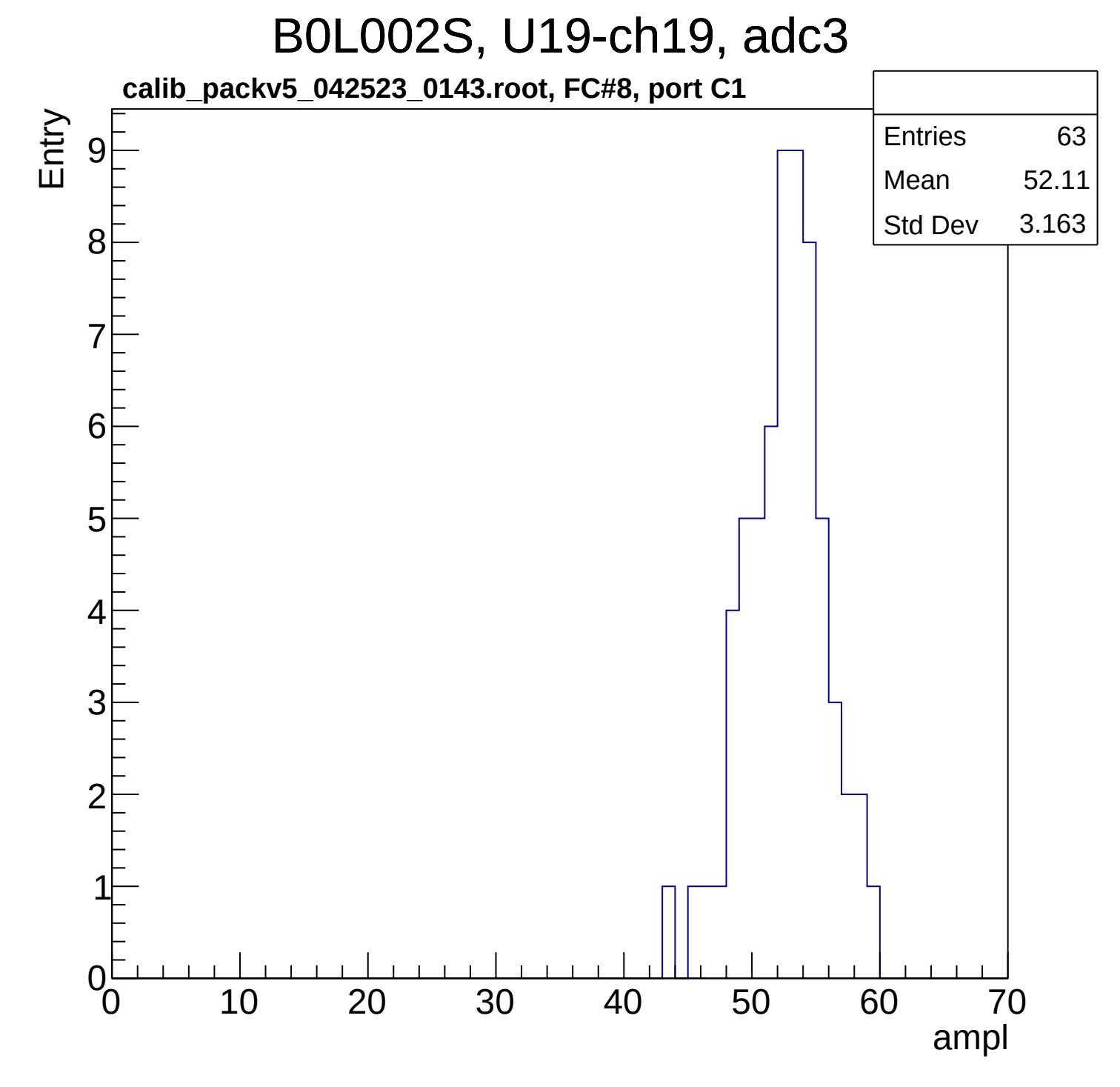

B0L002S, U19-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

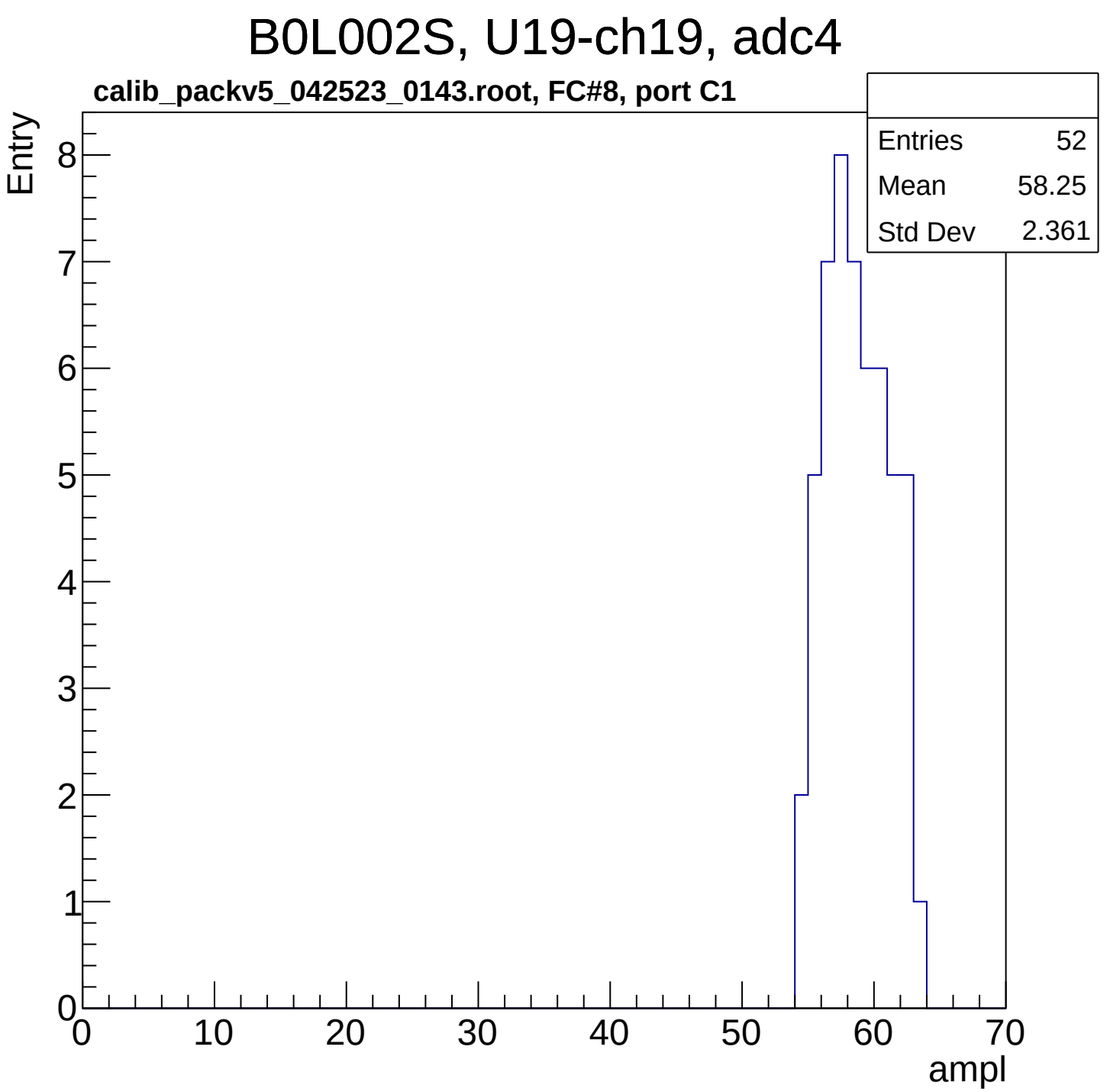

B0L002S, U19-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

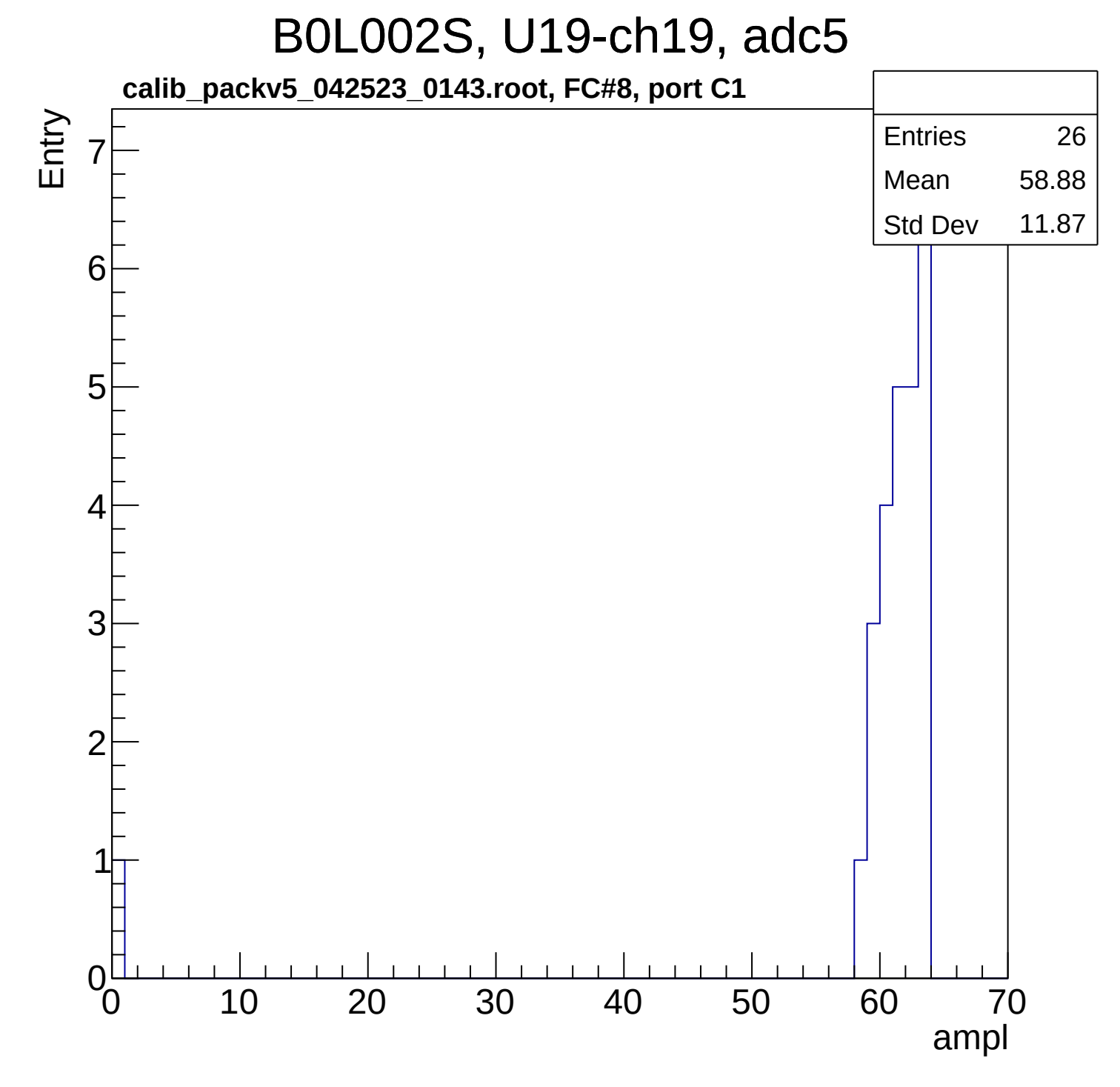

B0L002S, U19-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.59
Std Dev 3.349
Gaus mean : 31.5787
Gaus Width: 4.3813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

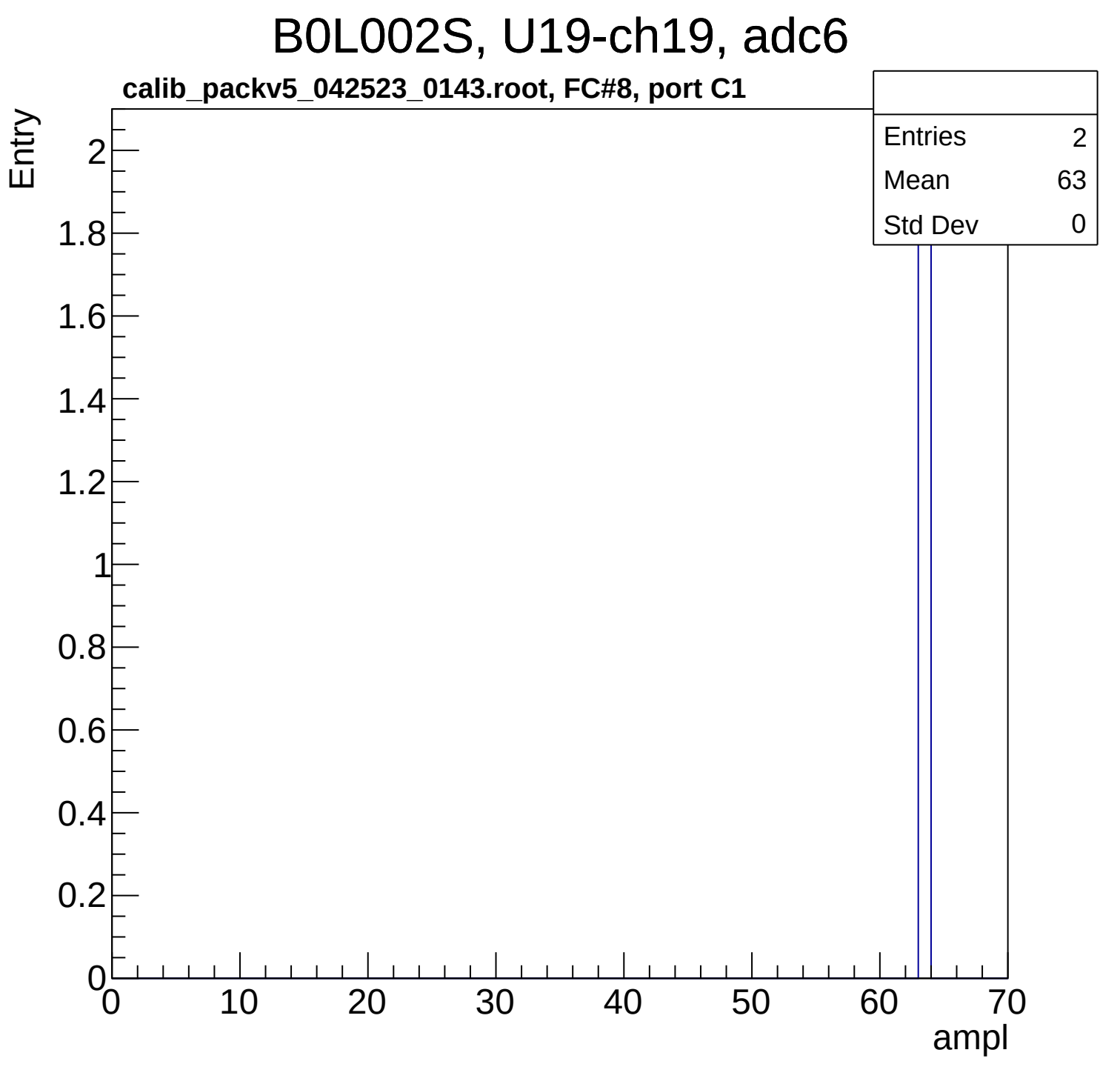

B0L002S, U19-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 39.71
Std Dev 3.459
Gaus mean : 40.2391
Gaus Width: 3.2793
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

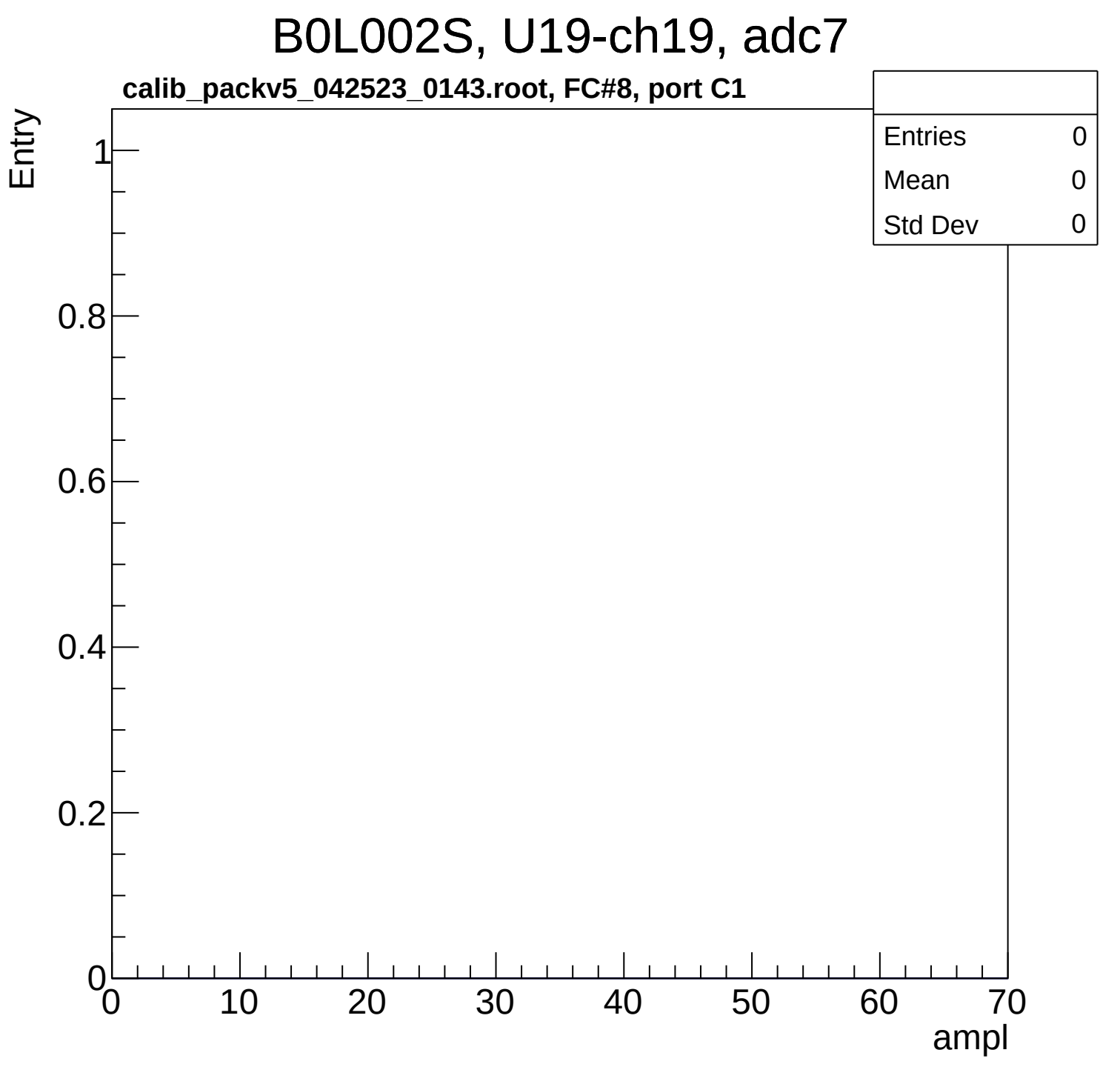

B0L002S, U19-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 46.62
Std Dev 3.018
Gaus mean : 47.2716
Gaus Width: 3.0551
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

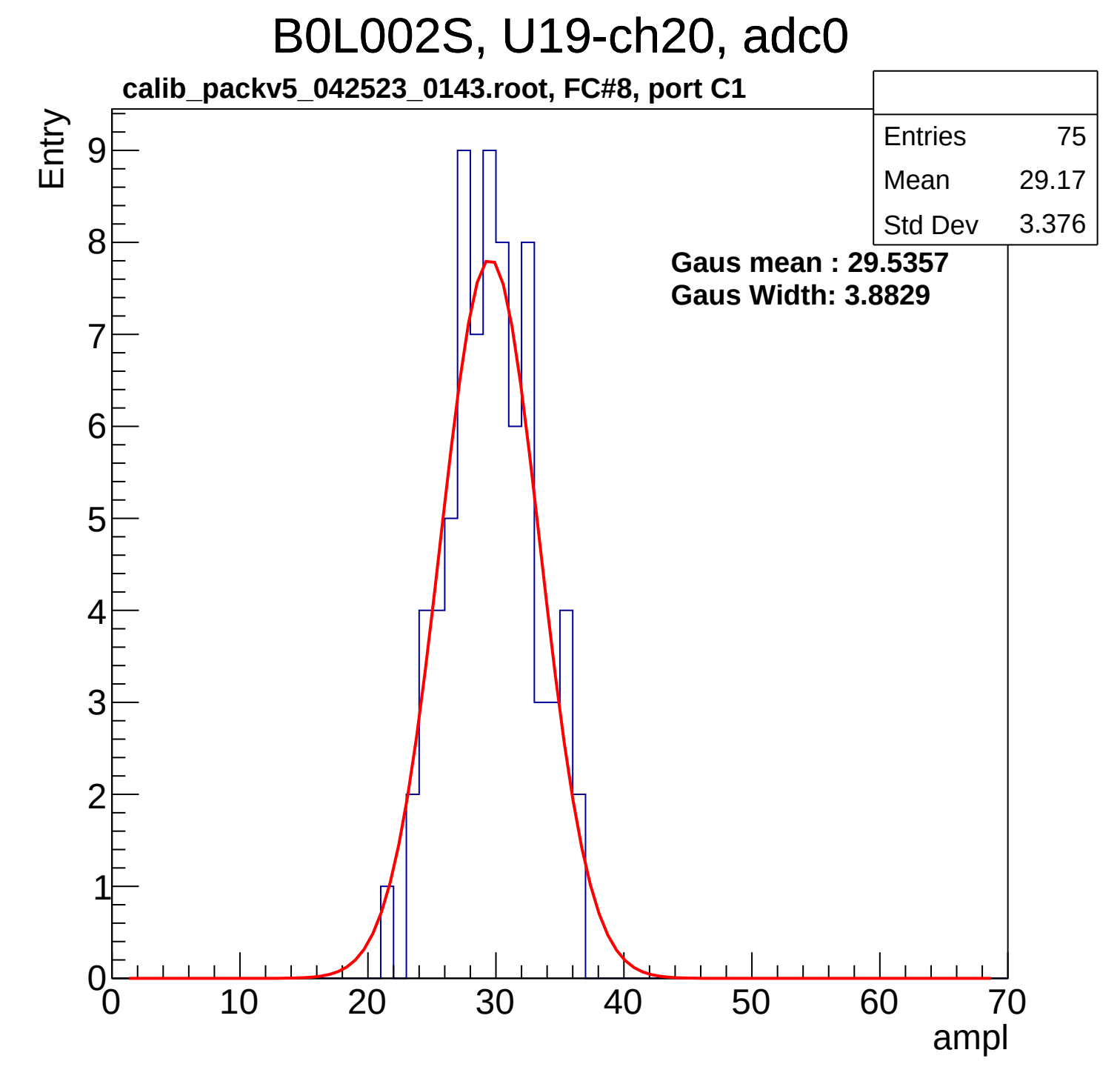

B0L002S, U19-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52.11
Std Dev 3.163
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

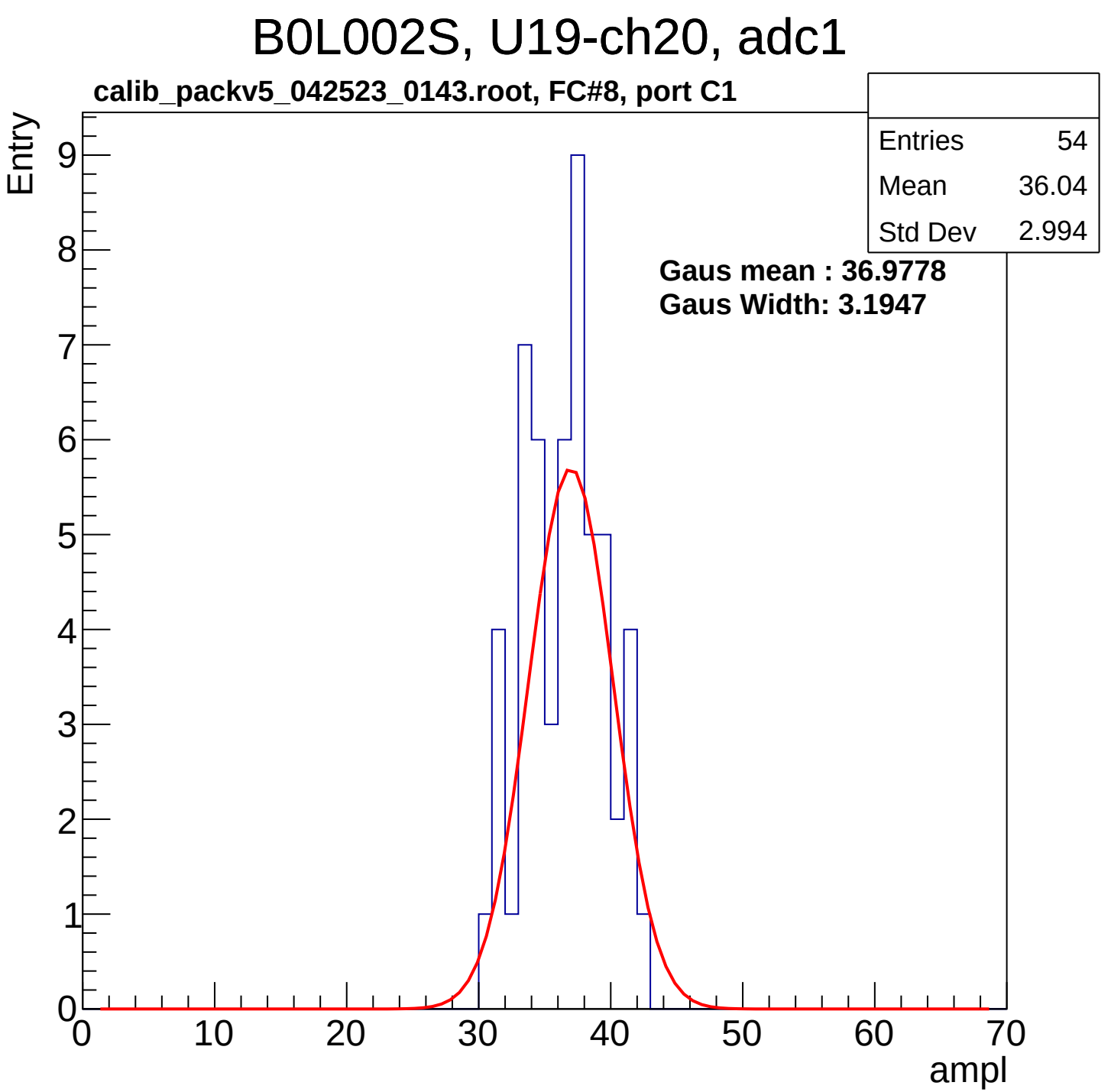

B0L002S, U19-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.25
Std Dev 2.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

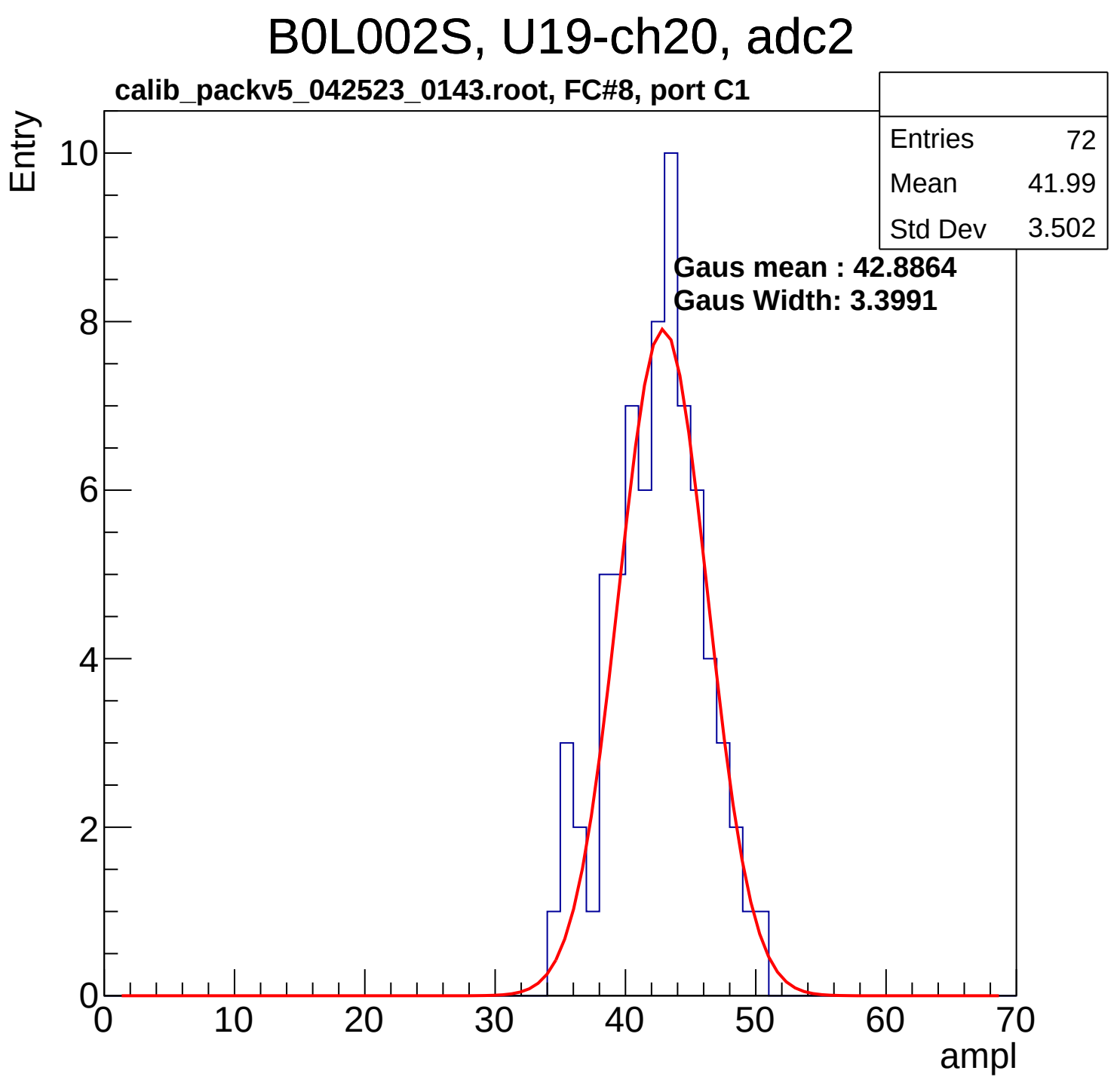

B0L002S, U19-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.88
Std Dev 11.87
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

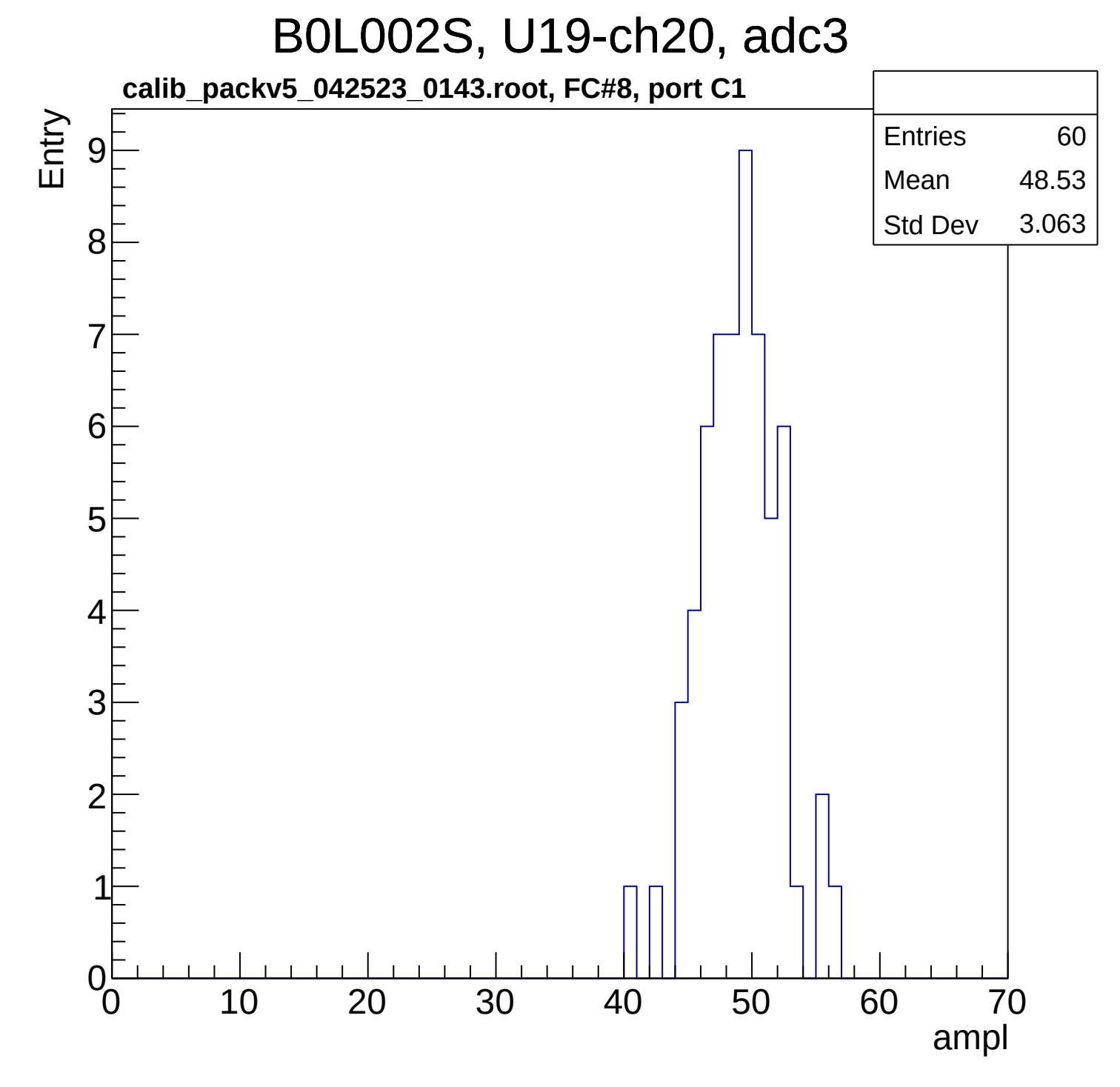

B0L002S, U19-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

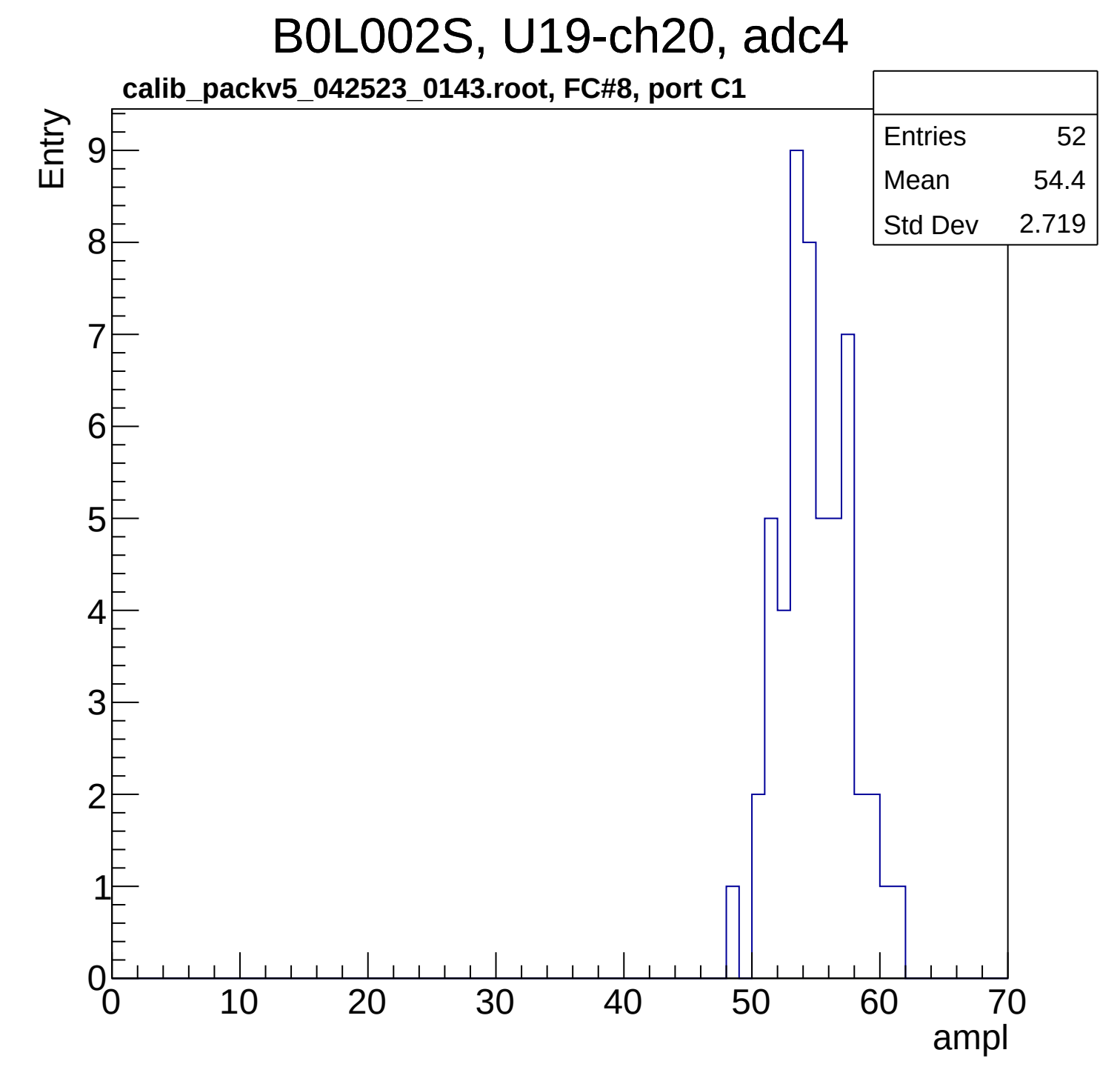

B0L002S, U19-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

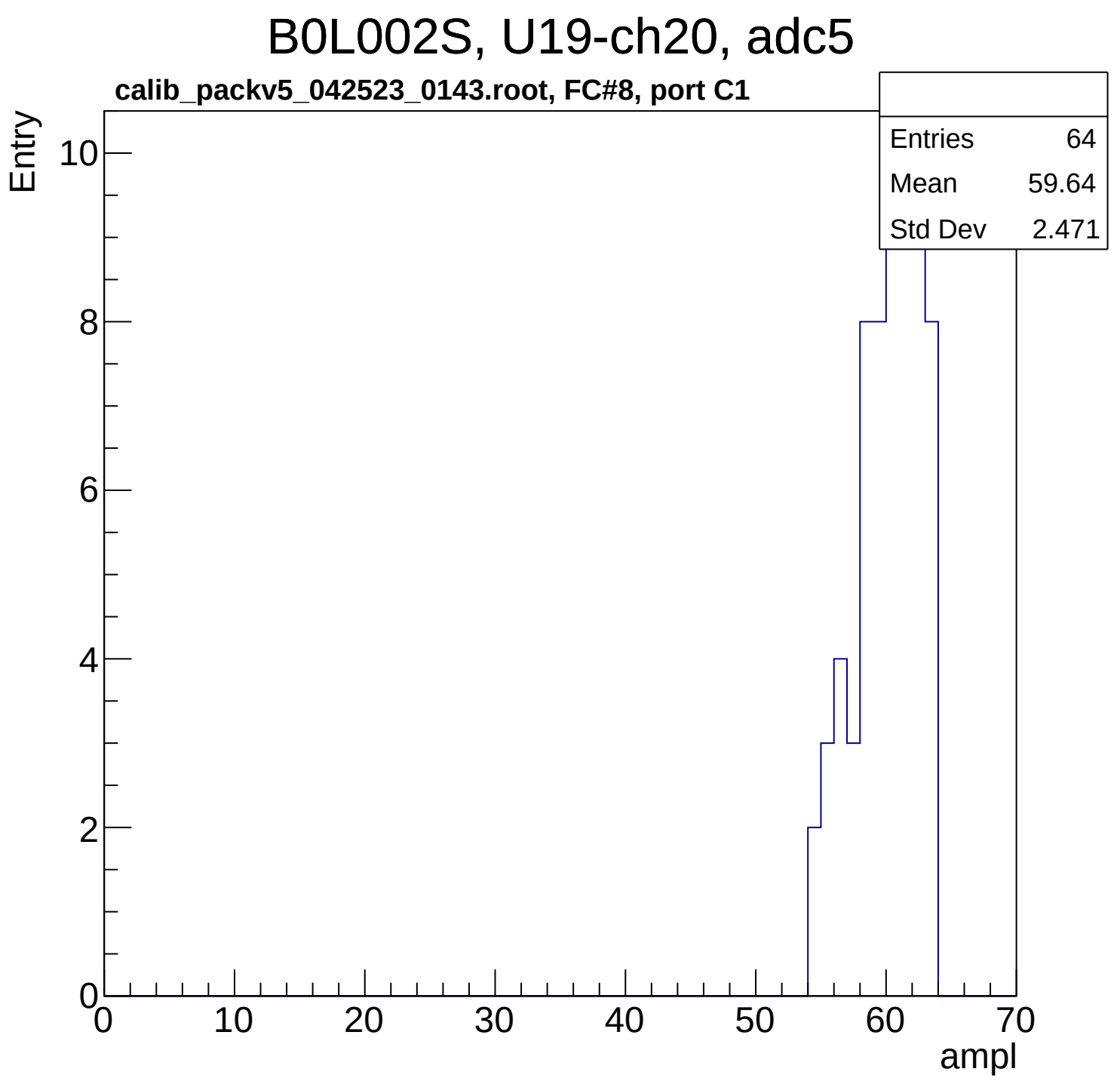

B0L002S, U19-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.17
Std Dev 3.376
Gaus mean : 29.5357
Gaus Width: 3.8829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

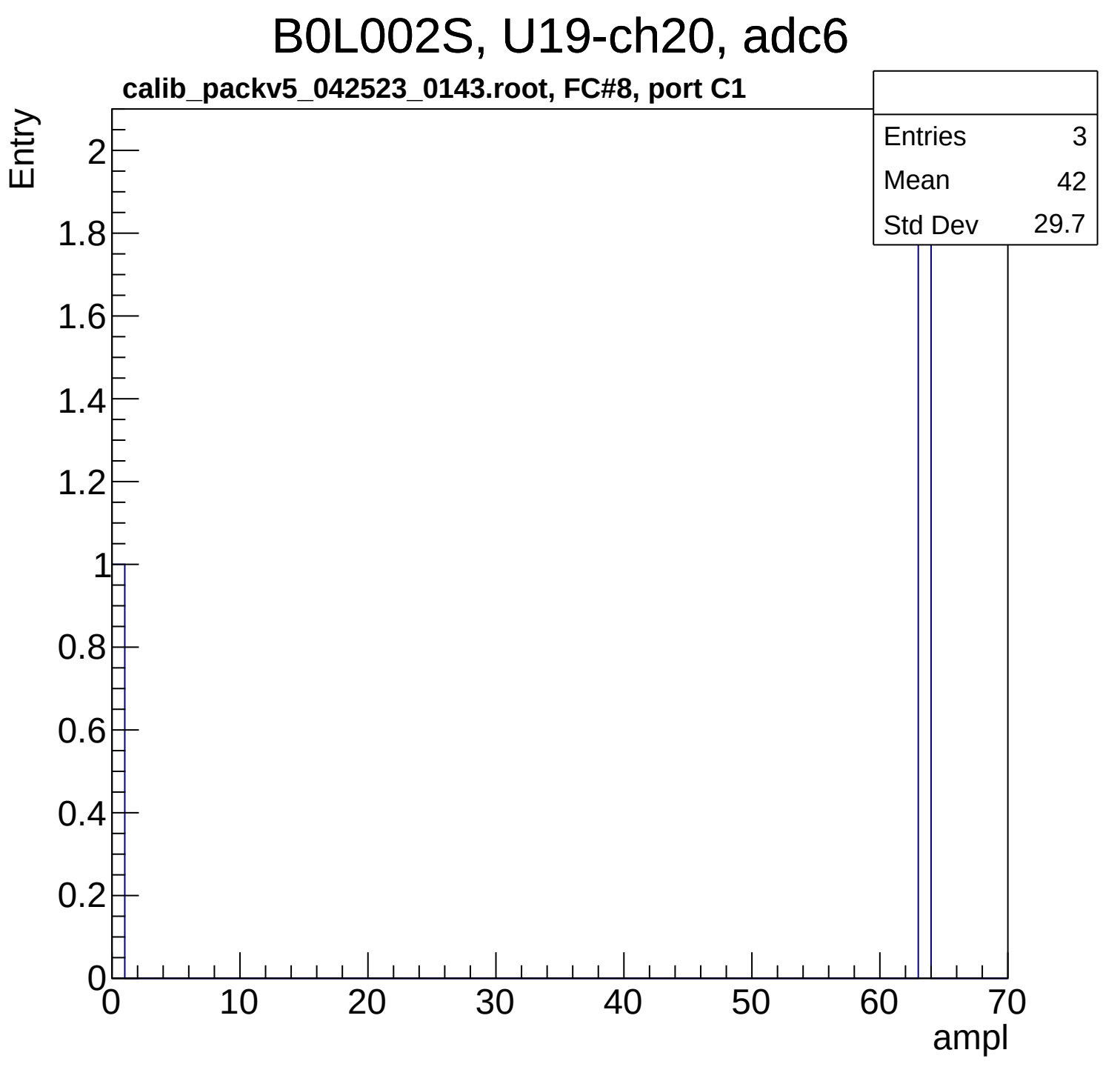

B0L002S, U19-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 36.04
Std Dev 2.994
Gaus mean : 36.9778
Gaus Width: 3.1947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

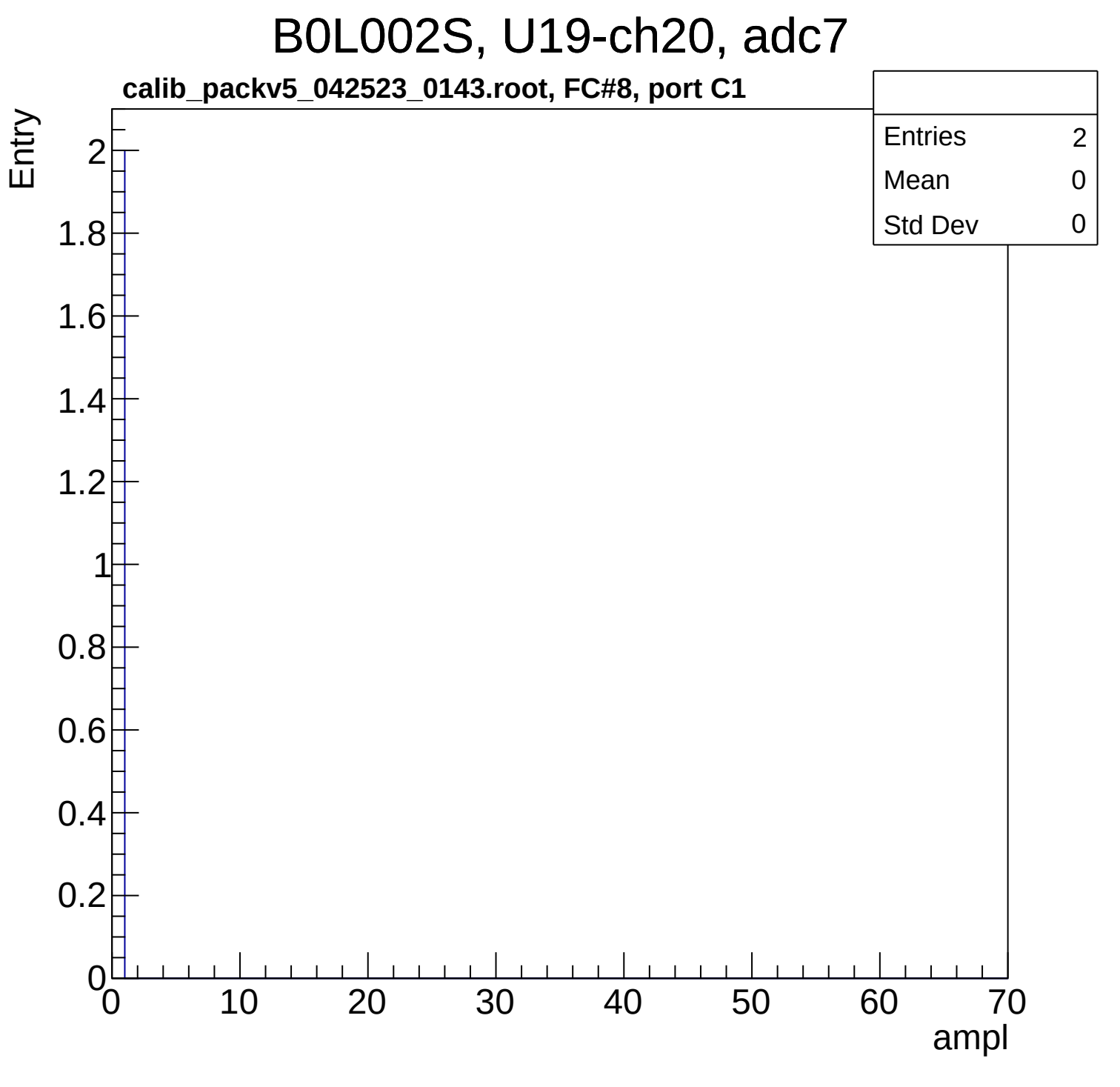

B0L002S, U19-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 41.99
Std Dev 3.502
Gaus mean : 42.8864
Gaus Width: 3.3991
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

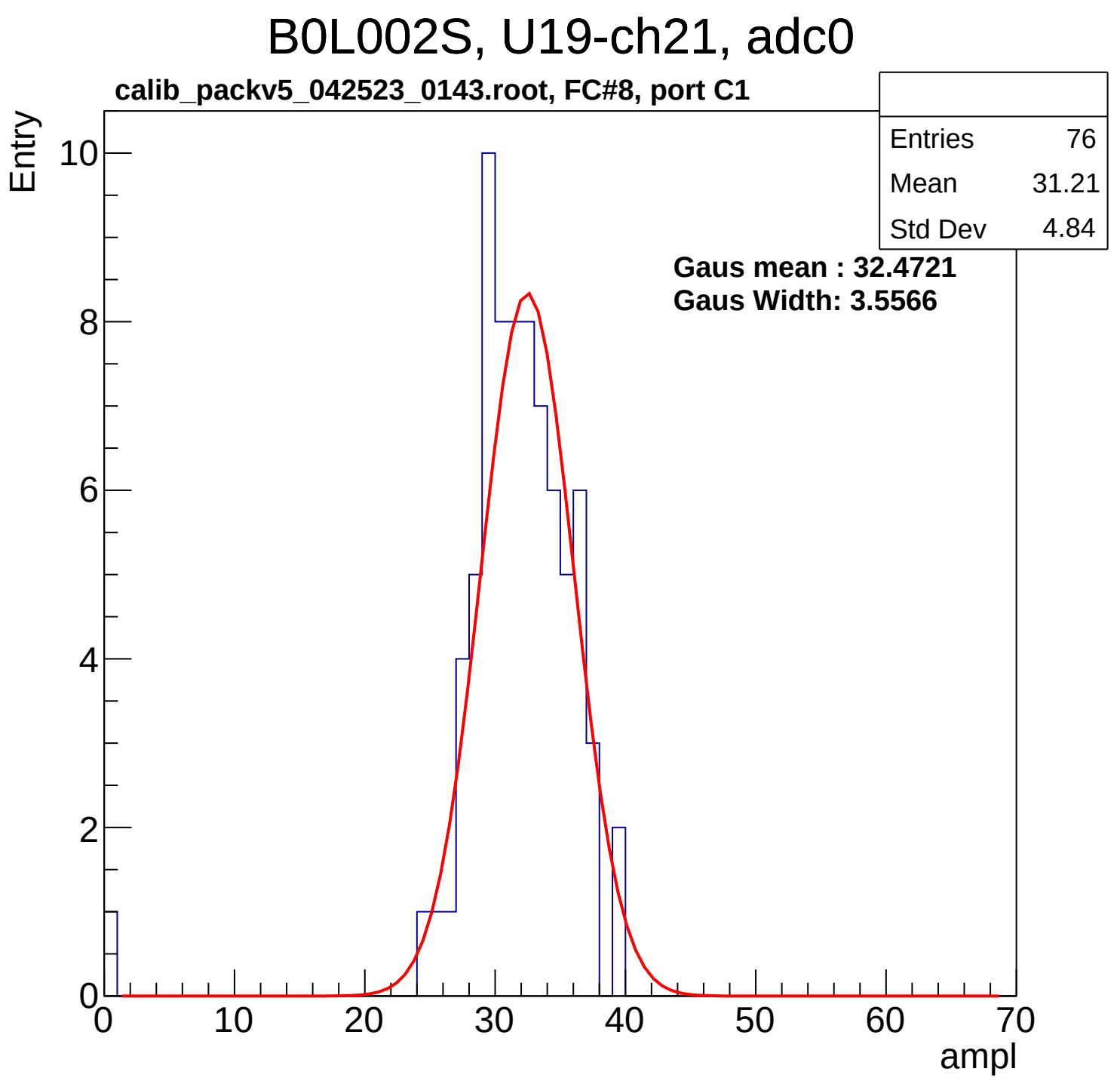

B0L002S, U19-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 48.53
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

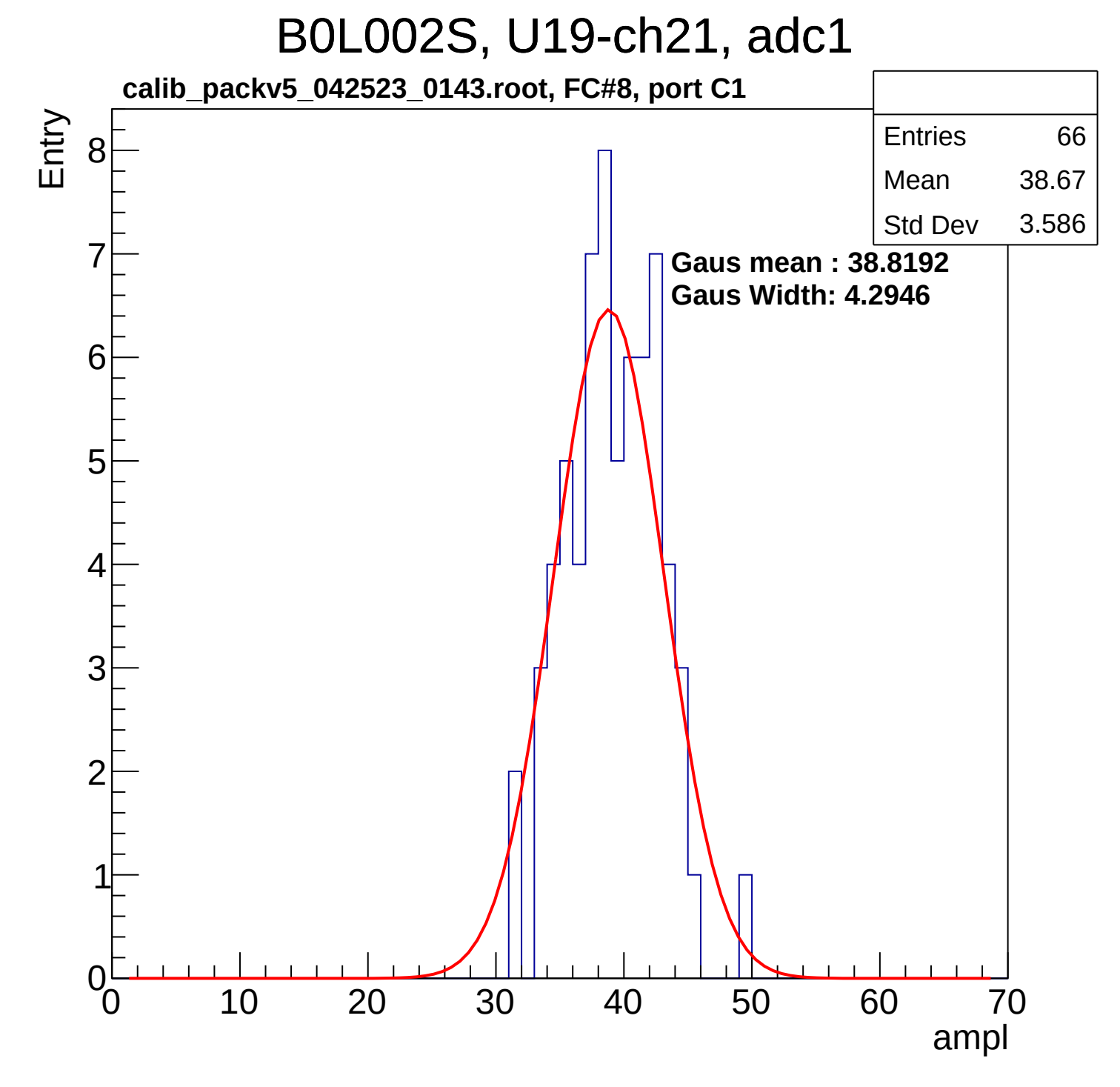

B0L002S, U19-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 54.4
Std Dev 2.719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

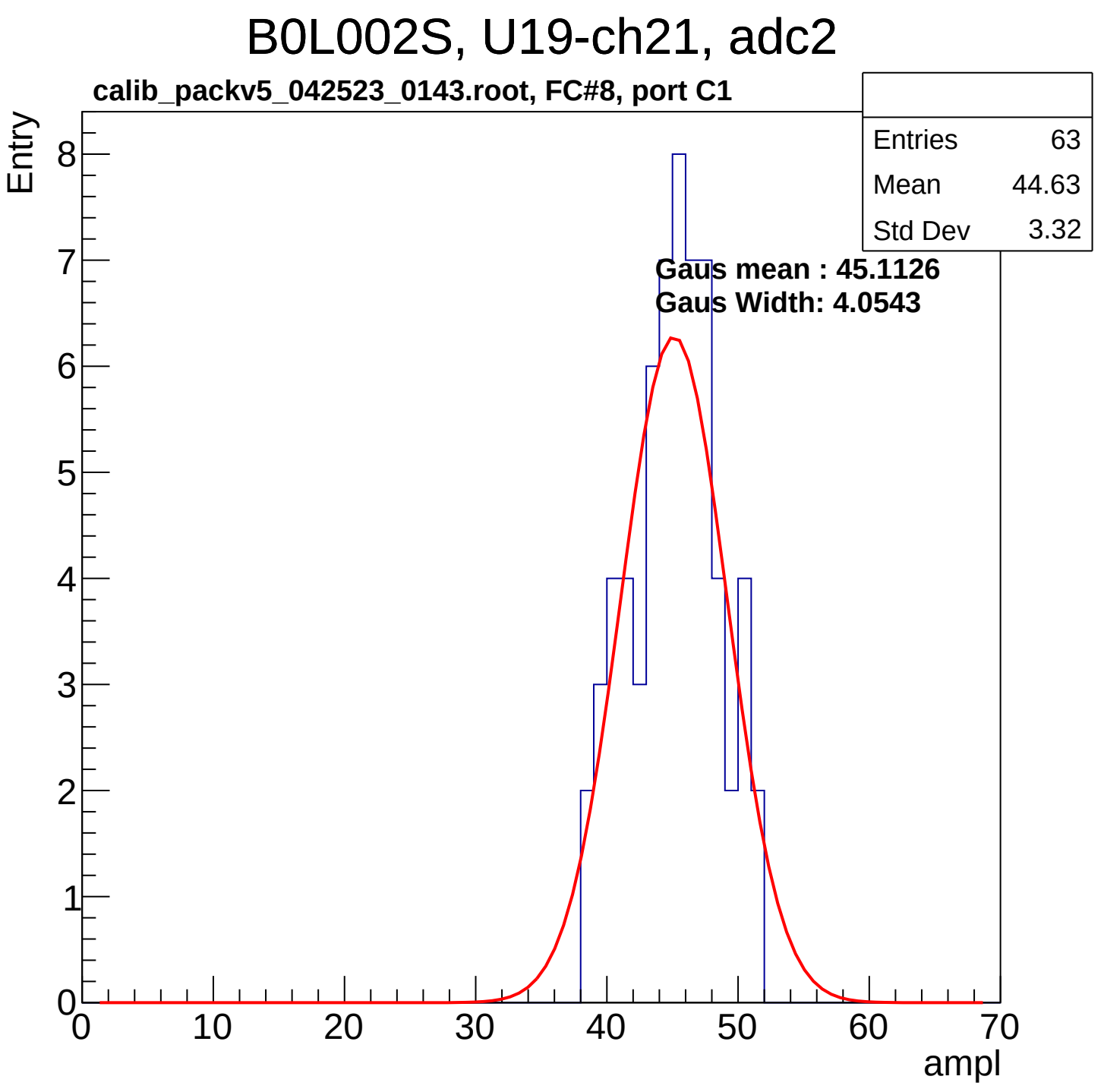

B0L002S, U19-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 59.64
Std Dev 2.471
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

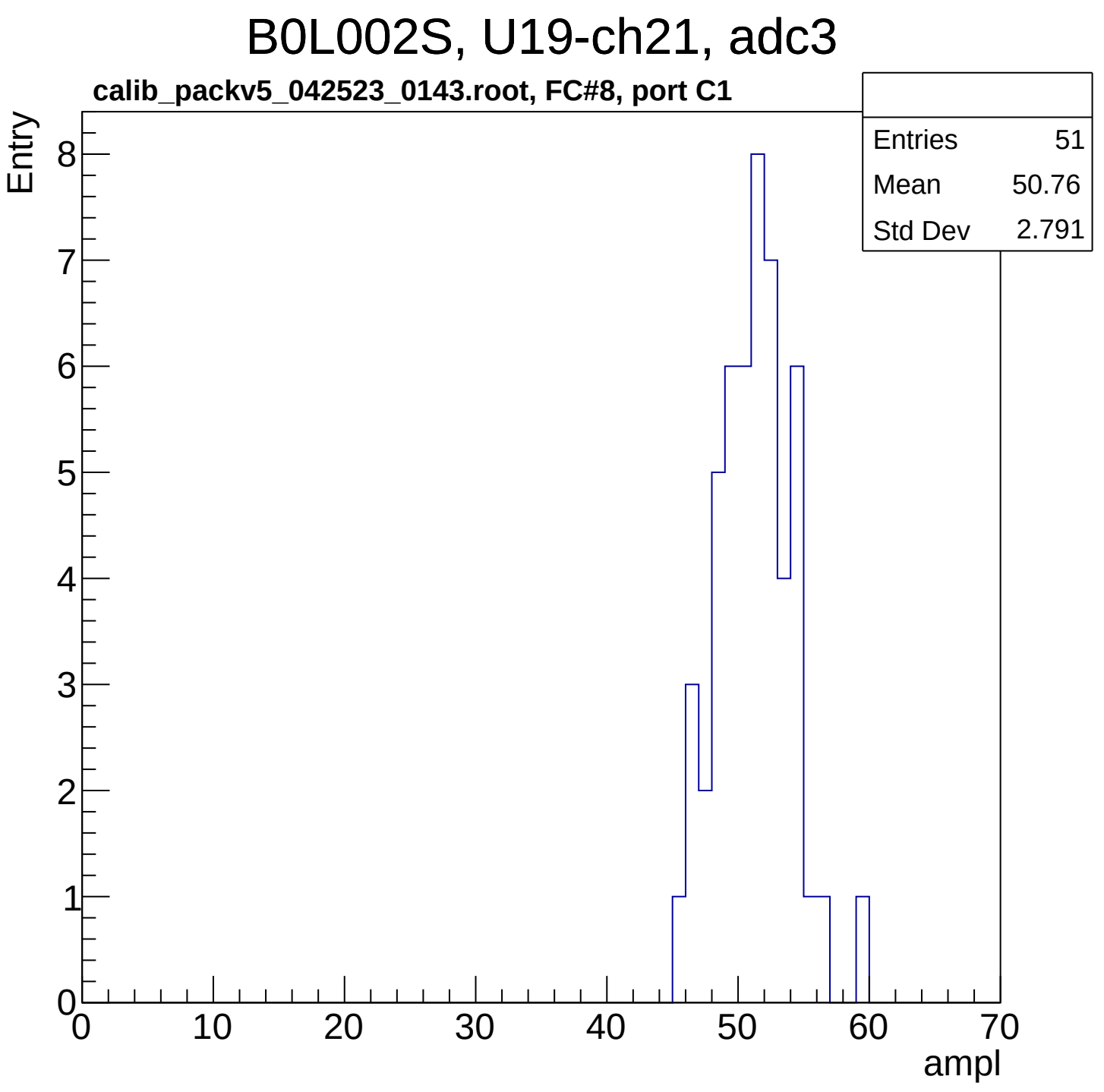

B0L002S, U19-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

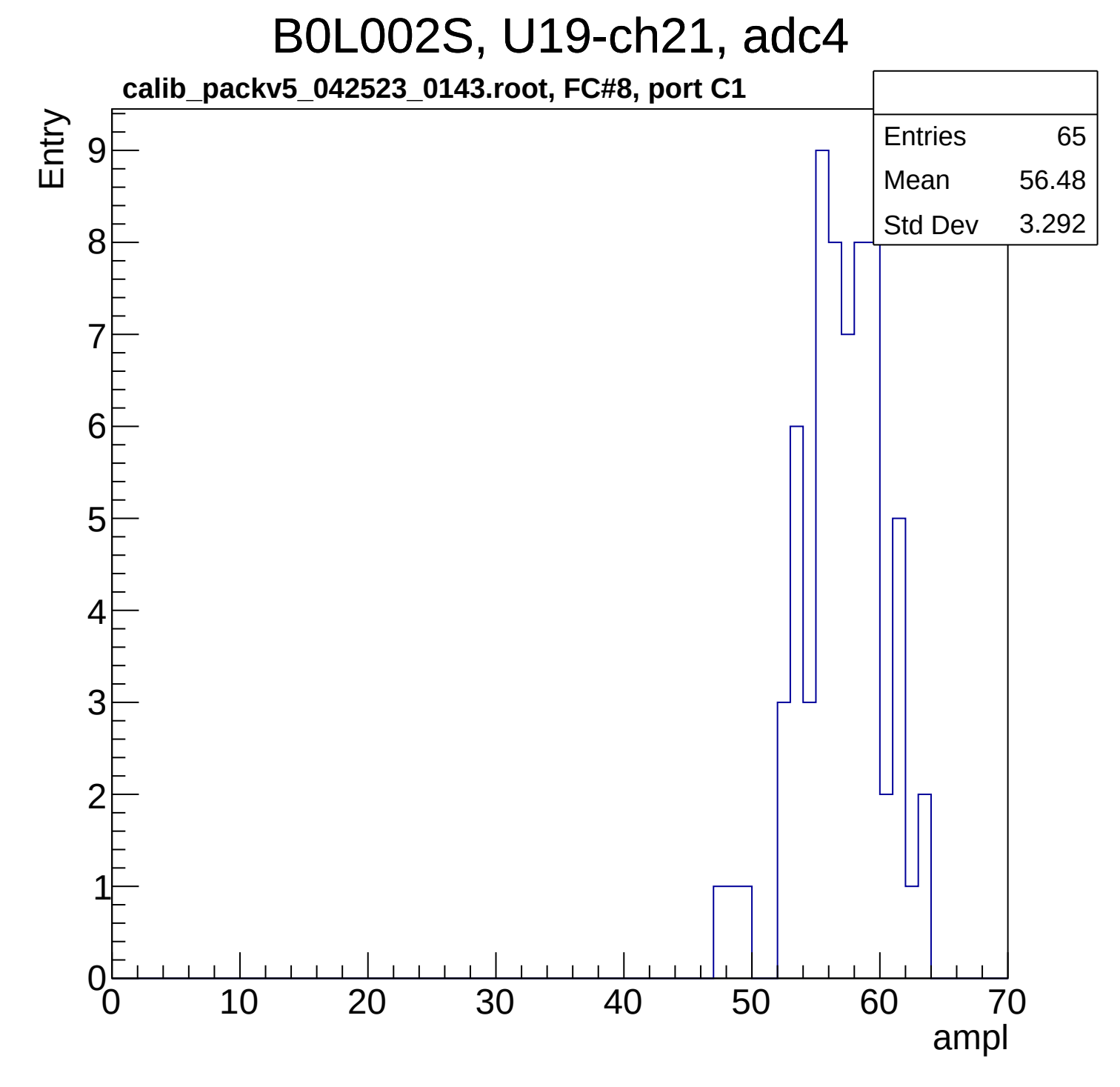

B0L002S, U19-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

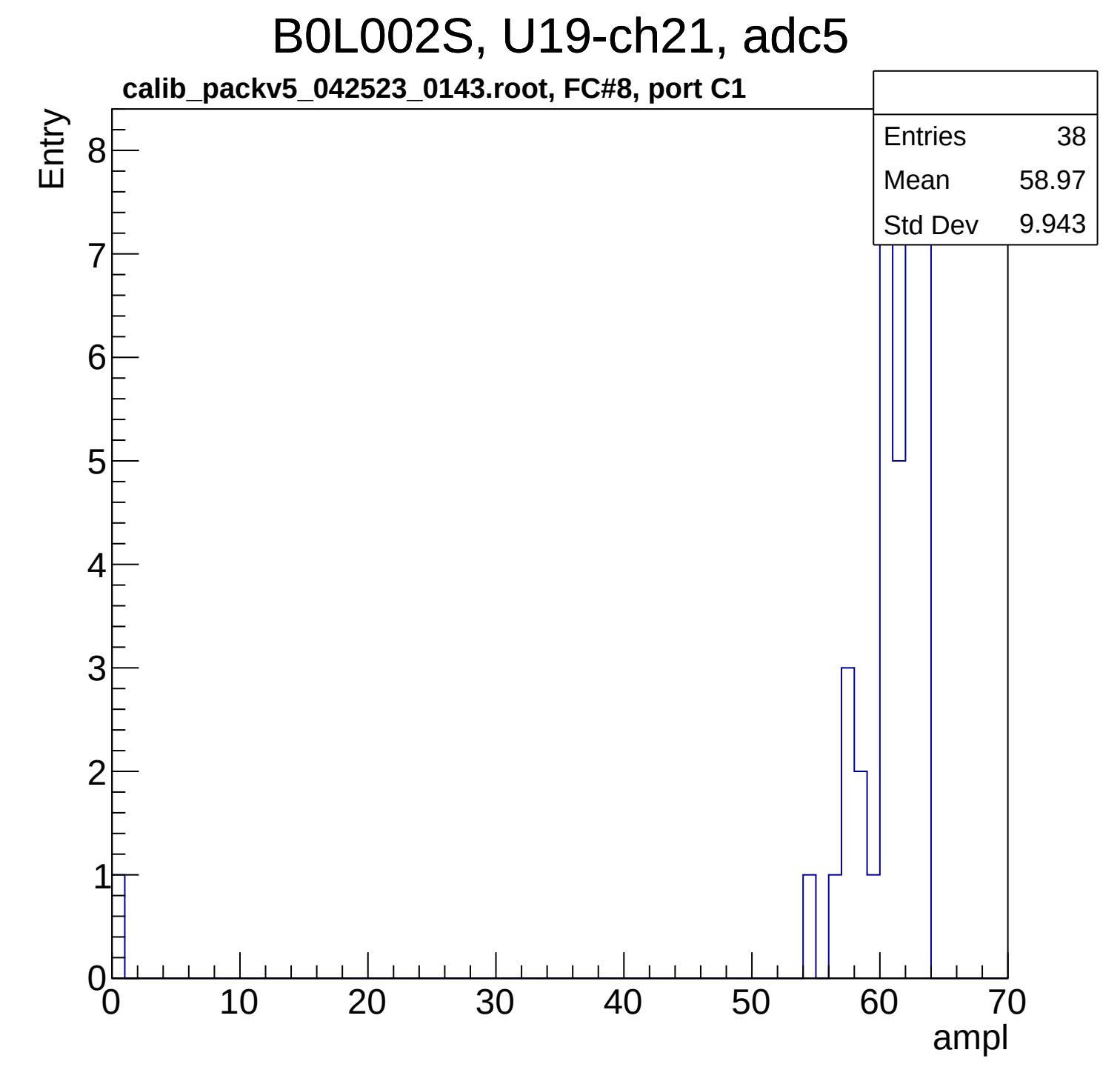

B0L002S, U19-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 31.21
Std Dev 4.84
Gaus mean : 32.4721
Gaus Width: 3.5566
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

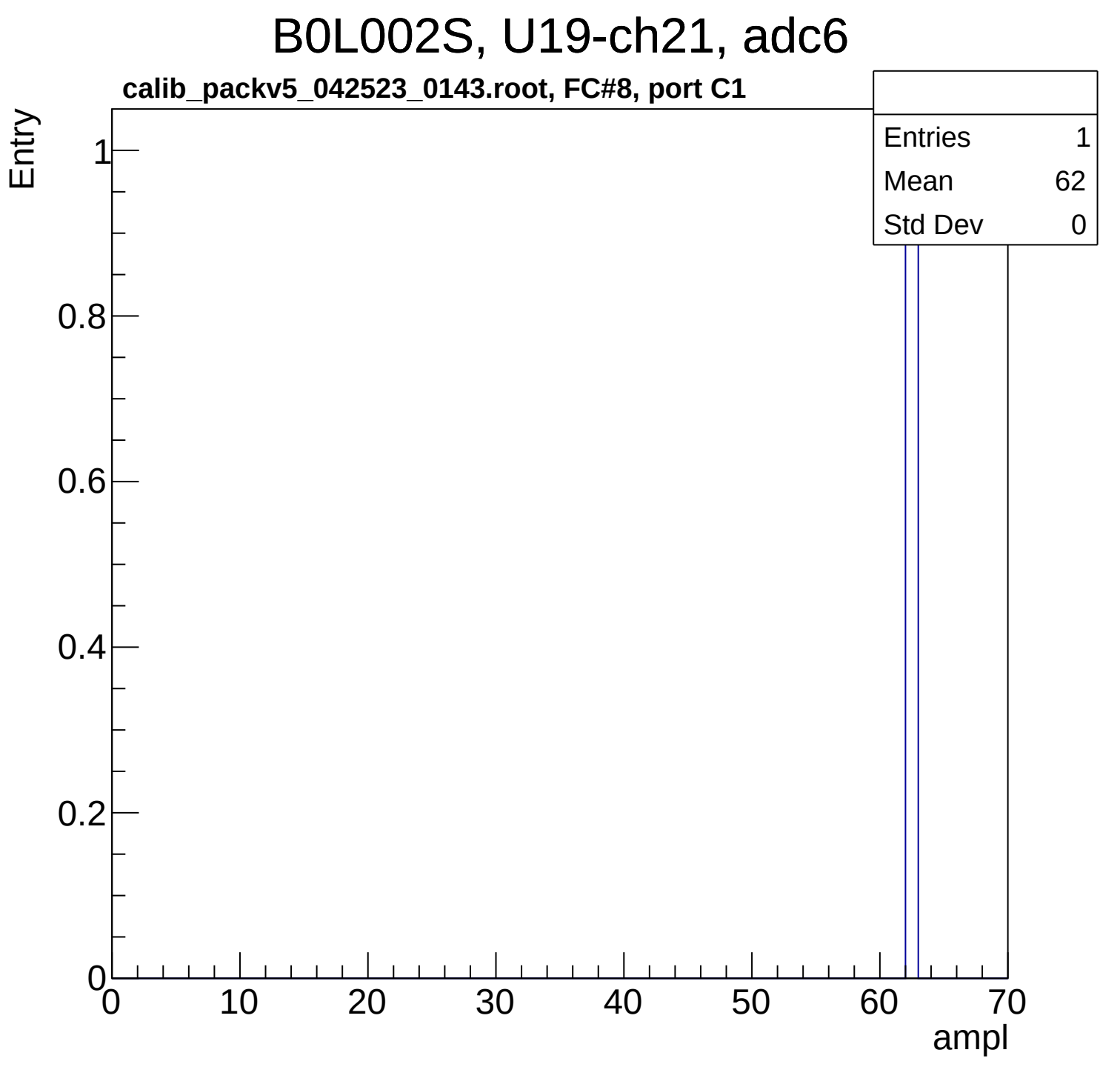

B0L002S, U19-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 38.67
Std Dev 3.586
Gaus mean : 38.8192
Gaus Width: 4.2946
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

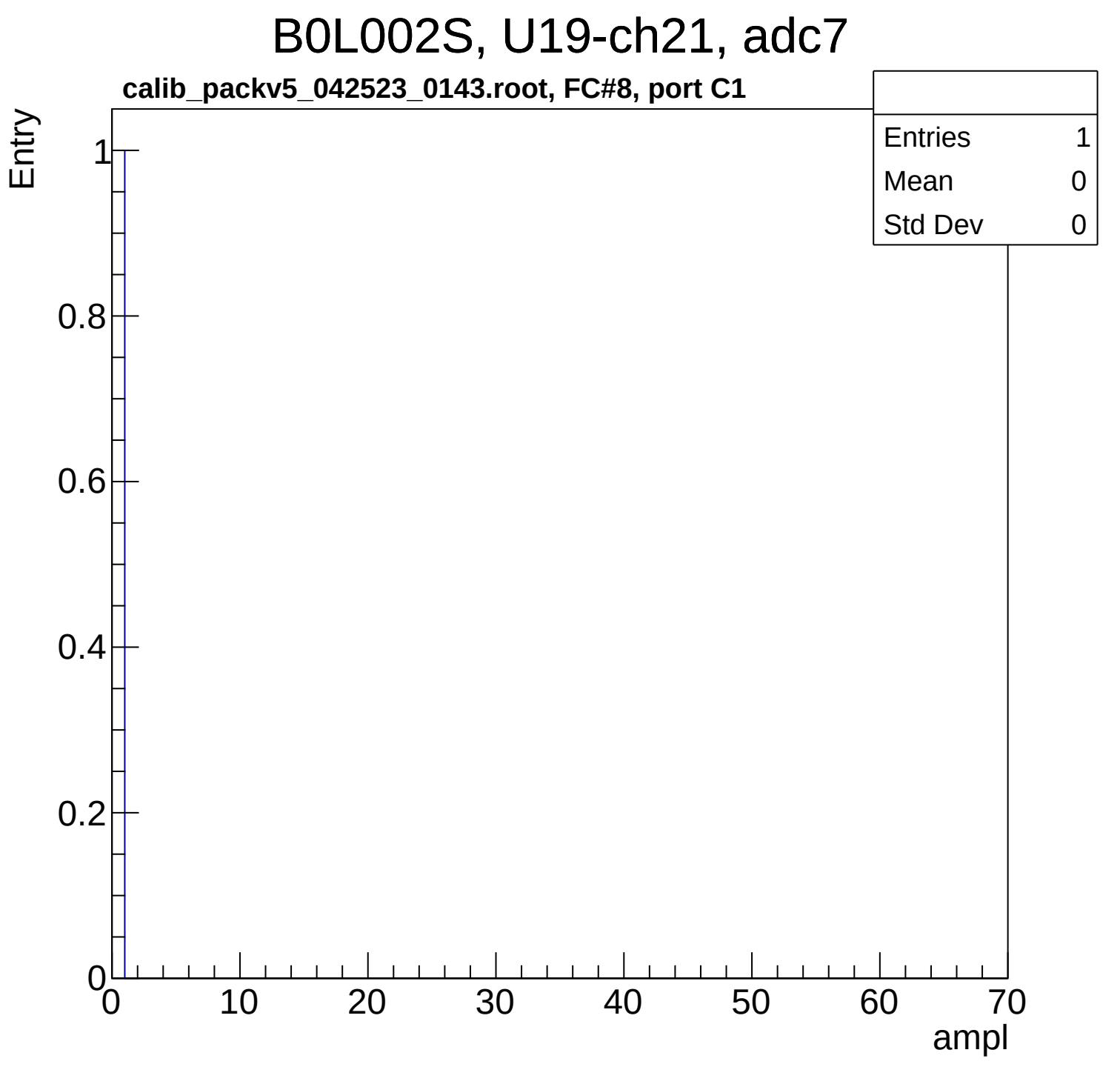

B0L002S, U19-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.63
Std Dev 3.32
Gaus mean : 45.1126
Gaus Width: 4.0543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

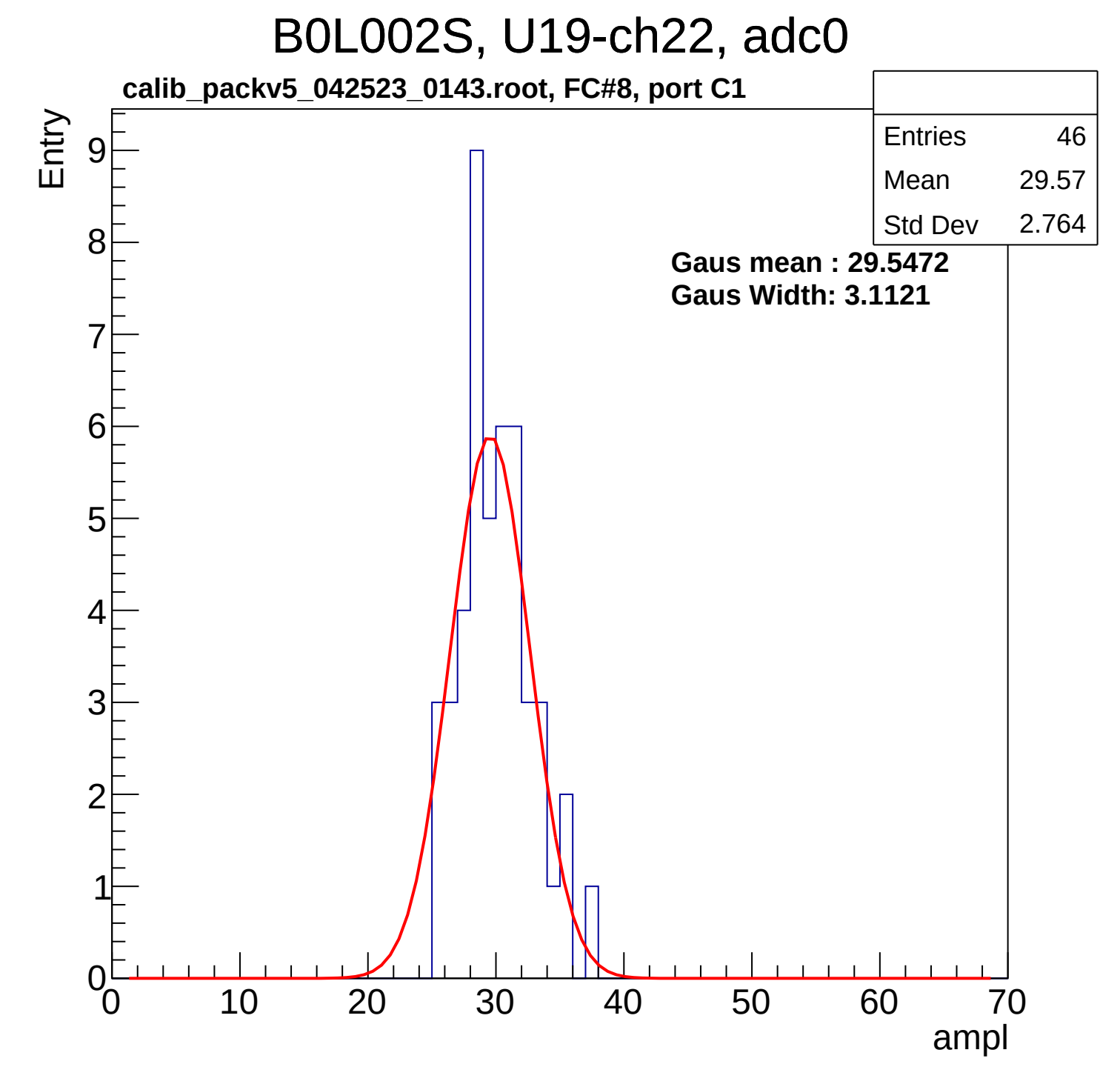

B0L002S, U19-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 50.76
Std Dev 2.791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

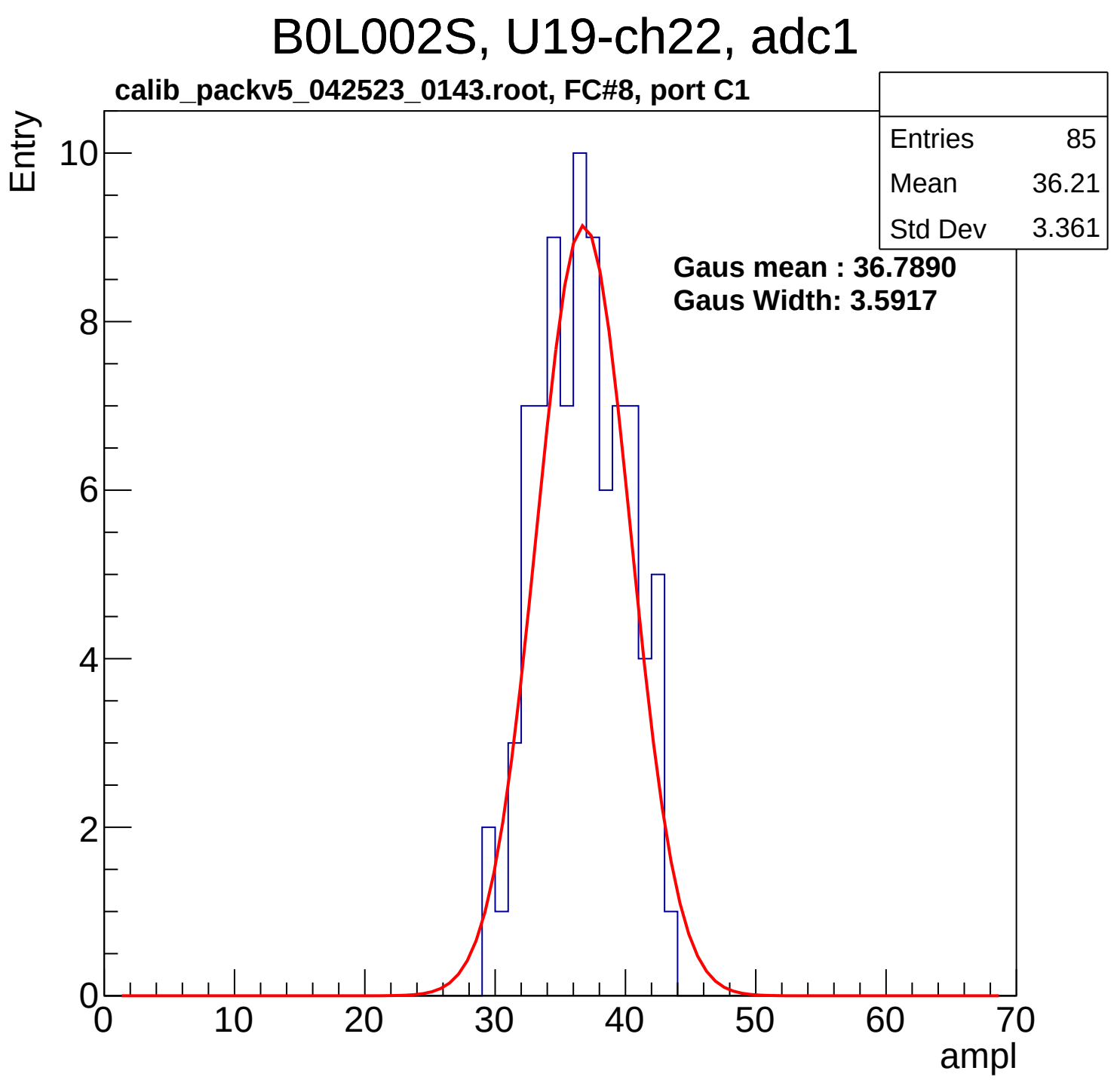

B0L002S, U19-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.48
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

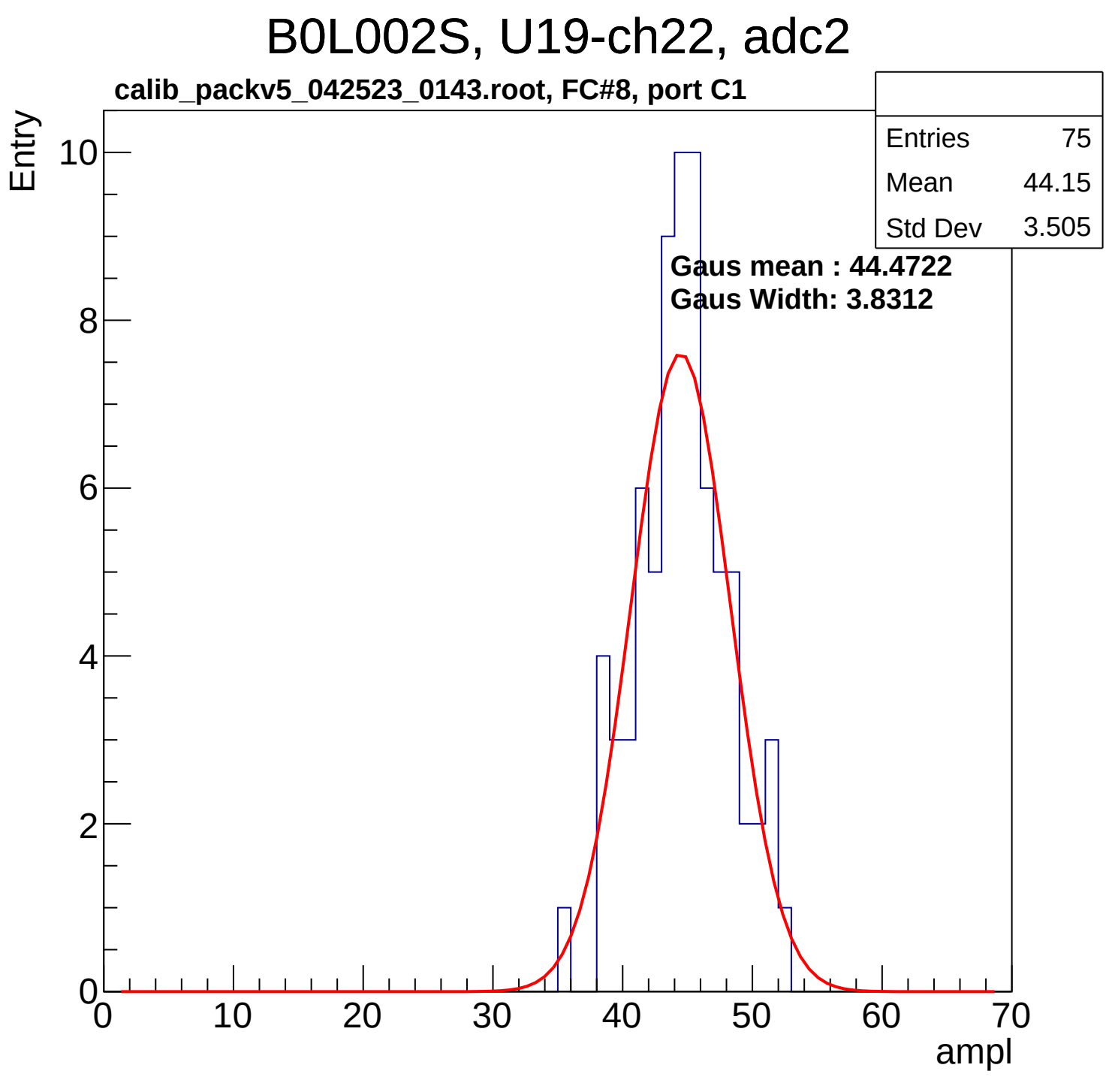

B0L002S, U19-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 58.97
Std Dev 9.943
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

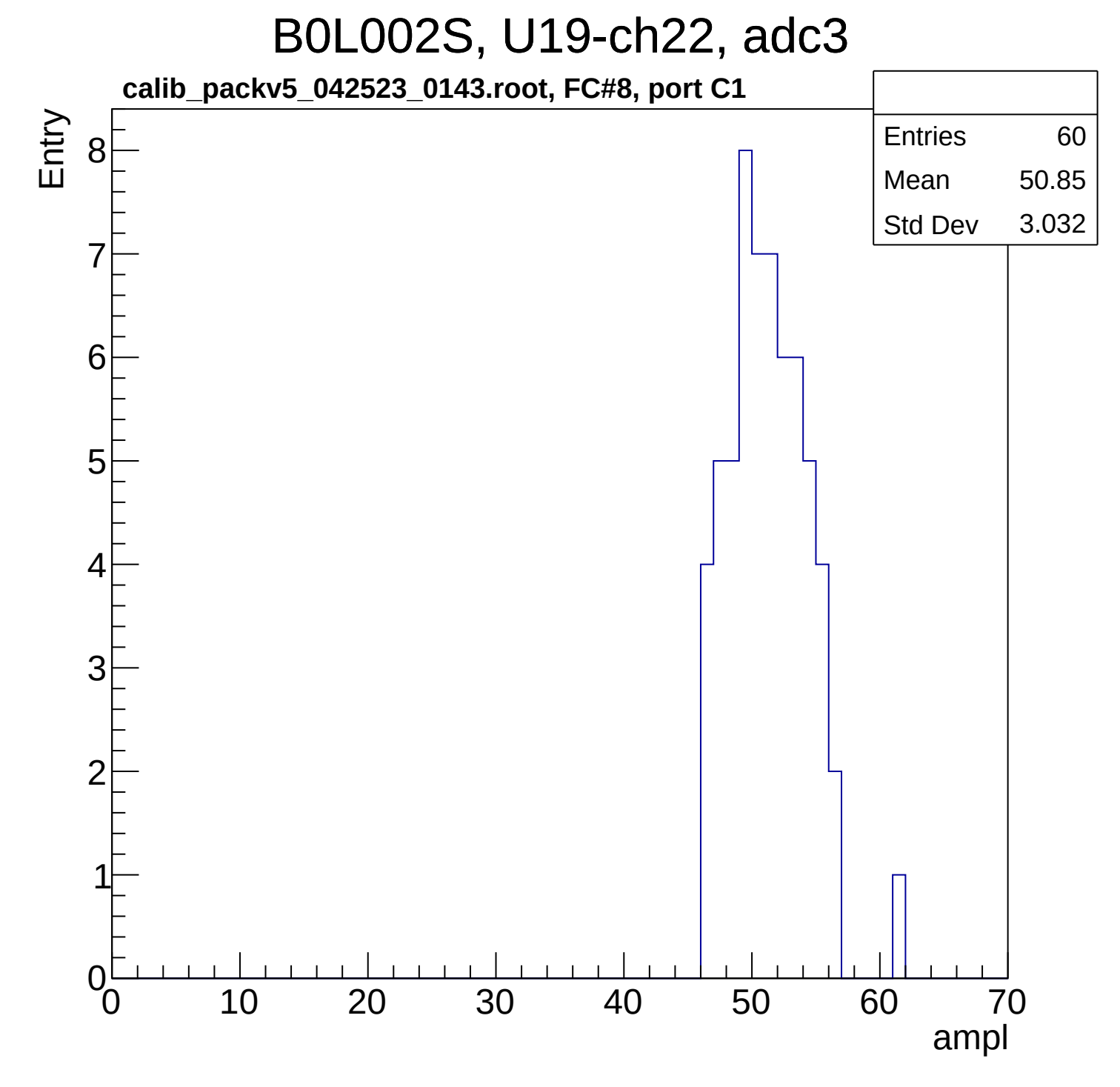

B0L002S, U19-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

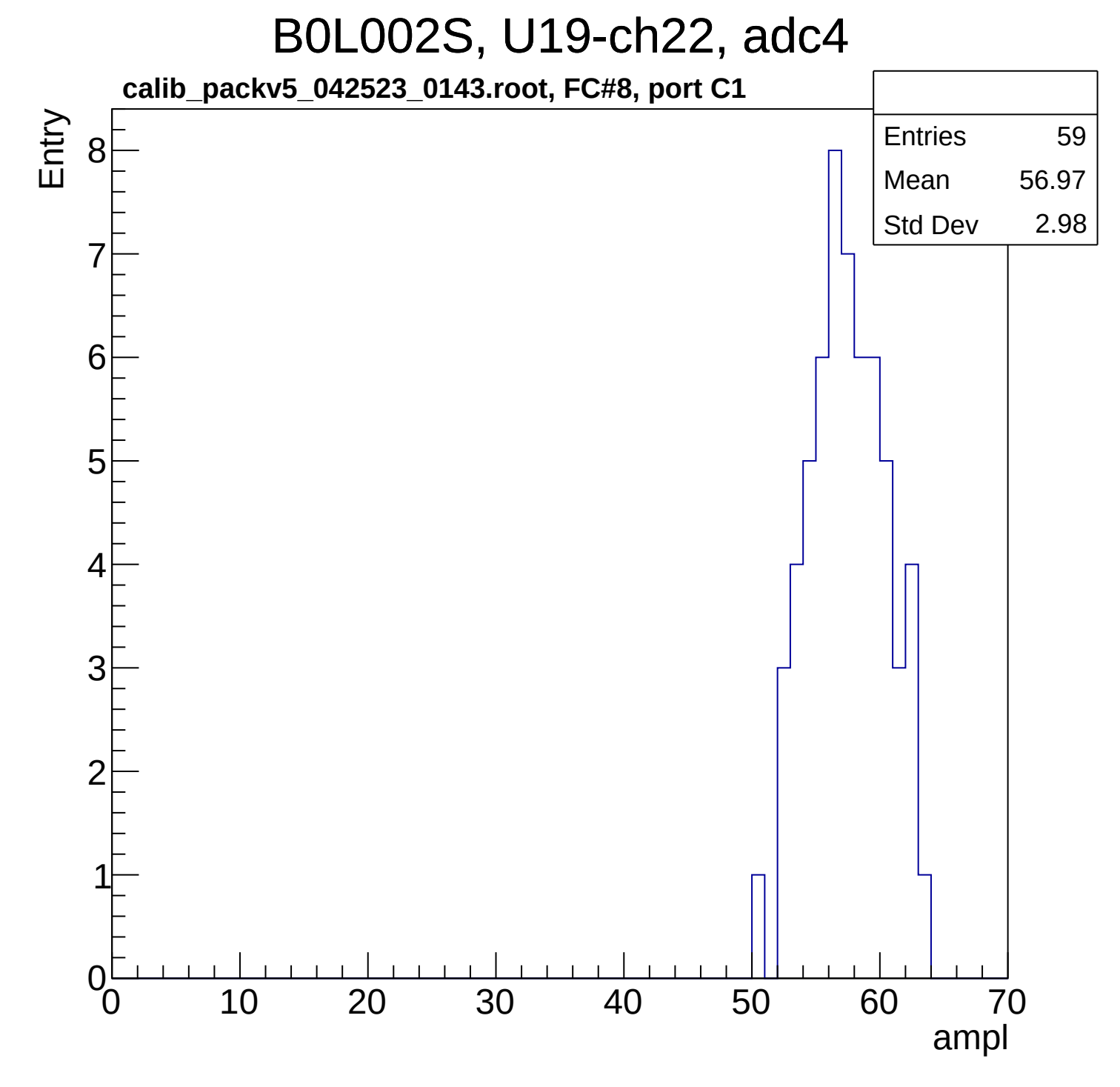

B0L002S, U19-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

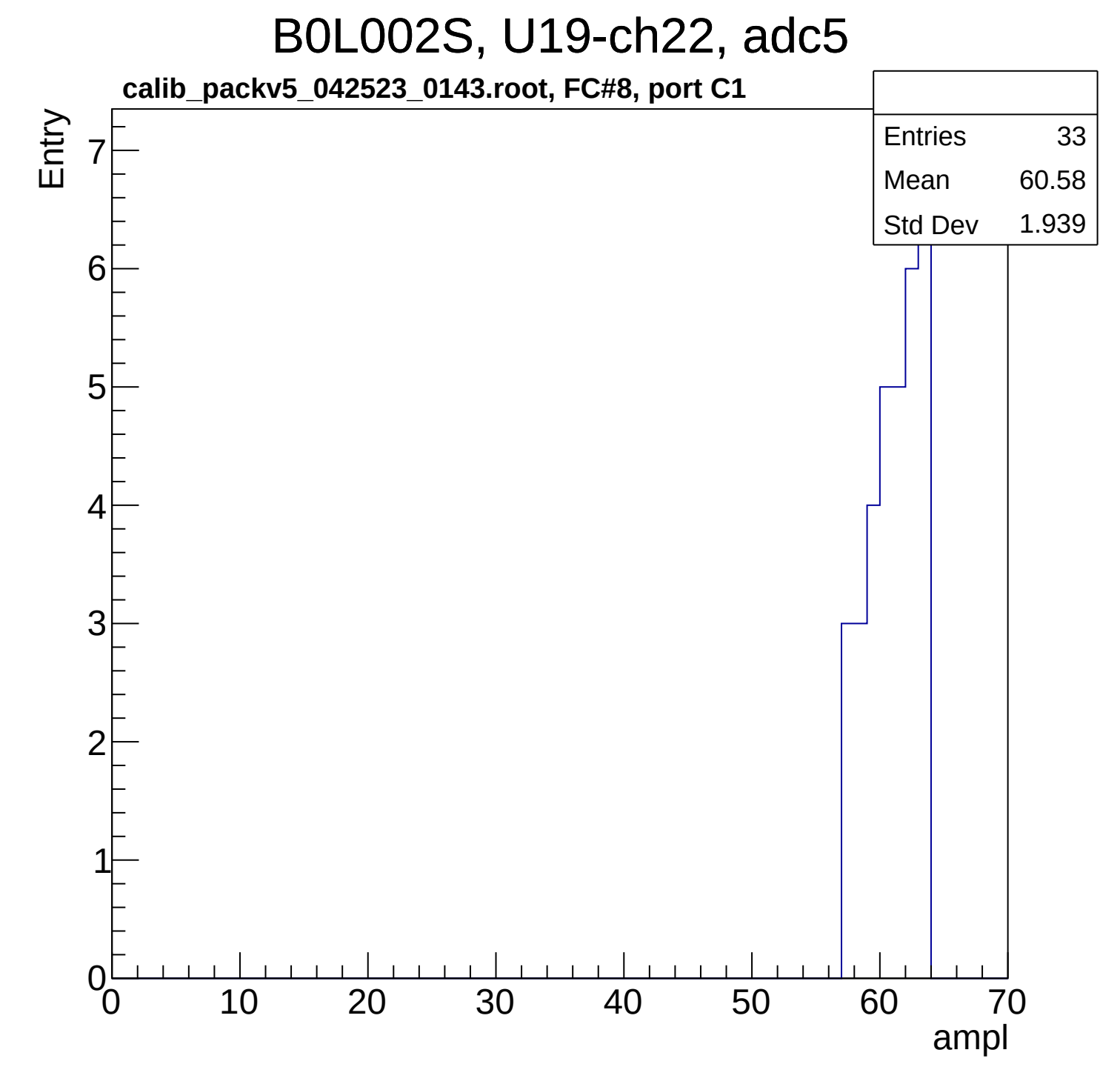

B0L002S, U19-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 29.57
Std Dev 2.764
Gaus mean : 29.5472
Gaus Width: 3.1121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

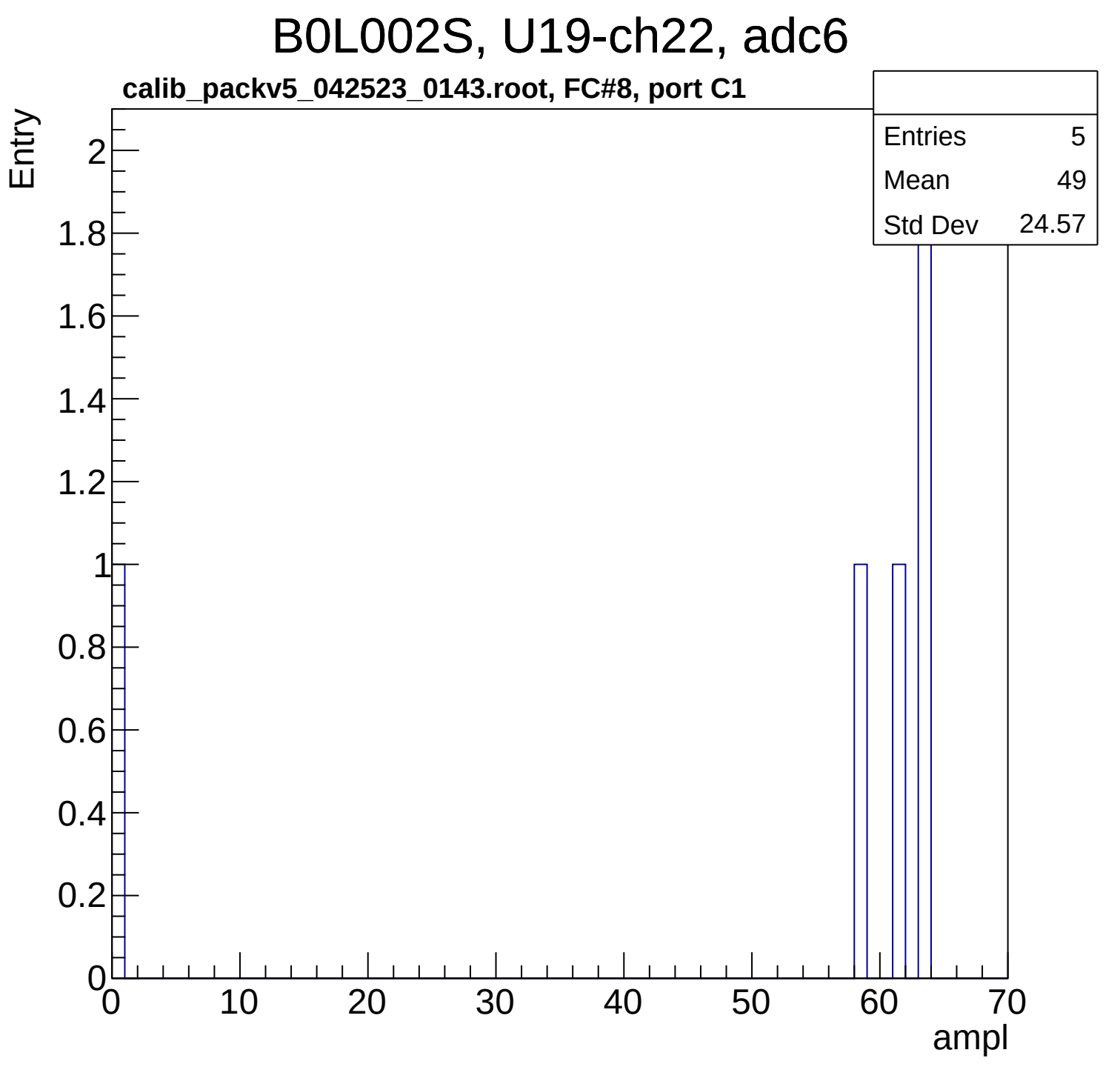

B0L002S, U19-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 36.21
Std Dev 3.361
Gaus mean : 36.7890
Gaus Width: 3.5917
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

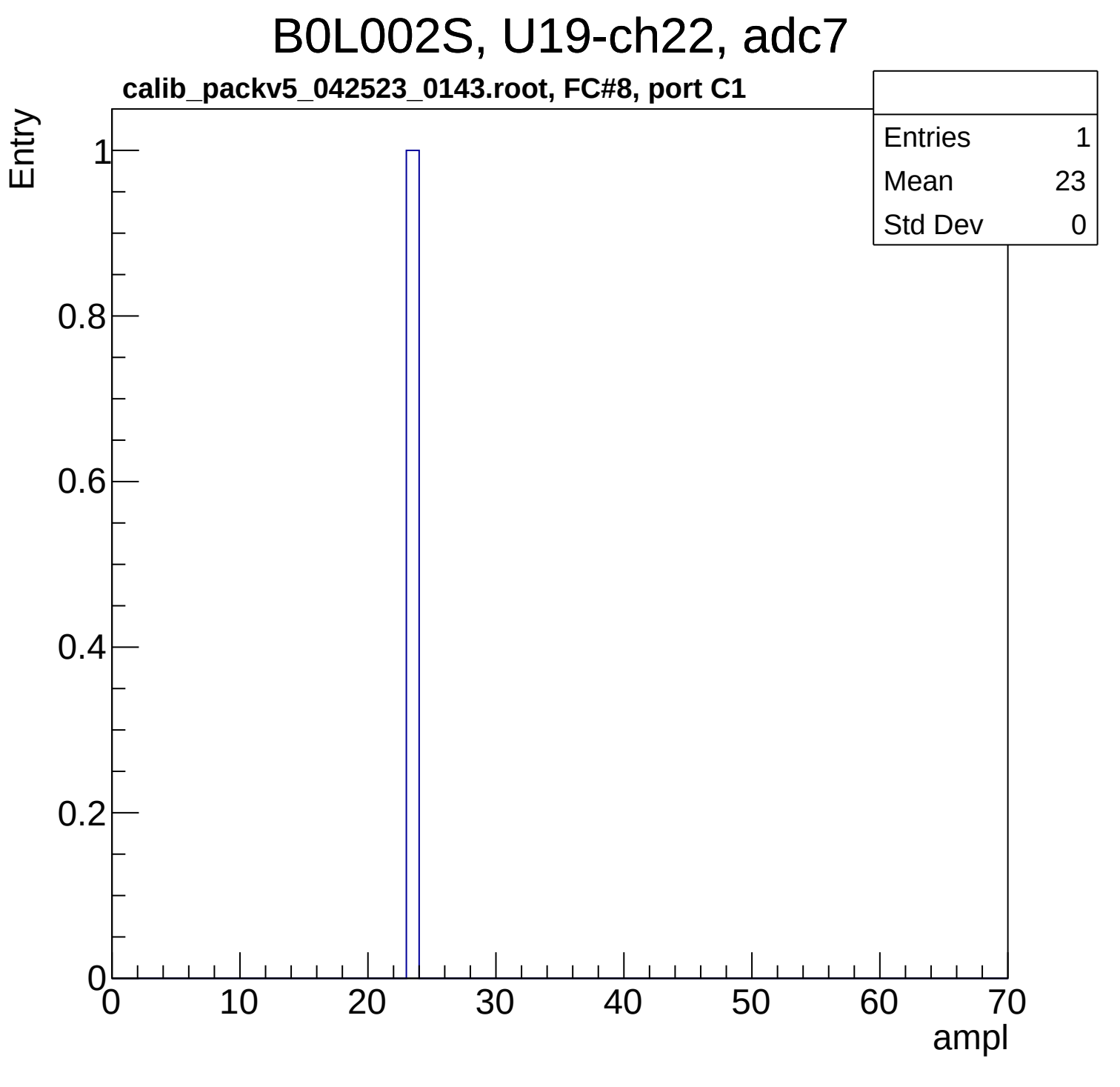

B0L002S, U19-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.15
Std Dev 3.505
Gaus mean : 44.4722
Gaus Width: 3.8312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

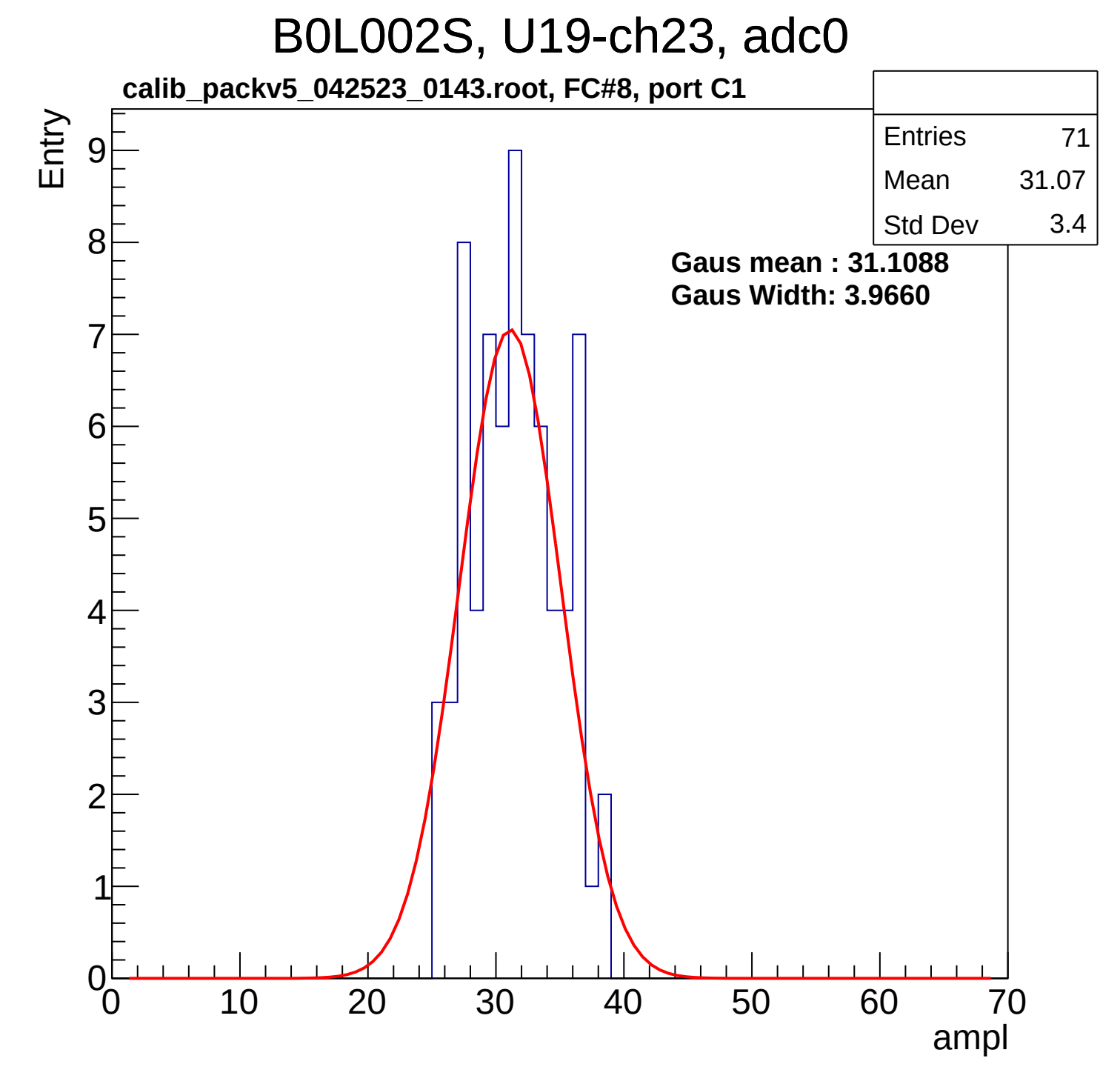

B0L002S, U19-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.85
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

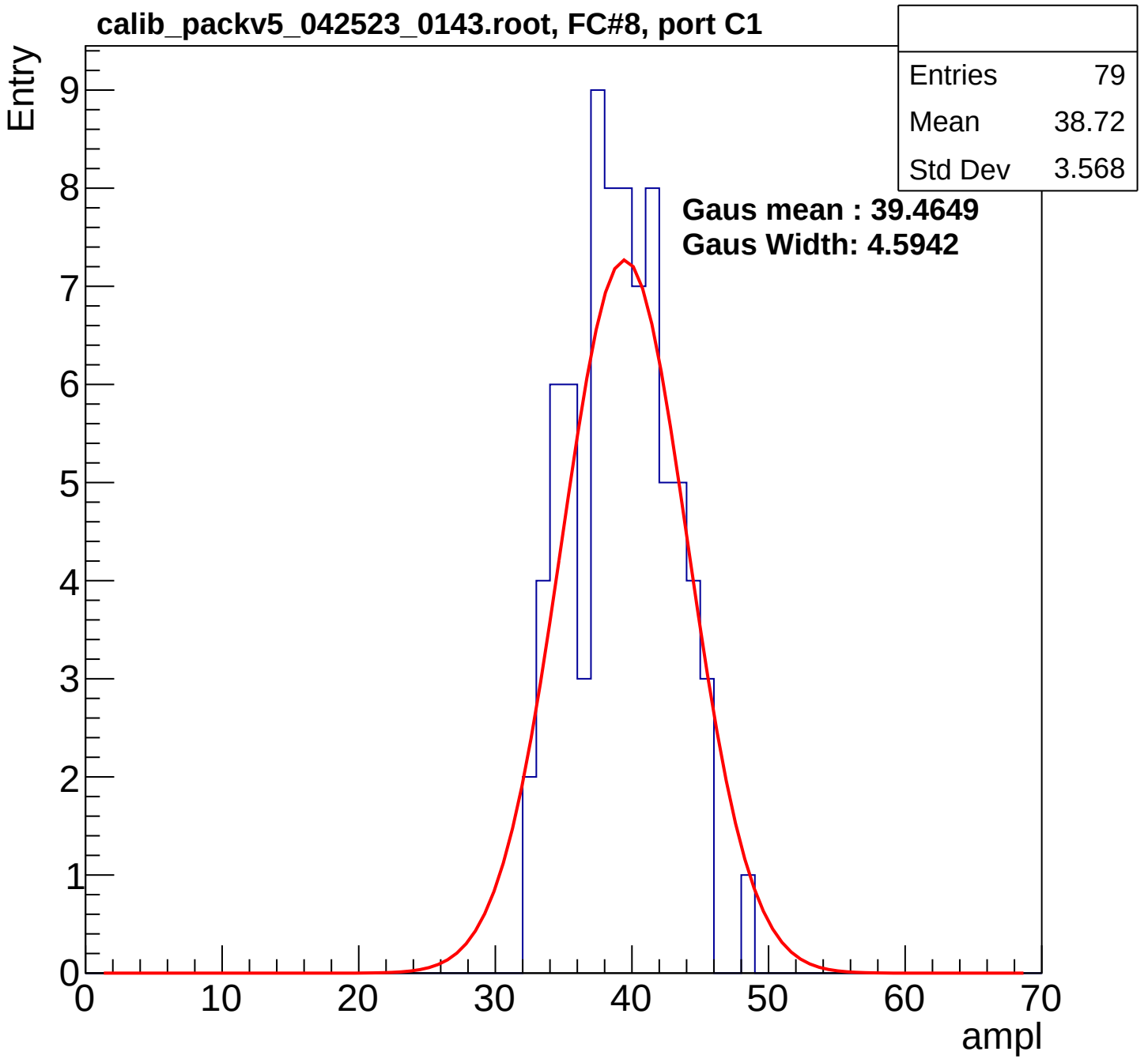

B0L002S, U19-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.97
Std Dev 2.98
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

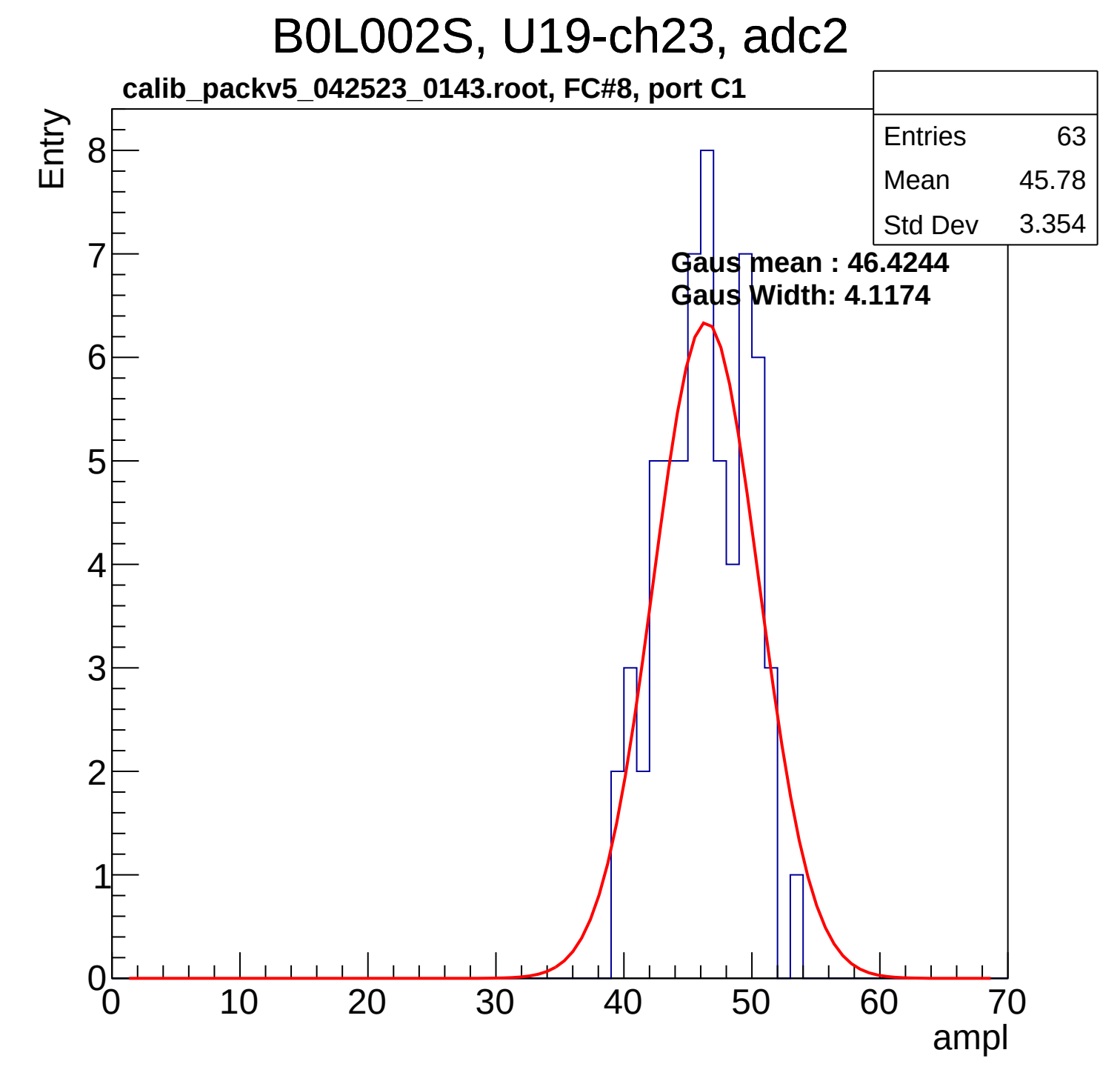

B0L002S, U19-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 60.58
Std Dev 1.939
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

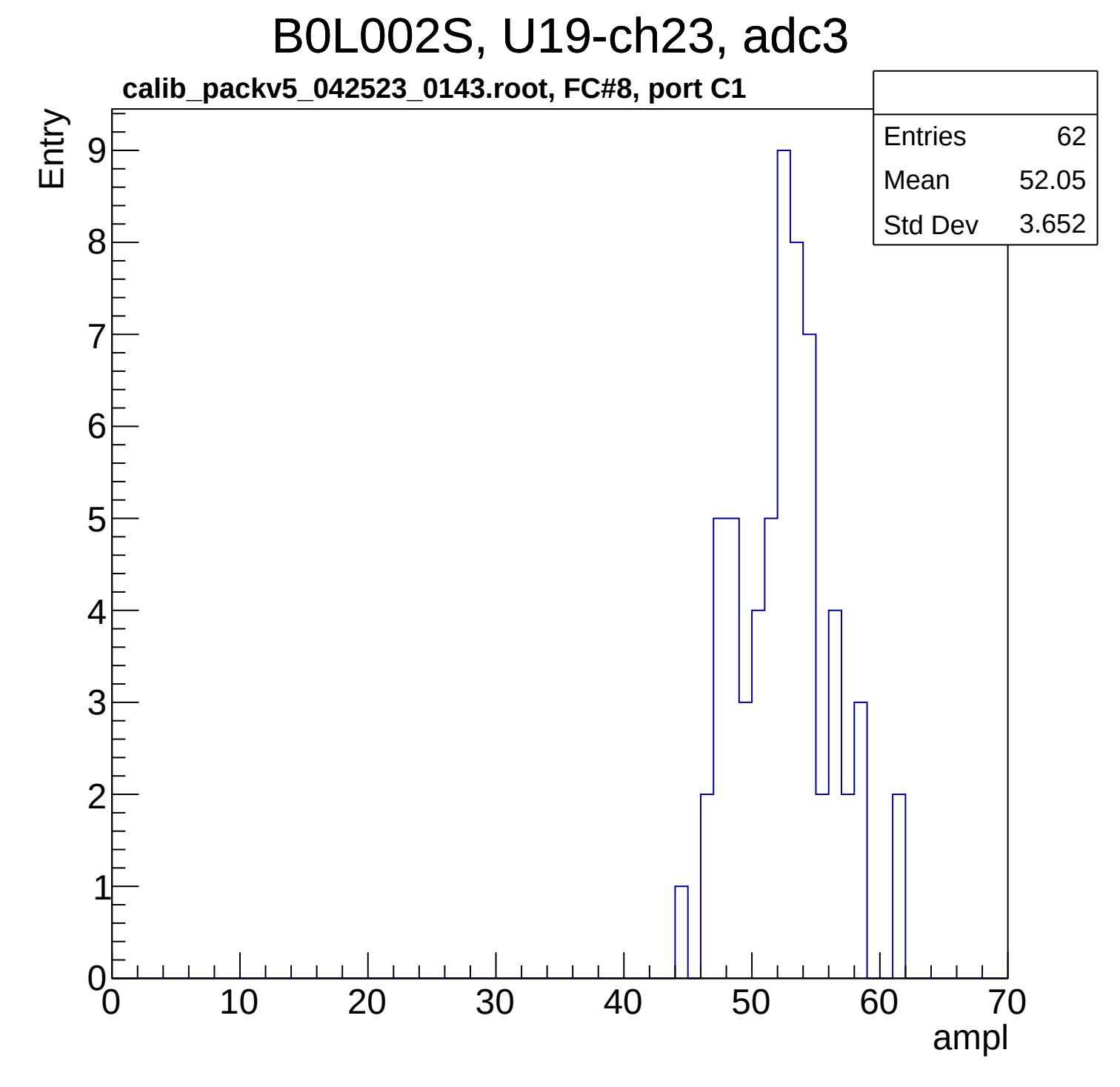

B0L002S, U19-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49
Std Dev 24.57
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

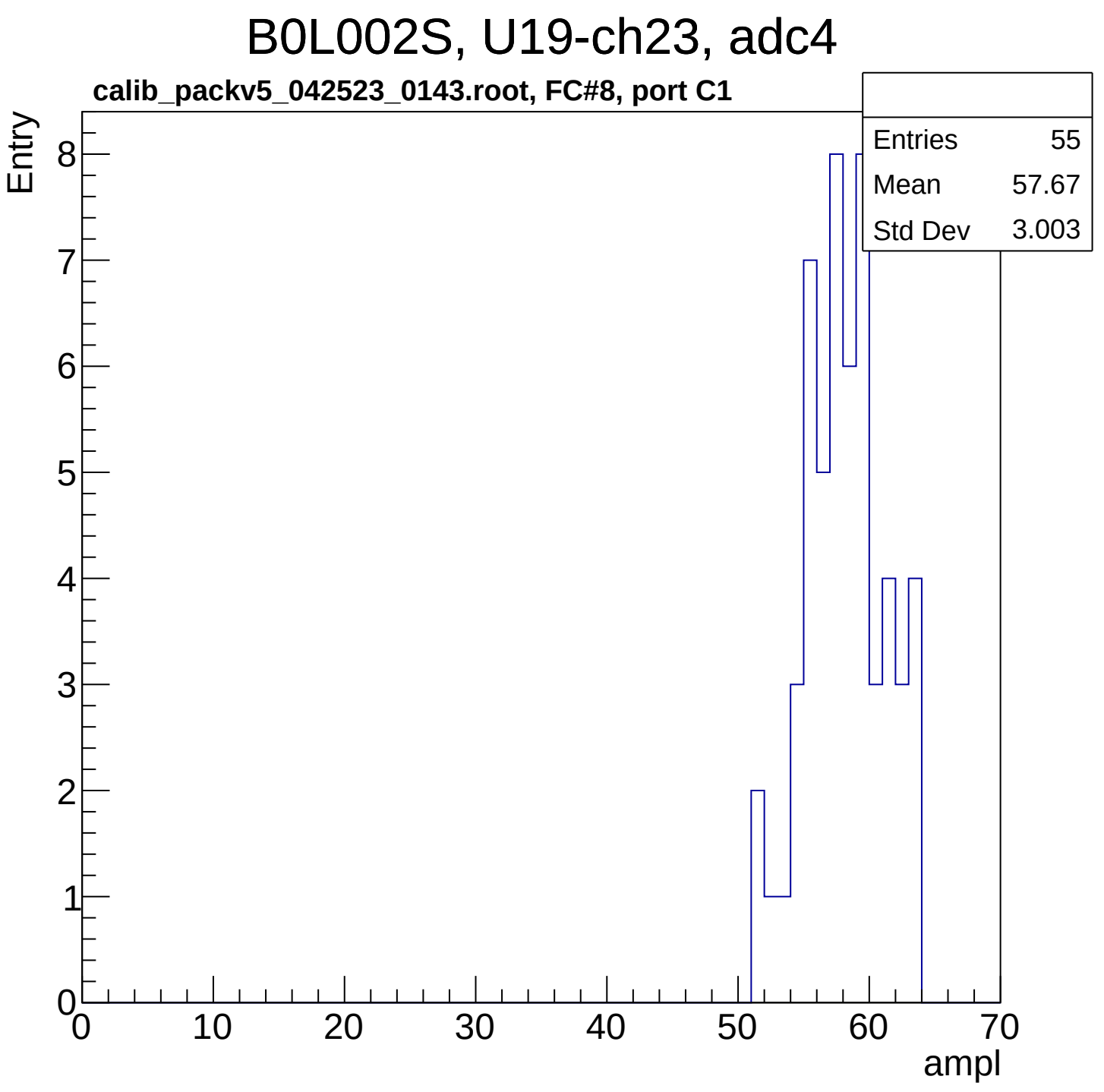

B0L002S, U19-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

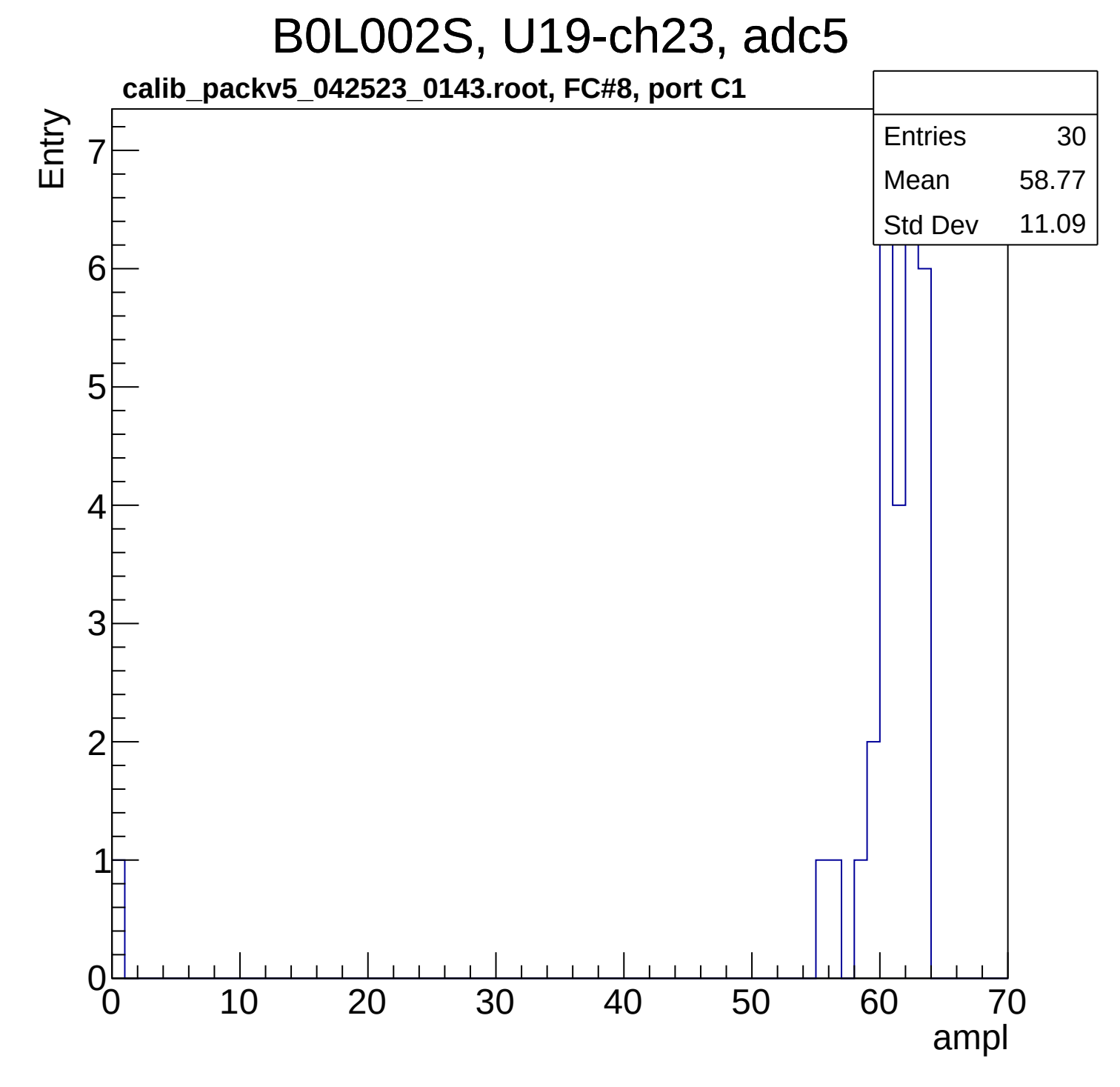

B0L002S, U19-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.07
Std Dev 3.4
Gaus mean : 31.1088
Gaus Width: 3.9660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

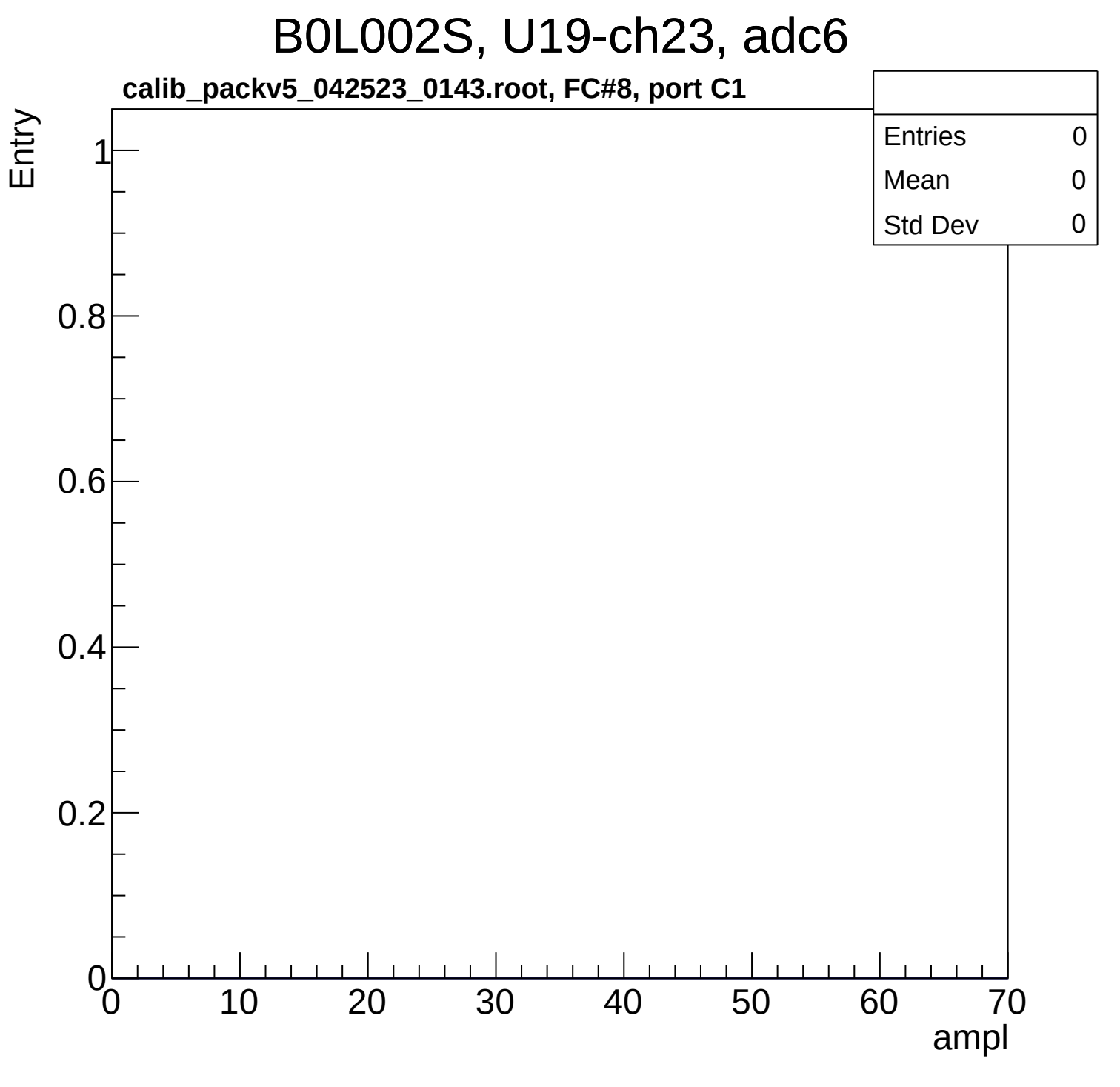

B0L002S, U19-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 38.72
Std Dev 3.568
Gaus mean : 39.4649
Gaus Width: 4.5942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

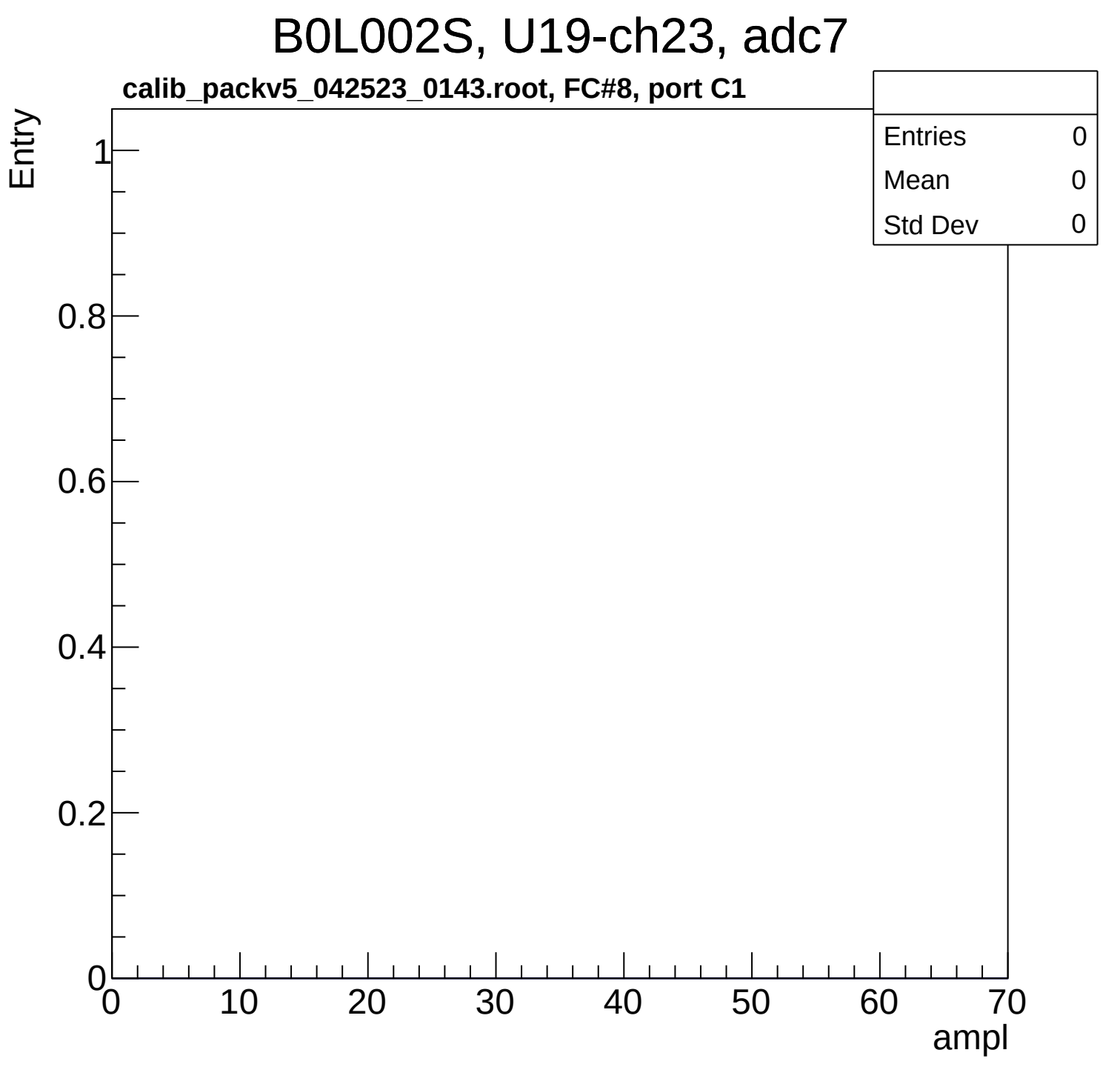

B0L002S, U19-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 45.78
Std Dev 3.354
Gaus mean : 46.4244
Gaus Width: 4.1174
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

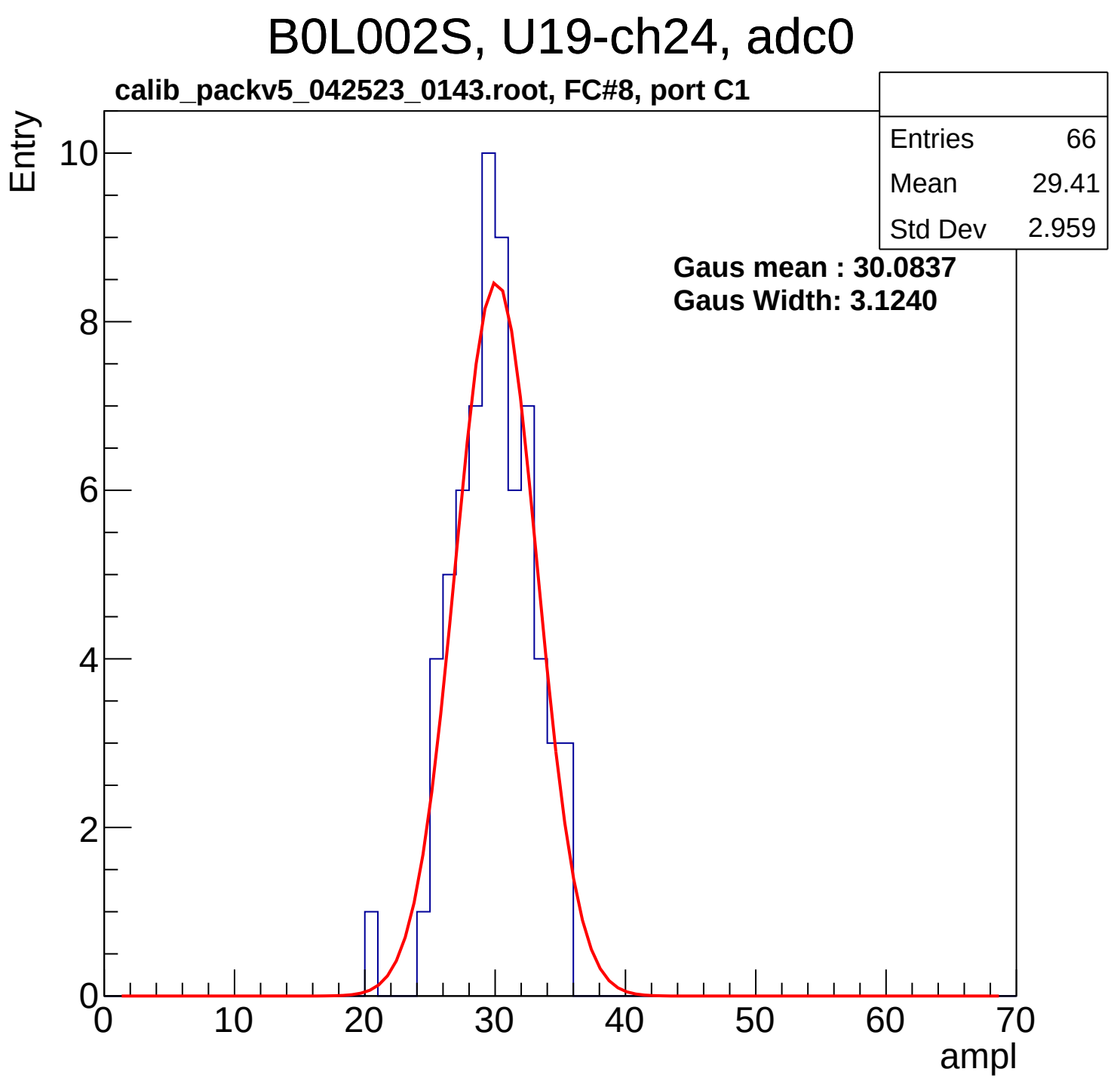

B0L002S, U19-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 52.05
Std Dev 3.652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

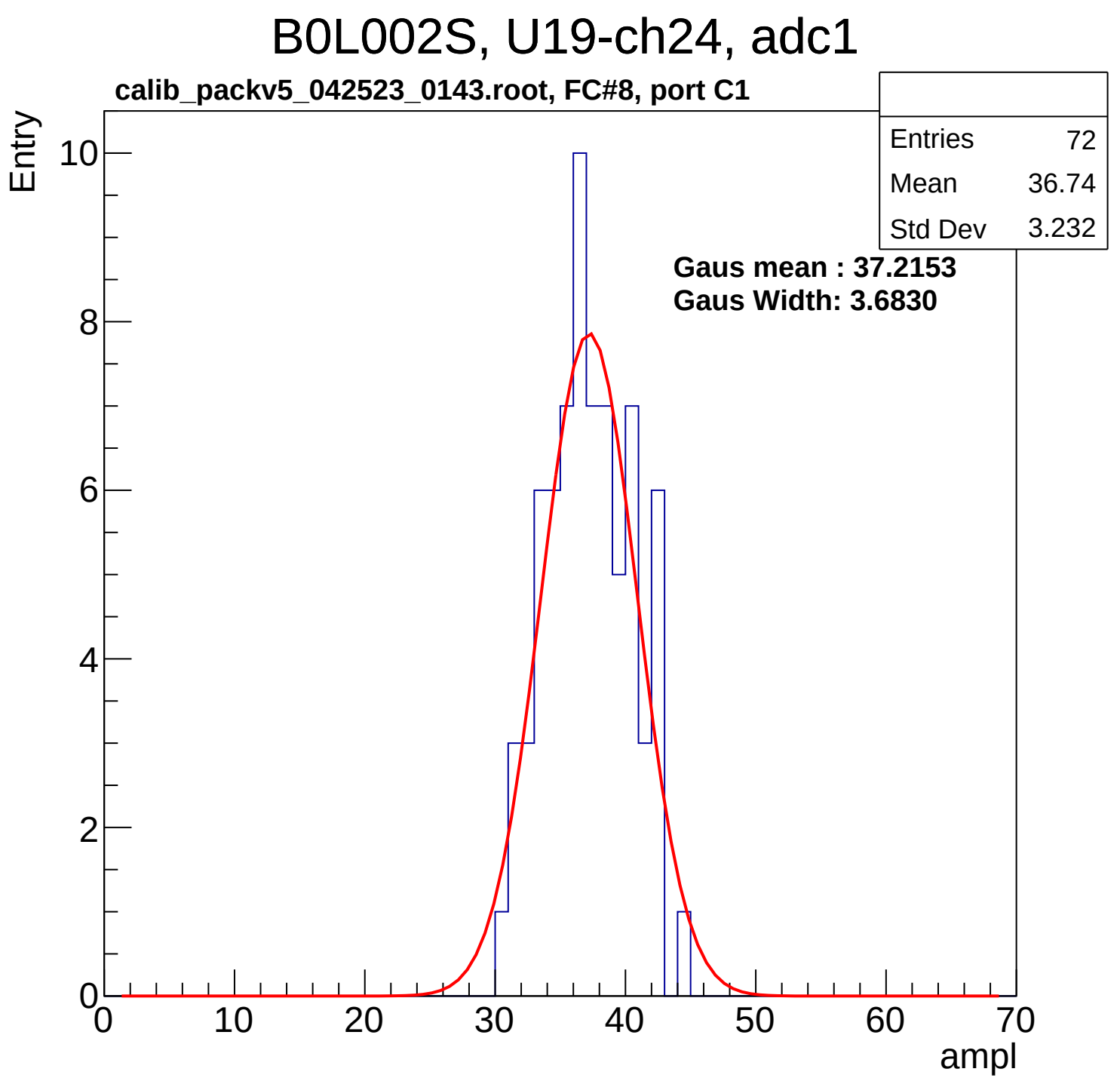

B0L002S, U19-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.67
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

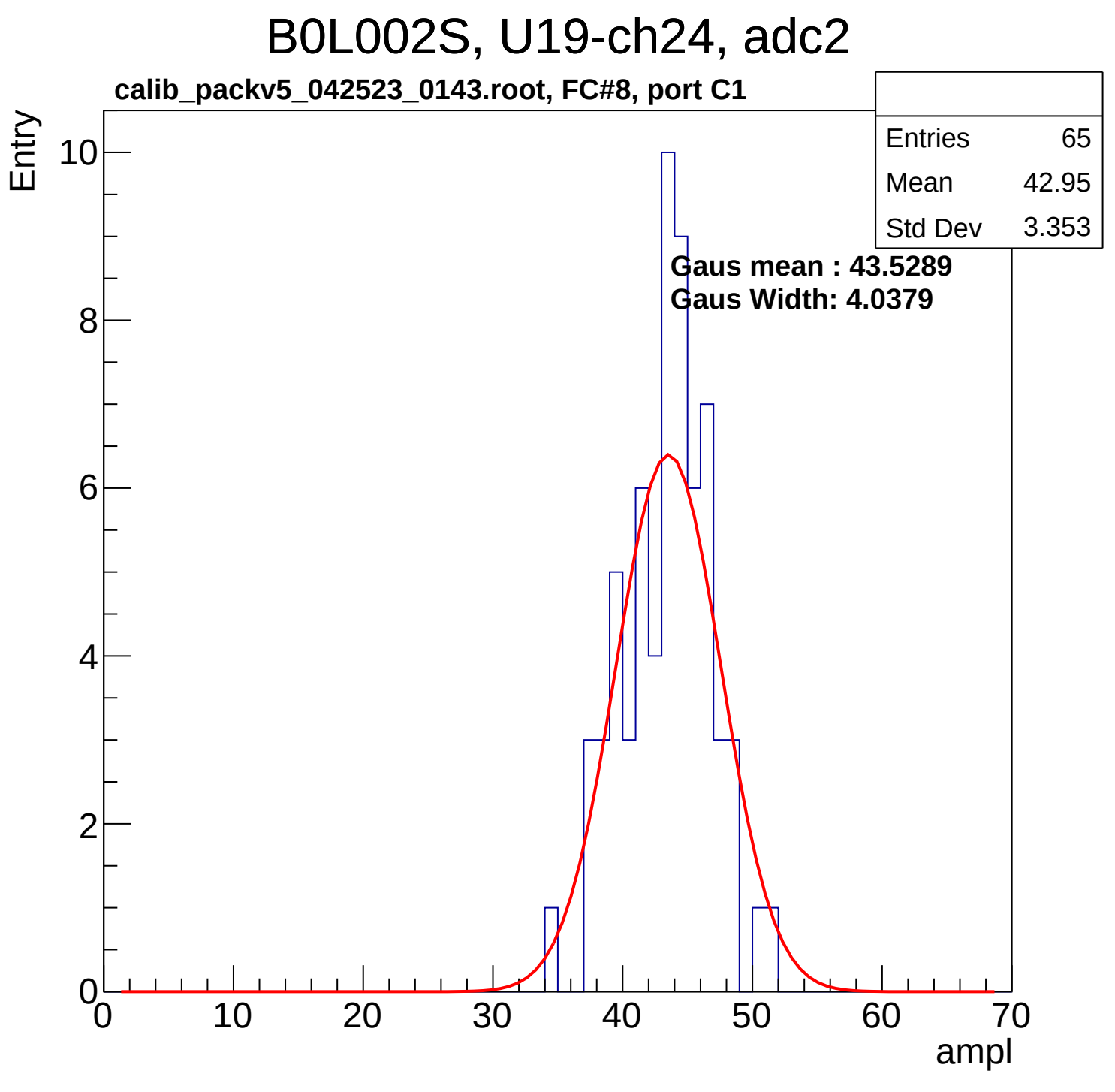

B0L002S, U19-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.77
Std Dev 11.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

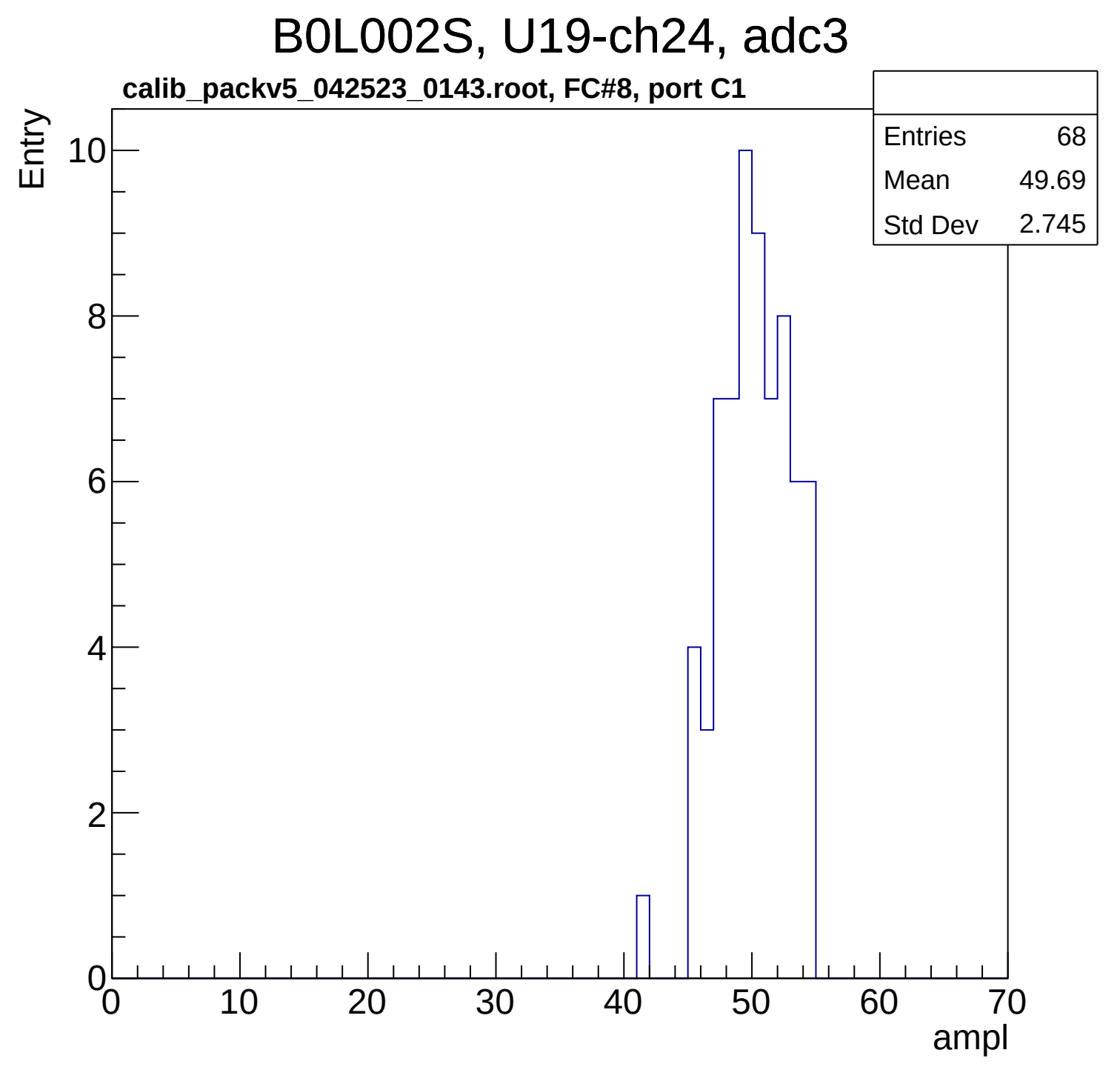

B0L002S, U19-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

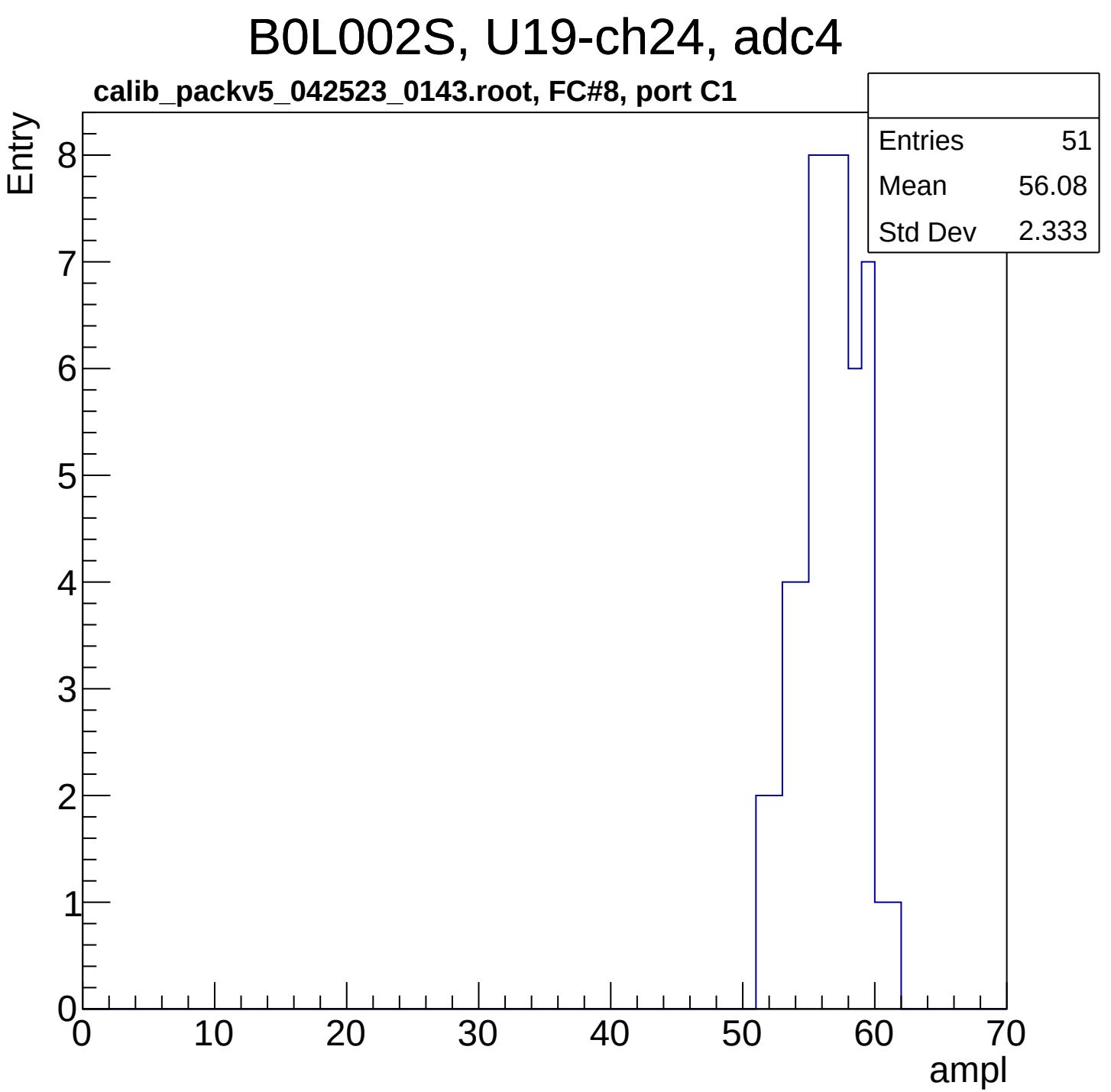

B0L002S, U19-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

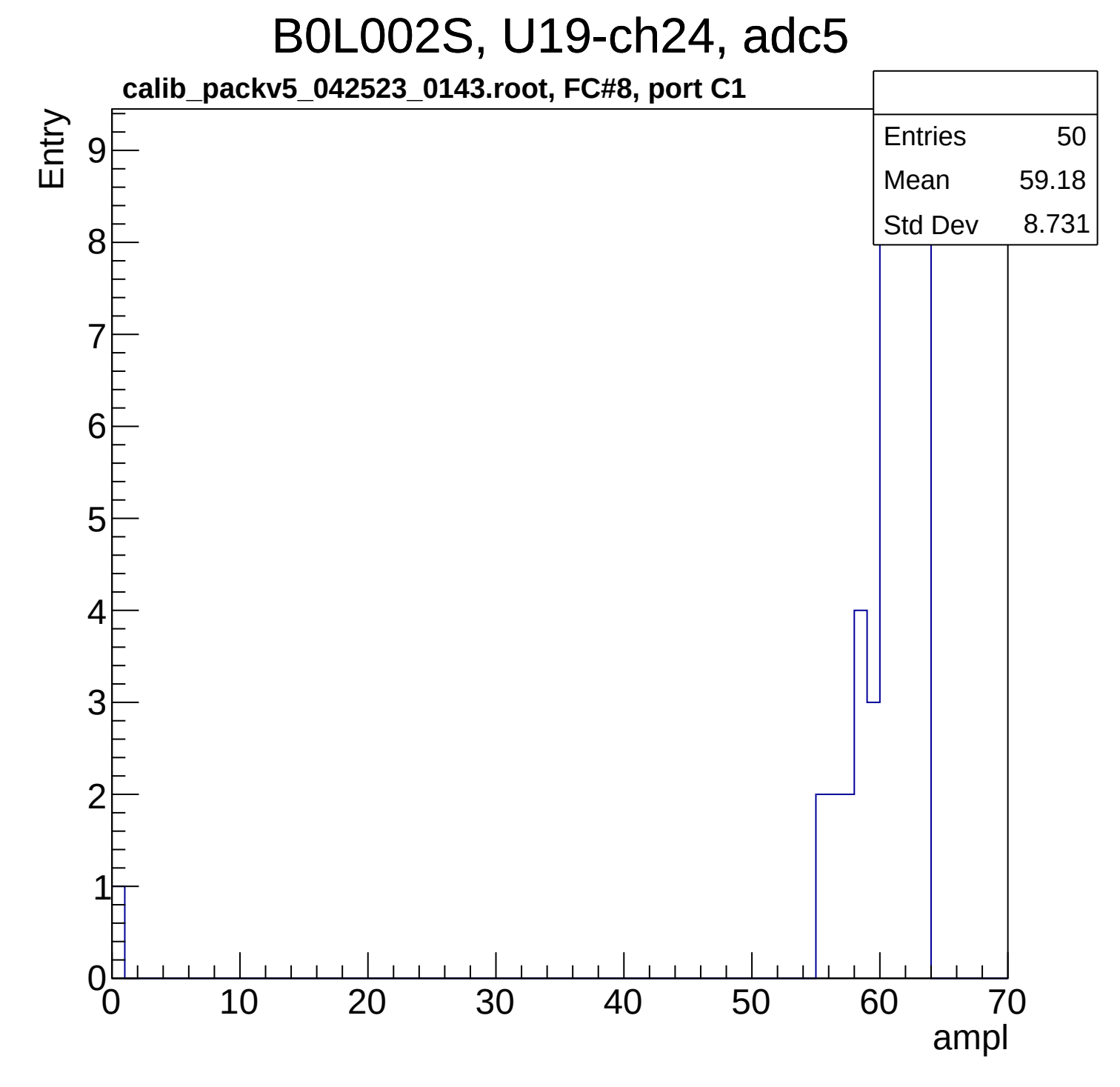

B0L002S, U19-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.41
Std Dev 2.959
Gaus mean : 30.0837
Gaus Width: 3.1240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

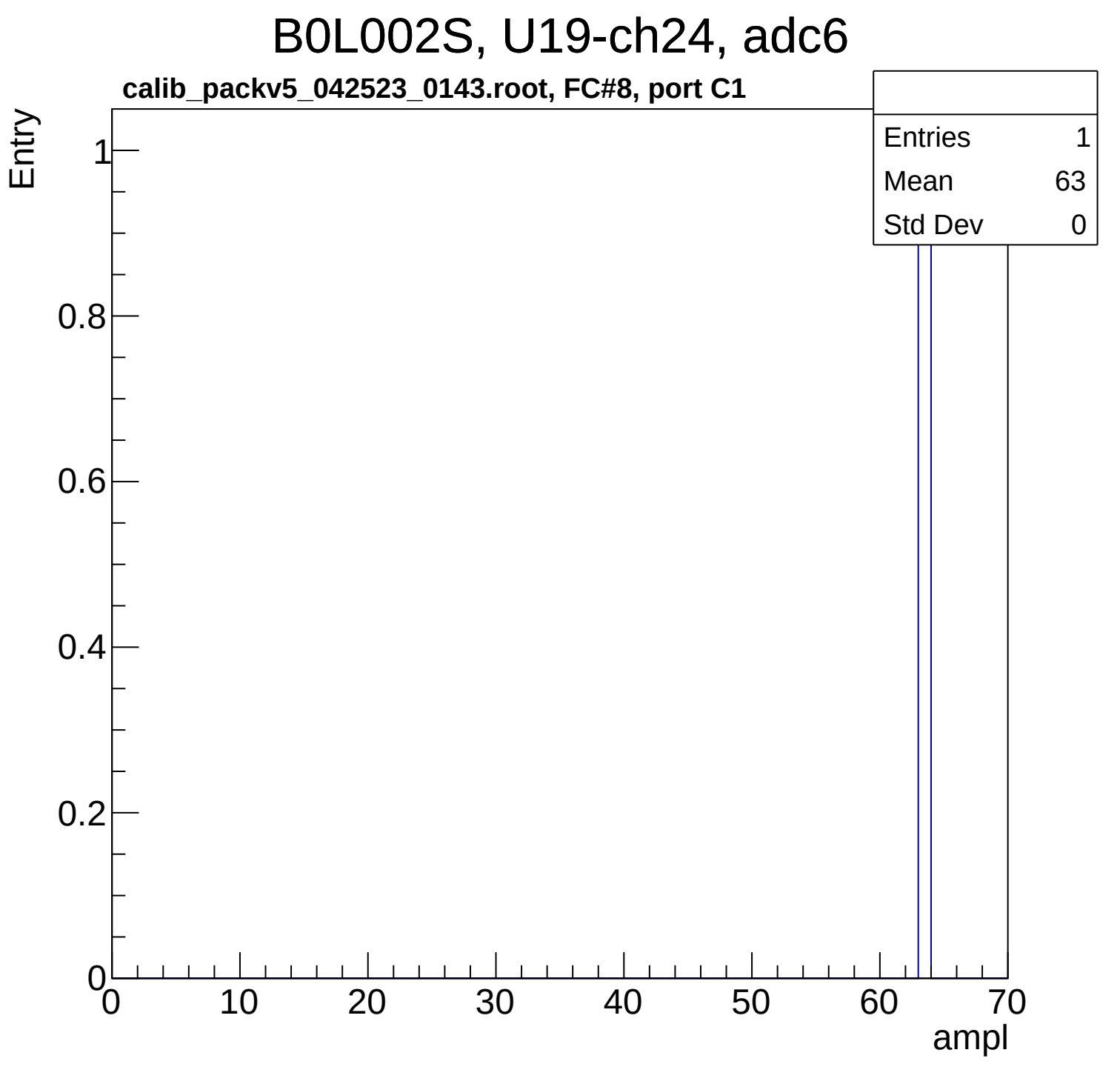

B0L002S, U19-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.74
Std Dev 3.232
Gaus mean : 37.2153
Gaus Width: 3.6830
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

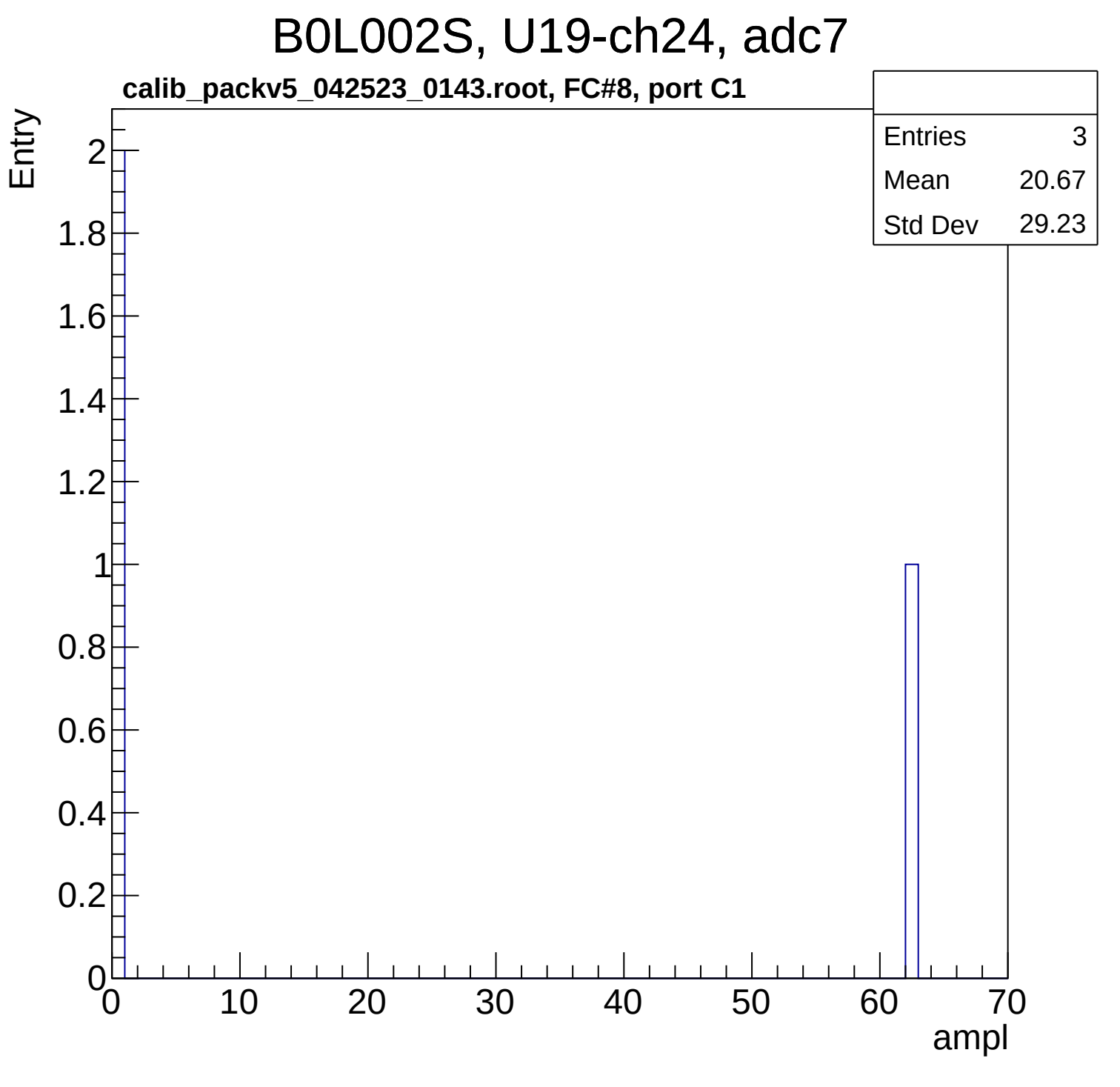

B0L002S, U19-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 42.95
Std Dev 3.353
Gaus mean : 43.5289
Gaus Width: 4.0379
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

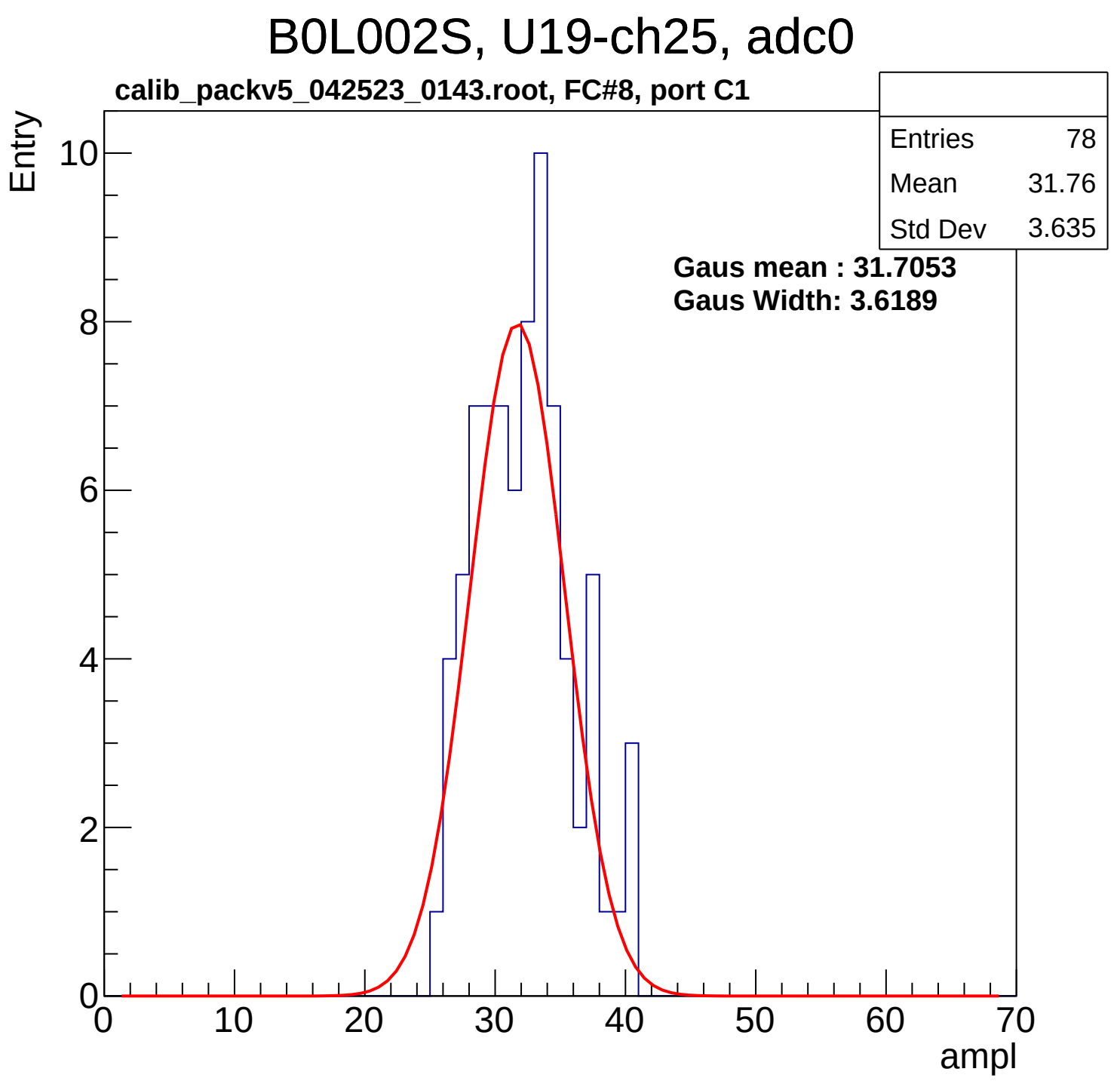

B0L002S, U19-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 49.69
Std Dev 2.745
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

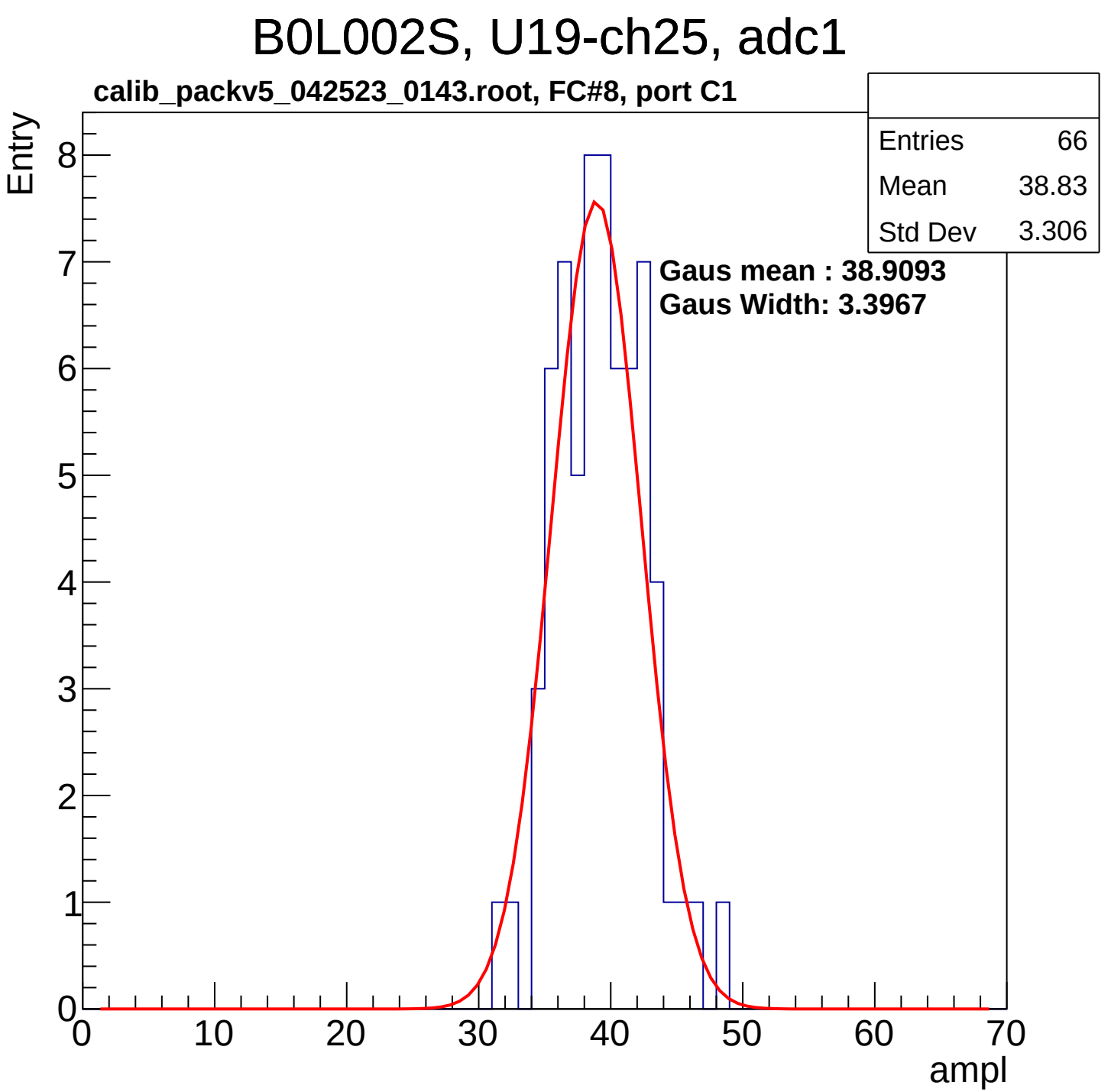

B0L002S, U19-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.08
Std Dev 2.333
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

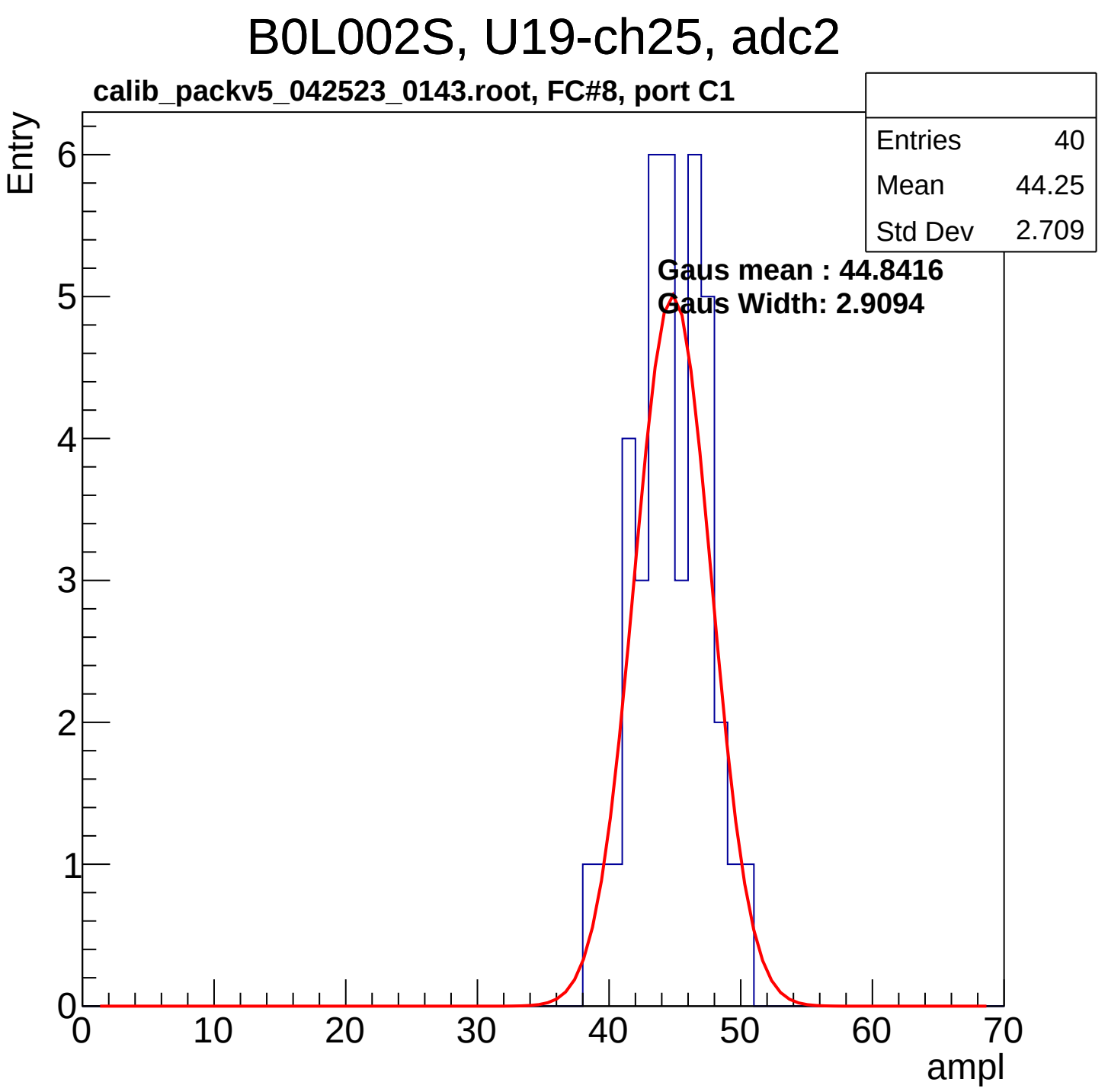

B0L002S, U19-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 59.18
Std Dev 8.731
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

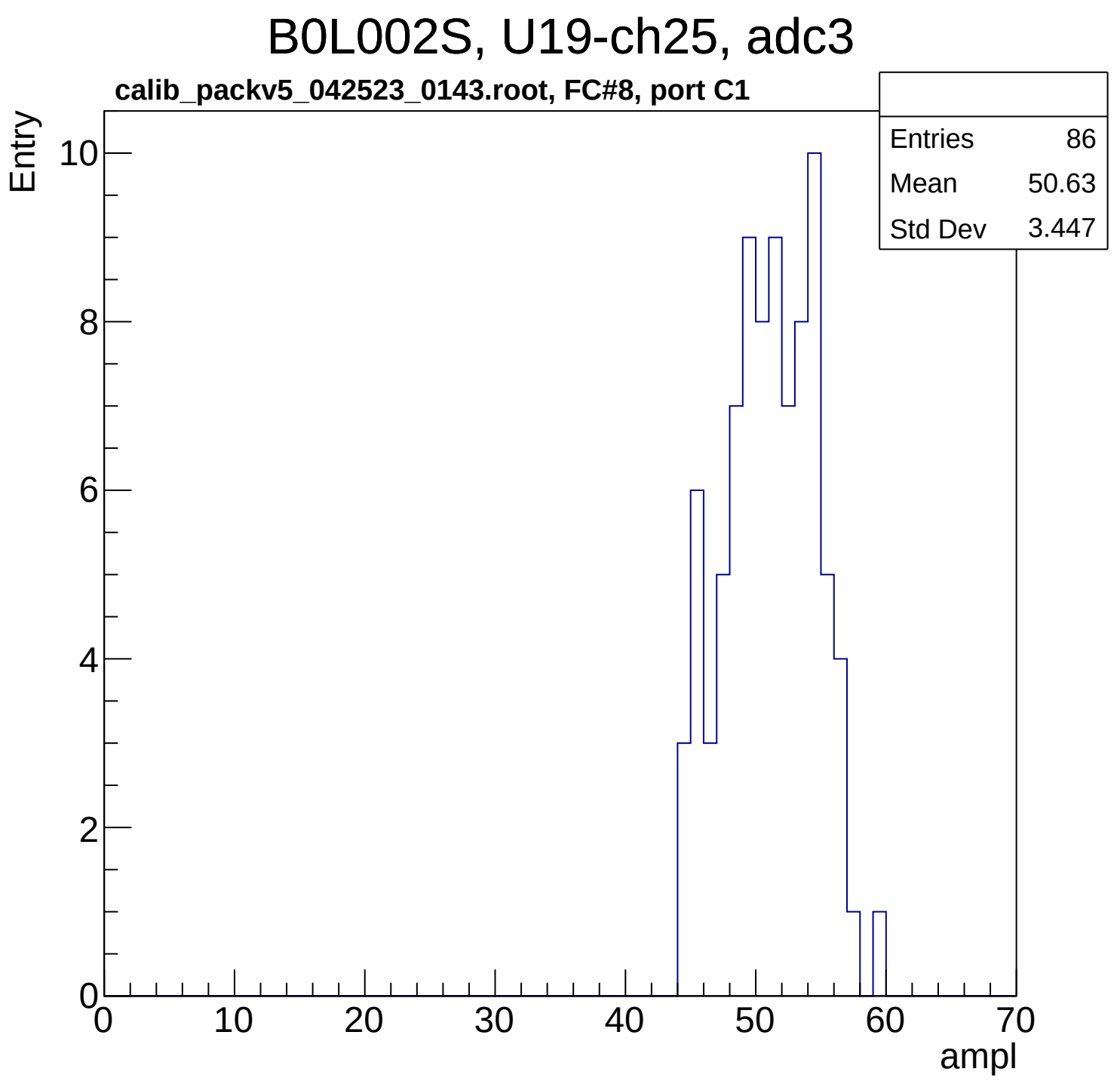

B0L002S, U19-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

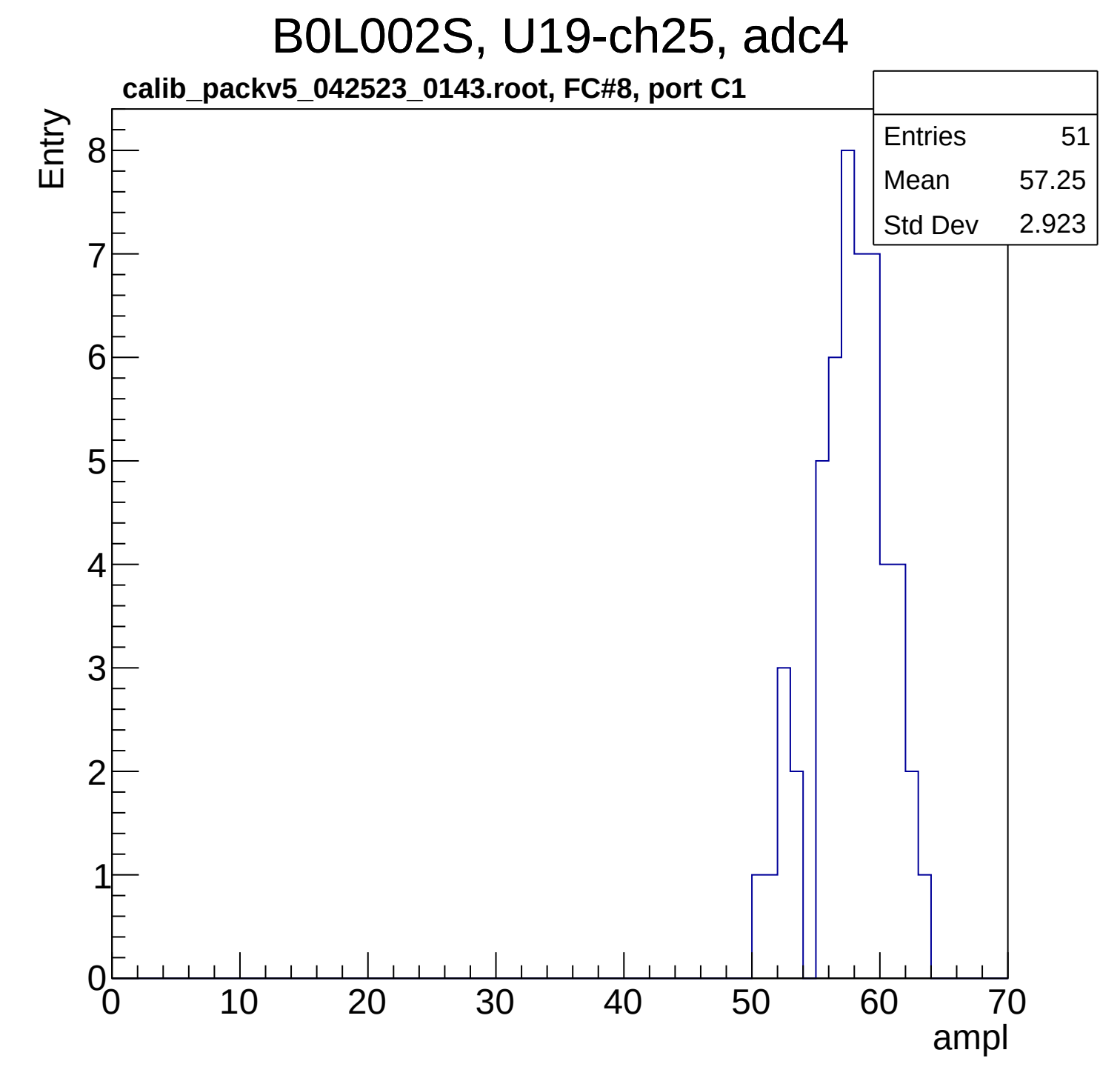

B0L002S, U19-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

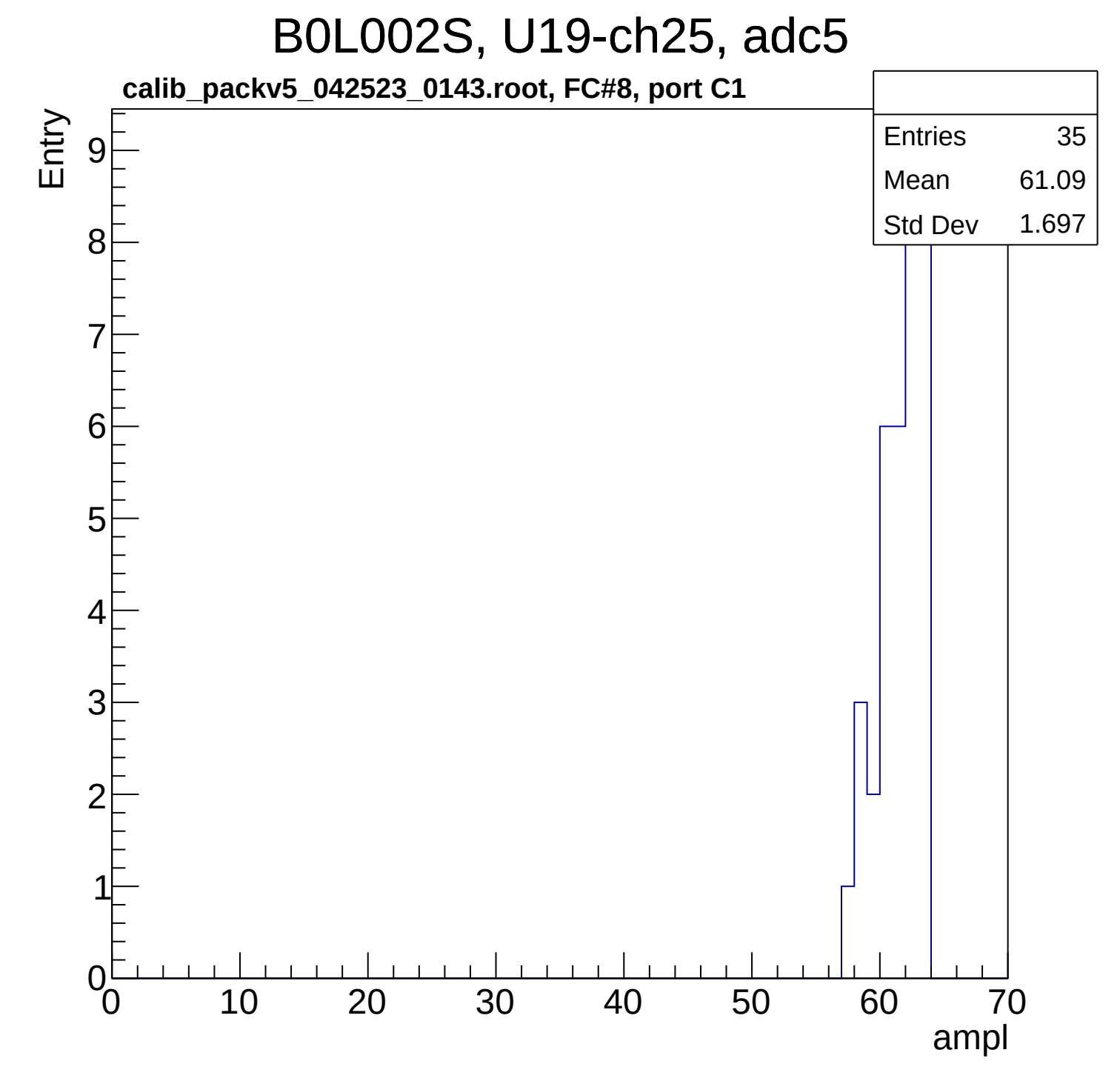

B0L002S, U19-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 31.76
Std Dev 3.635
Gaus mean : 31.7053
Gaus Width: 3.6189
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

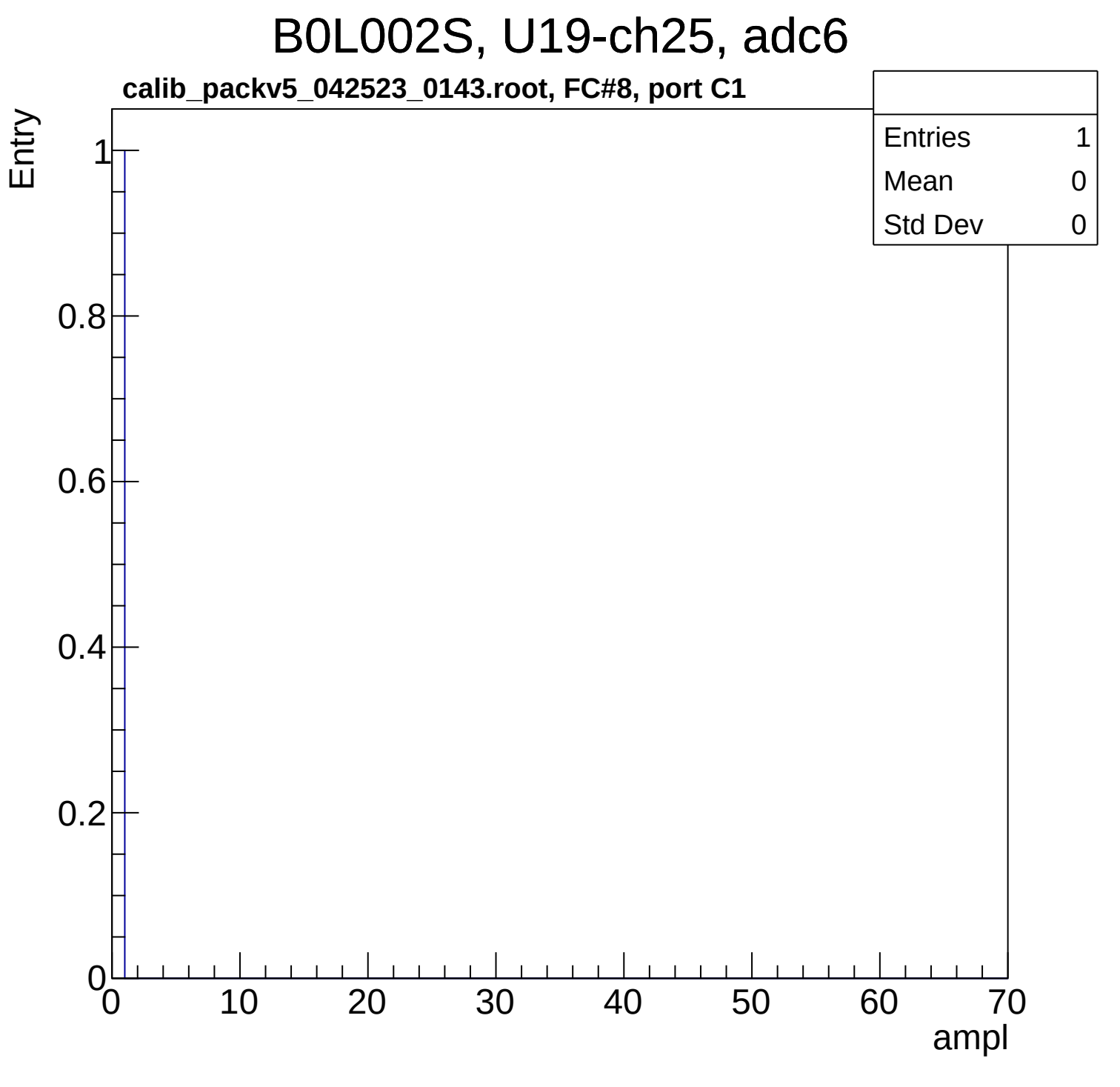

B0L002S, U19-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 38.83
Std Dev 3.306
Gaus mean : 38.9093
Gaus Width: 3.3967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

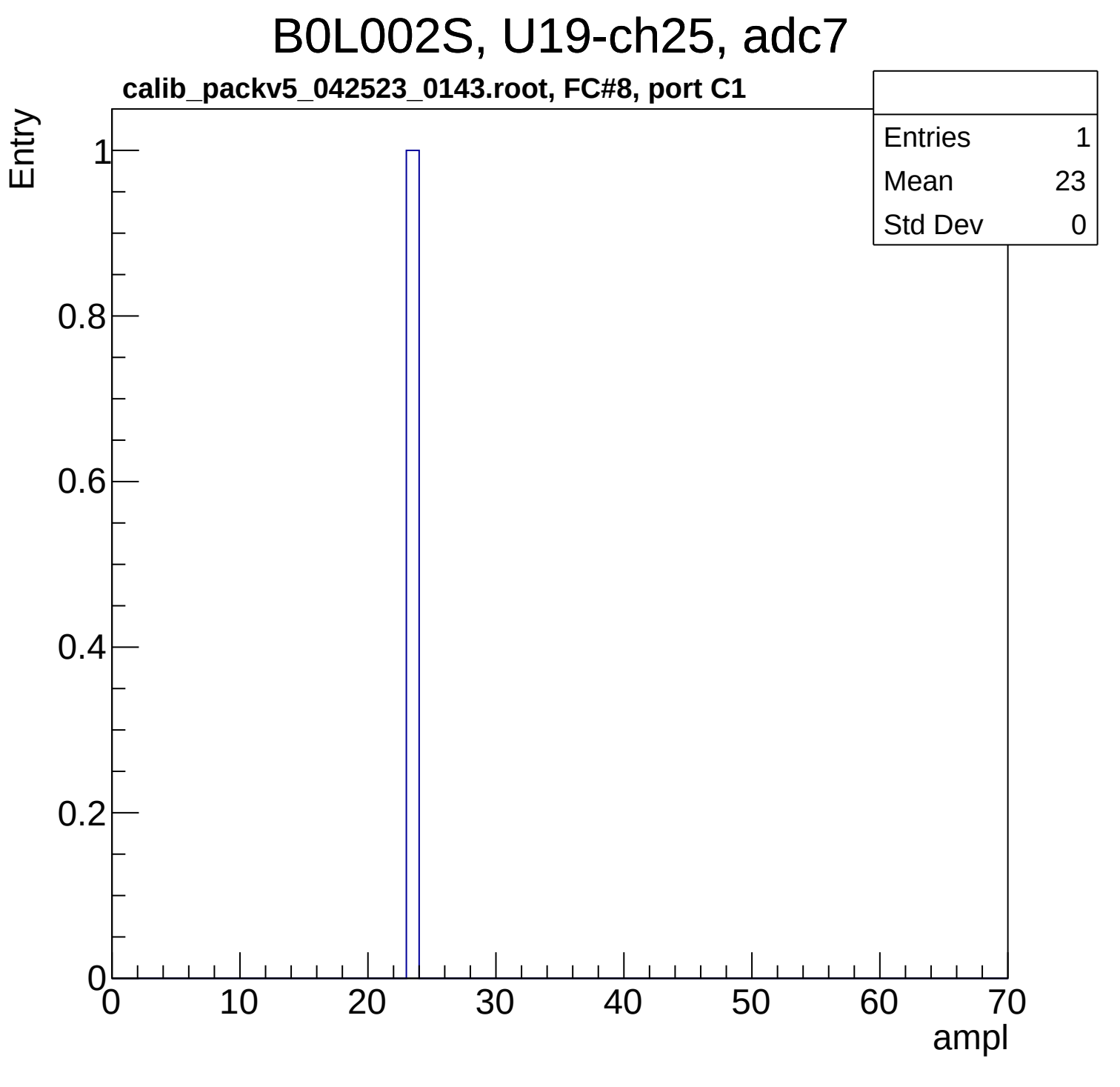

B0L002S, U19-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 44.25
Std Dev 2.709
Gaus mean : 44.8416
Gaus Width: 2.9094
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

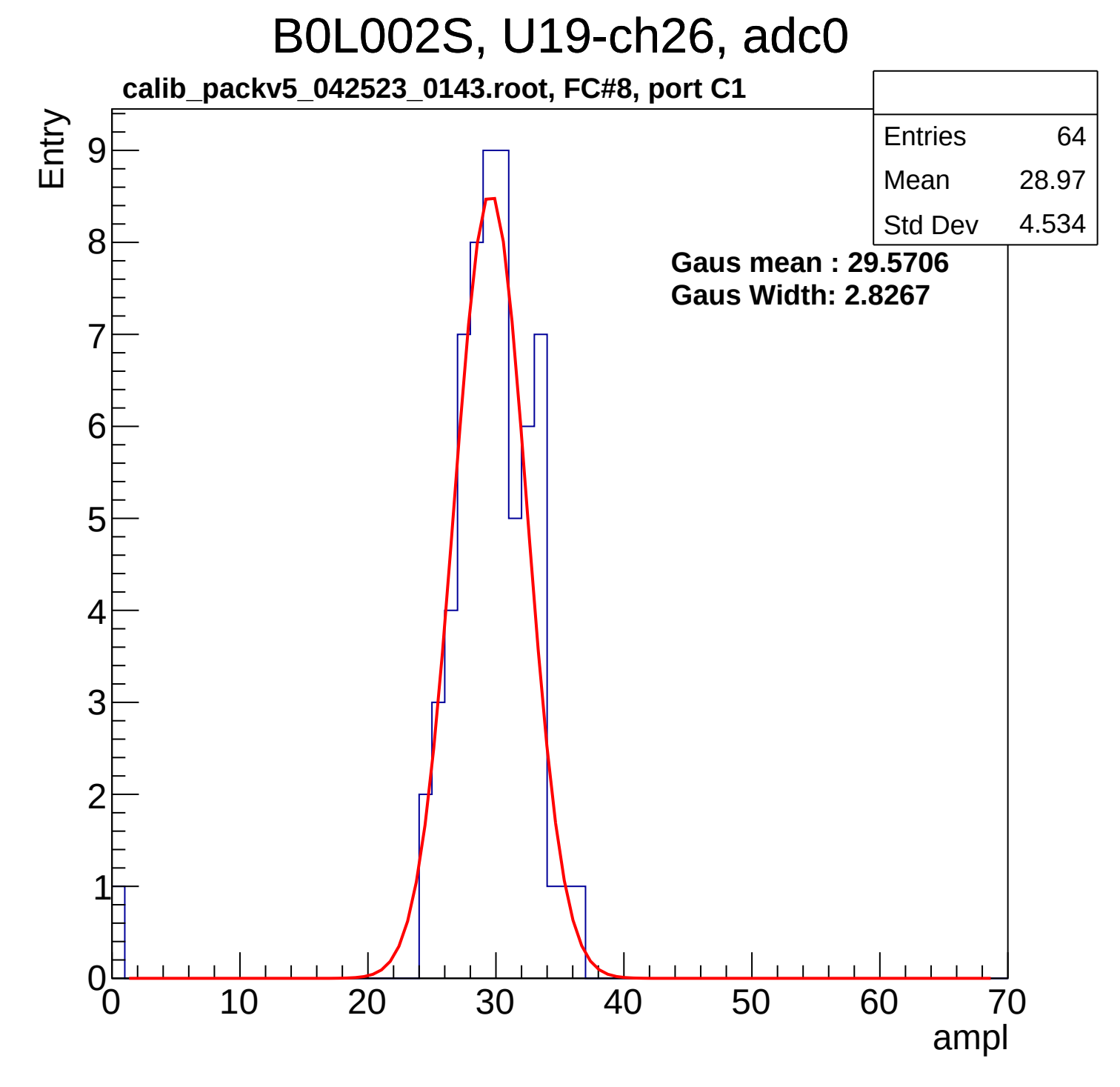

B0L002S, U19-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 50.63
Std Dev 3.447
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

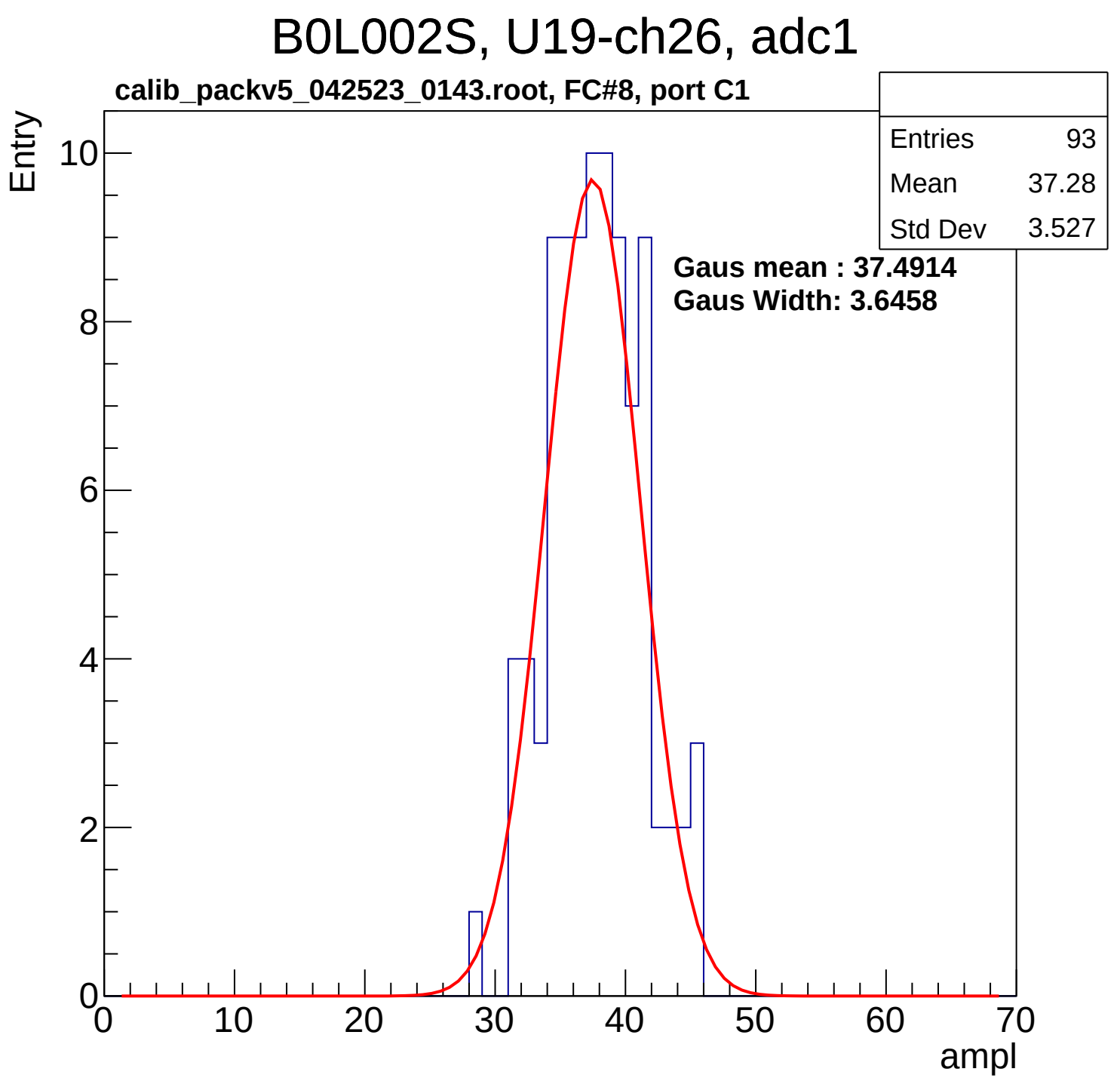

B0L002S, U19-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.25
Std Dev 2.923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

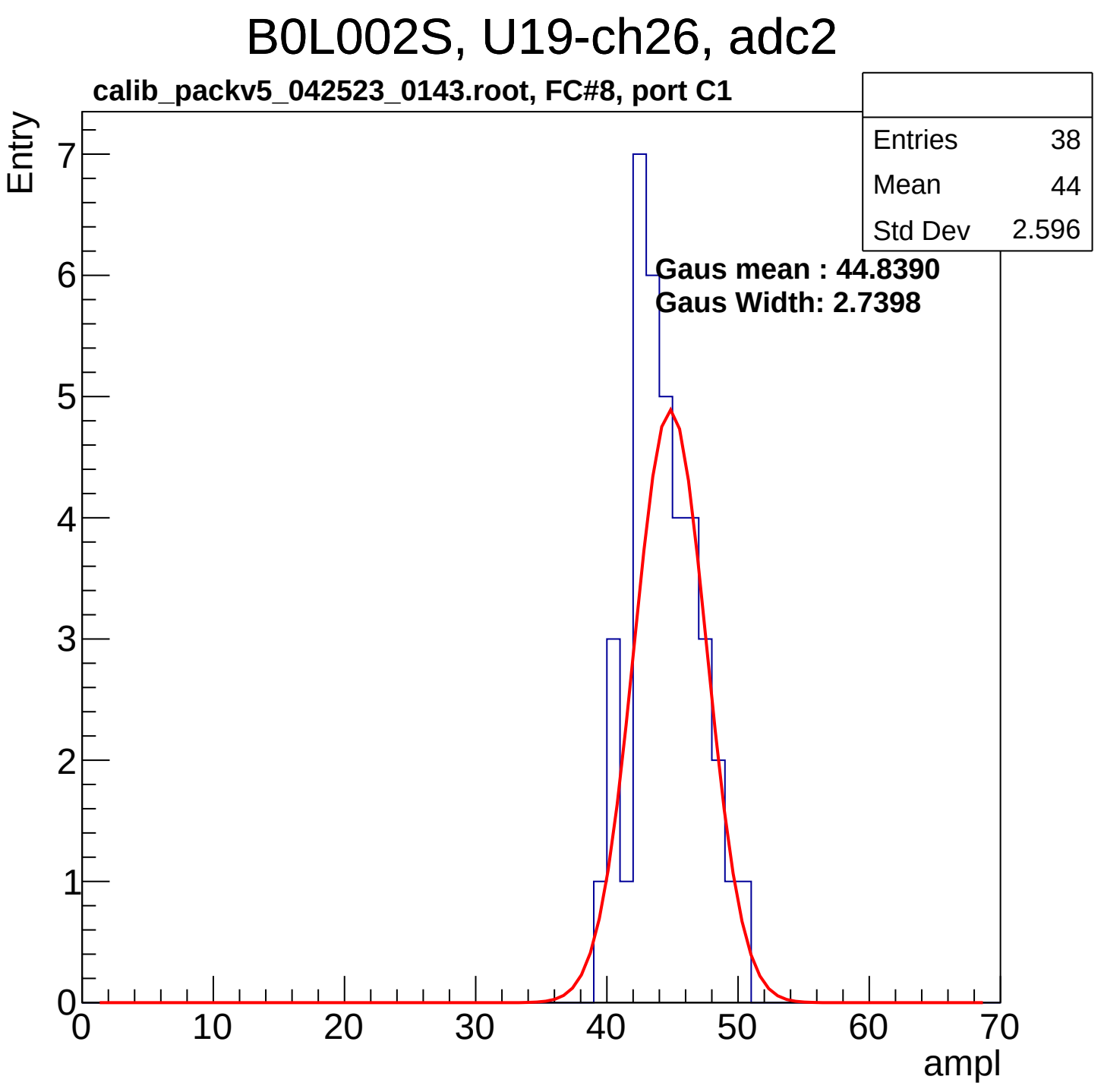

B0L002S, U19-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 61.09
Std Dev 1.697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

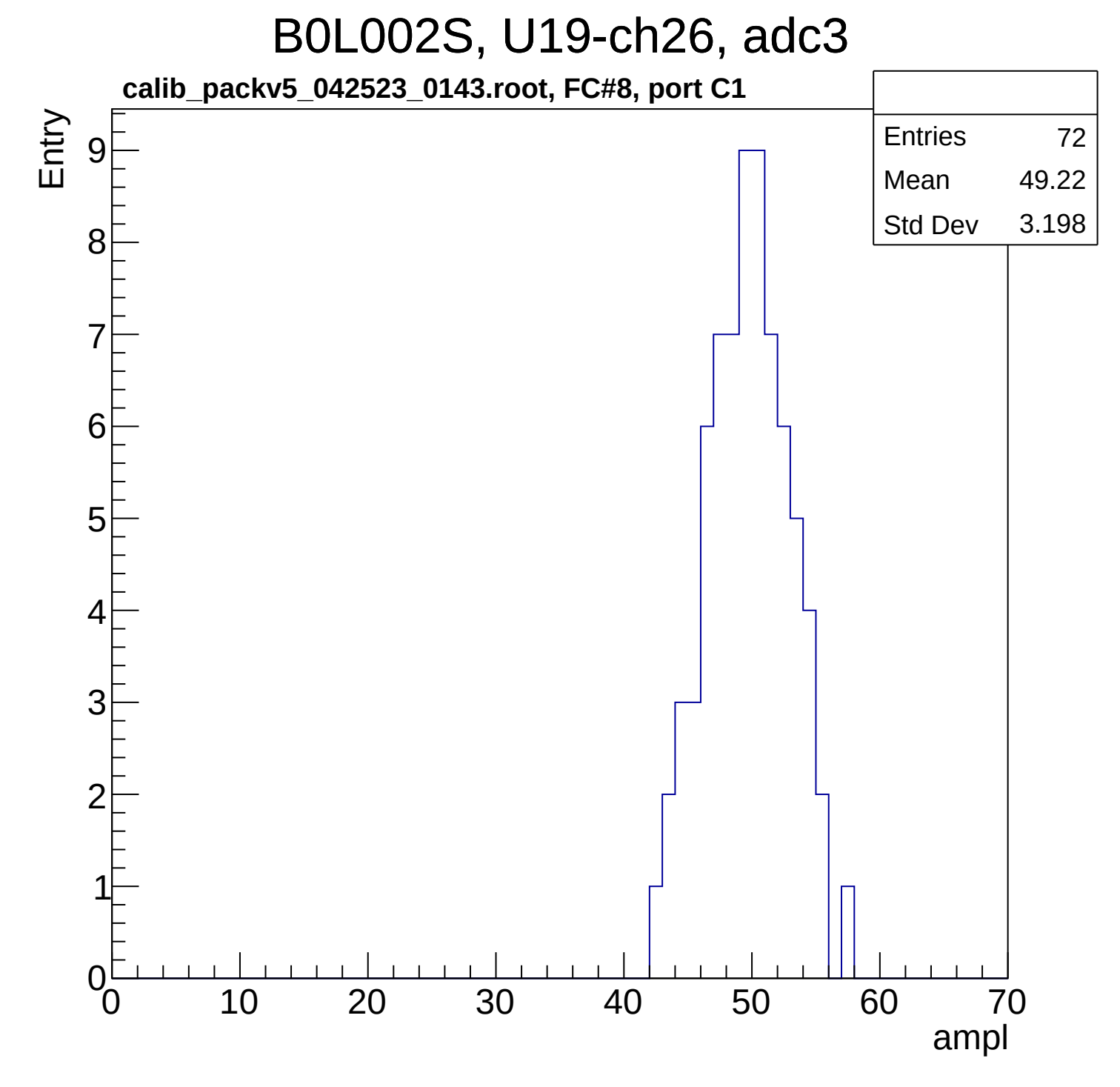

B0L002S, U19-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

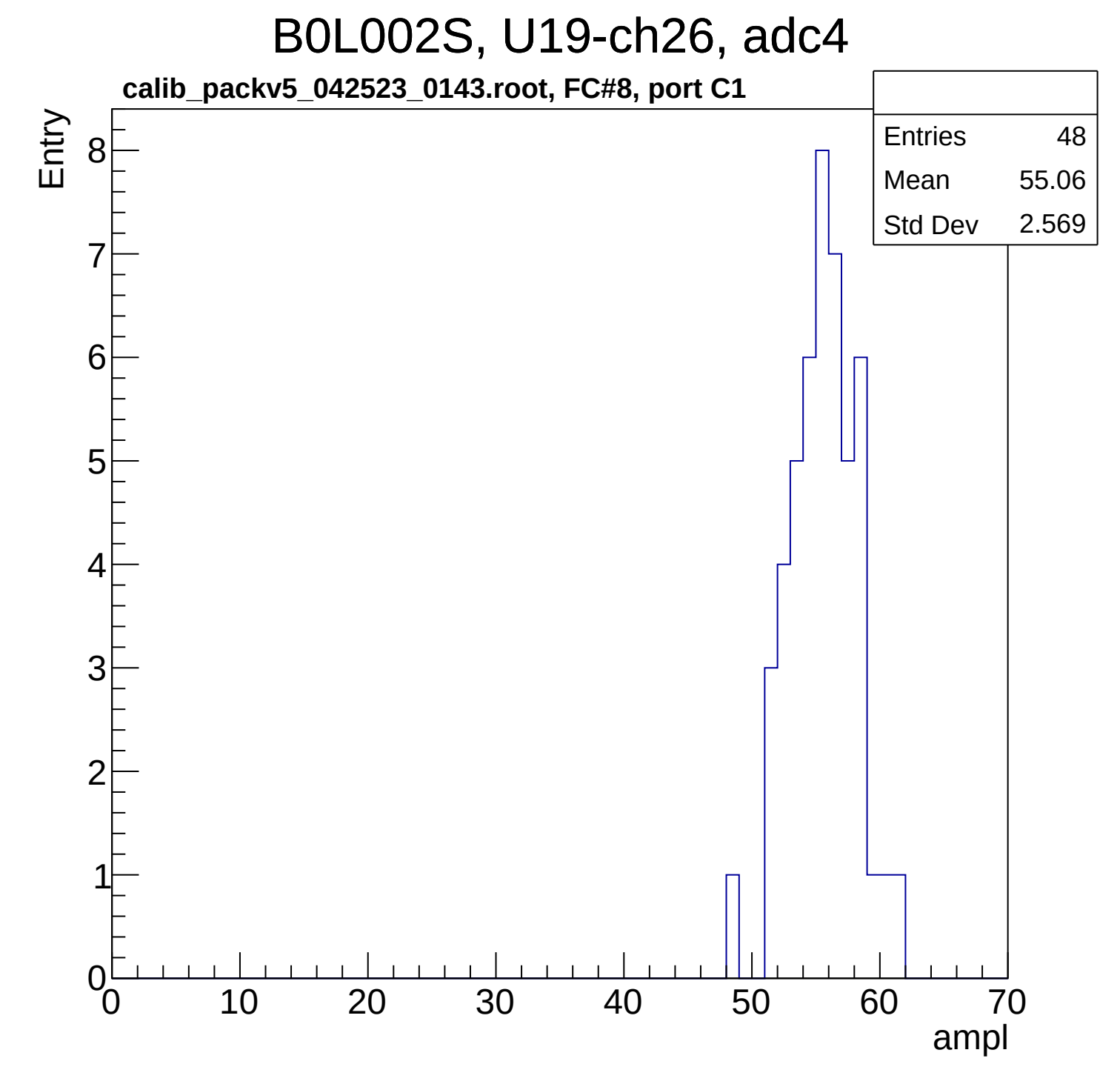

B0L002S, U19-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

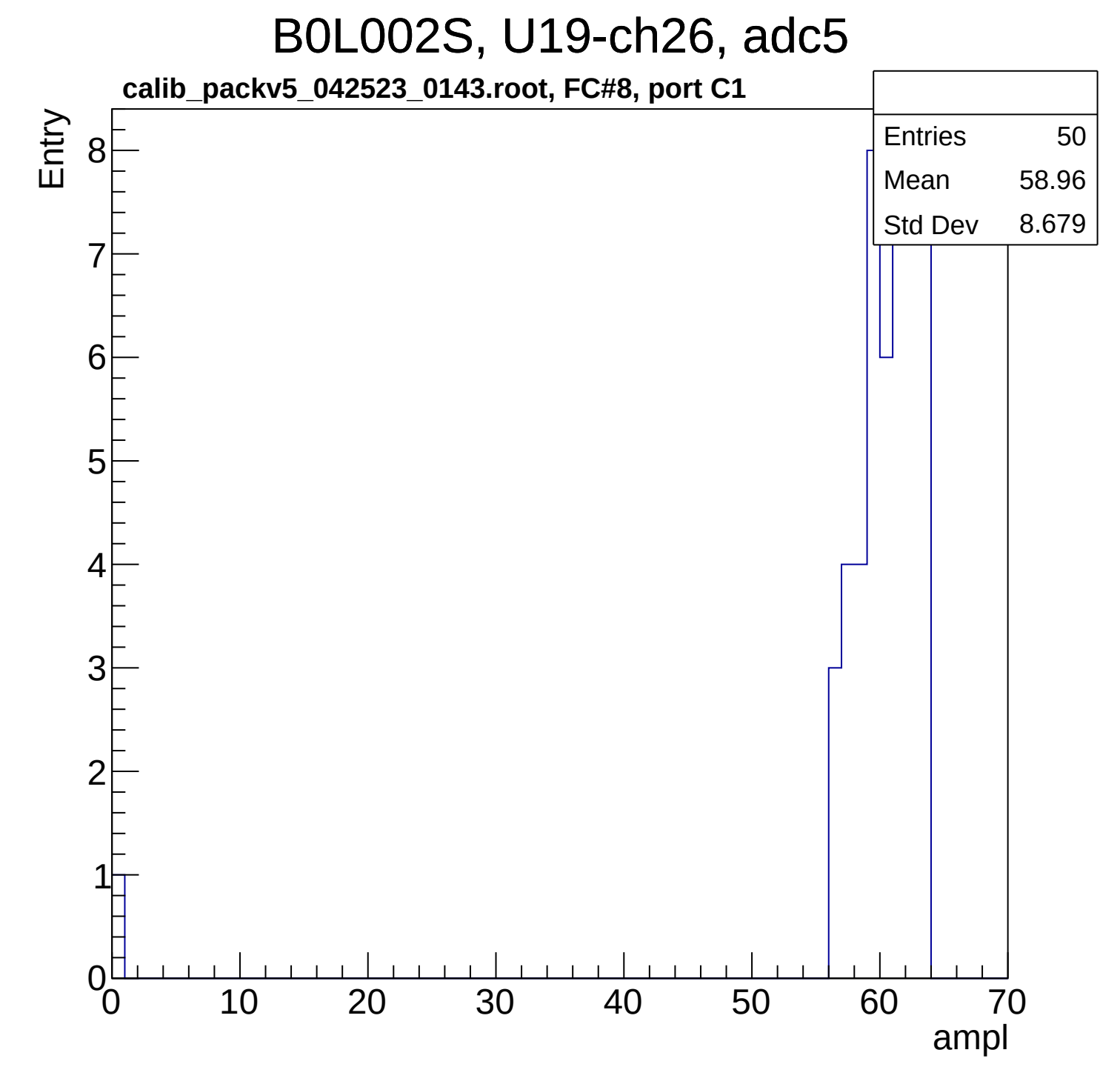

B0L002S, U19-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 28.97
Std Dev 4.534
Gaus mean : 29.5706
Gaus Width: 2.8267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

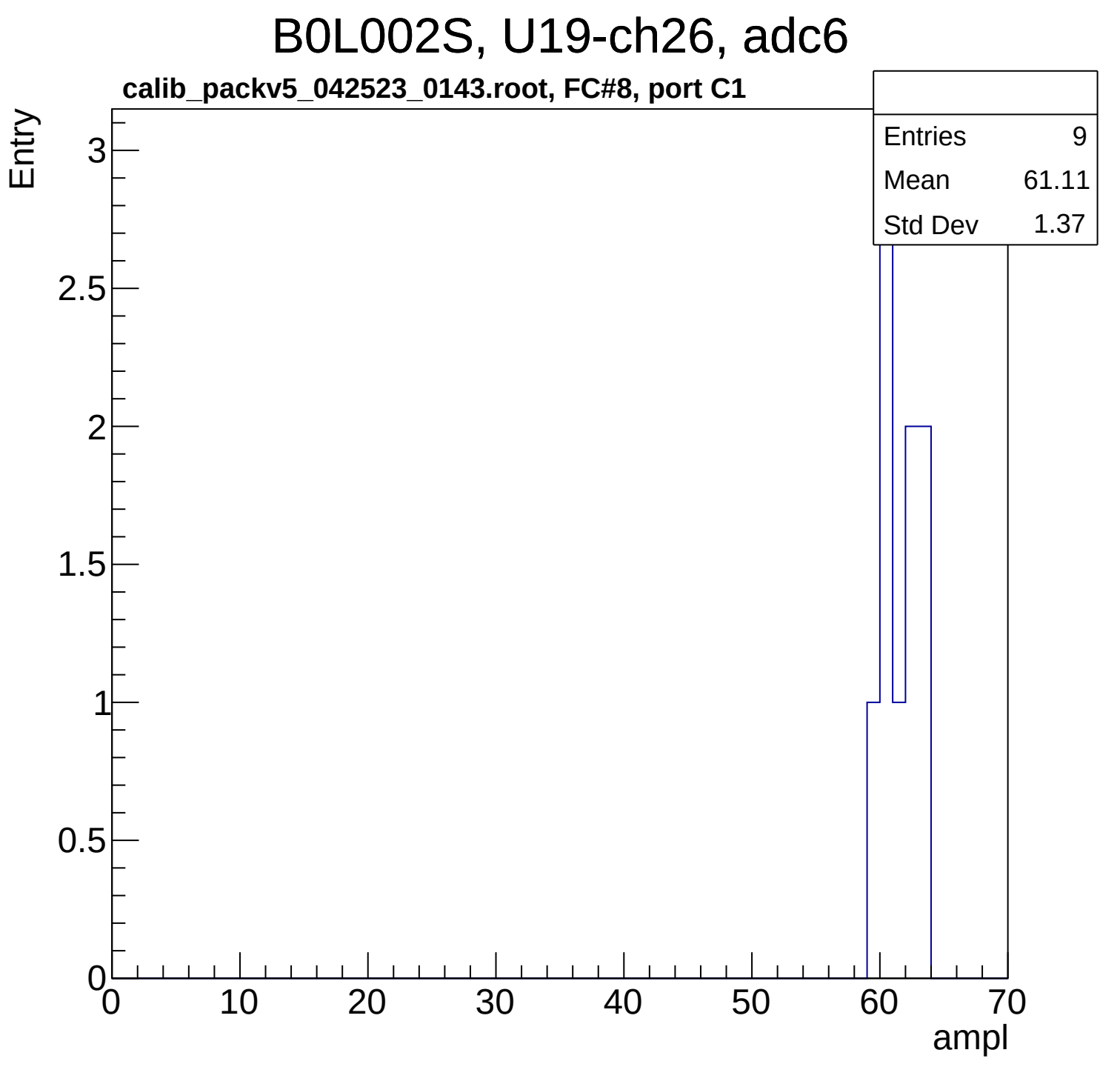

B0L002S, U19-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 93
Mean 37.28
Std Dev 3.527
Gaus mean : 37.4914
Gaus Width: 3.6458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

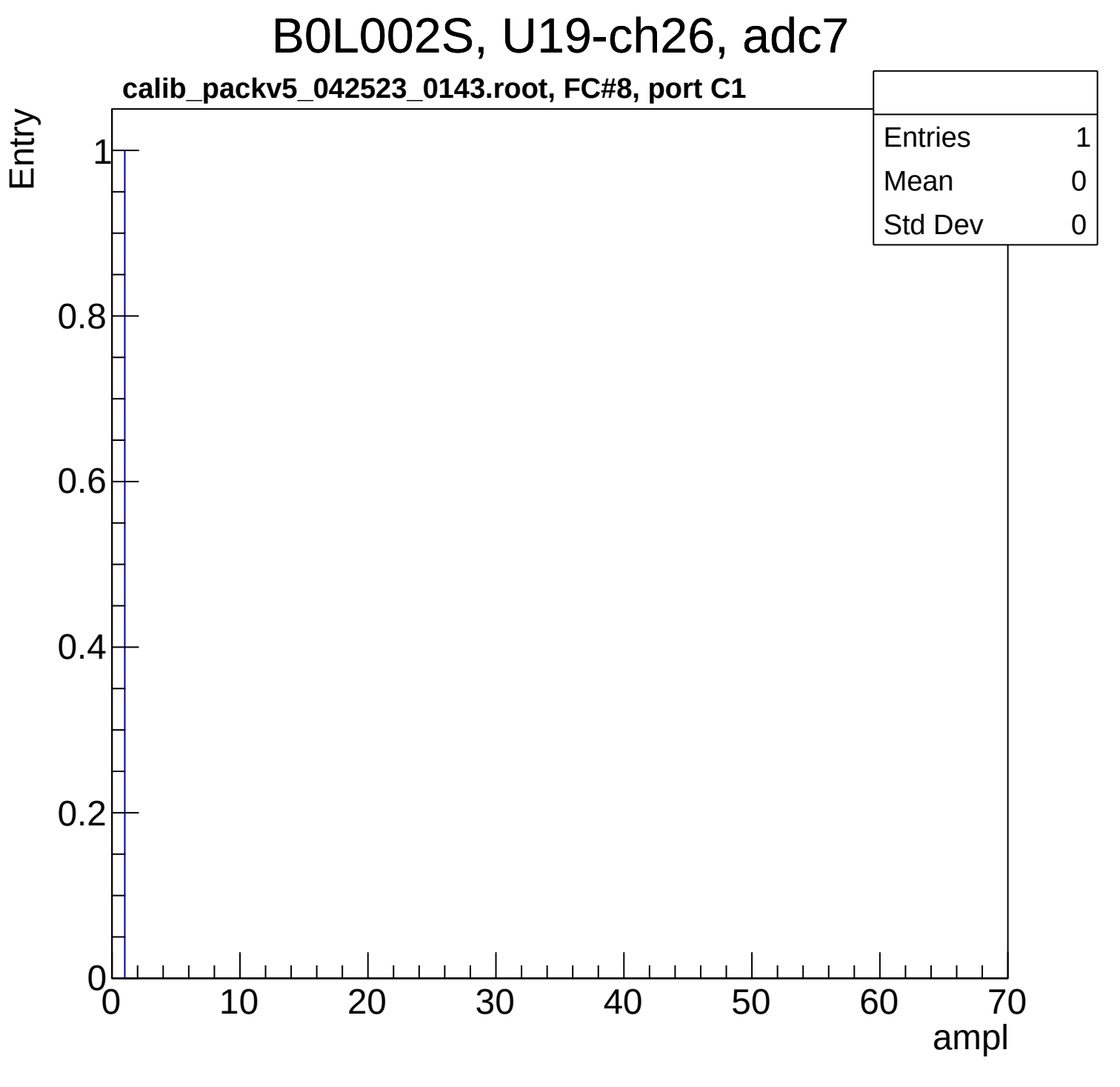

B0L002S, U19-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 44
Std Dev 2.596
Gaus mean : 44.8390
Gaus Width: 2.7398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

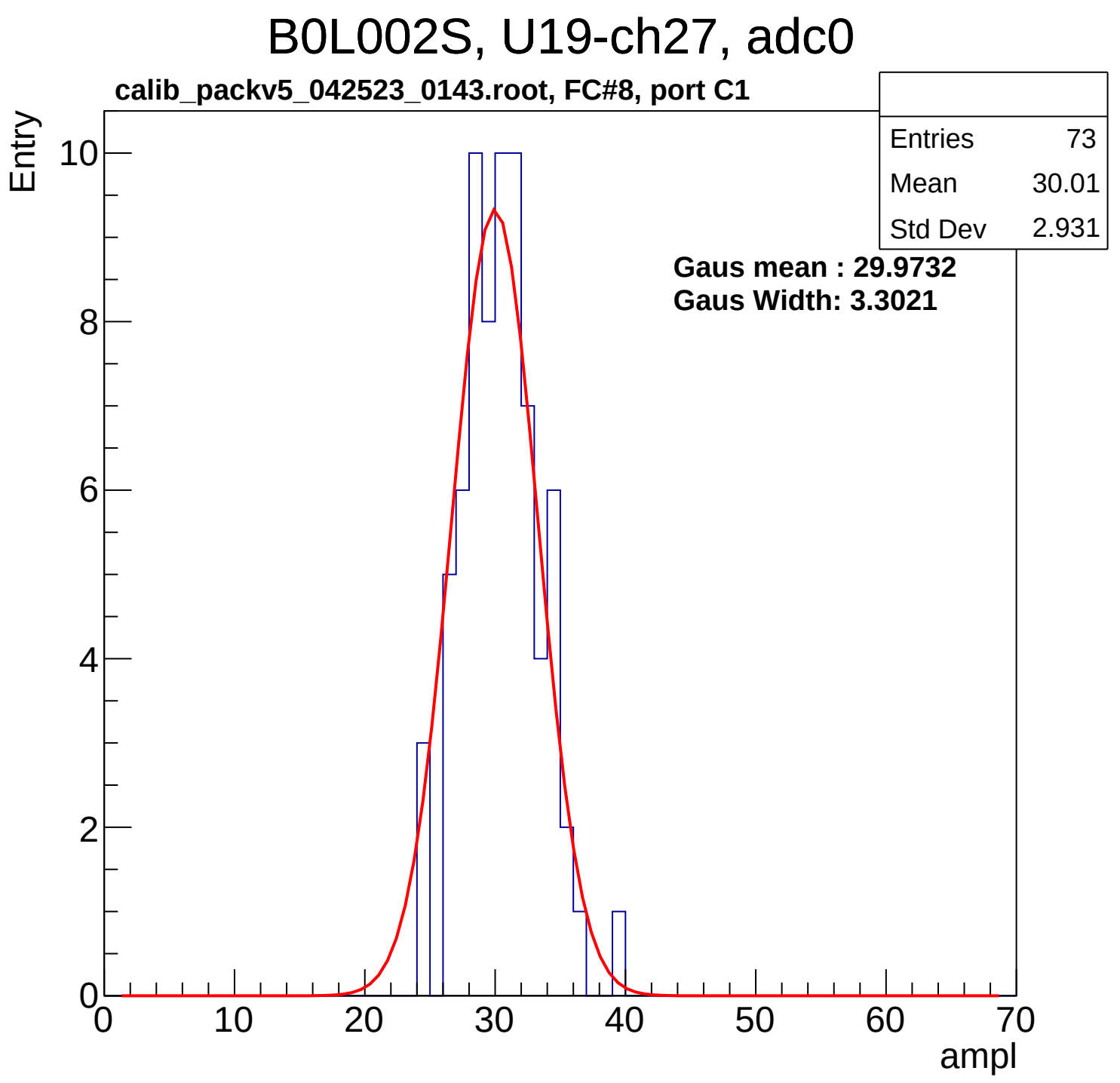

B0L002S, U19-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 49.22
Std Dev 3.198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

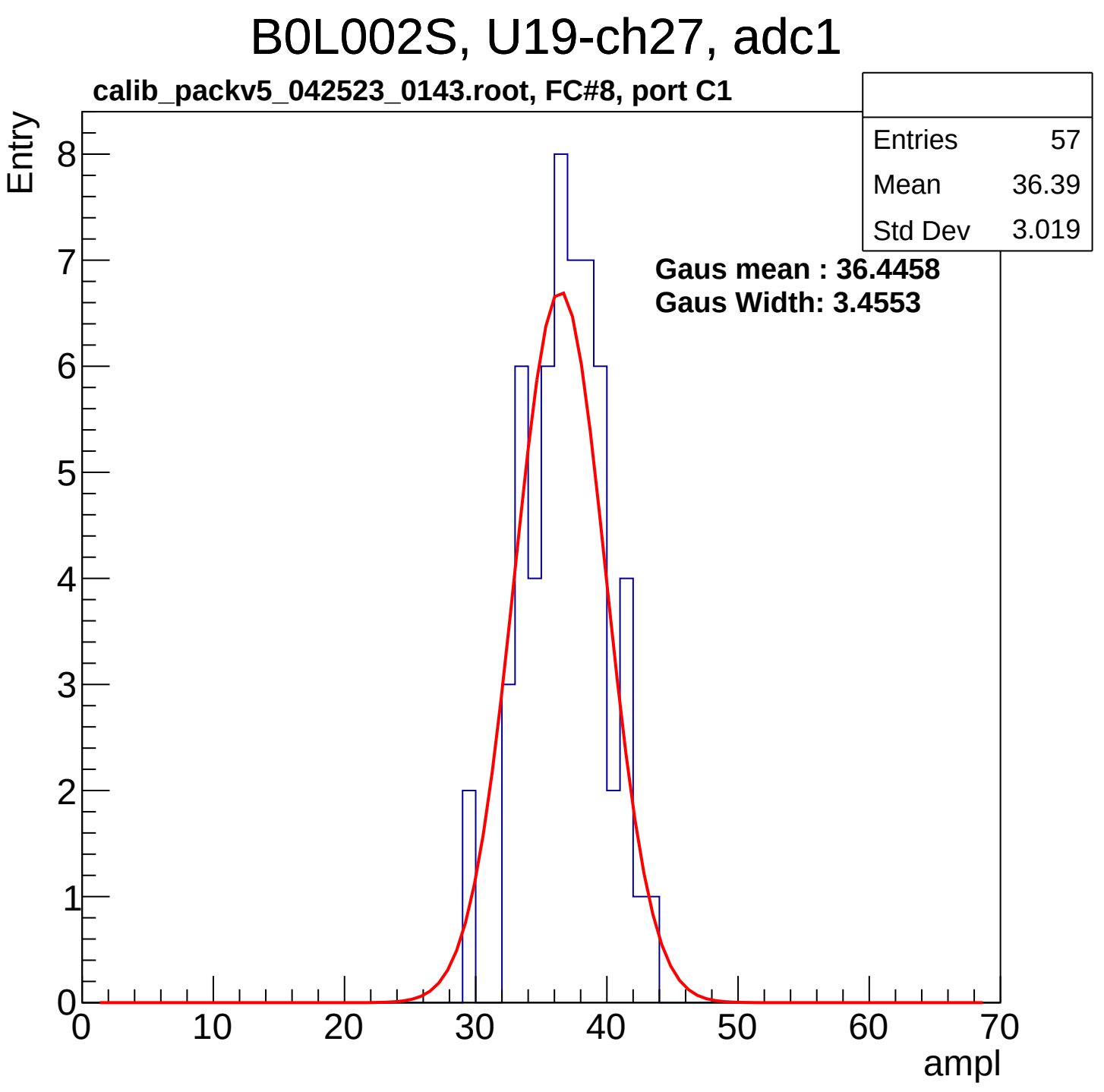

B0L002S, U19-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 55.06
Std Dev 2.569
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

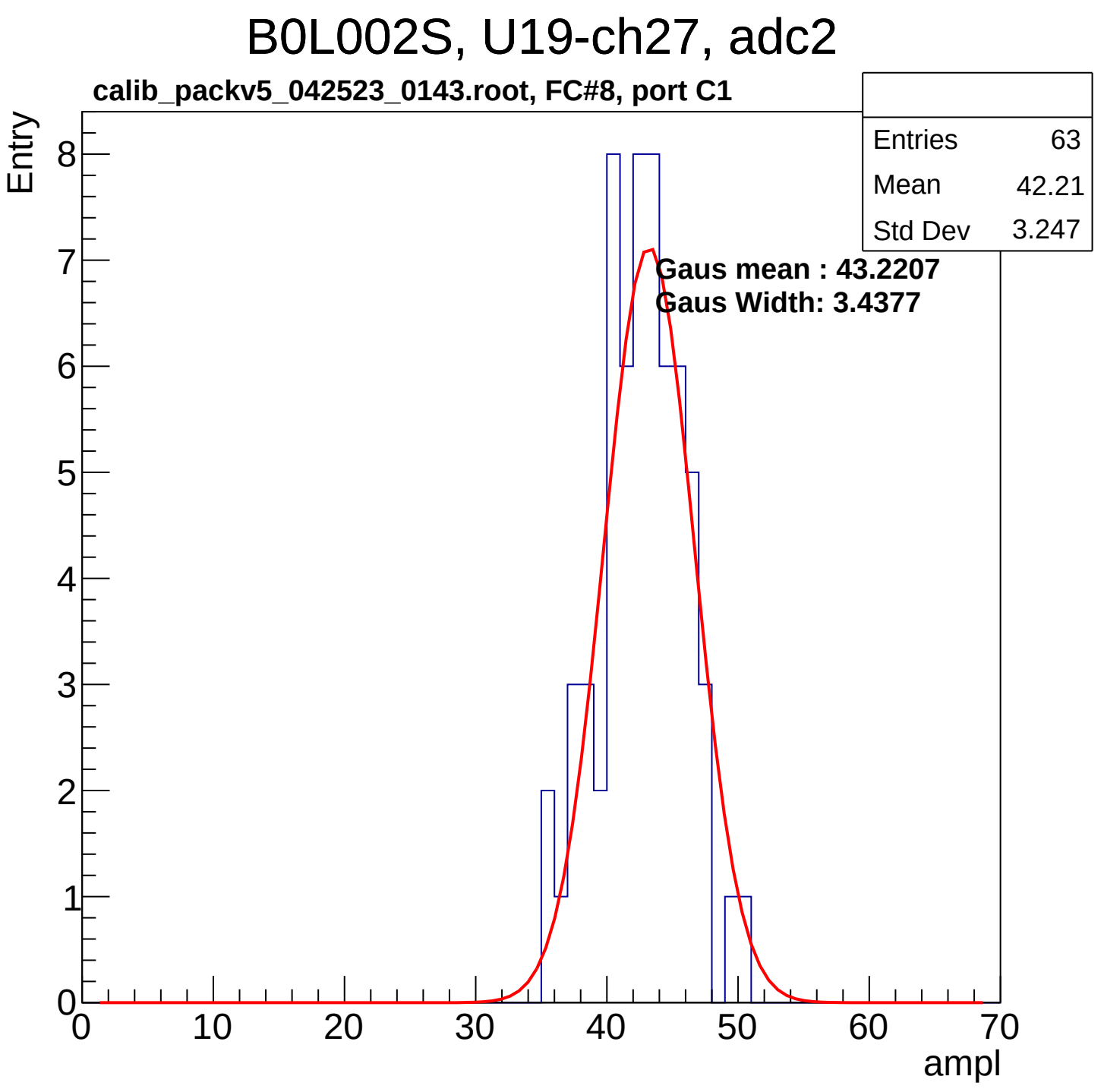

B0L002S, U19-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.96
Std Dev 8.679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

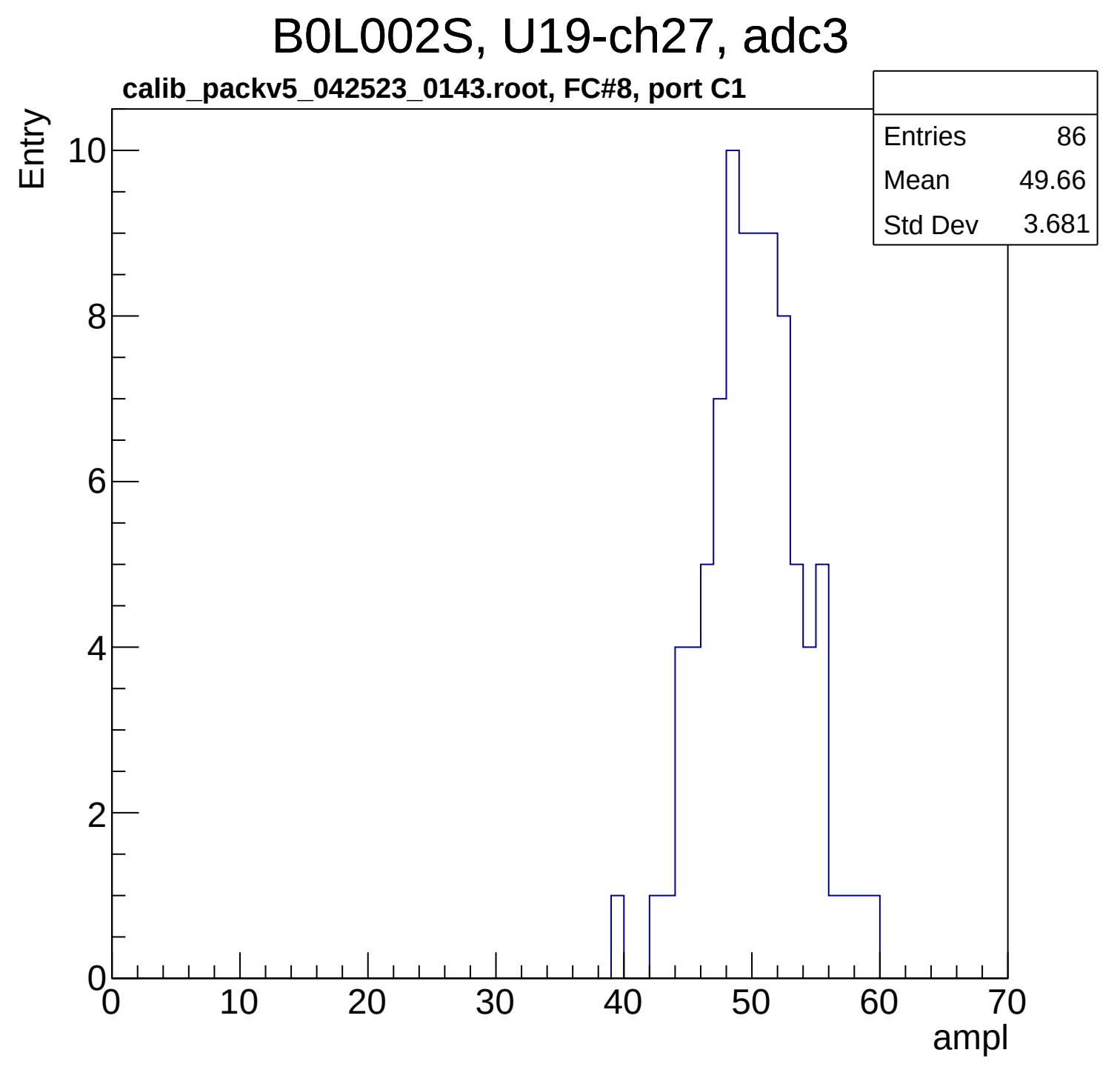

B0L002S, U19-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.11
Std Dev 1.37
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

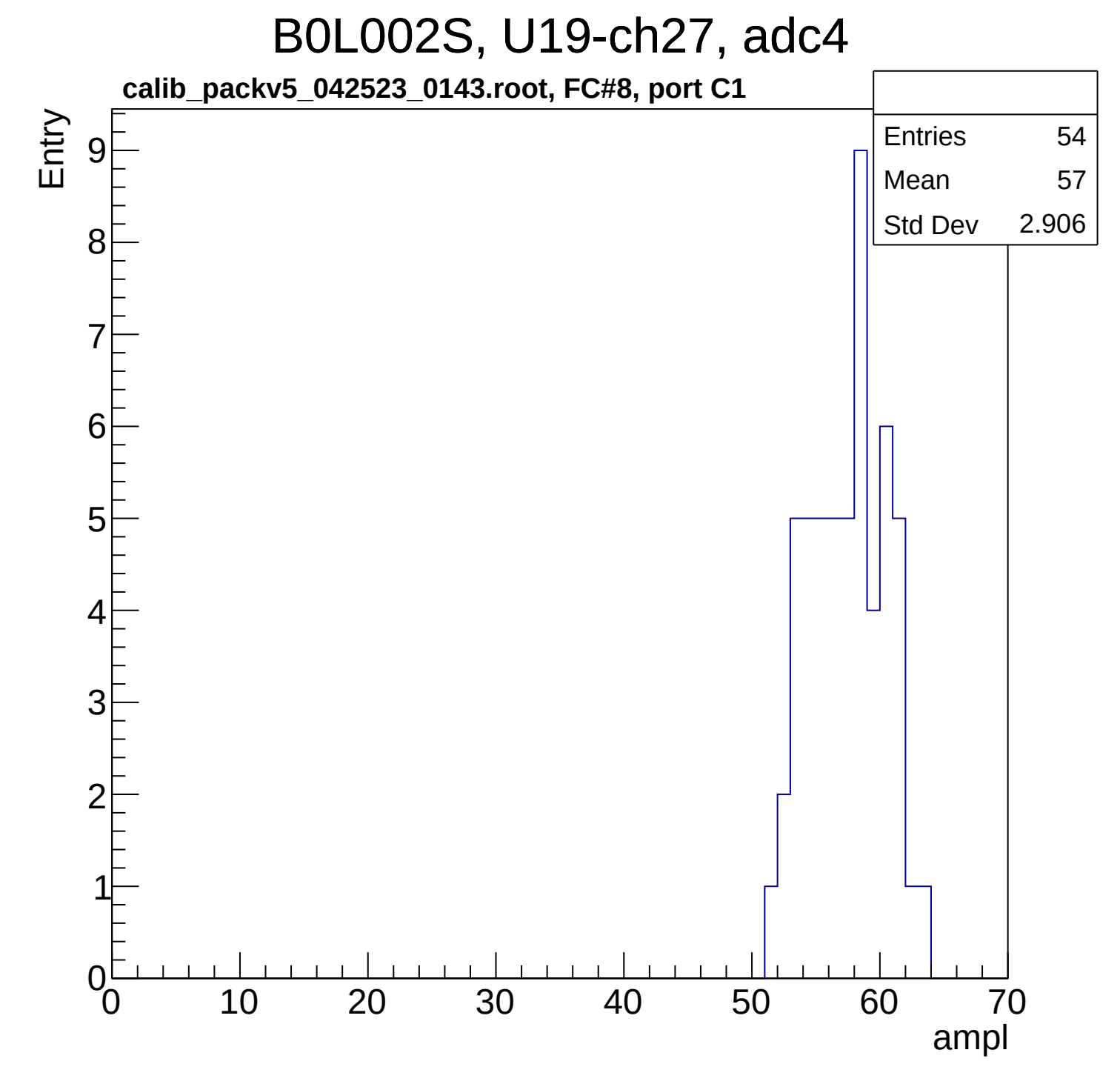

B0L002S, U19-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

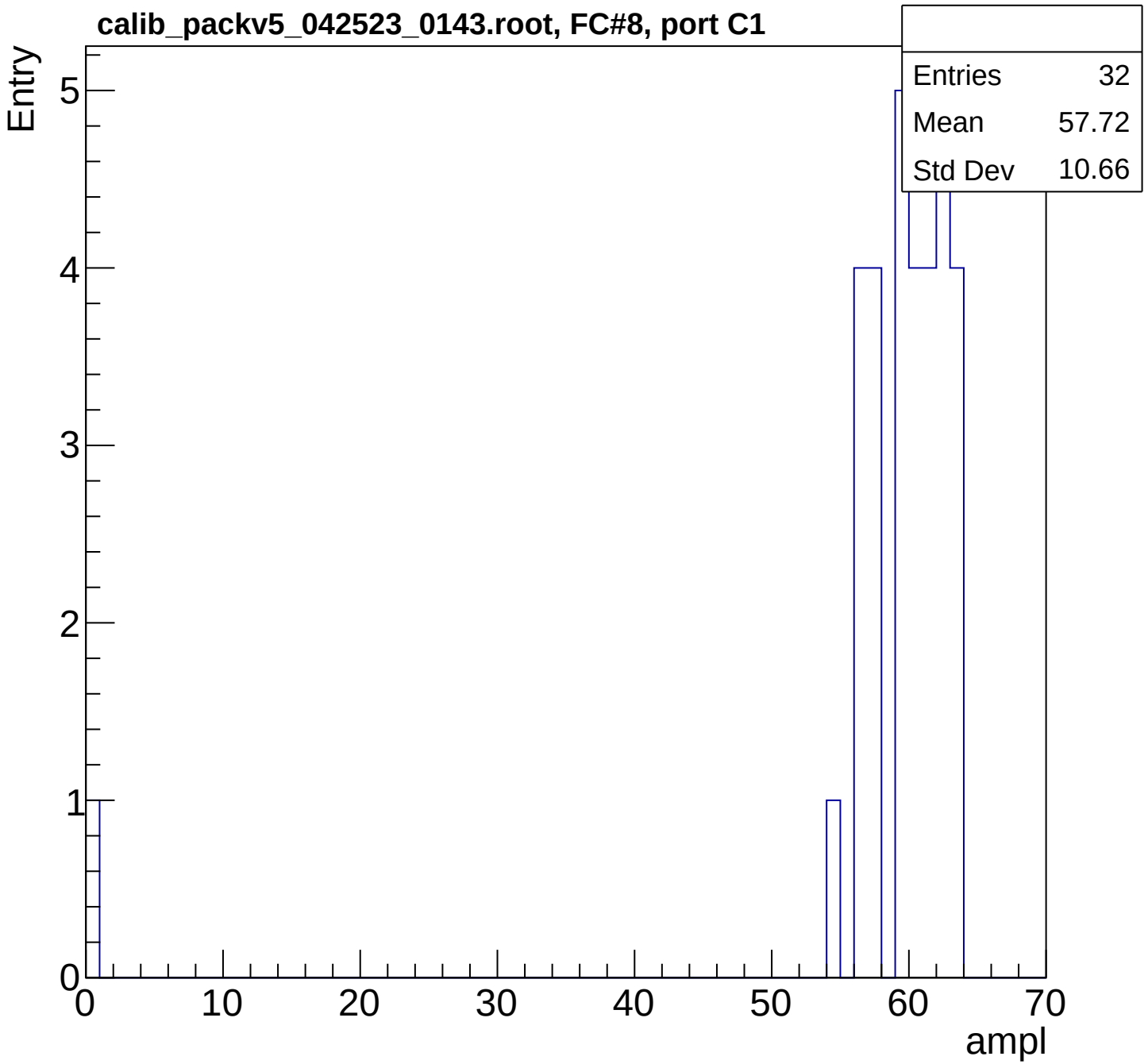

B0L002S, U19-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.01
Std Dev 2.931
Gaus mean : 29.9732
Gaus Width: 3.3021
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

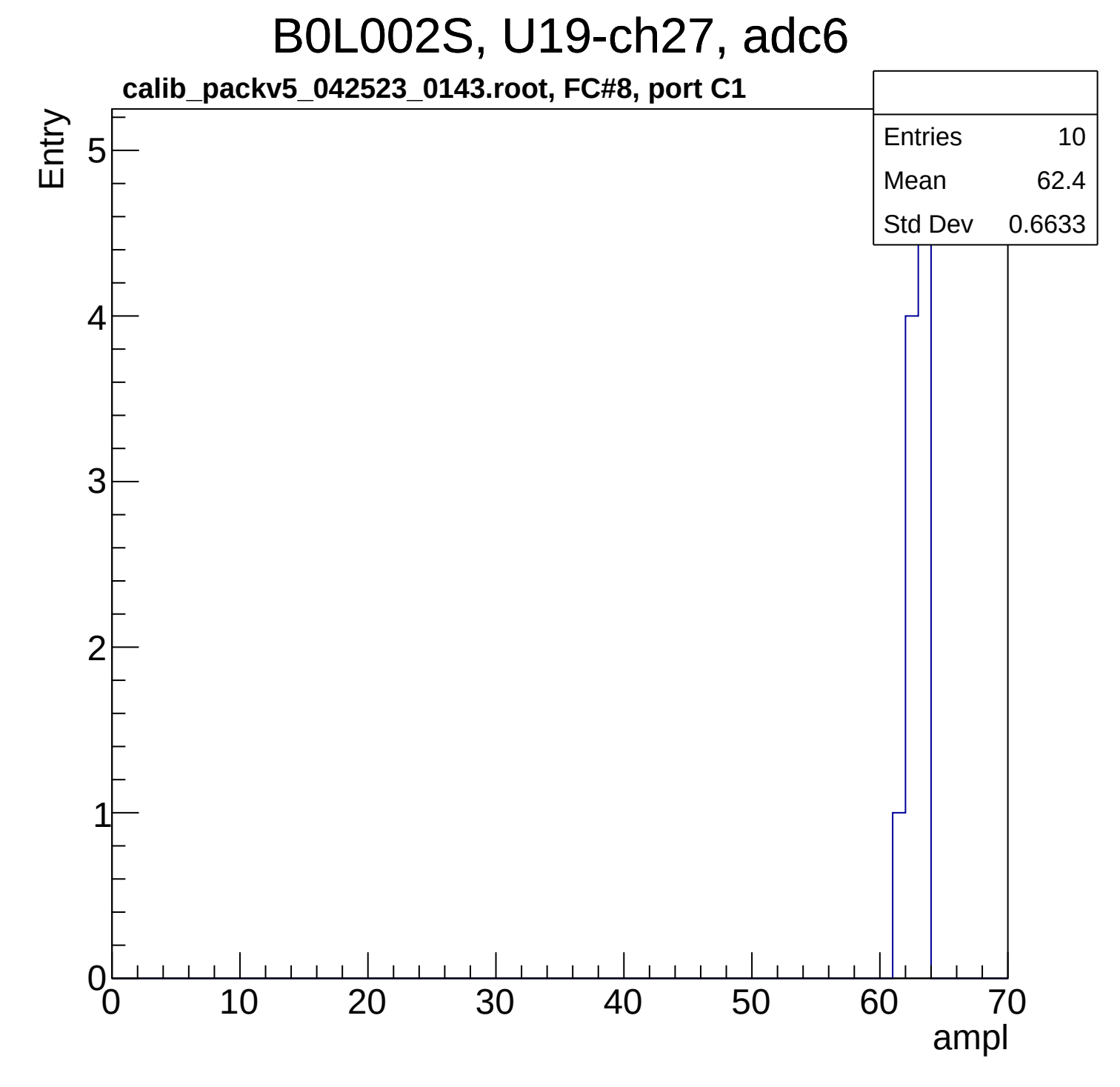

B0L002S, U19-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 36.39
Std Dev 3.019
Gaus mean : 36.4458
Gaus Width: 3.4553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

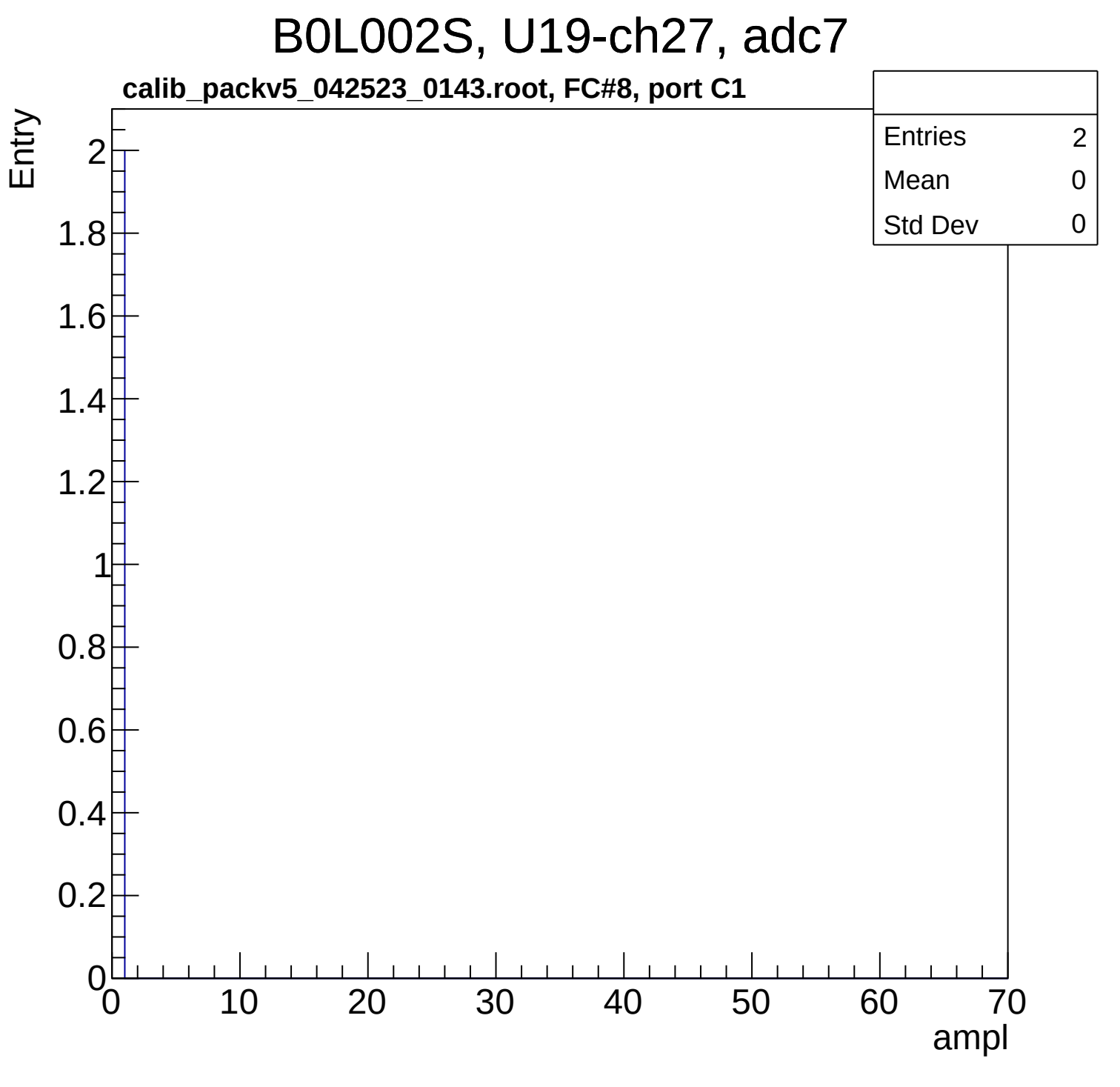

B0L002S, U19-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 42.21
Std Dev 3.247
Gaus mean : 43.2207
Gaus Width: 3.4377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

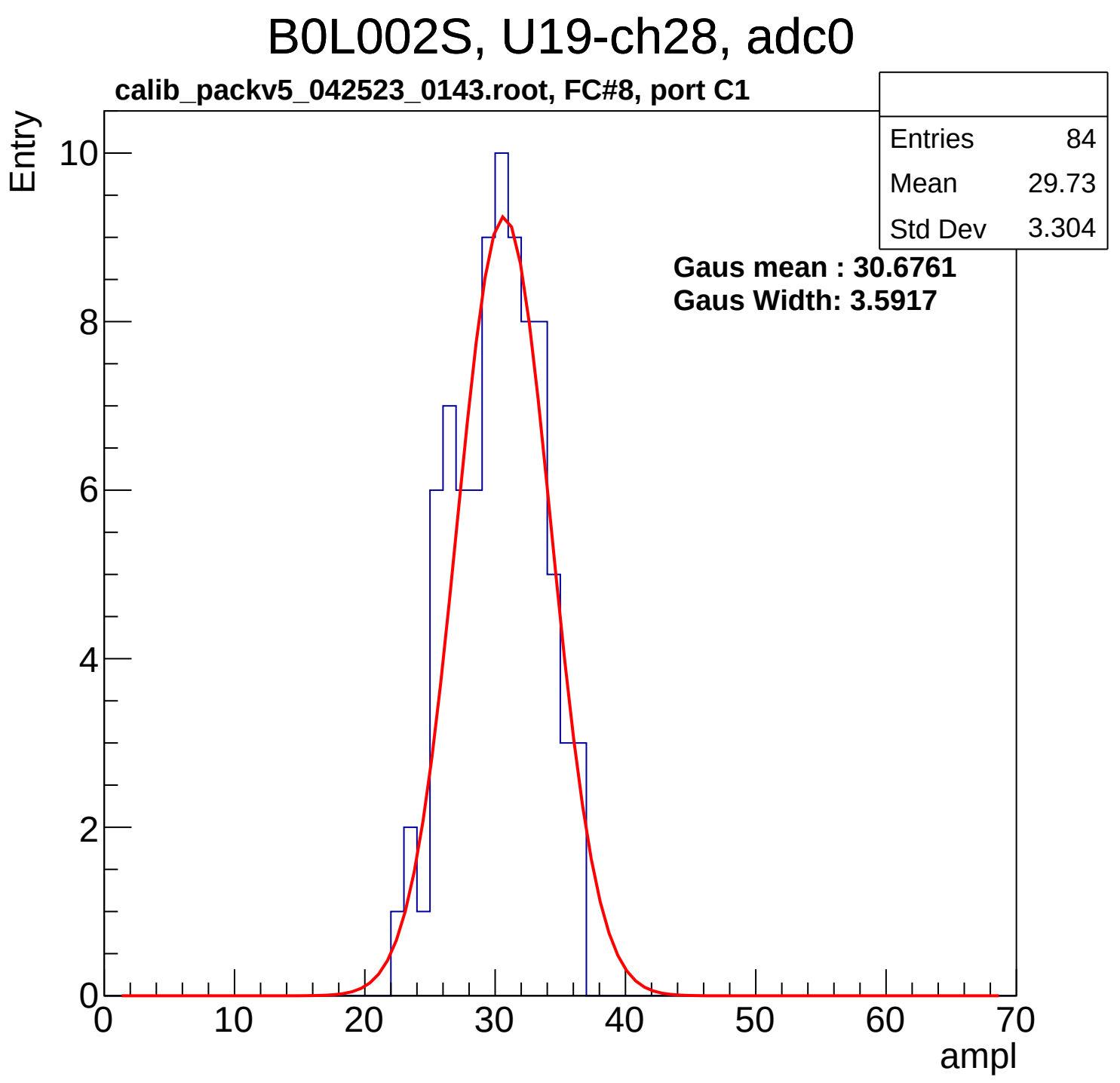

B0L002S, U19-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 49.66
Std Dev 3.681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

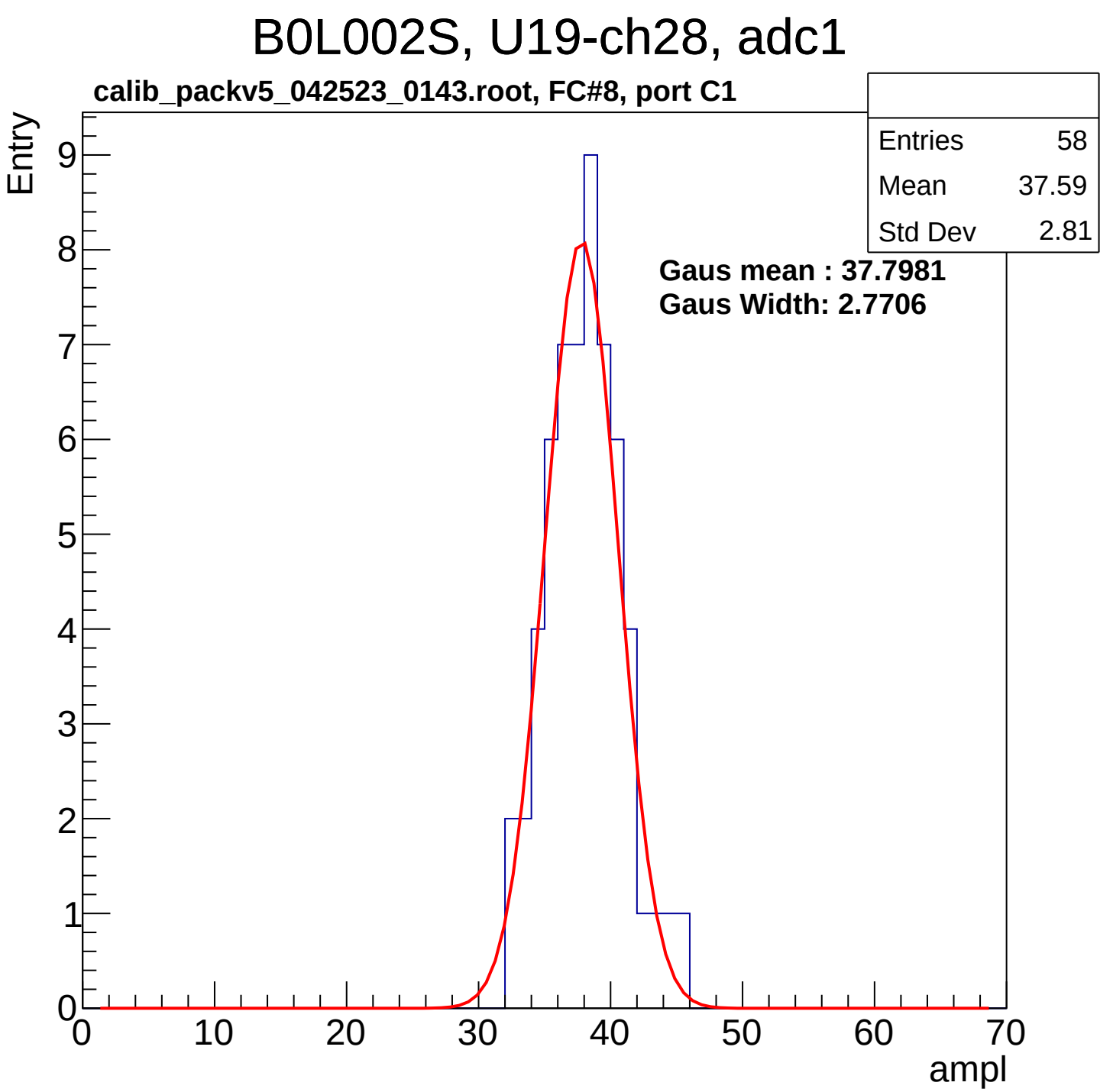

B0L002S, U19-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57
Std Dev 2.906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

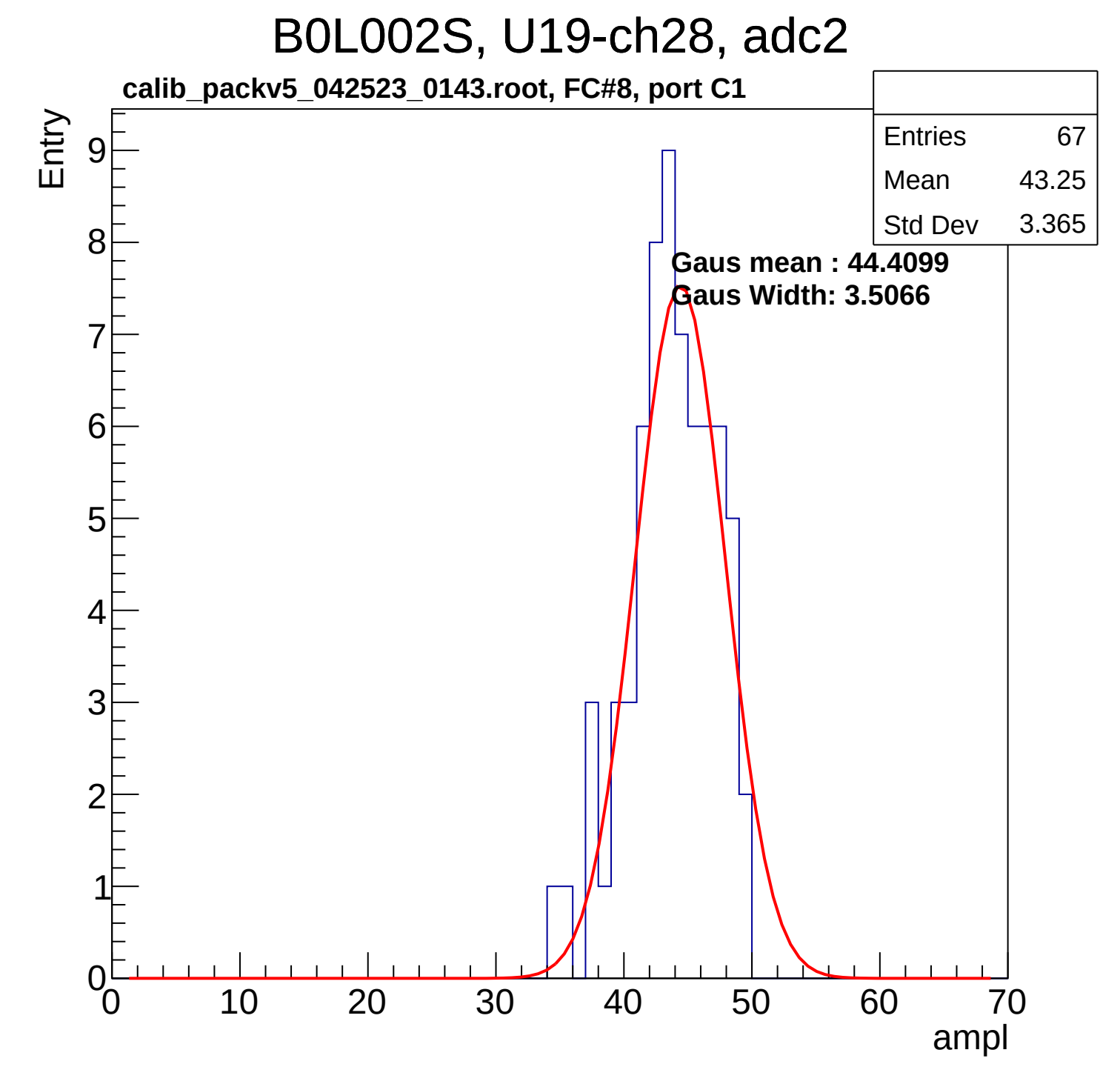

B0L002S, U19-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 57.72
Std Dev 10.66
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

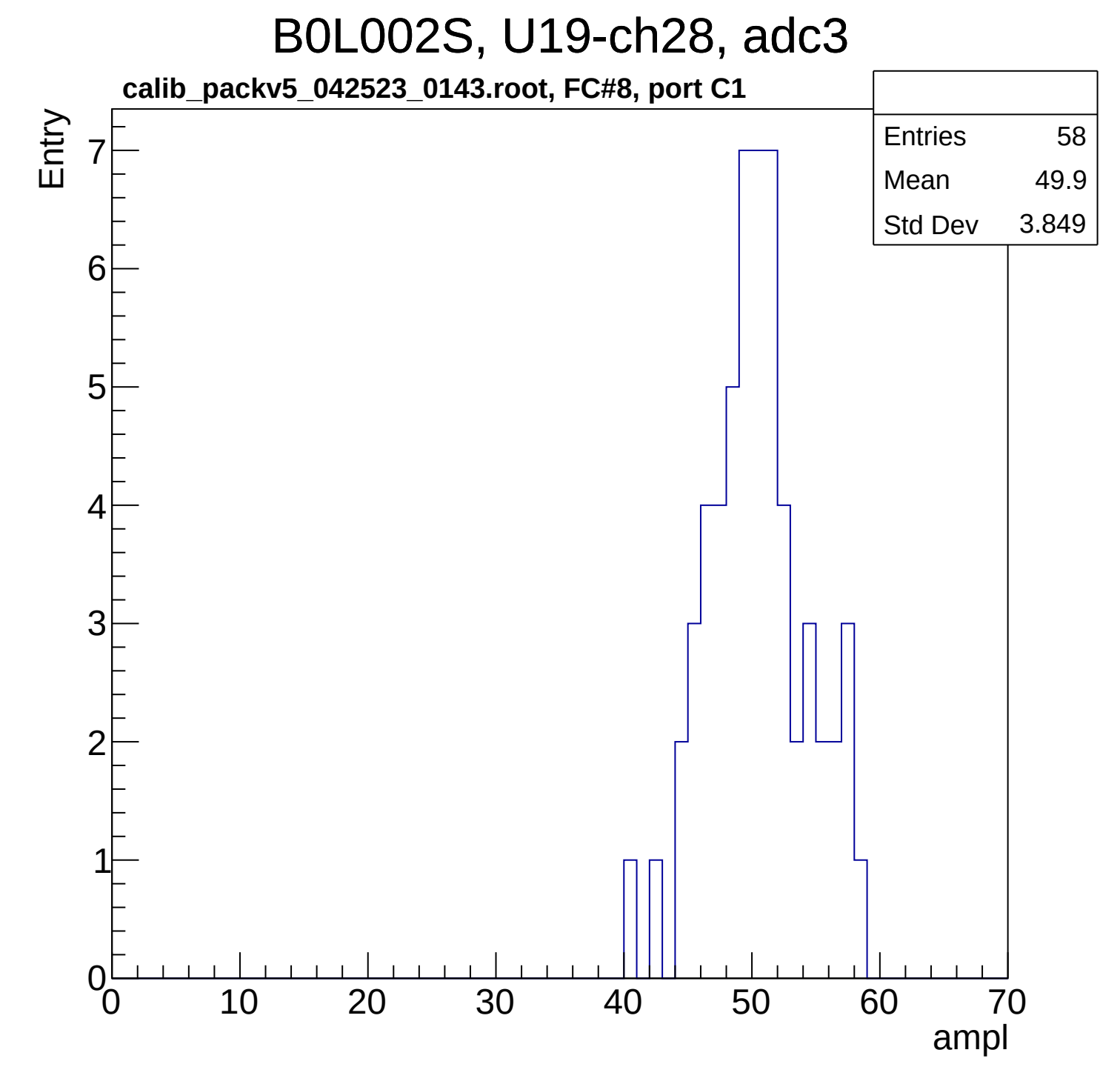

B0L002S, U19-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 62.4
Std Dev 0.6633
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

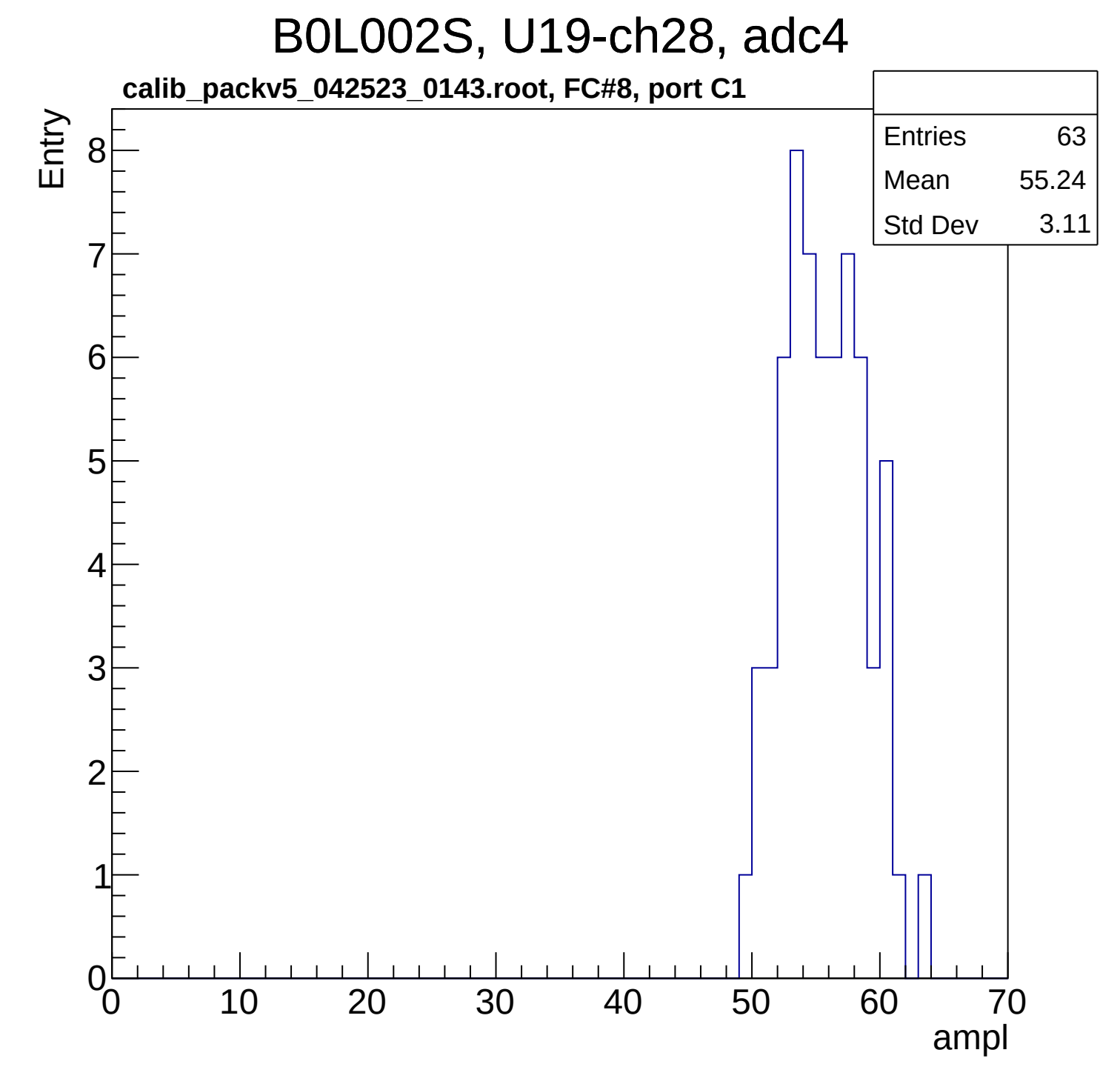

B0L002S, U19-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

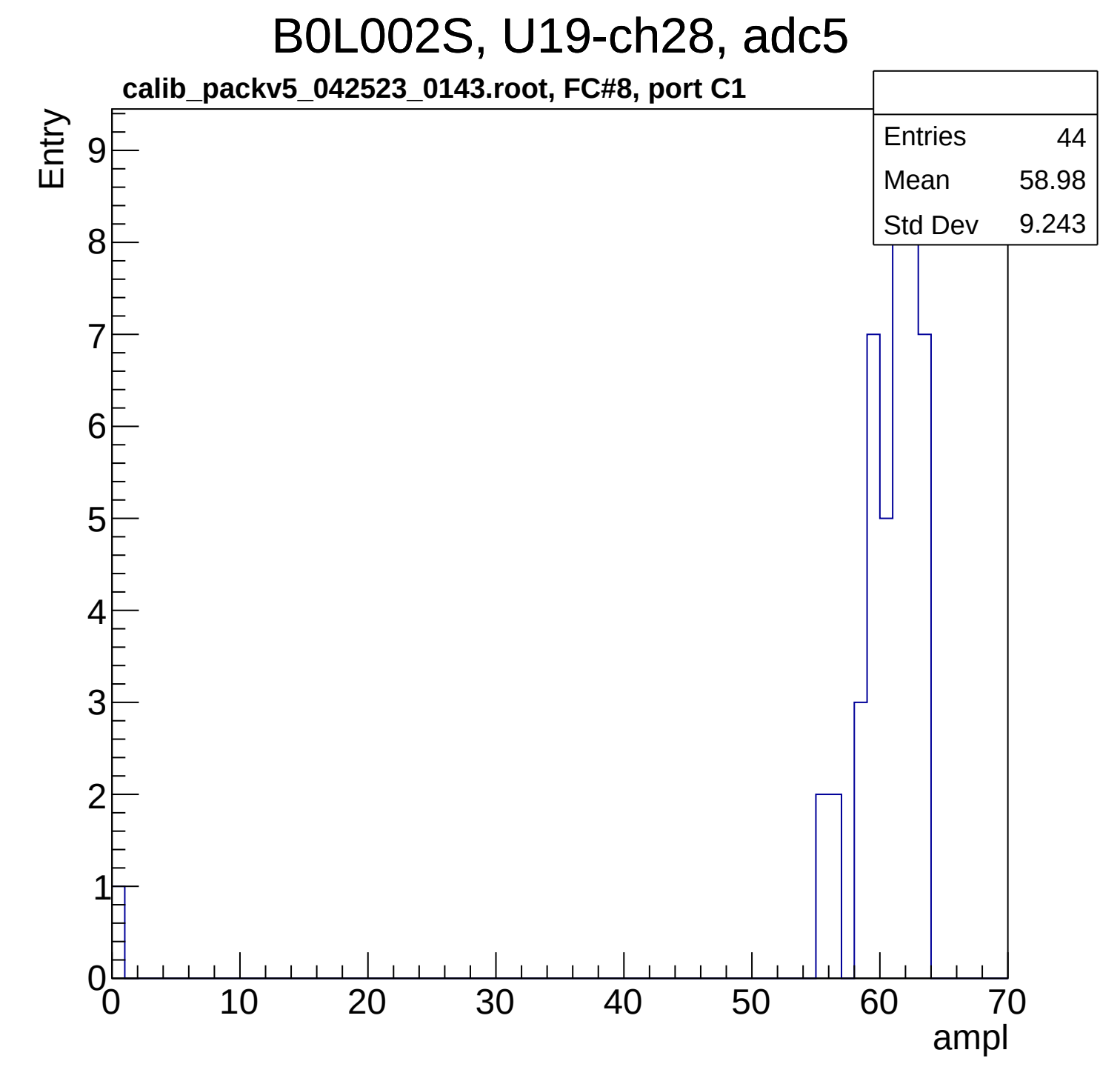

B0L002S, U19-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.73
Std Dev 3.304
Gaus mean : 30.6761
Gaus Width: 3.5917
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

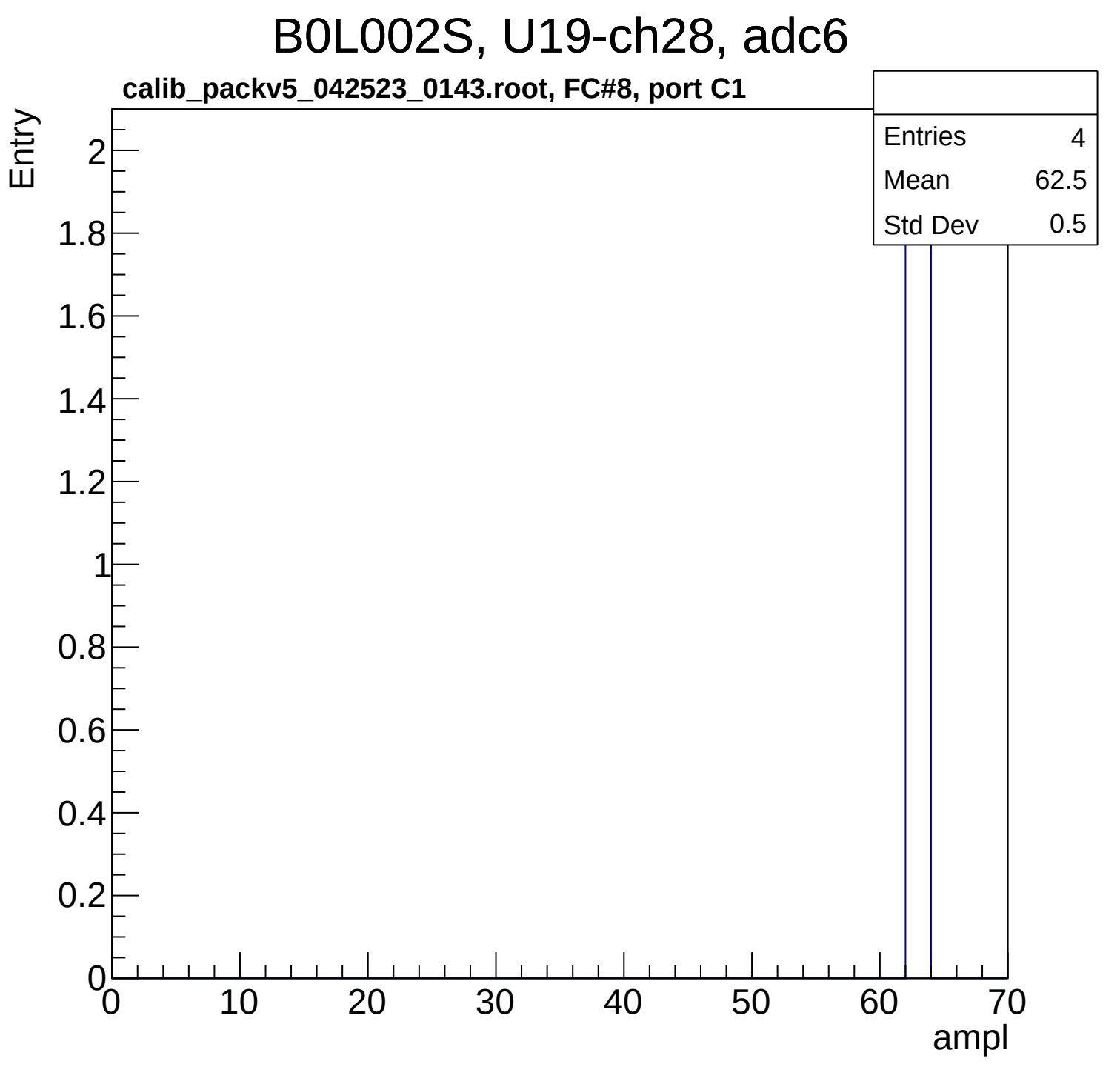

B0L002S, U19-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.59
Std Dev 2.81
Gaus mean : 37.7981
Gaus Width: 2.7706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

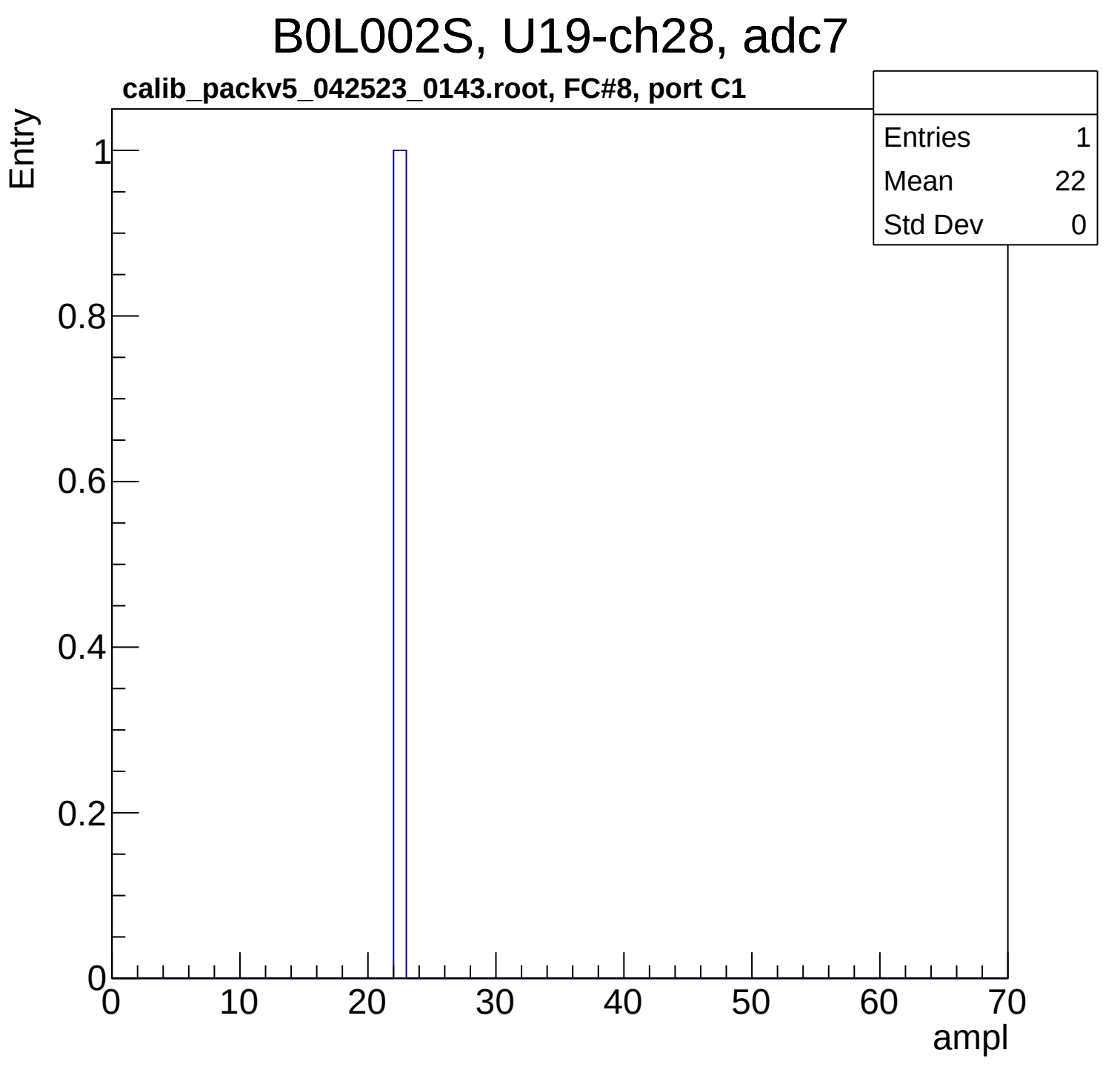

B0L002S, U19-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.25
Std Dev 3.365
Gaus mean : 44.4099
Gaus Width: 3.5066
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

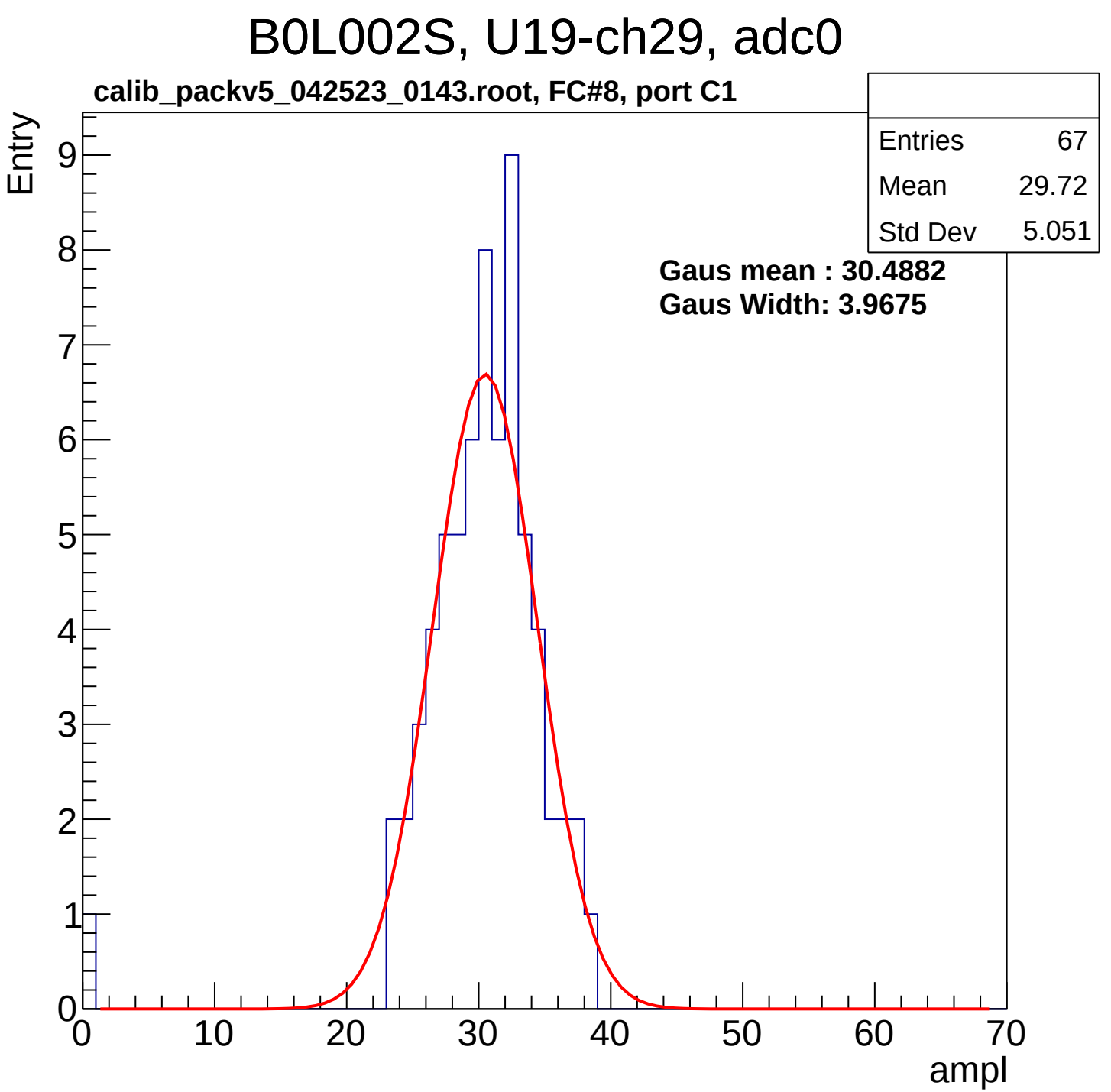

B0L002S, U19-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 49.9
Std Dev 3.849
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

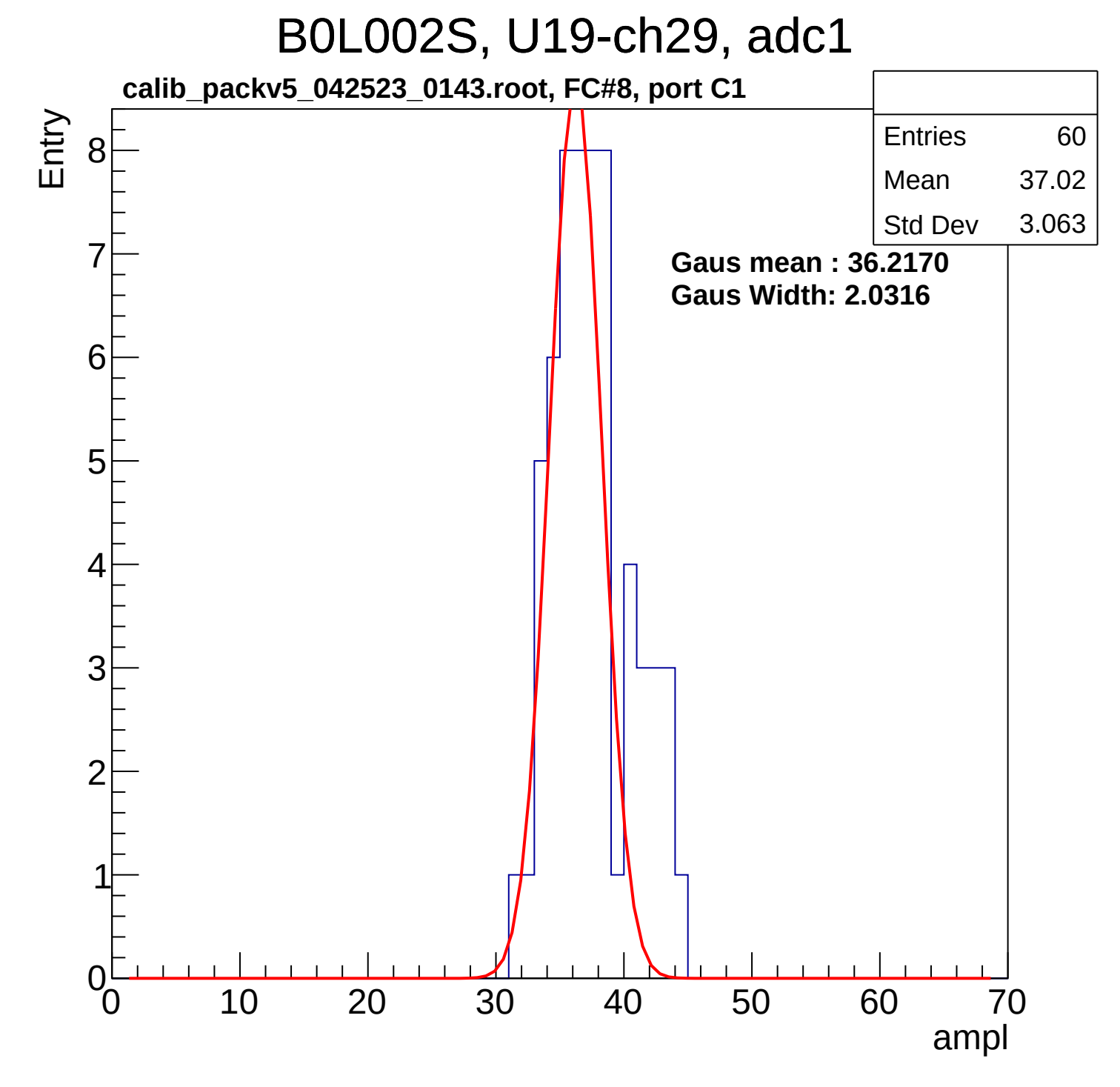

B0L002S, U19-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.24
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

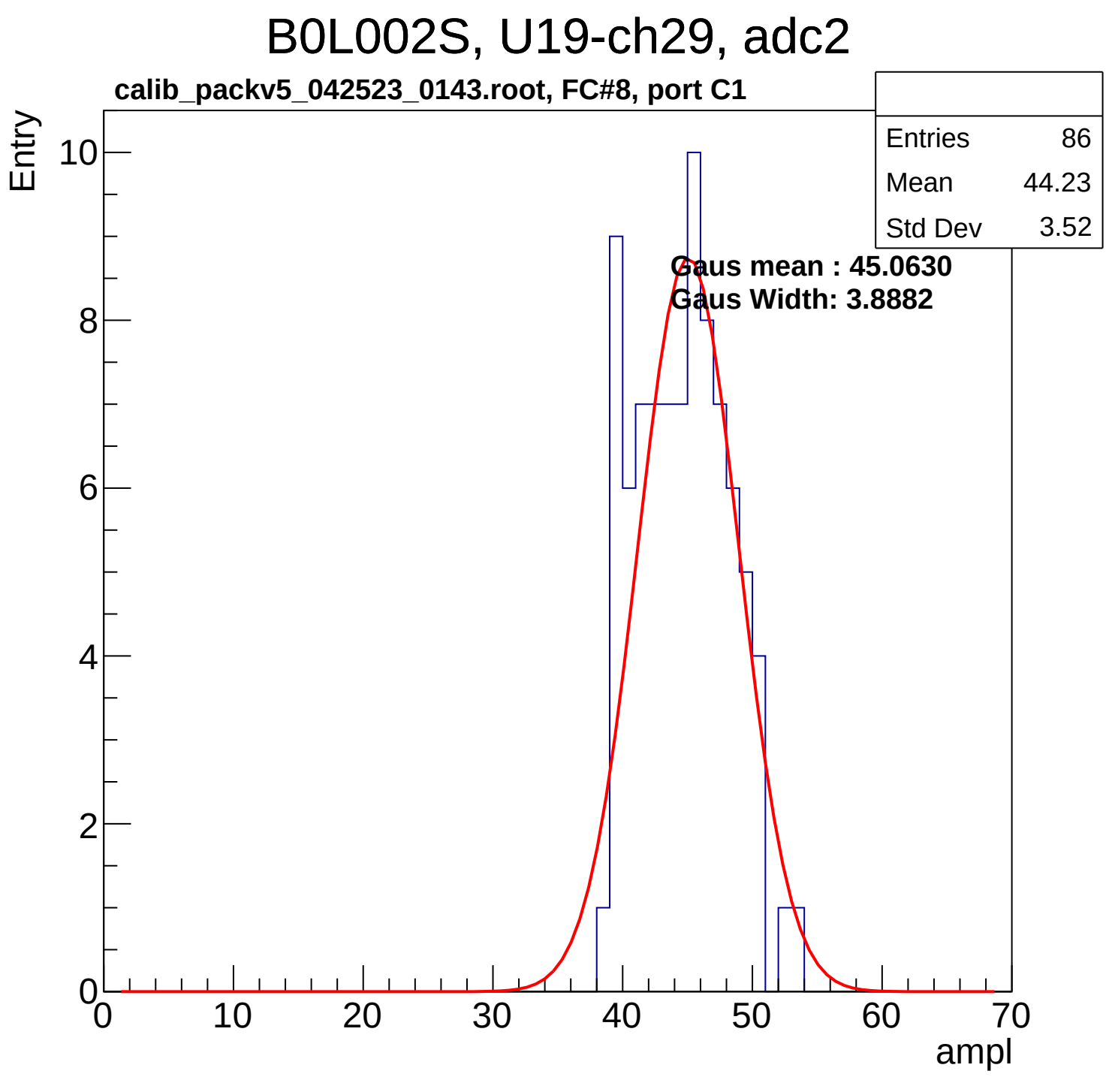

B0L002S, U19-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.98
Std Dev 9.243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

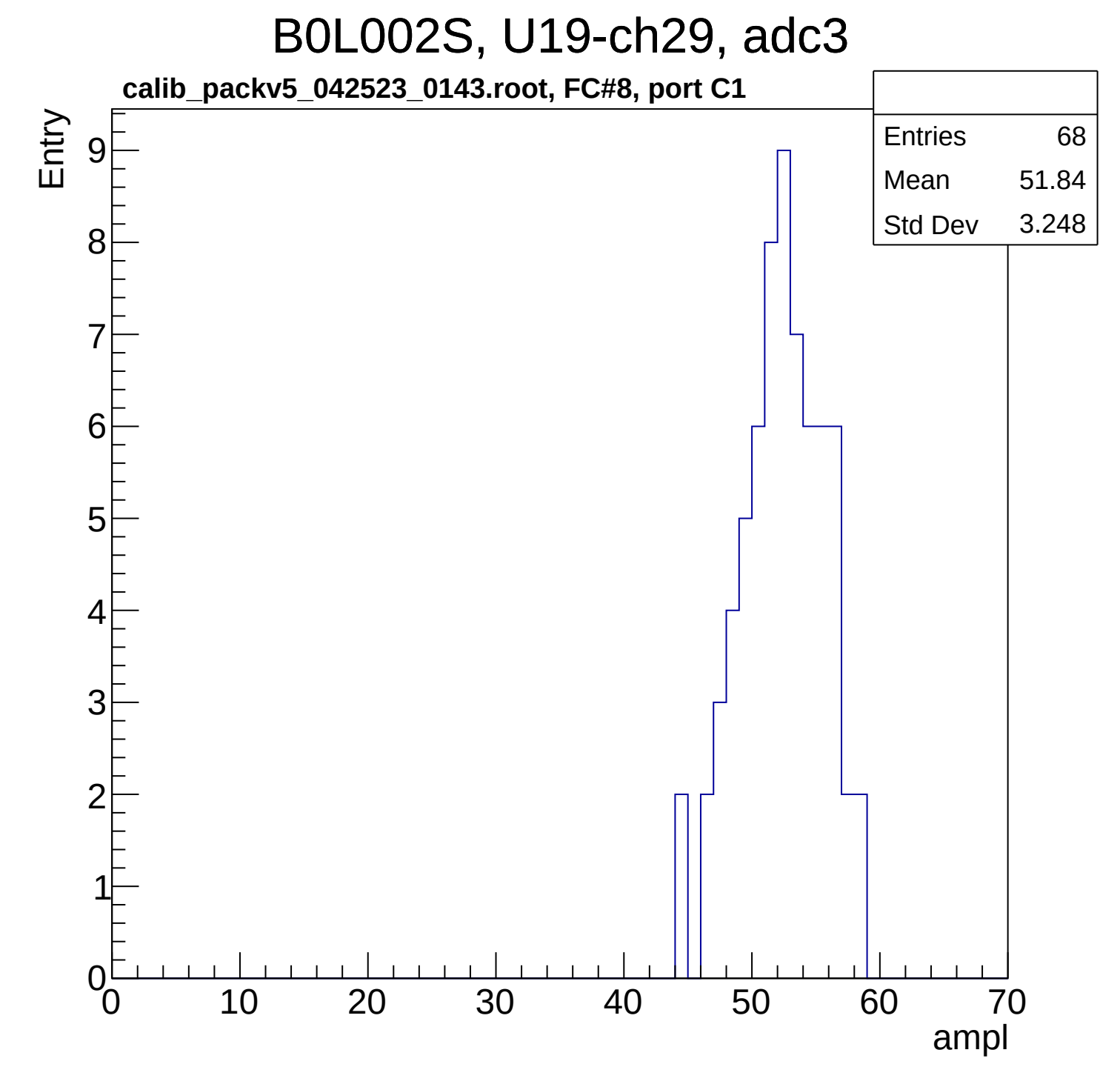

B0L002S, U19-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

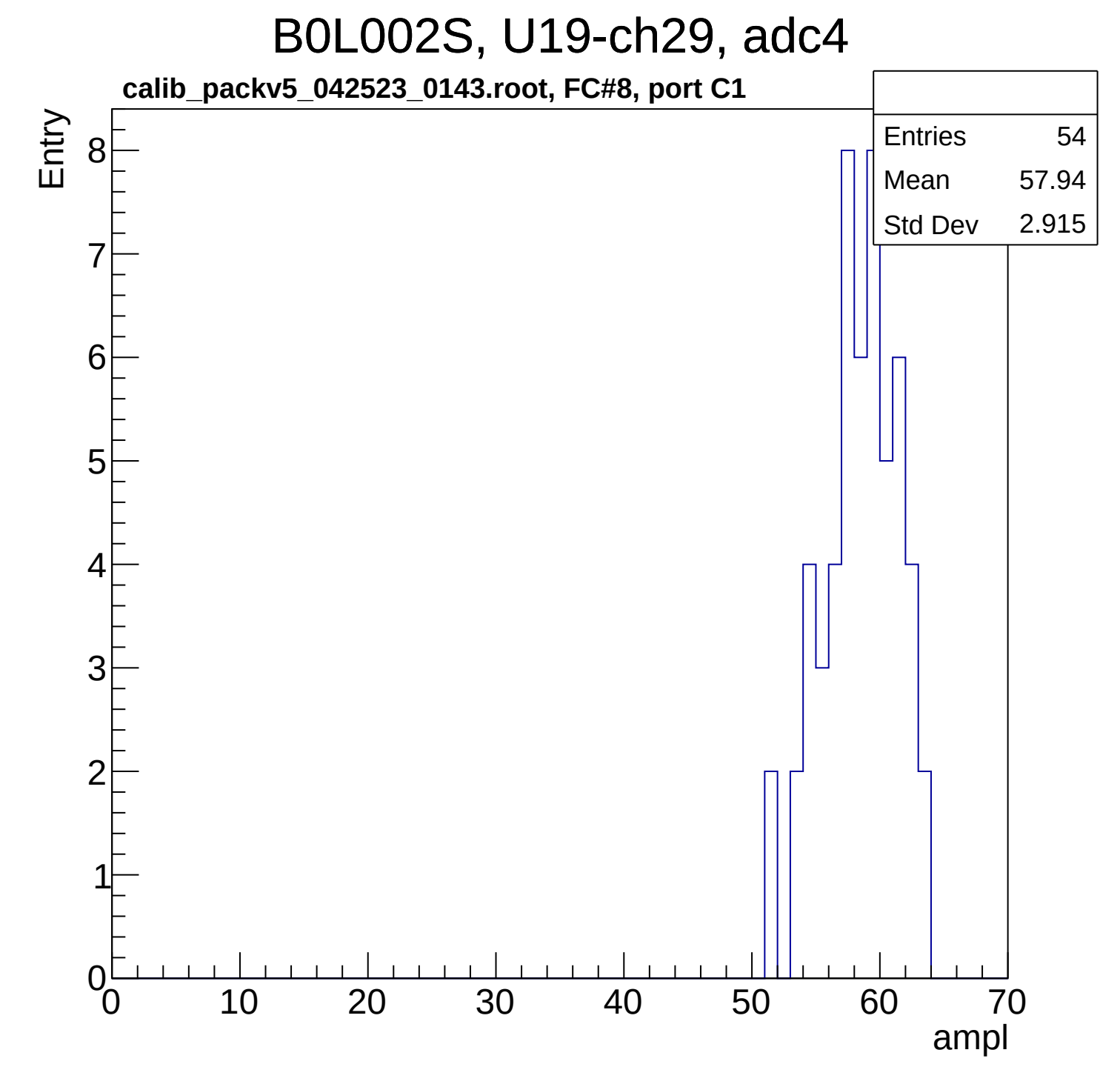

B0L002S, U19-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

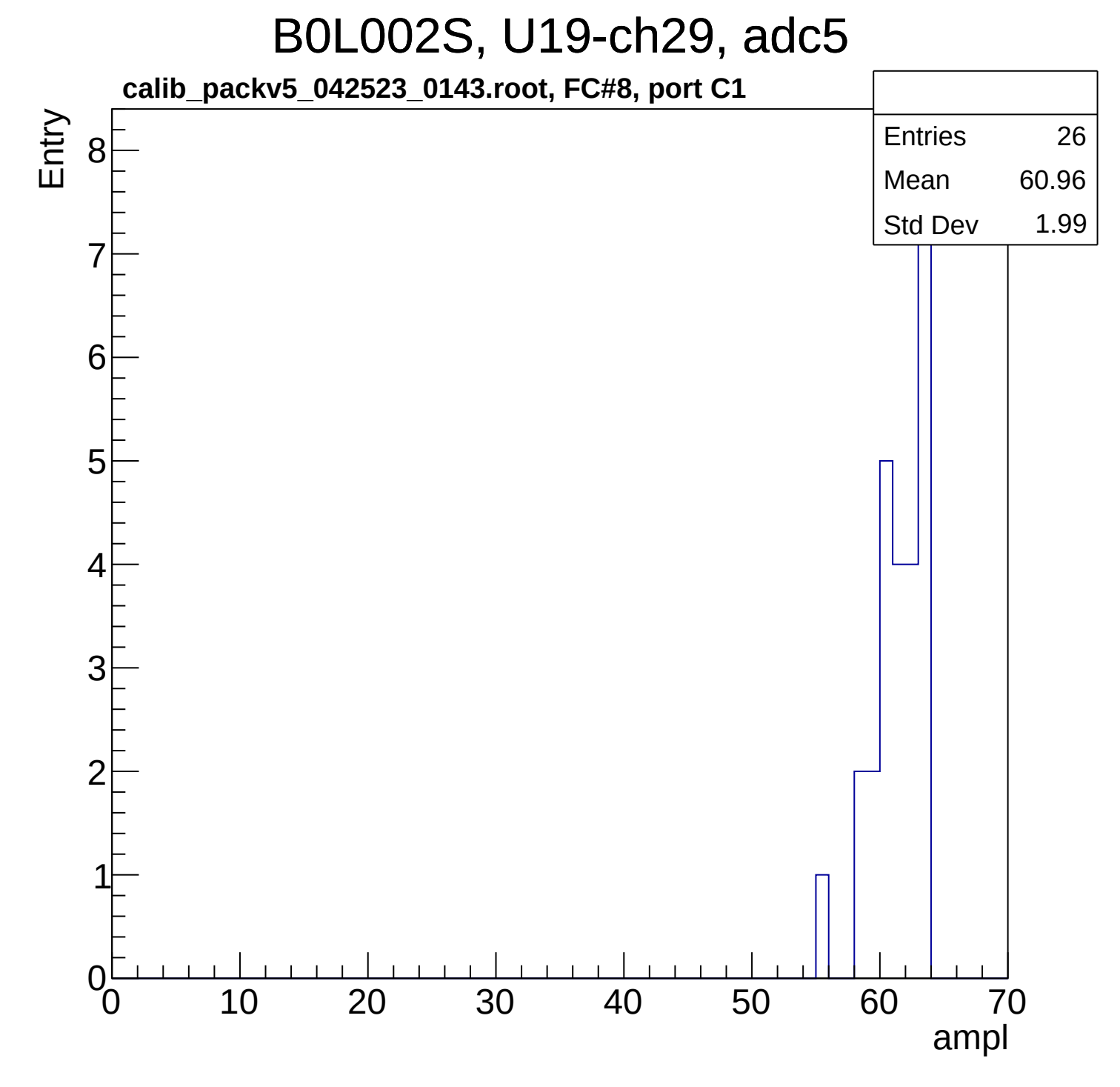

B0L002S, U19-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.72
Std Dev 5.051
Gaus mean : 30.4882
Gaus Width: 3.9675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

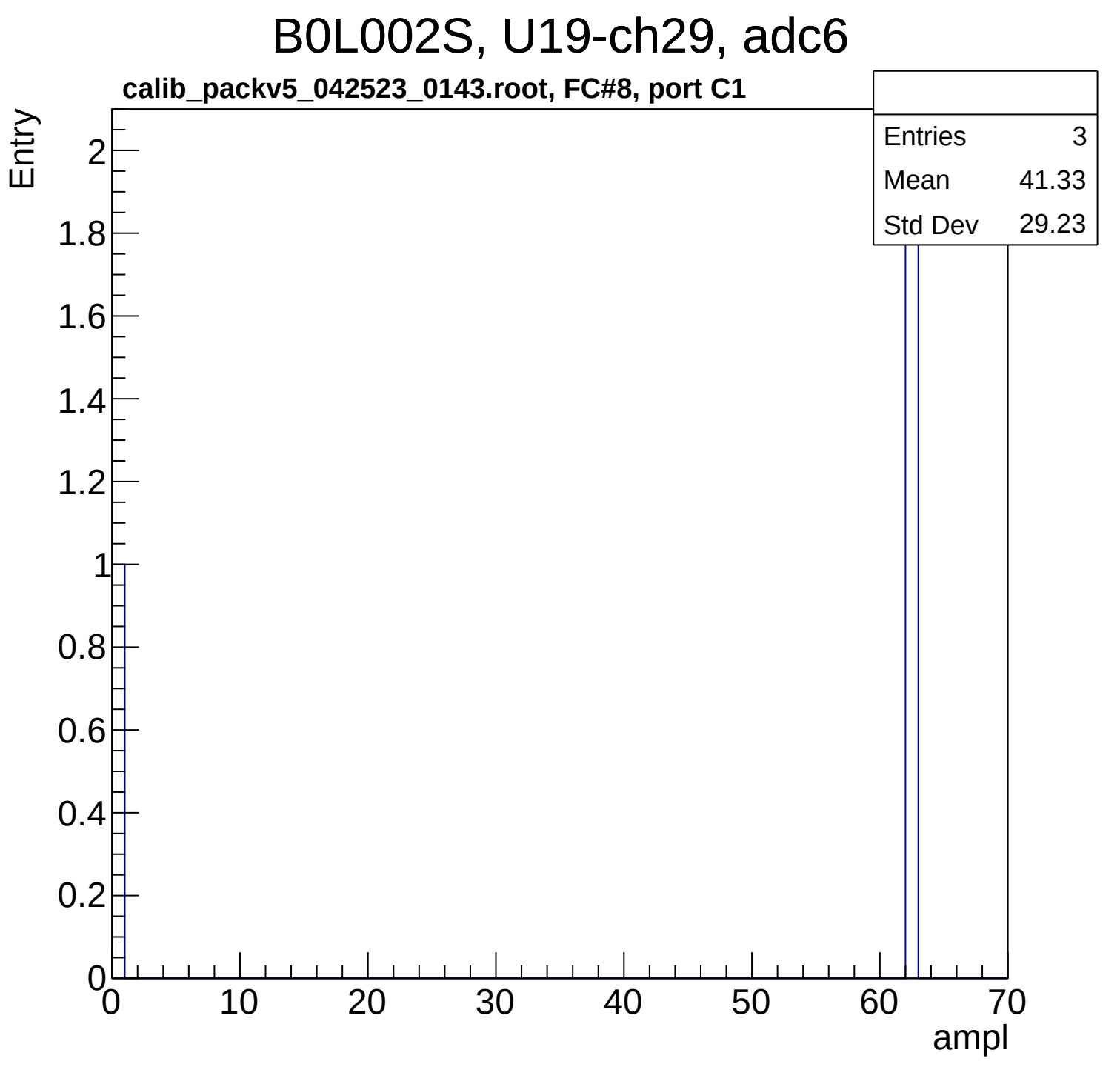

B0L002S, U19-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.02
Std Dev 3.063
Gaus mean : 36.2170
Gaus Width: 2.0316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

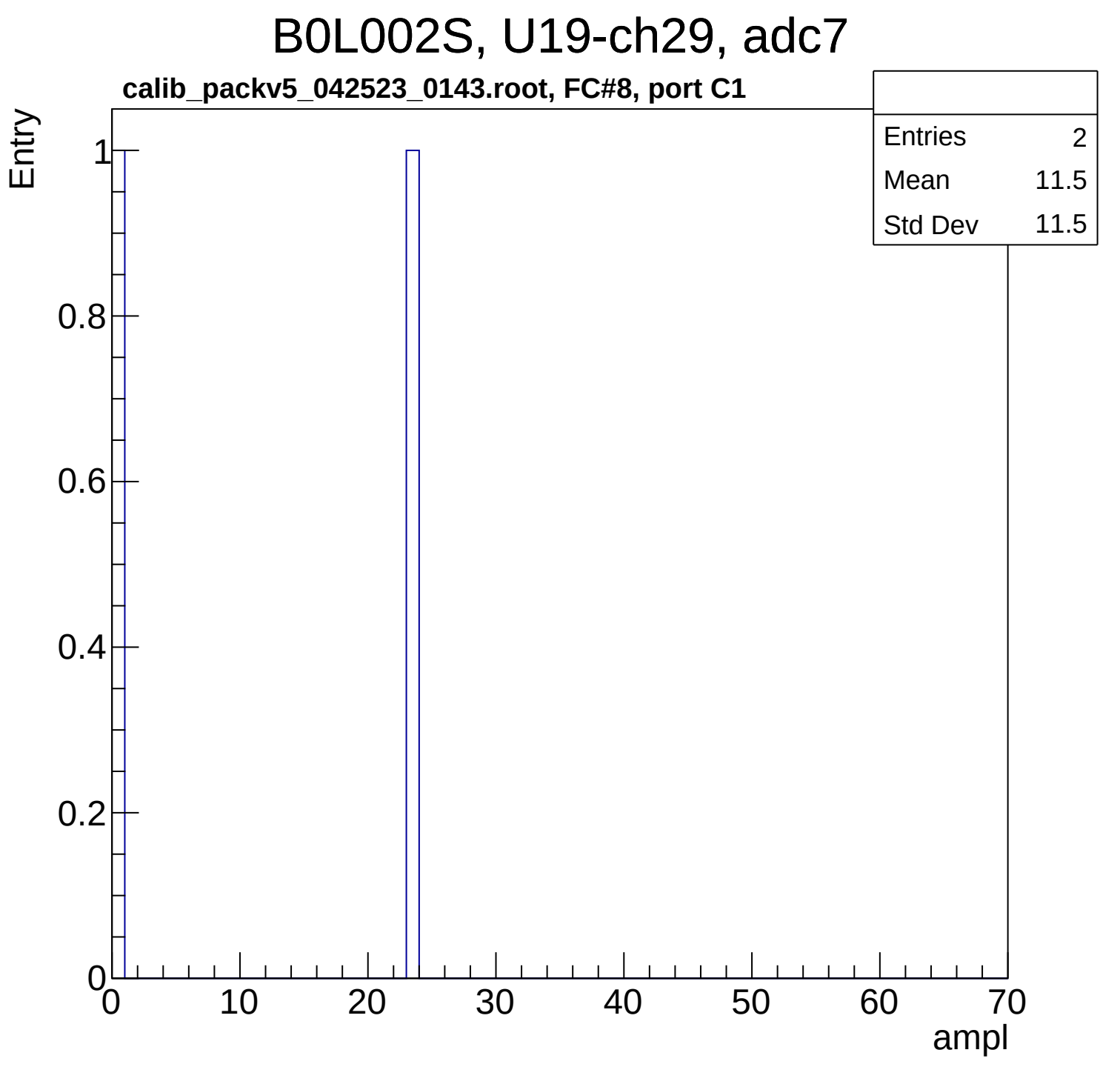

B0L002S, U19-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 44.23
Std Dev 3.52
Gaus mean : 45.0630
Gaus Width: 3.8882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

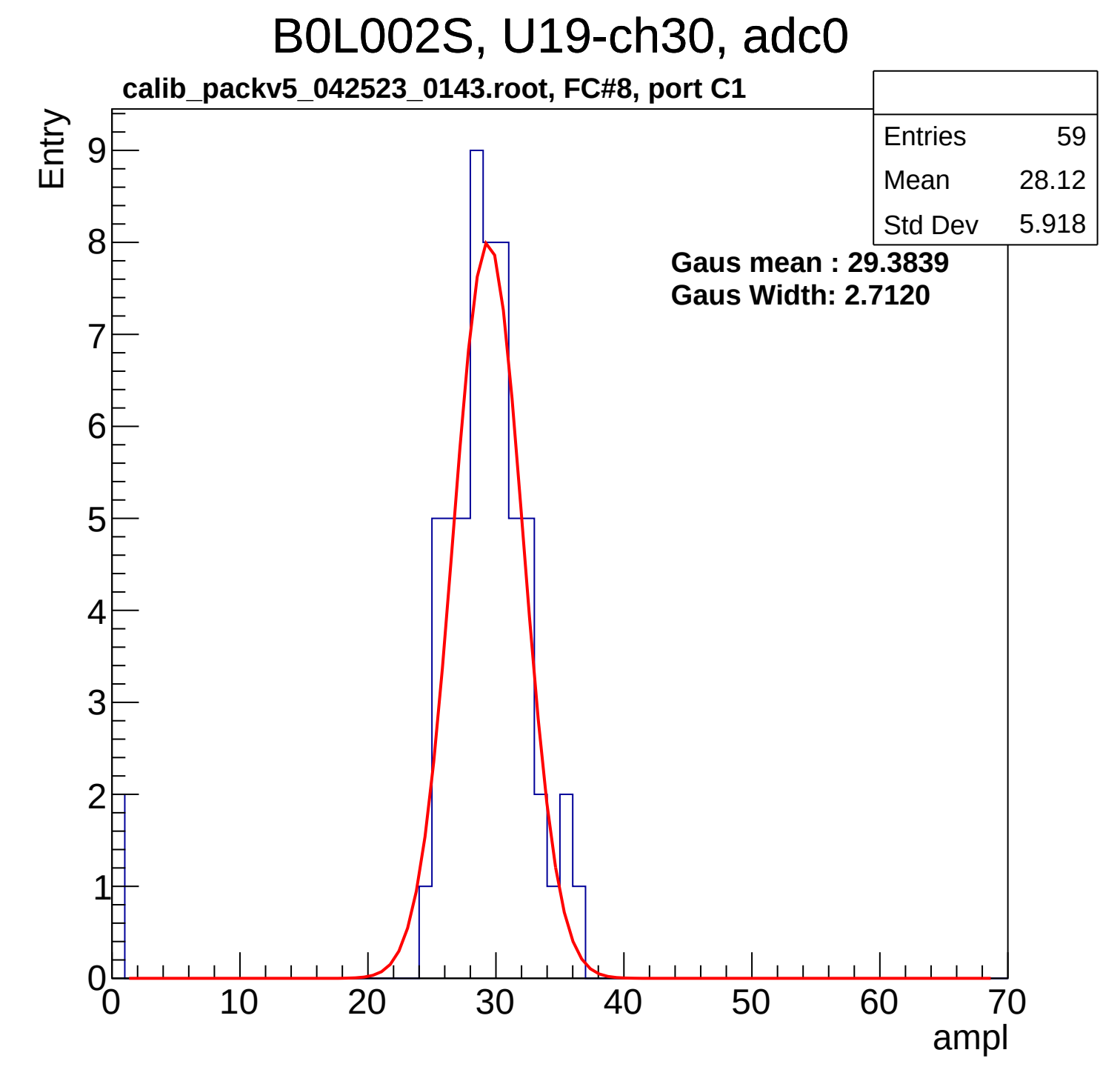

B0L002S, U19-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.84
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

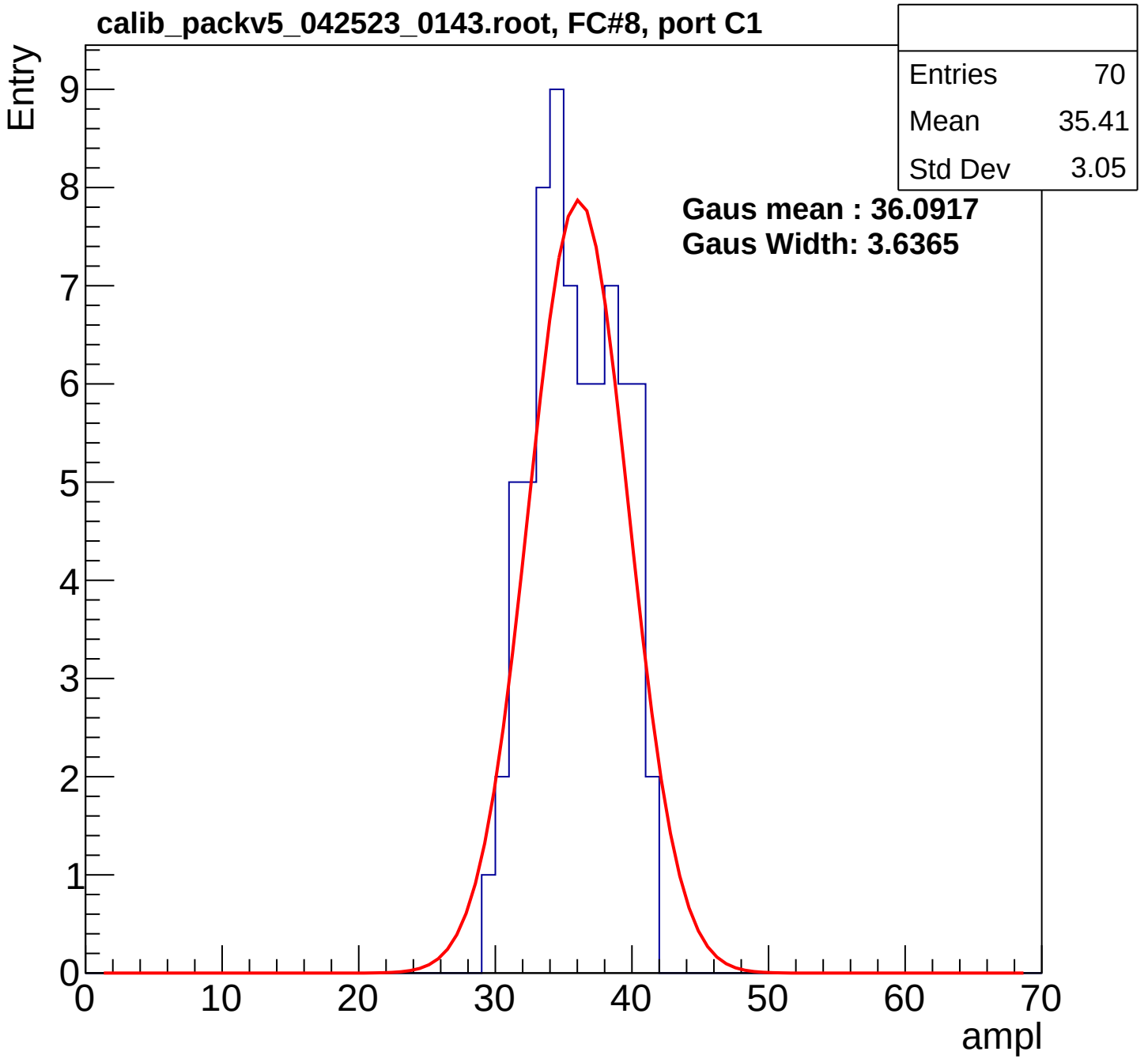

B0L002S, U19-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.94
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

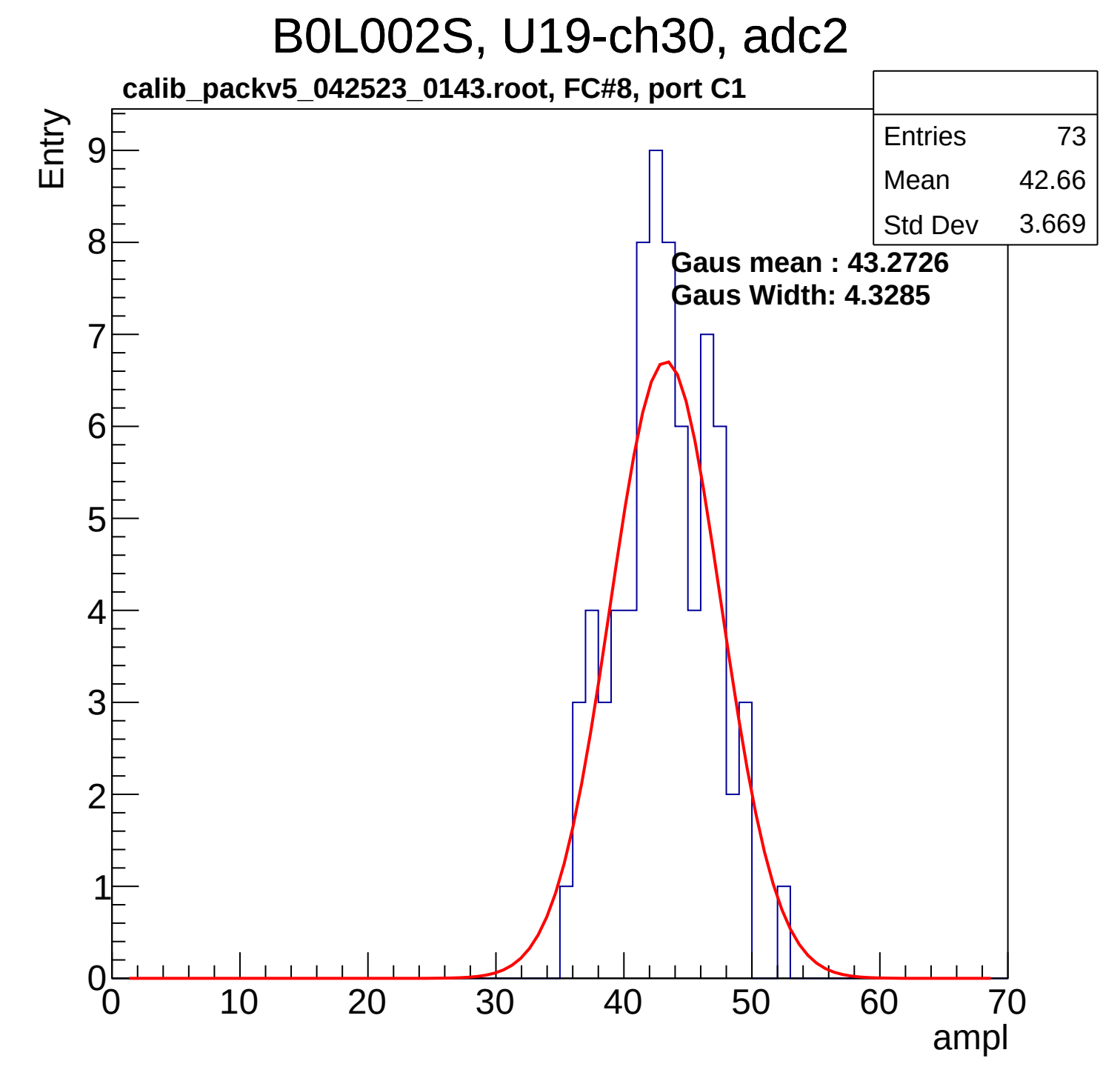

B0L002S, U19-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 60.96
Std Dev 1.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

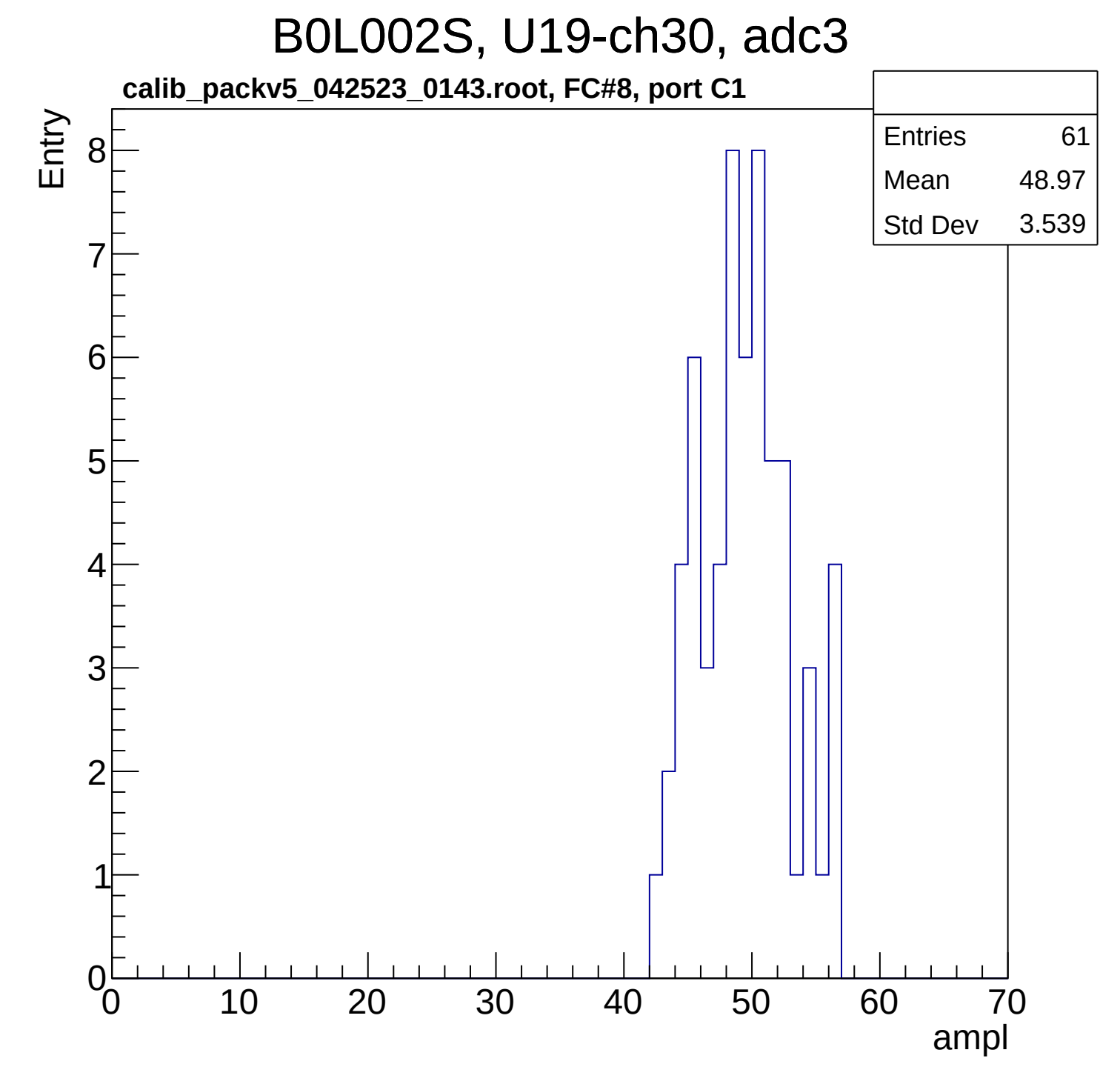

B0L002S, U19-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

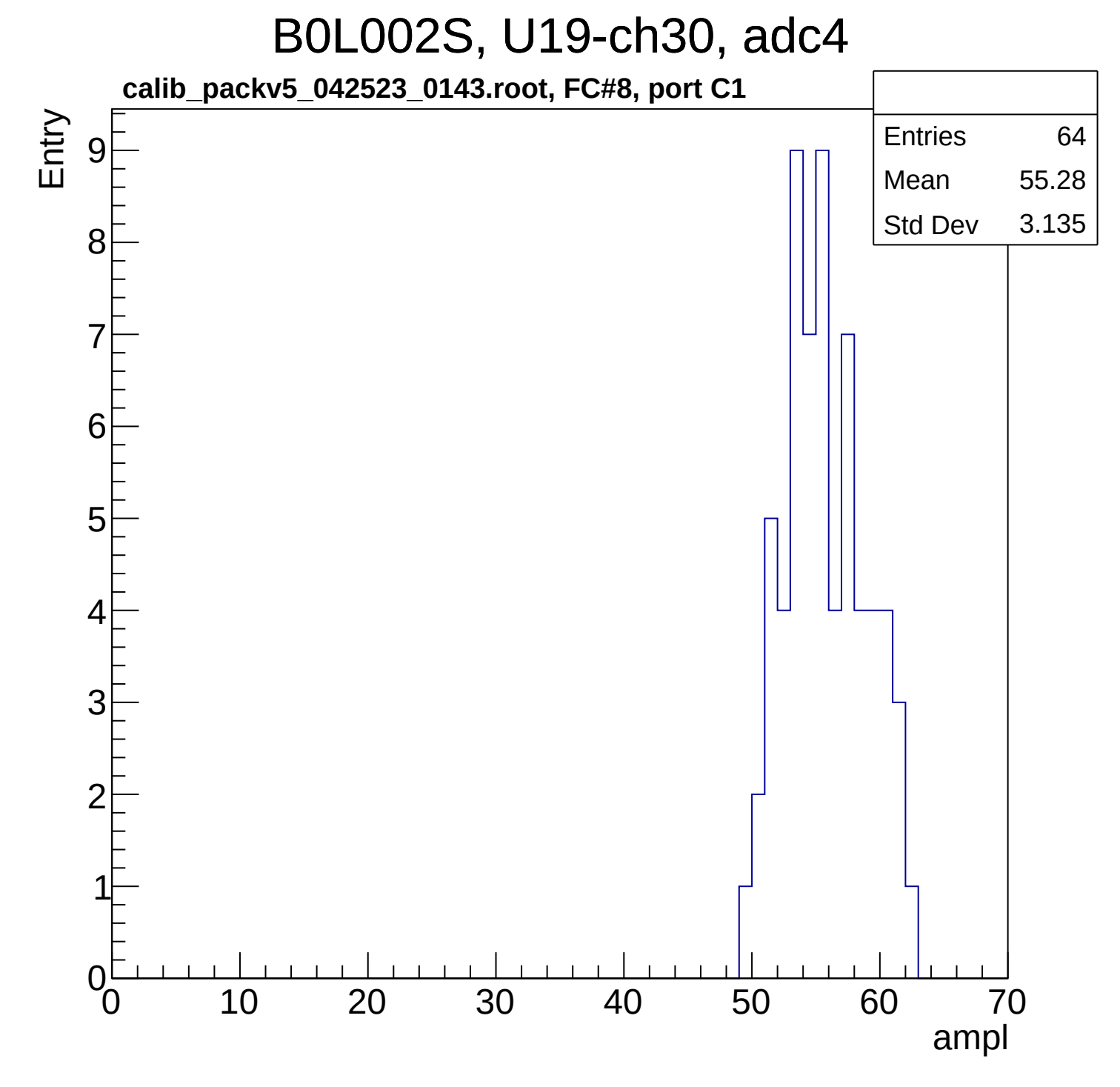

B0L002S, U19-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

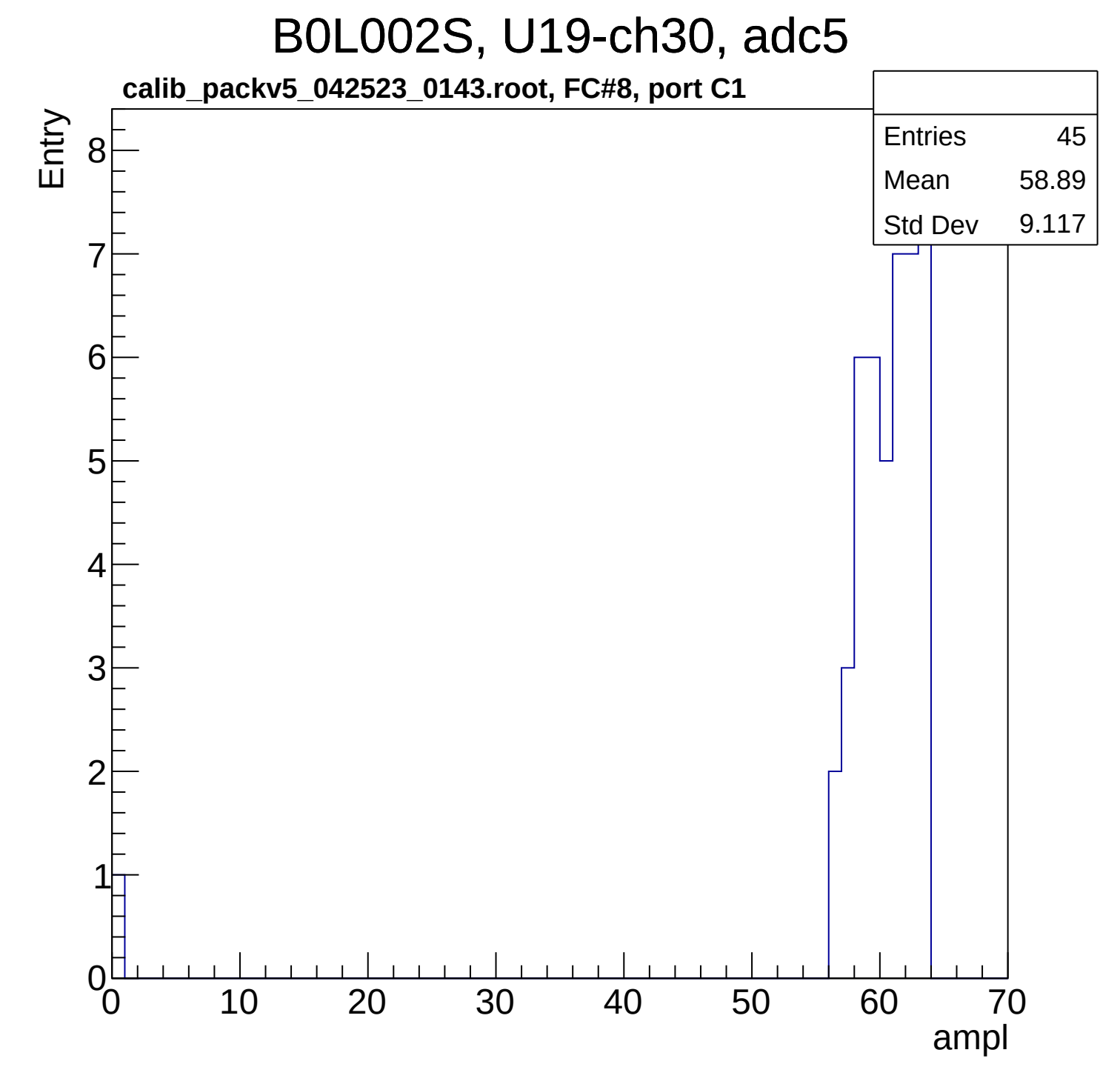

B0L002S, U19-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 28.12
Std Dev 5.918
Gaus mean : 29.3839
Gaus Width: 2.7120
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

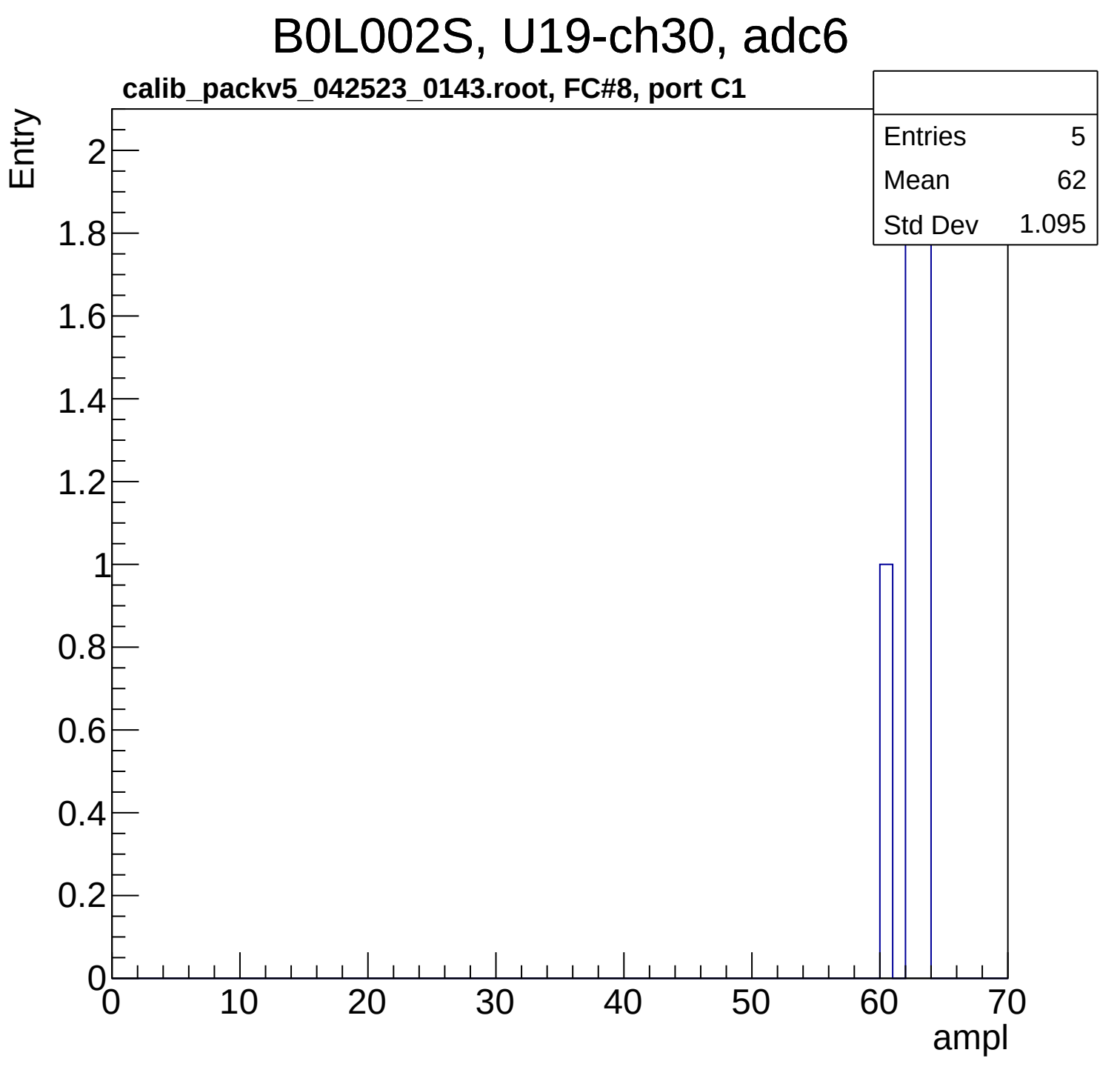

B0L002S, U19-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 35.41
Std Dev 3.05
Gaus mean : 36.0917
Gaus Width: 3.6365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

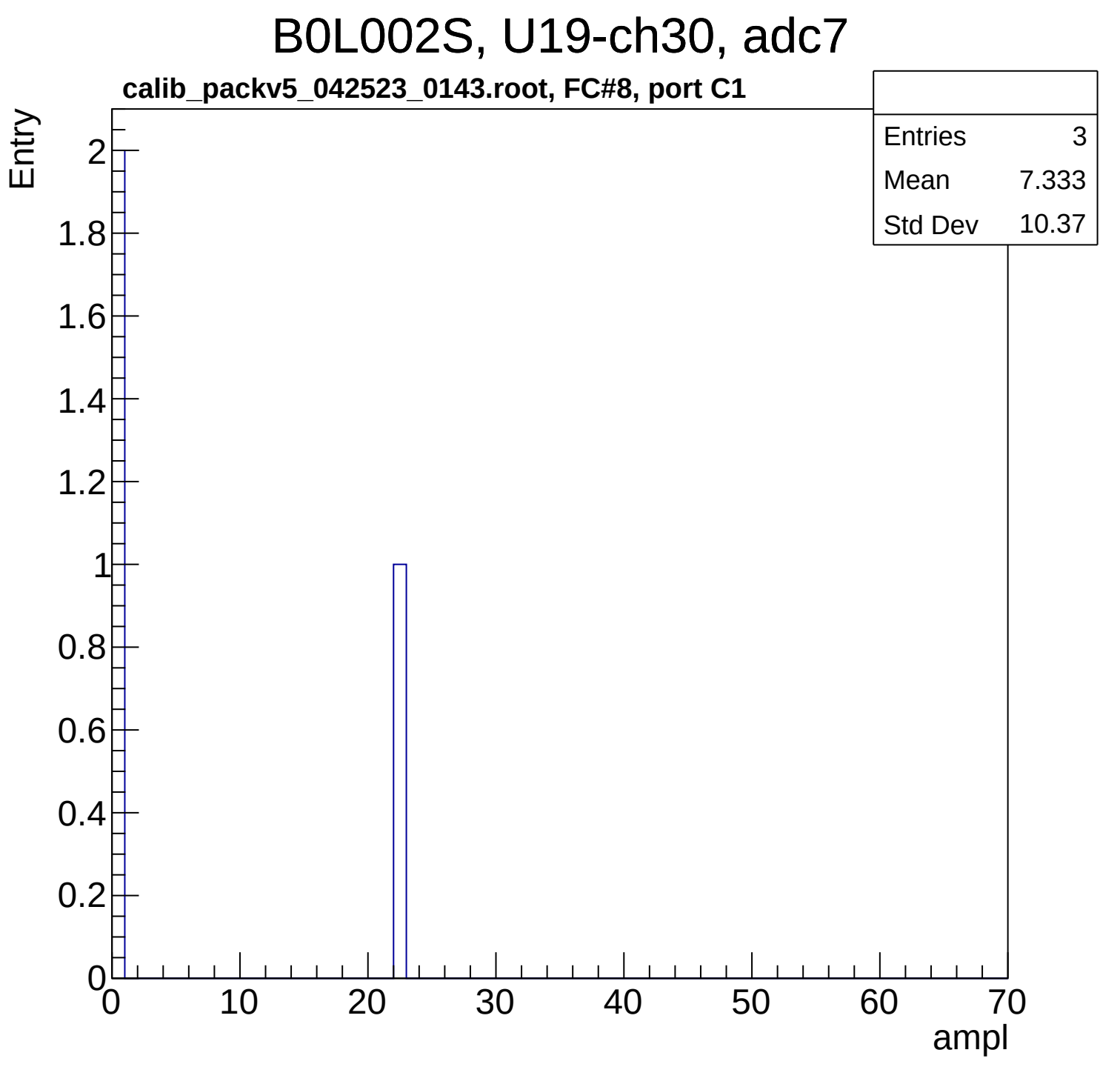

B0L002S, U19-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 42.66
Std Dev 3.669
Gaus mean : 43.2726
Gaus Width: 4.3285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

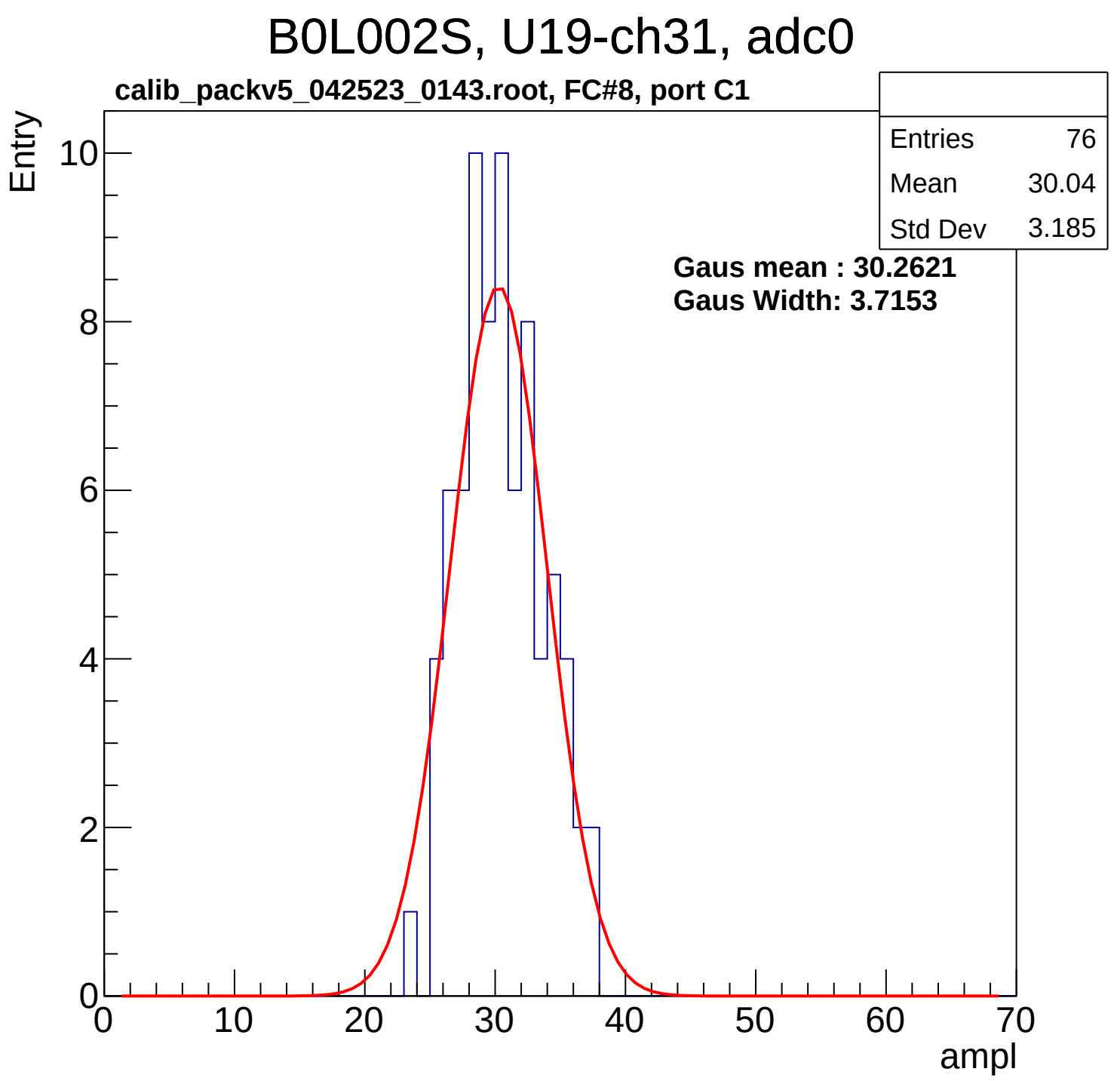

B0L002S, U19-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 48.97
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

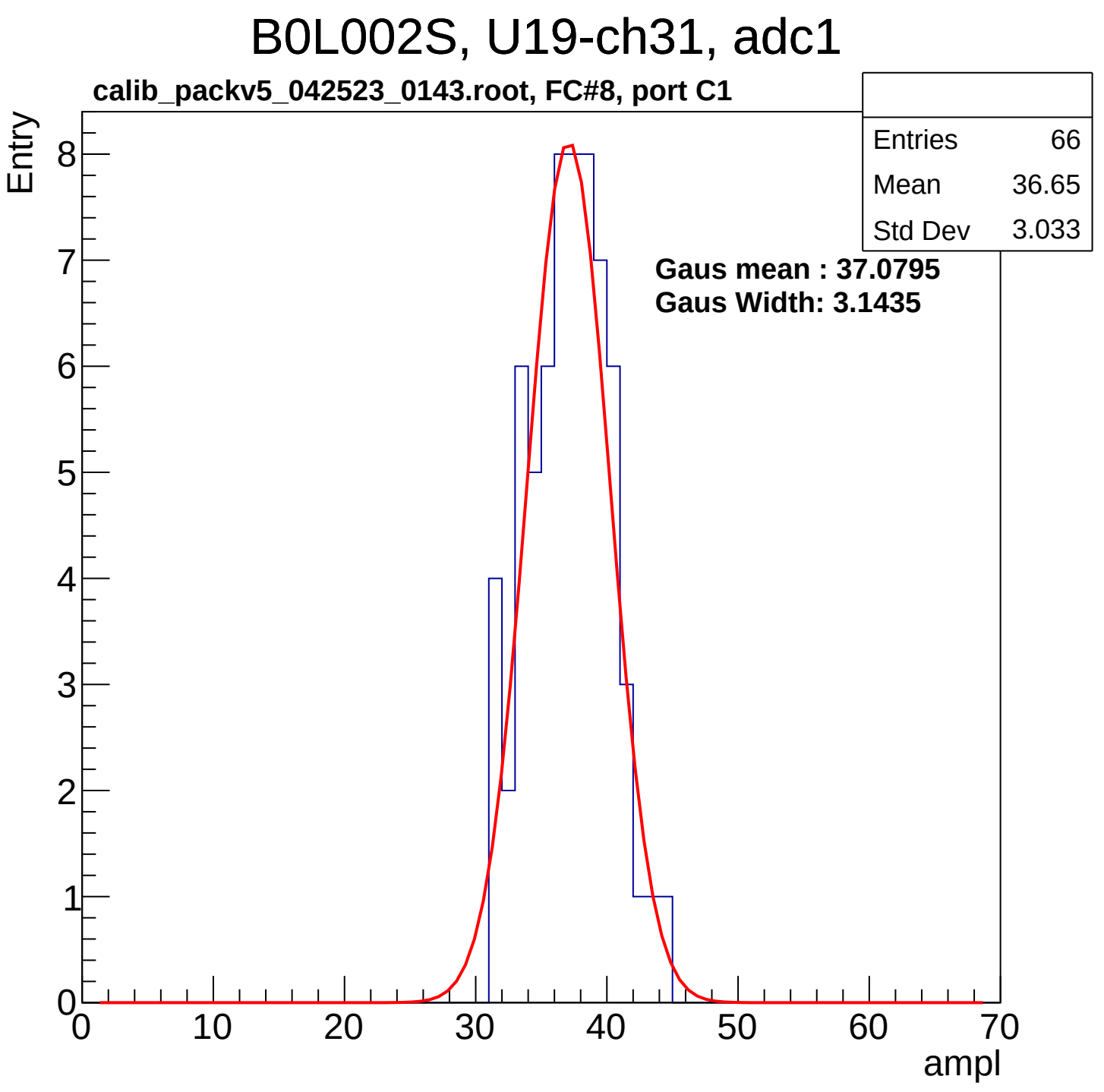

B0L002S, U19-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.28
Std Dev 3.135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

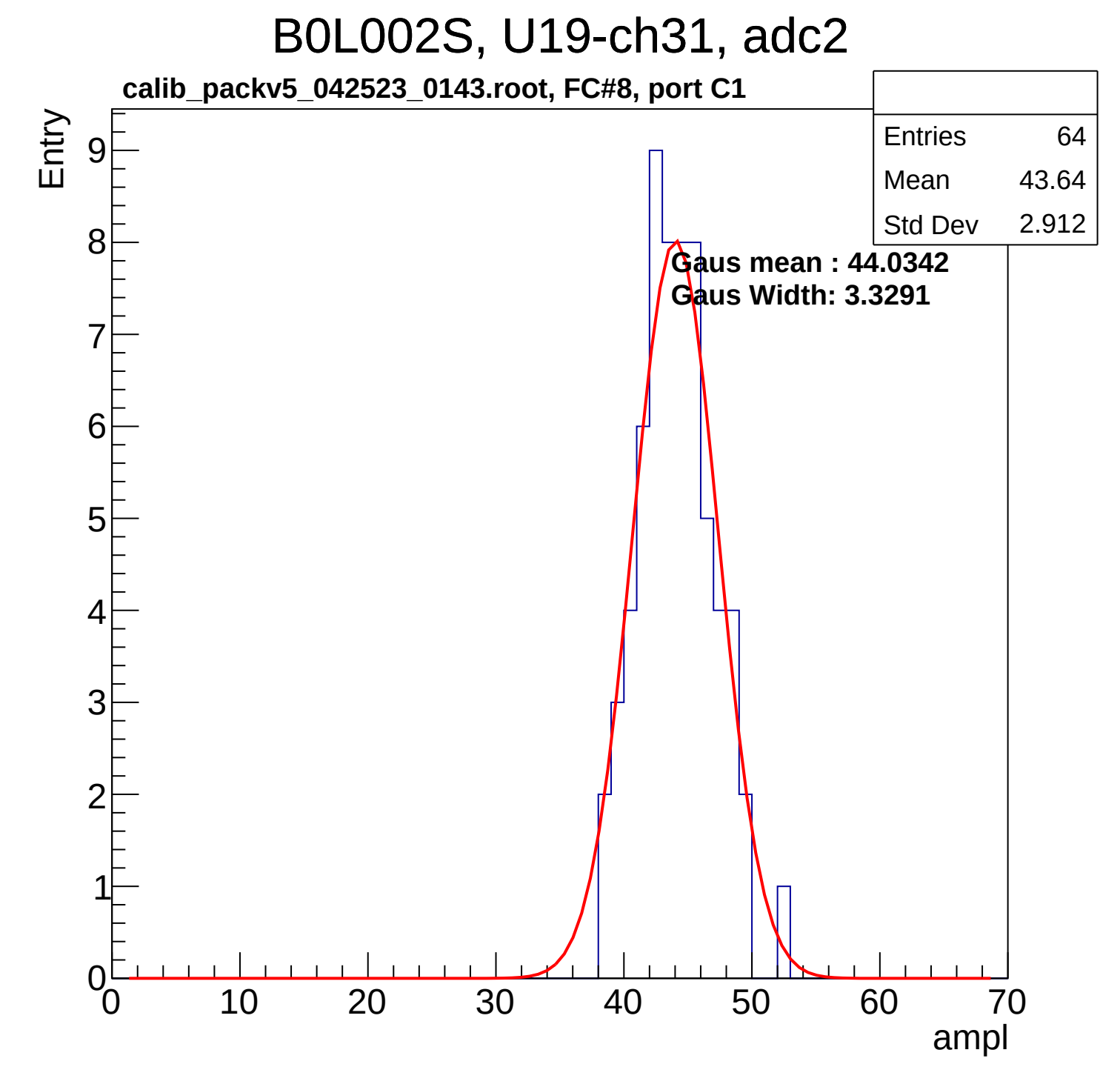

B0L002S, U19-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.89
Std Dev 9.117
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

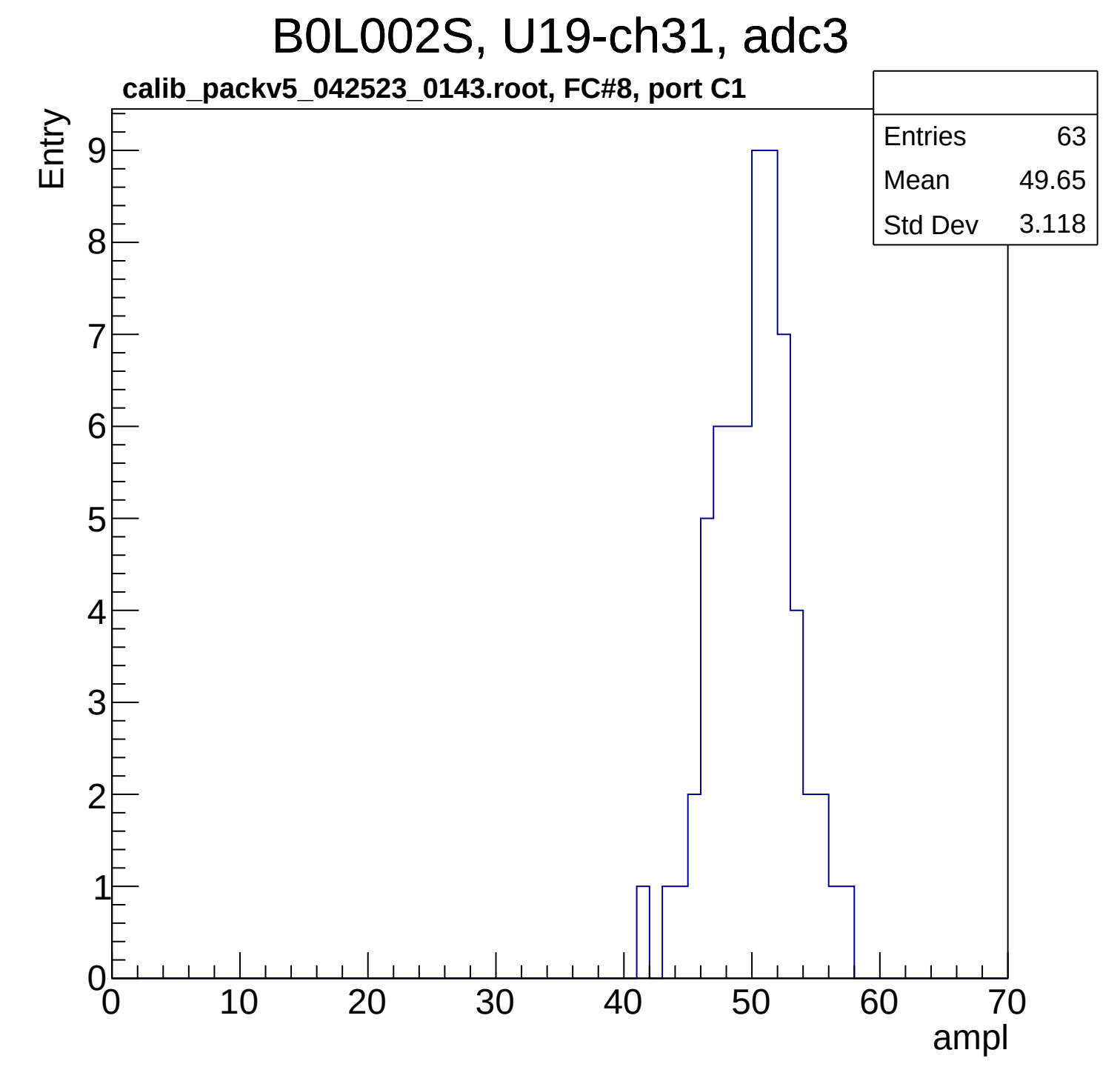

B0L002S, U19-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

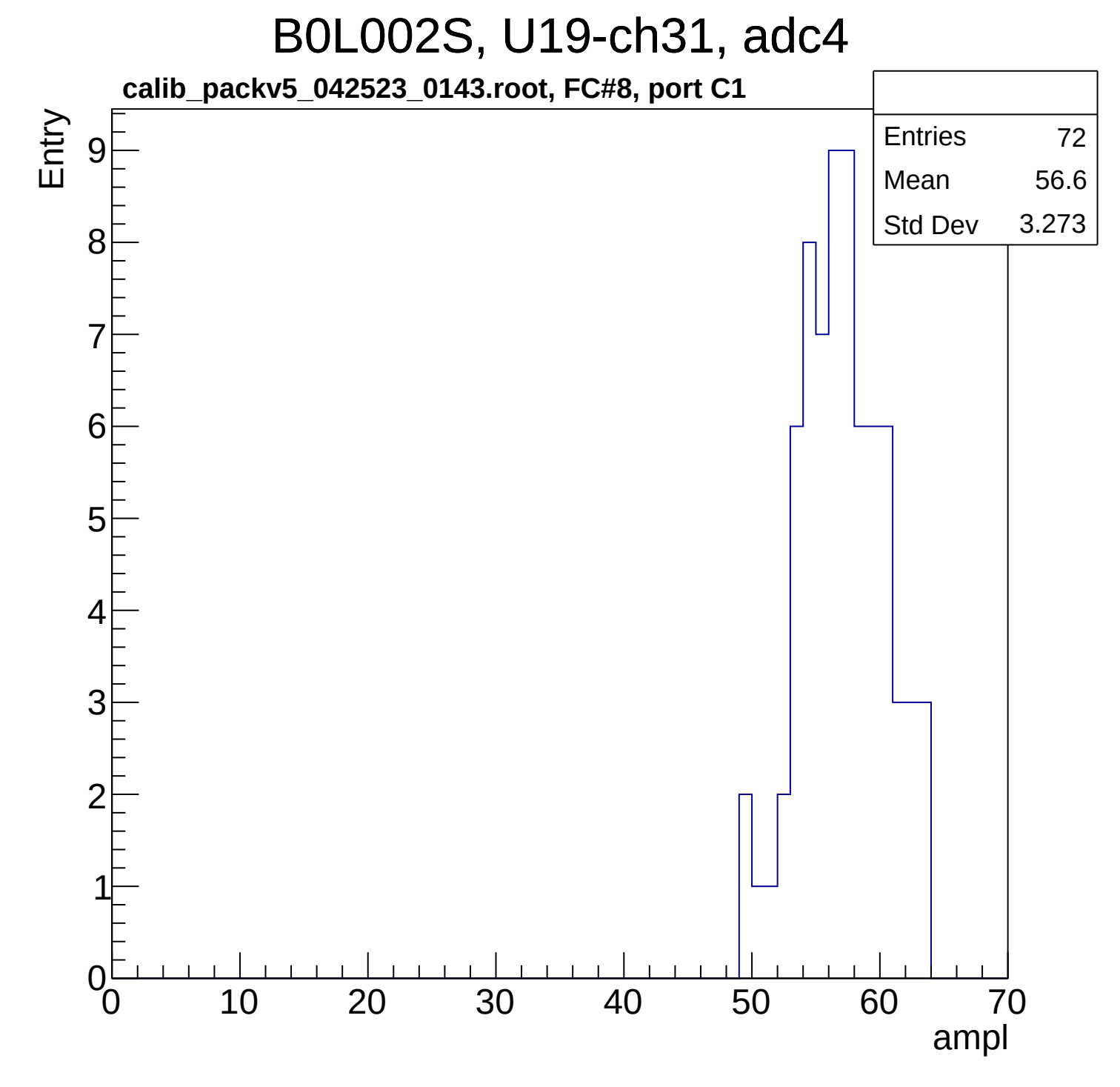

B0L002S, U19-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

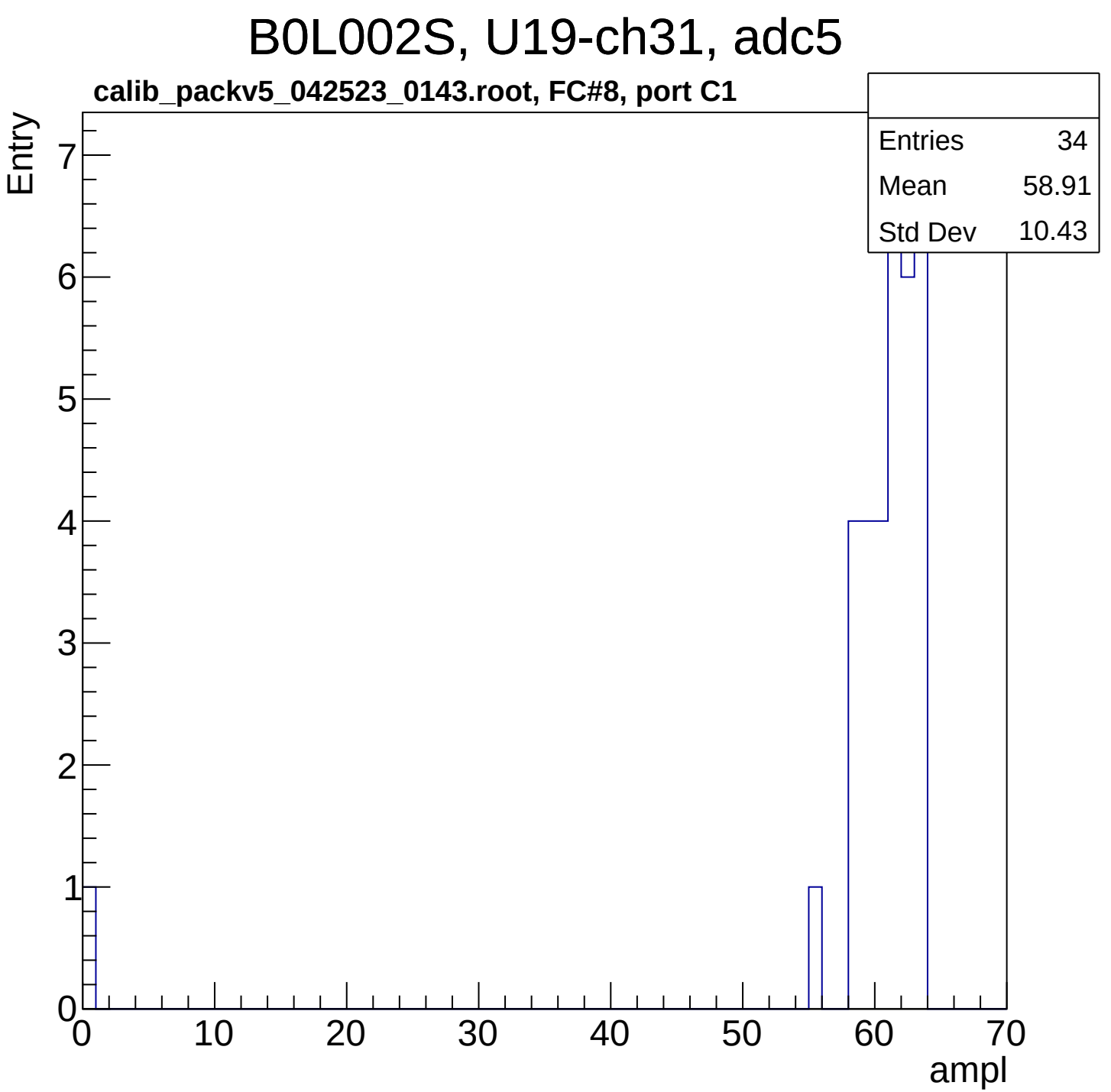

B0L002S, U19-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.04
Std Dev 3.185
Gaus mean : 30.2621
Gaus Width: 3.7153
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

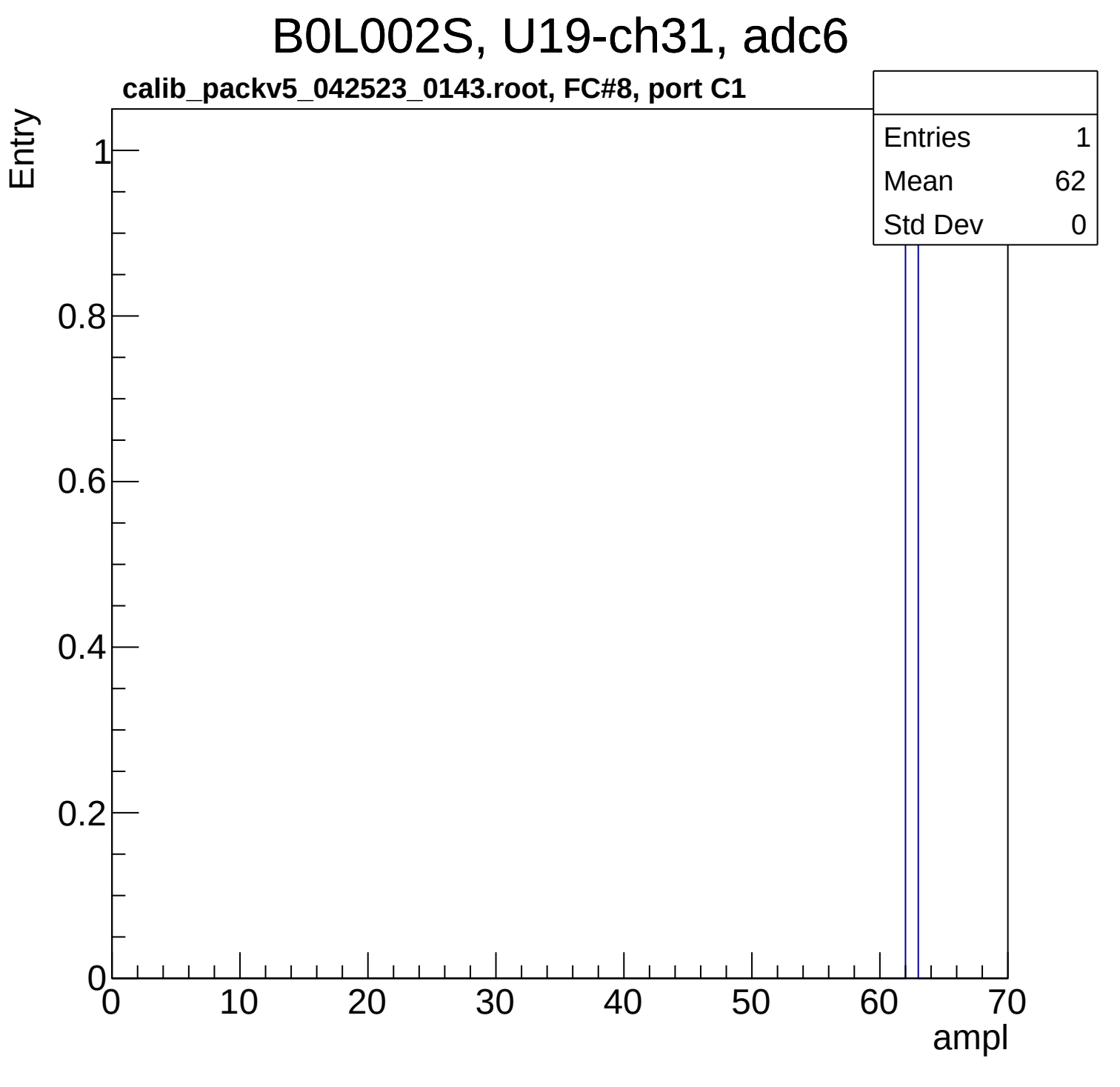

B0L002S, U19-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.65
Std Dev 3.033
Gaus mean : 37.0795
Gaus Width: 3.1435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

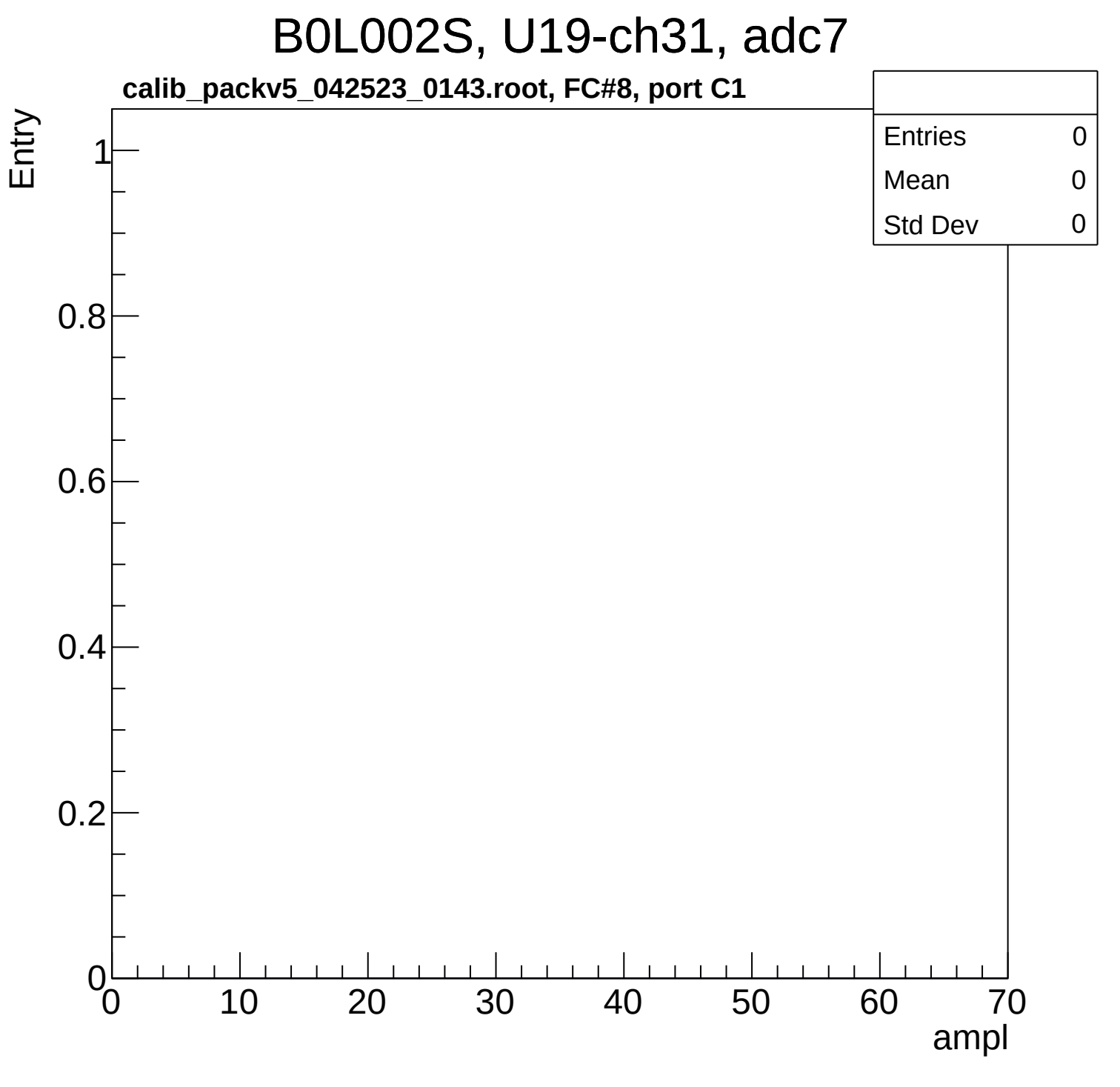

B0L002S, U19-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.64
Std Dev 2.912
Gaus mean : 44.0342
Gaus Width: 3.3291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

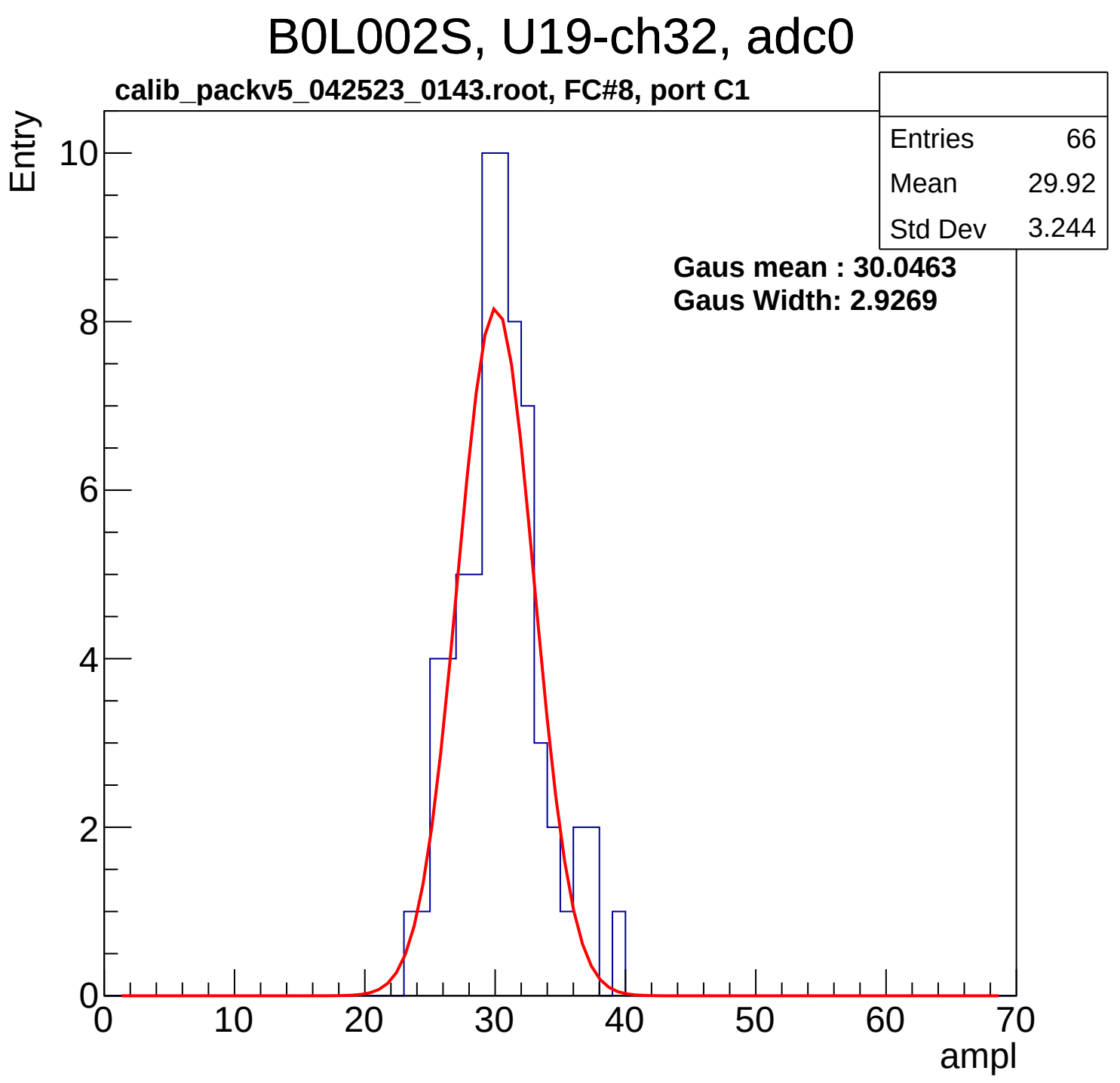

B0L002S, U19-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 49.65
Std Dev 3.118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

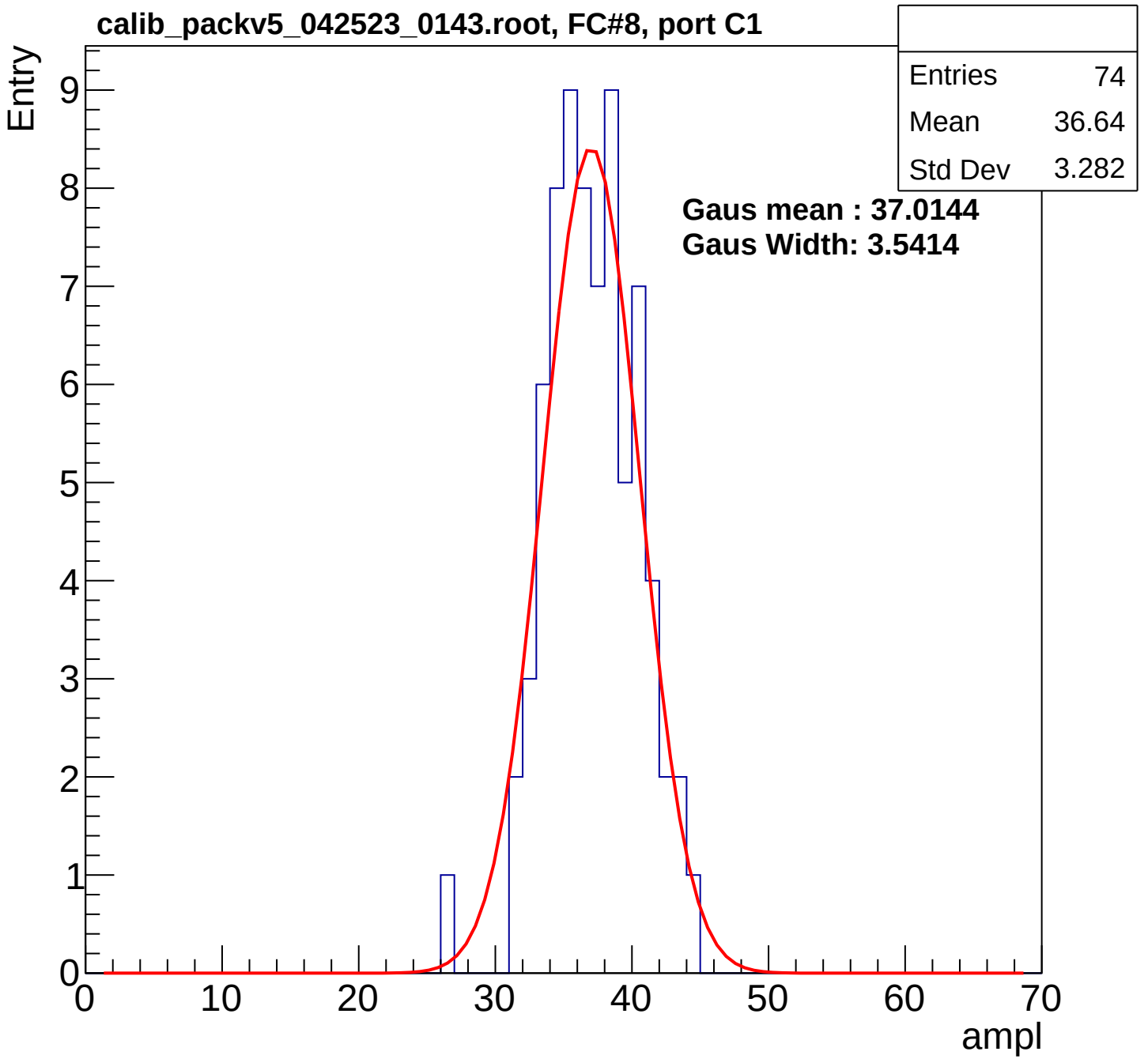

B0L002S, U19-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 56.6
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

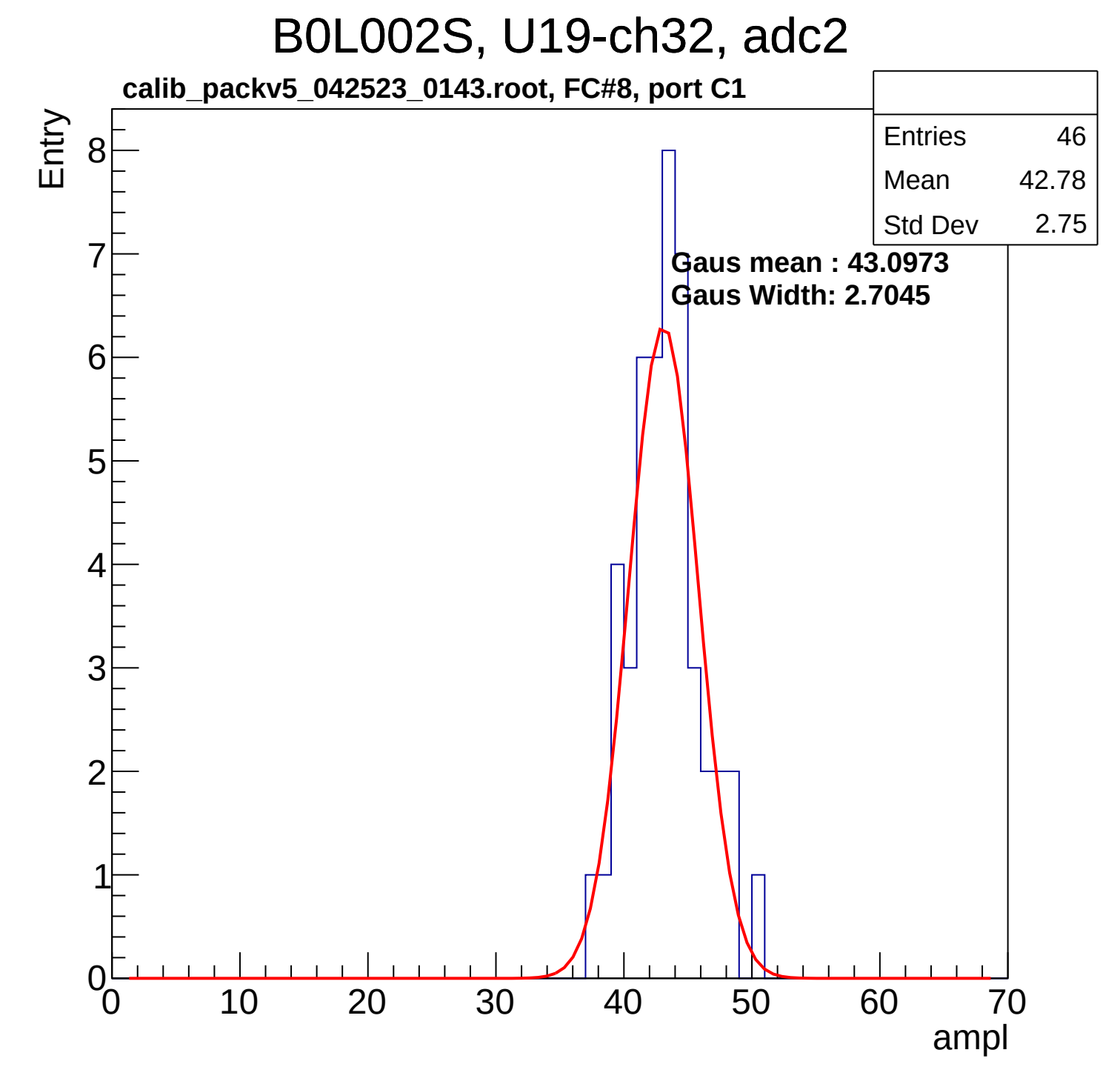

B0L002S, U19-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.91
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

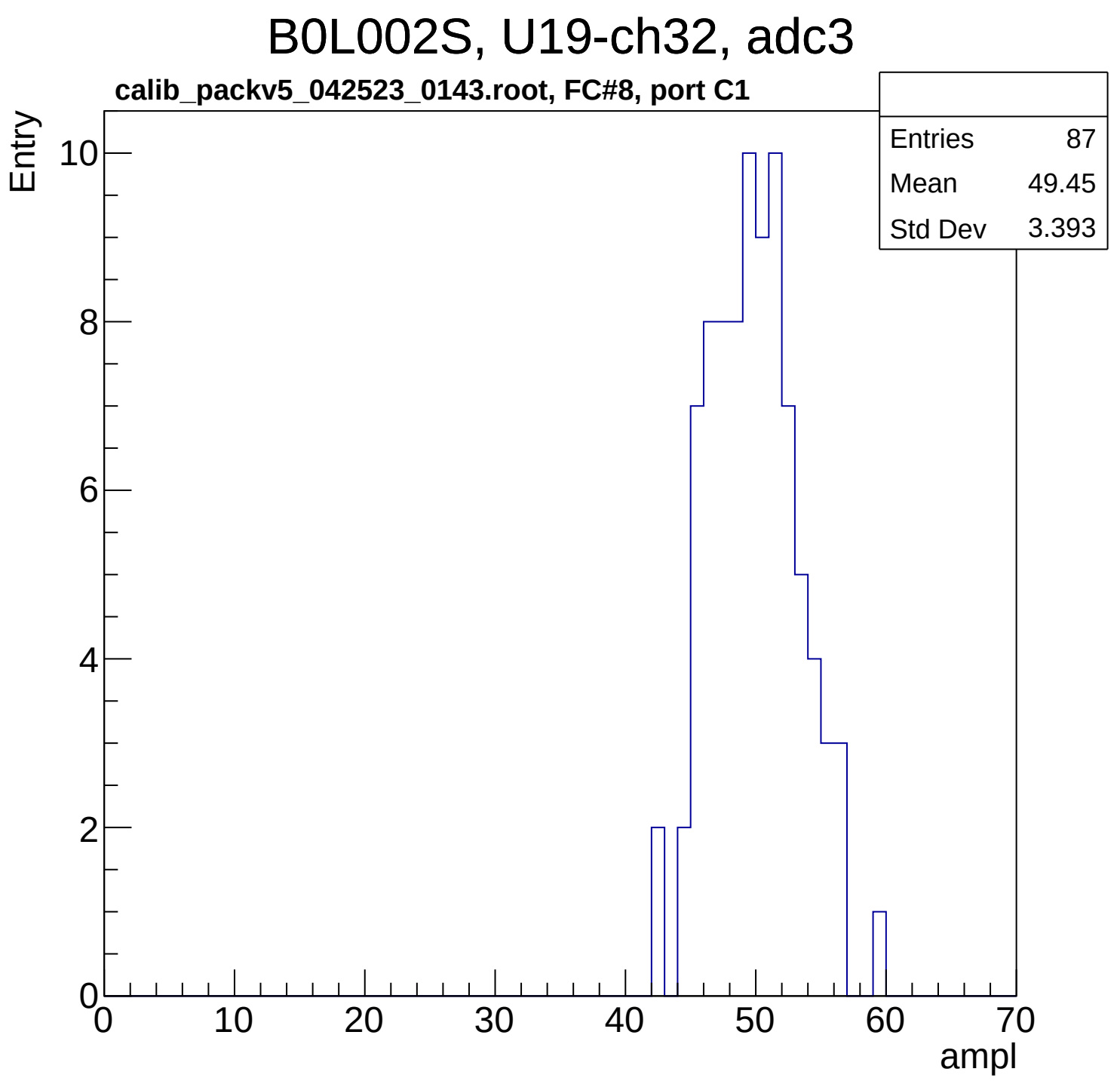

B0L002S, U19-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

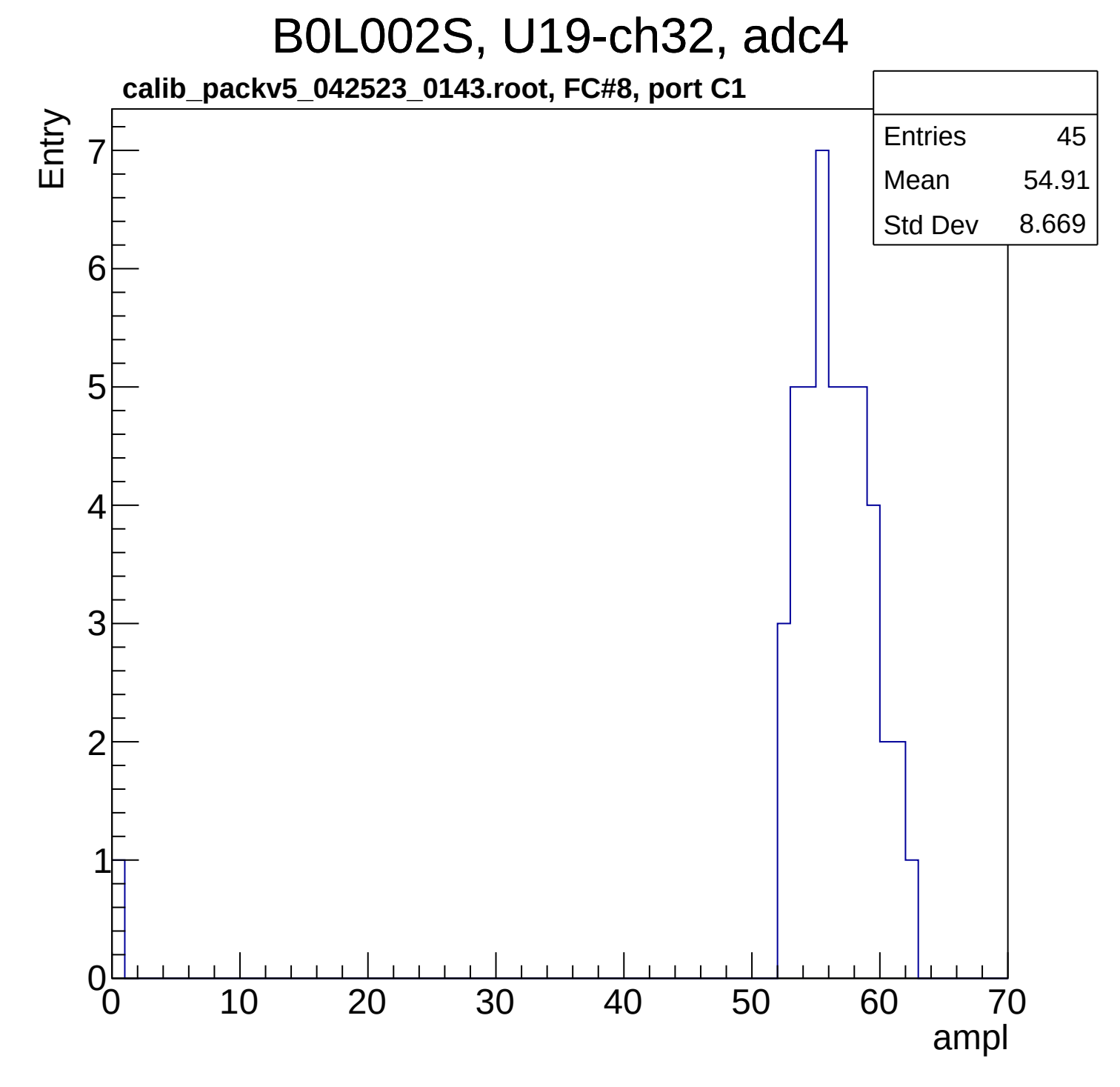

B0L002S, U19-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

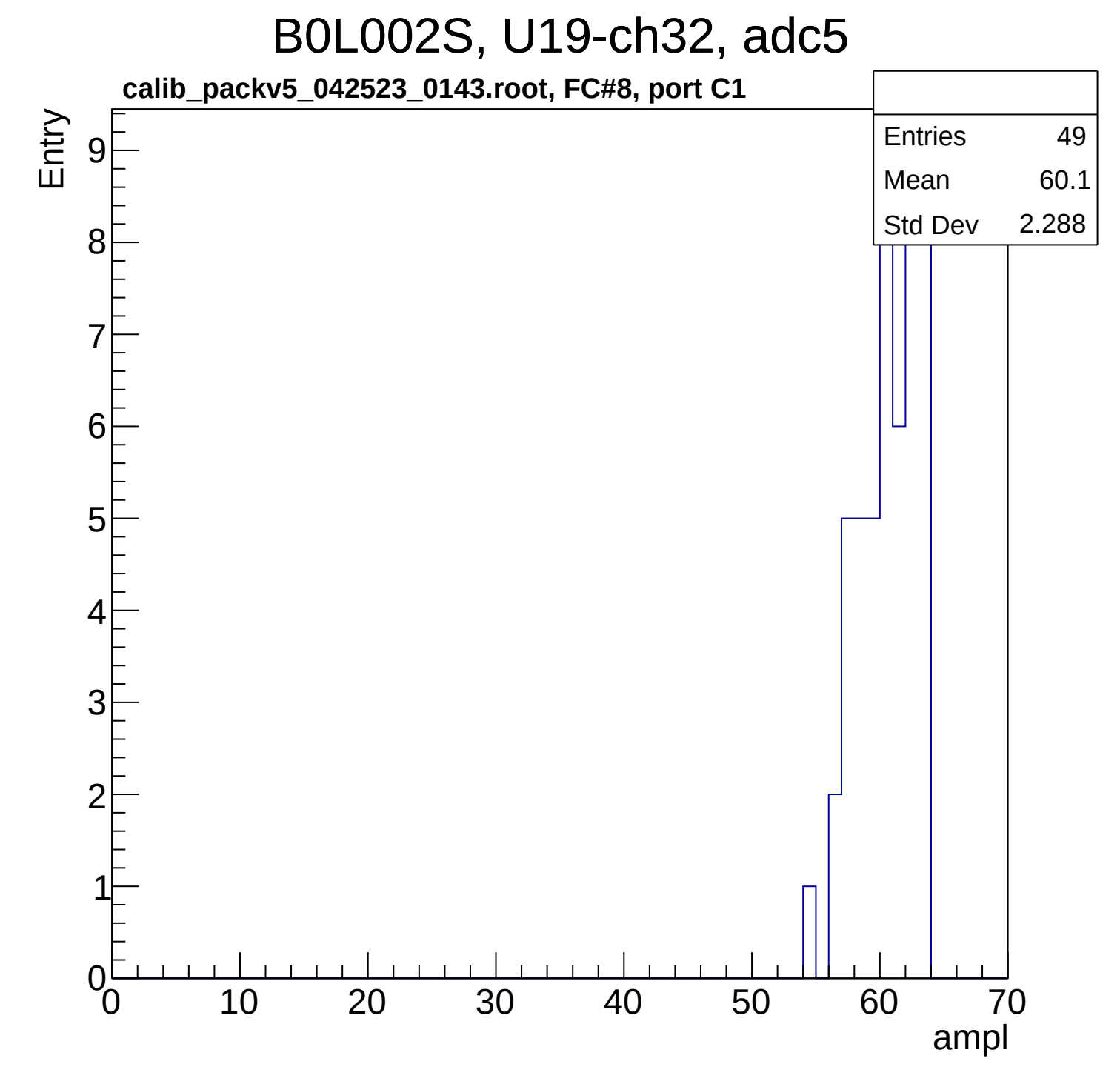

B0L002S, U19-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.92
Std Dev 3.244
Gaus mean : 30.0463
Gaus Width: 2.9269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

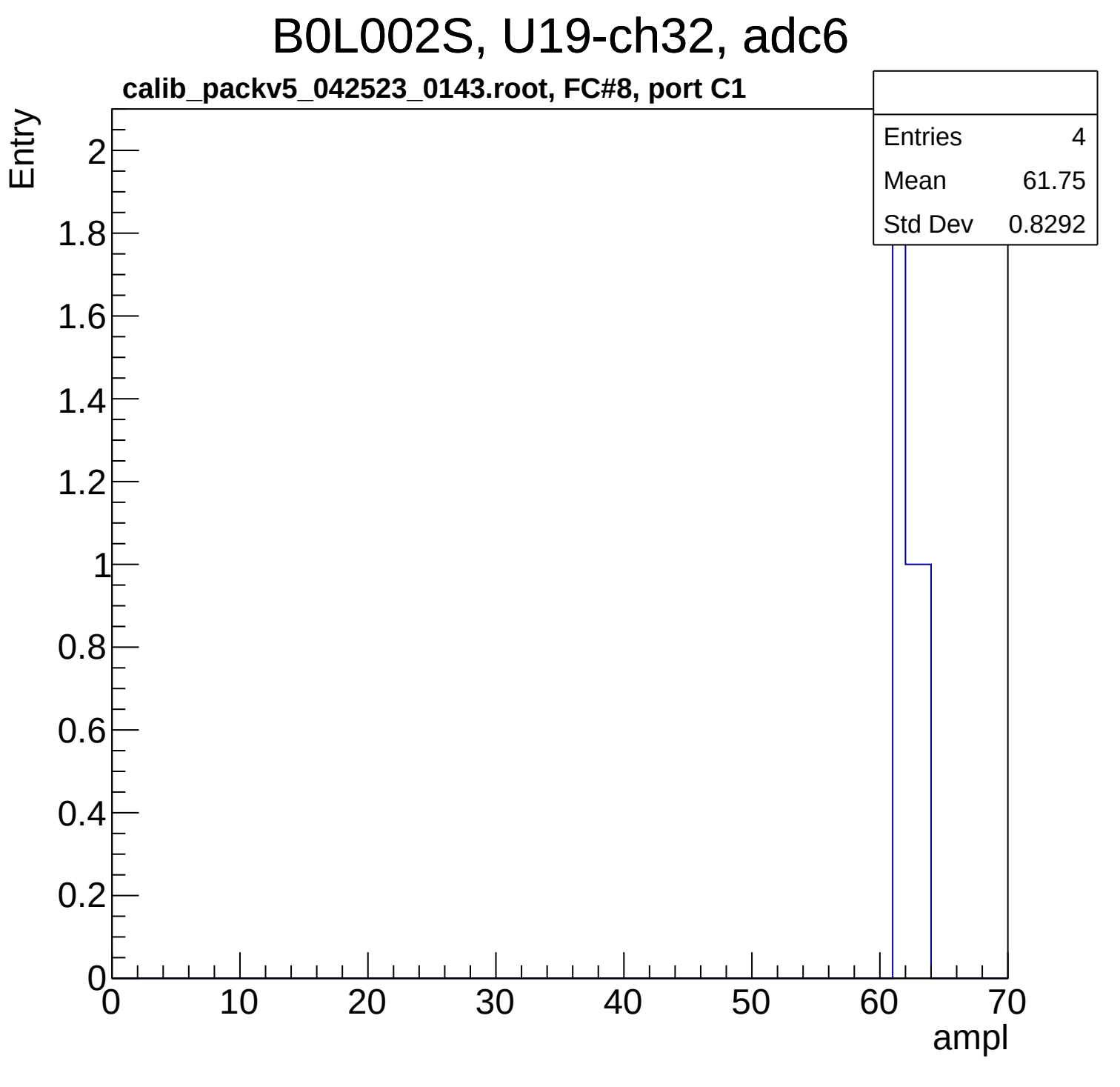

B0L002S, U19-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.64
Std Dev 3.282
Gaus mean : 37.0144
Gaus Width: 3.5414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

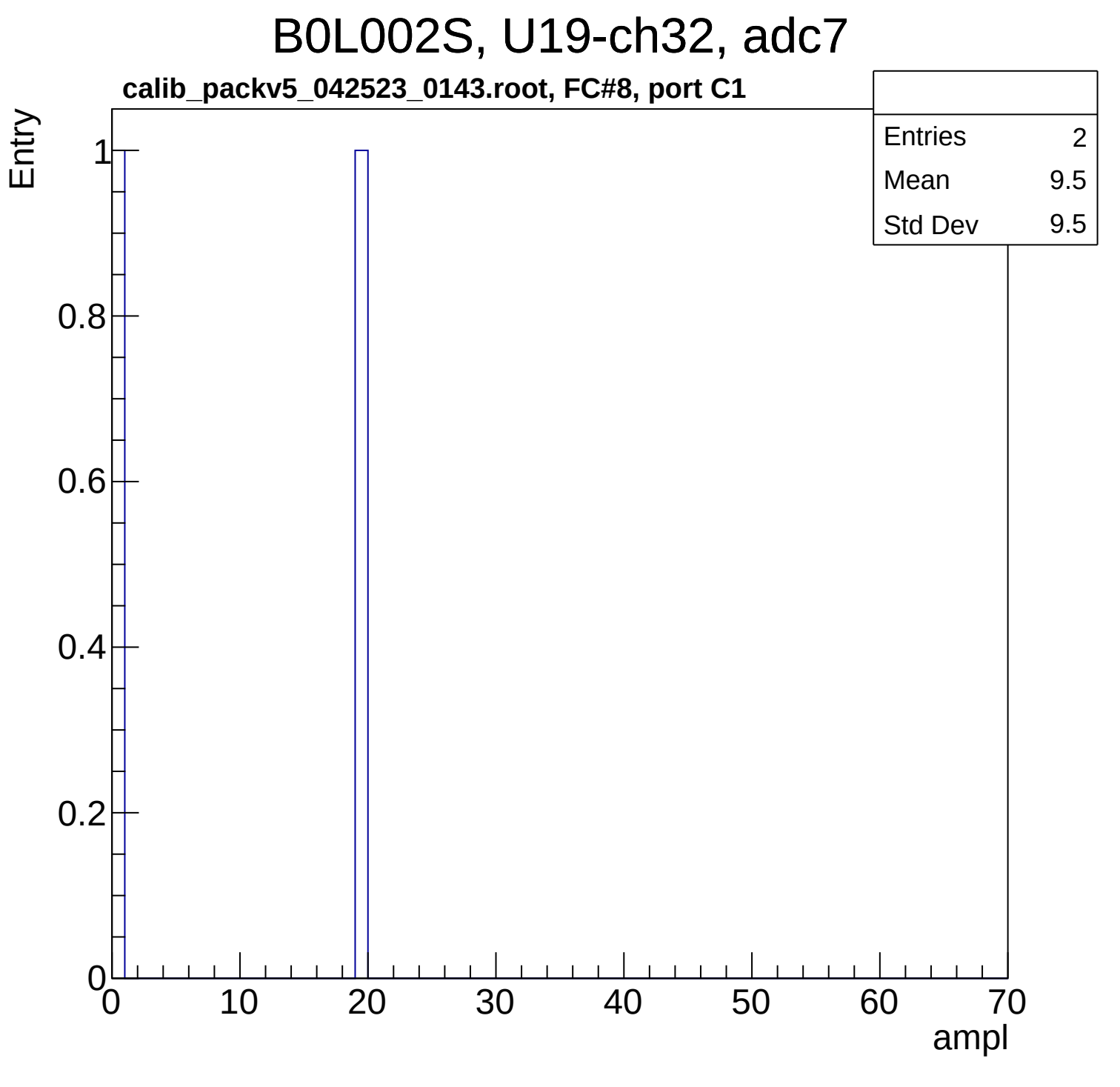

B0L002S, U19-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 42.78
Std Dev 2.75
Gaus mean : 43.0973
Gaus Width: 2.7045
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

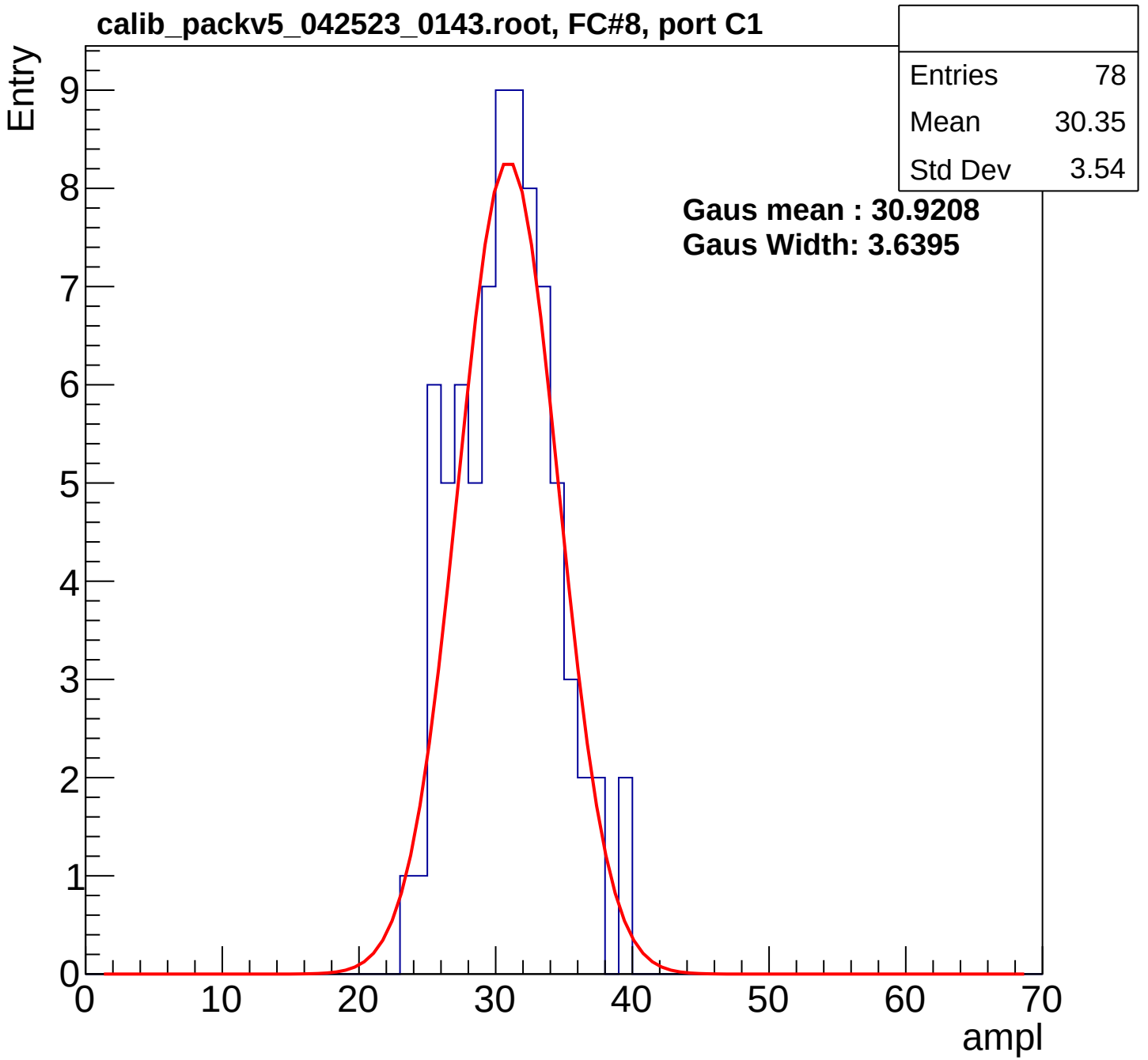

B0L002S, U19-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 49.45
Std Dev 3.393
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

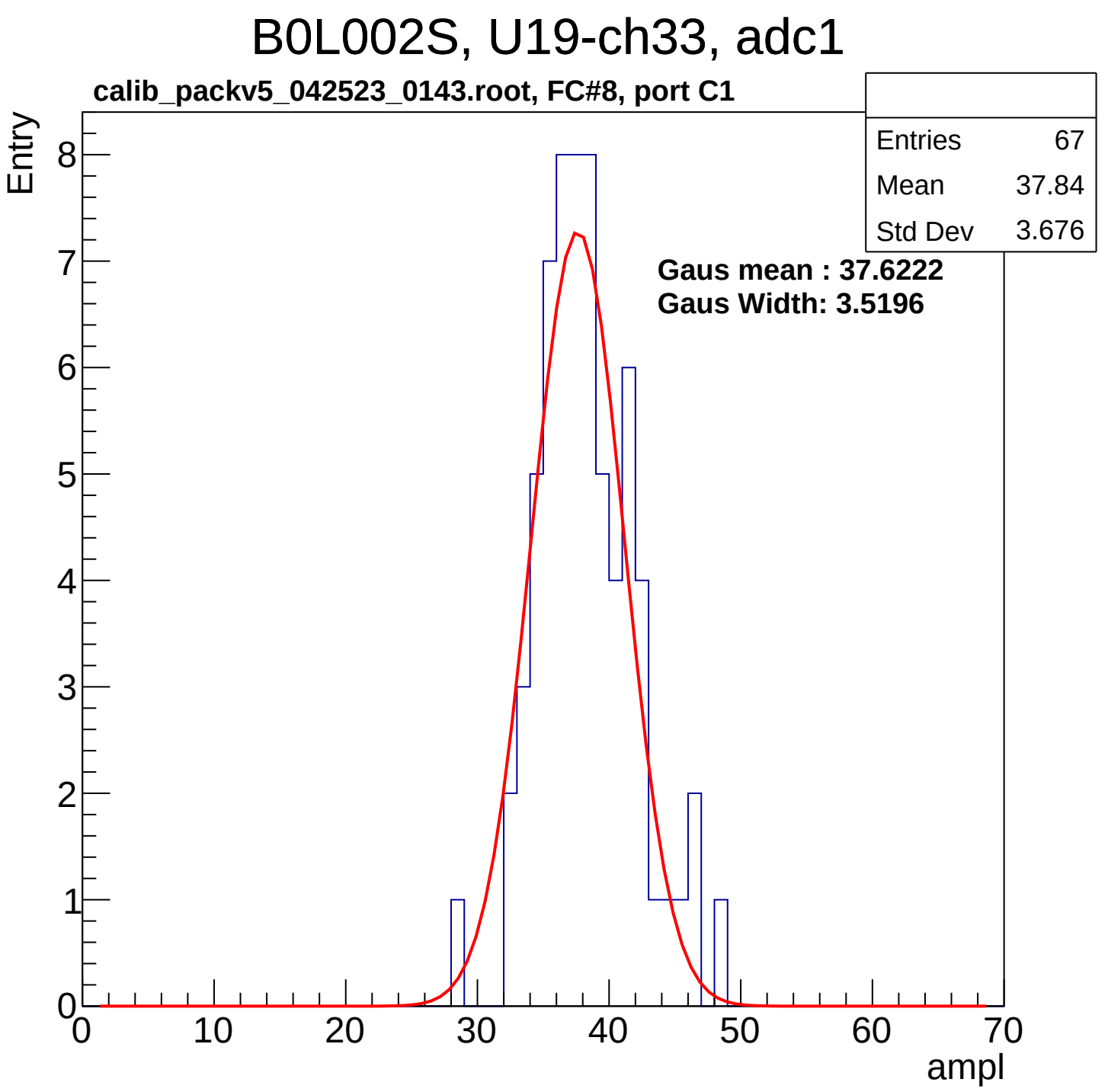

B0L002S, U19-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 54.91
Std Dev 8.669
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

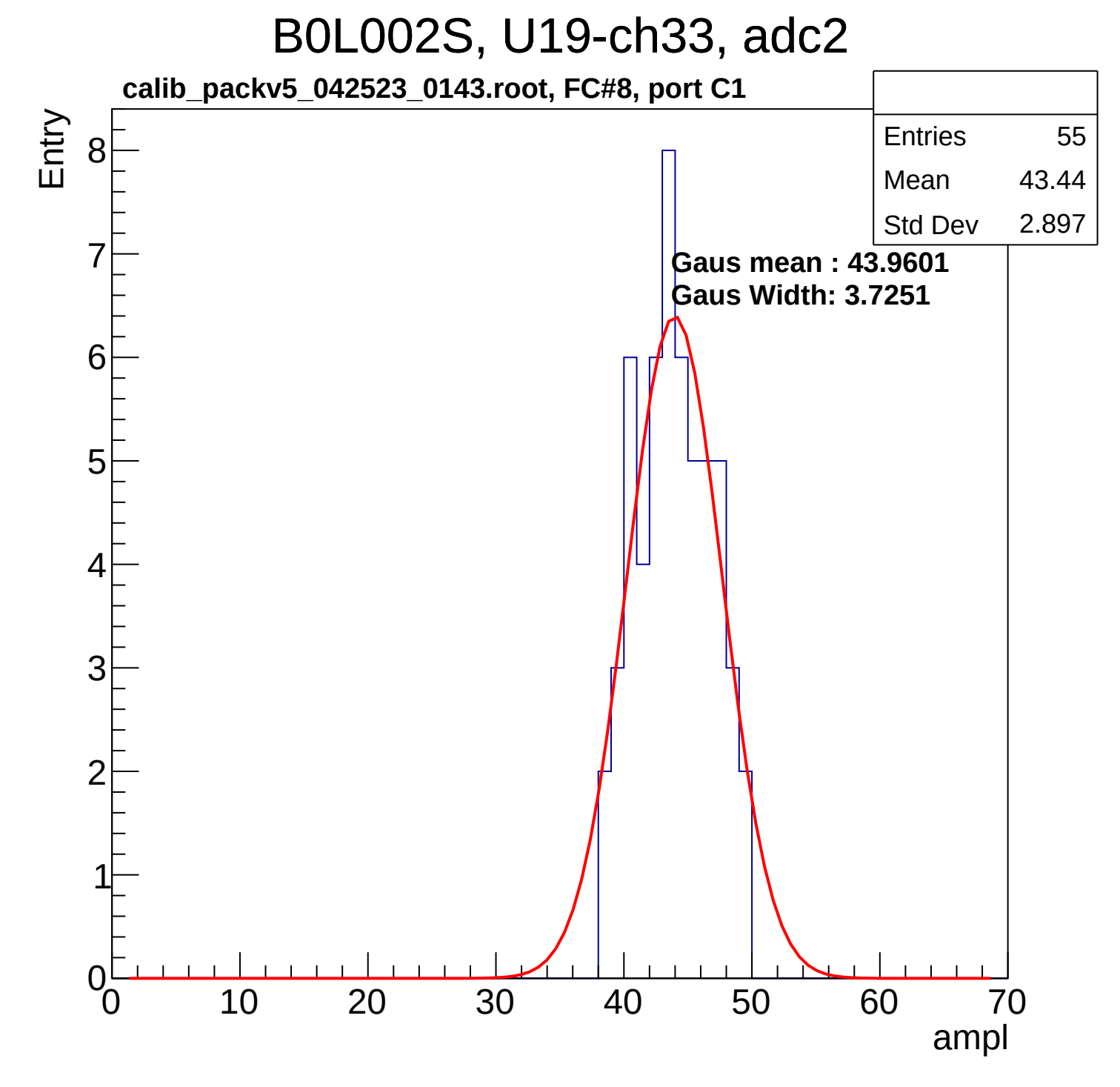

B0L002S, U19-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 60.1
Std Dev 2.288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

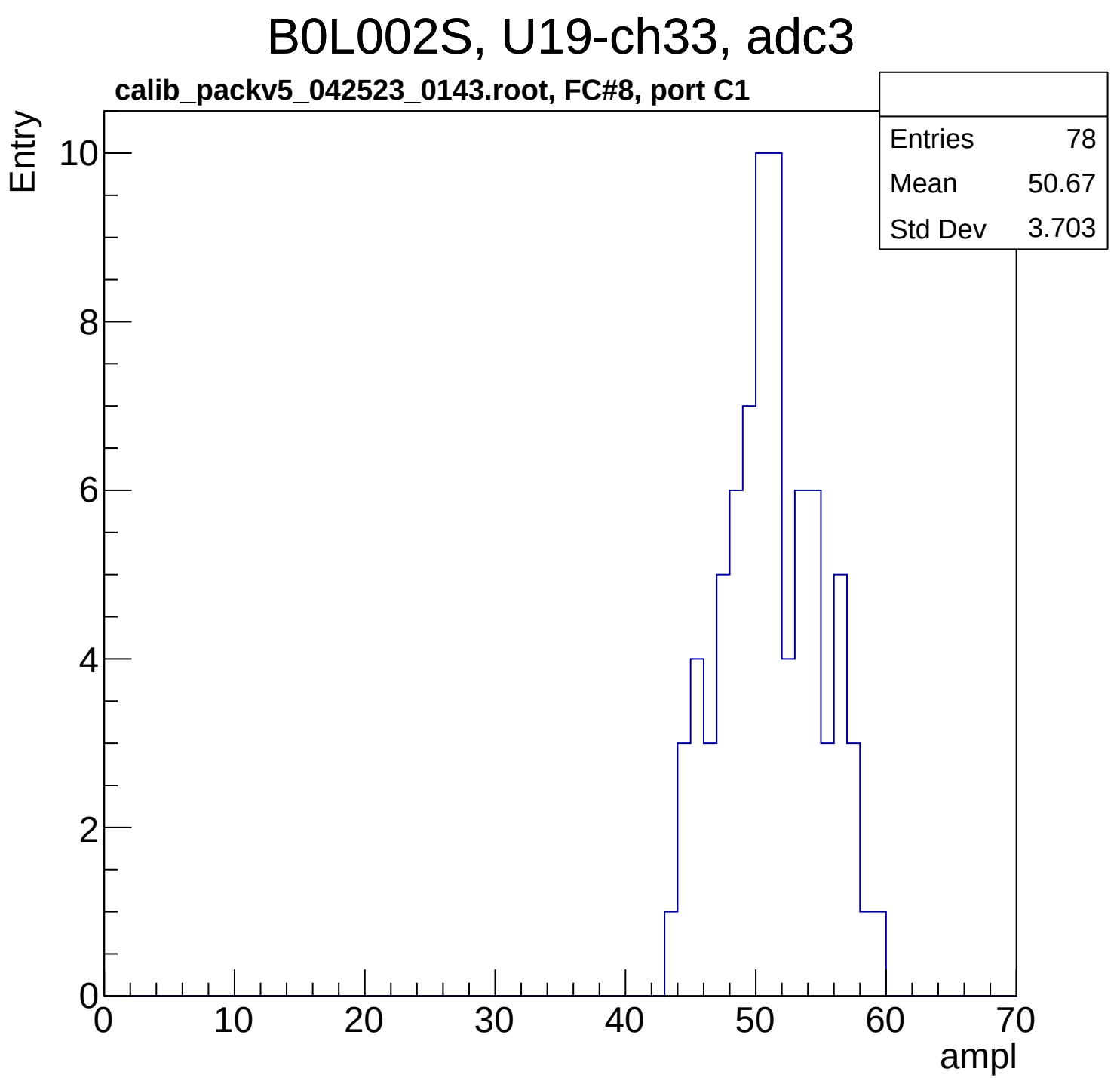

B0L002S, U19-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

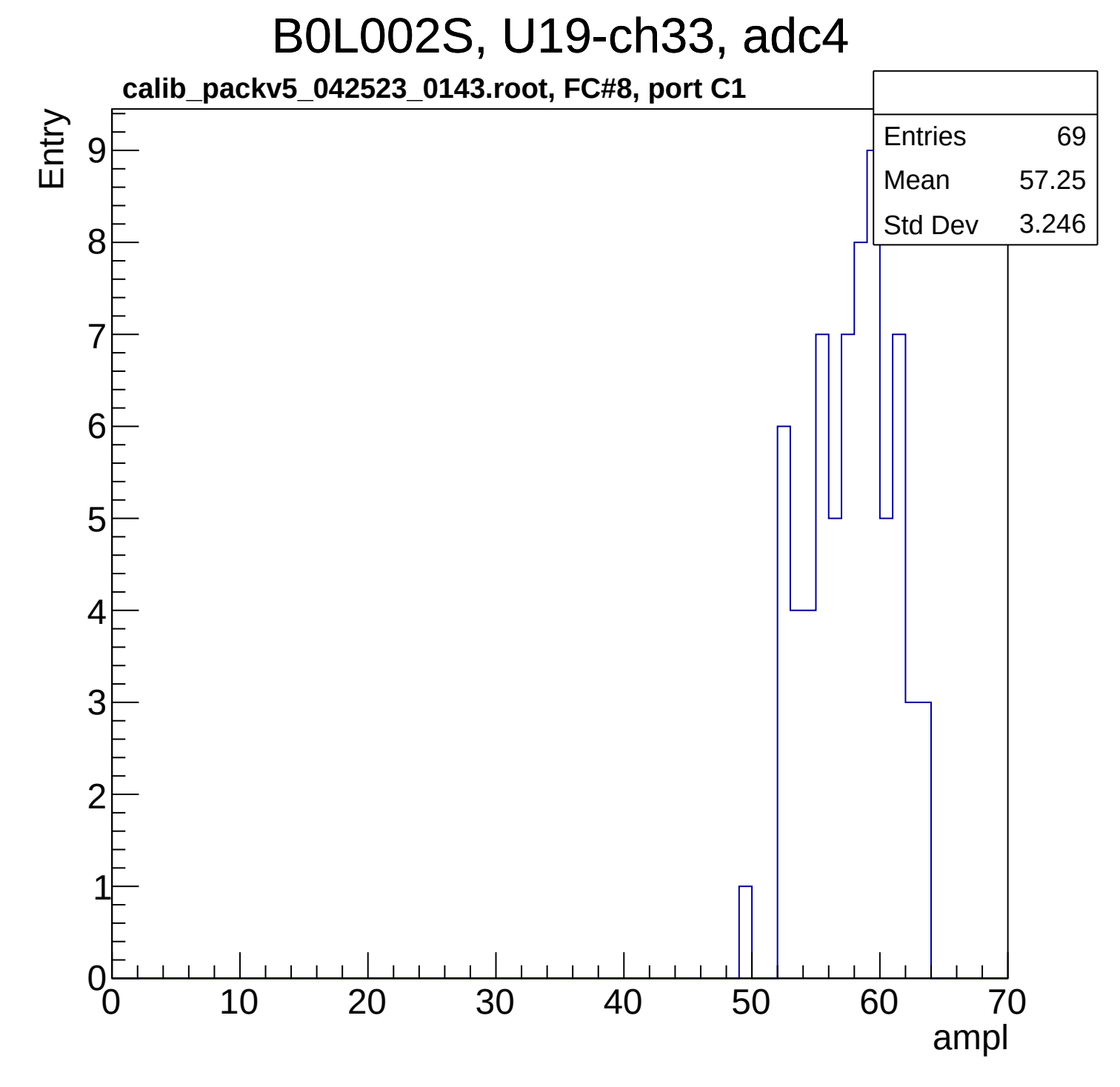

B0L002S, U19-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

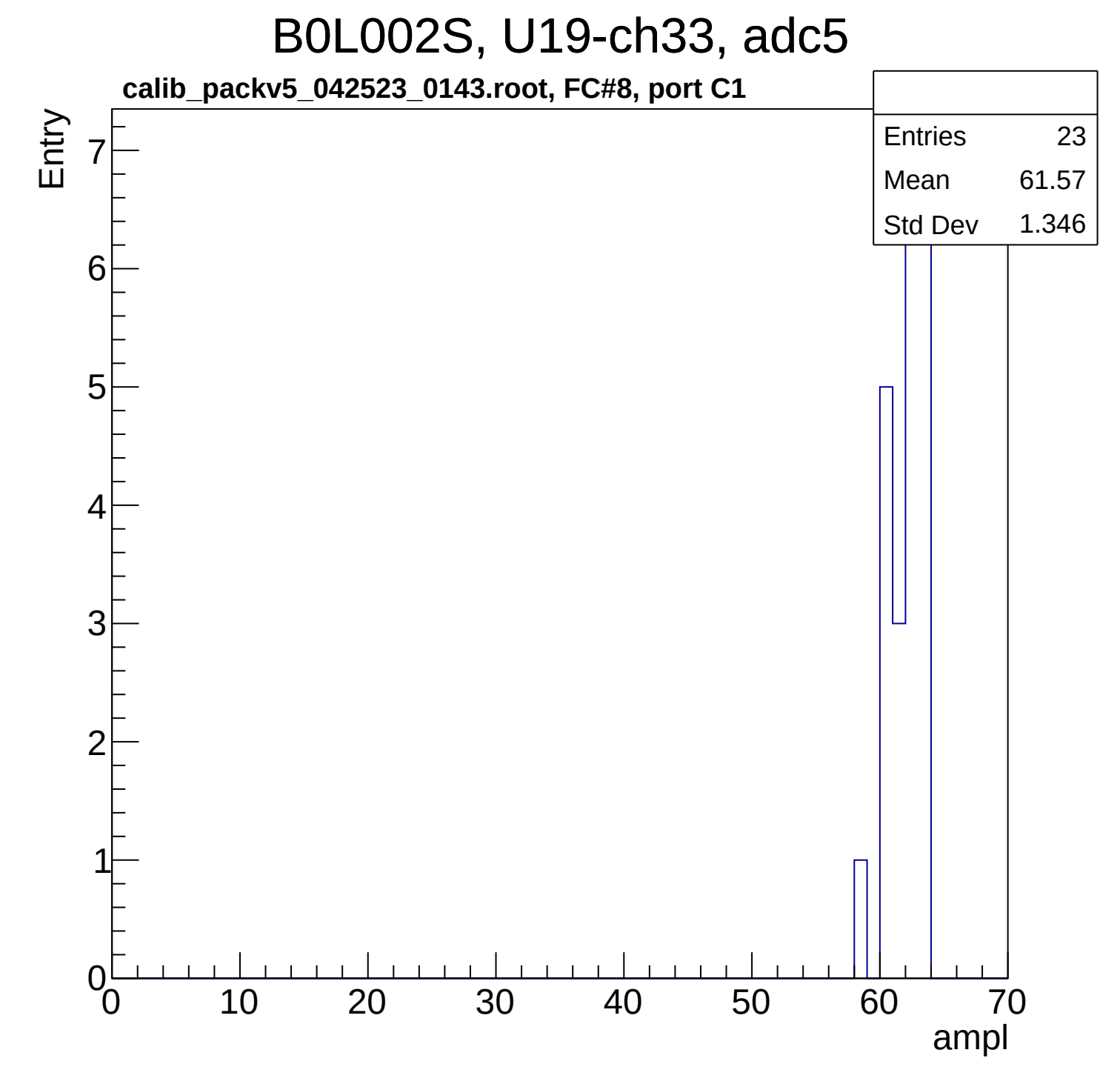

B0L002S, U19-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 30.35
Std Dev 3.54
Gaus mean : 30.9208
Gaus Width: 3.6395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

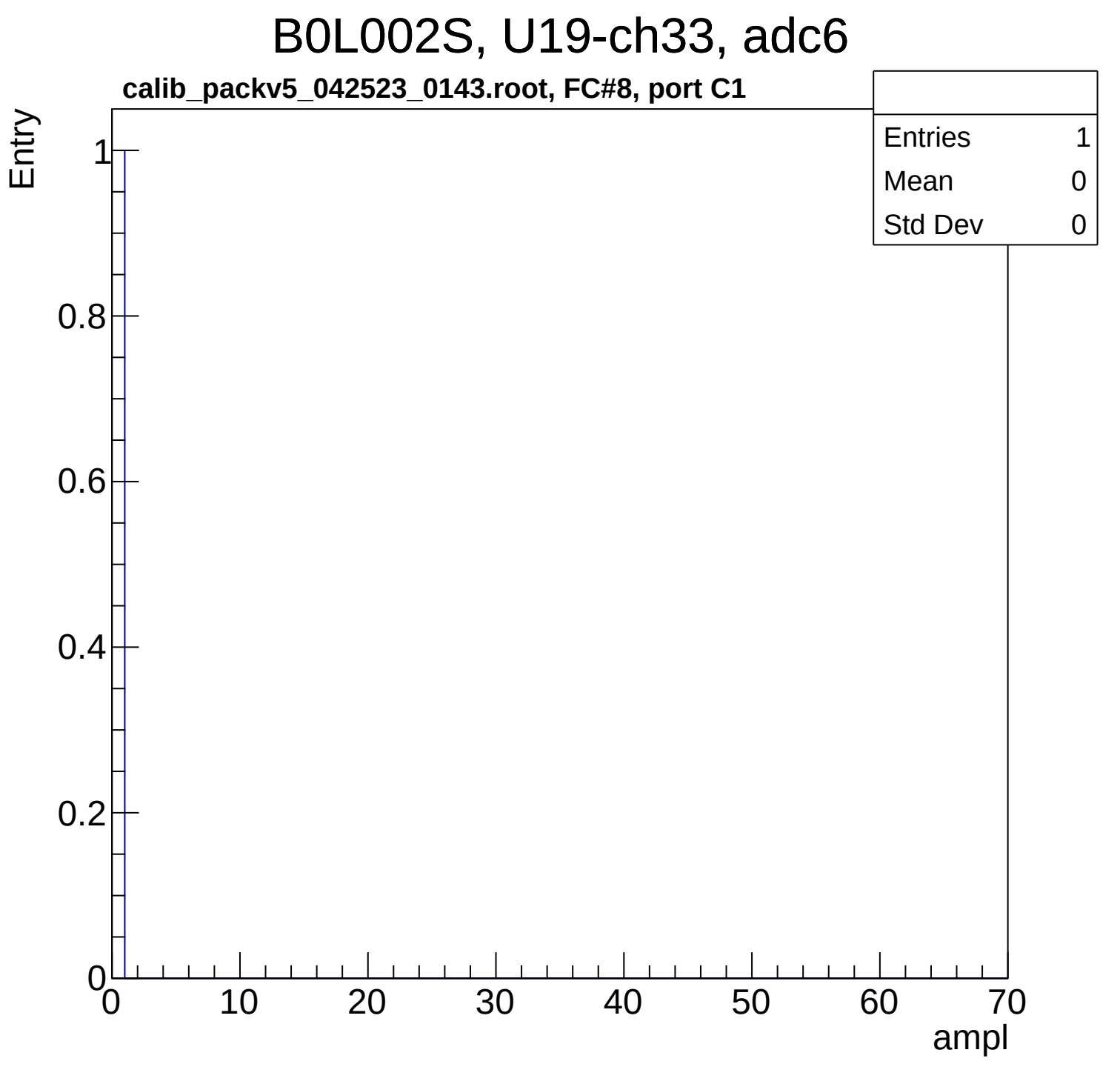

B0L002S, U19-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.84
Std Dev 3.676
Gaus mean : 37.6222
Gaus Width: 3.5196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

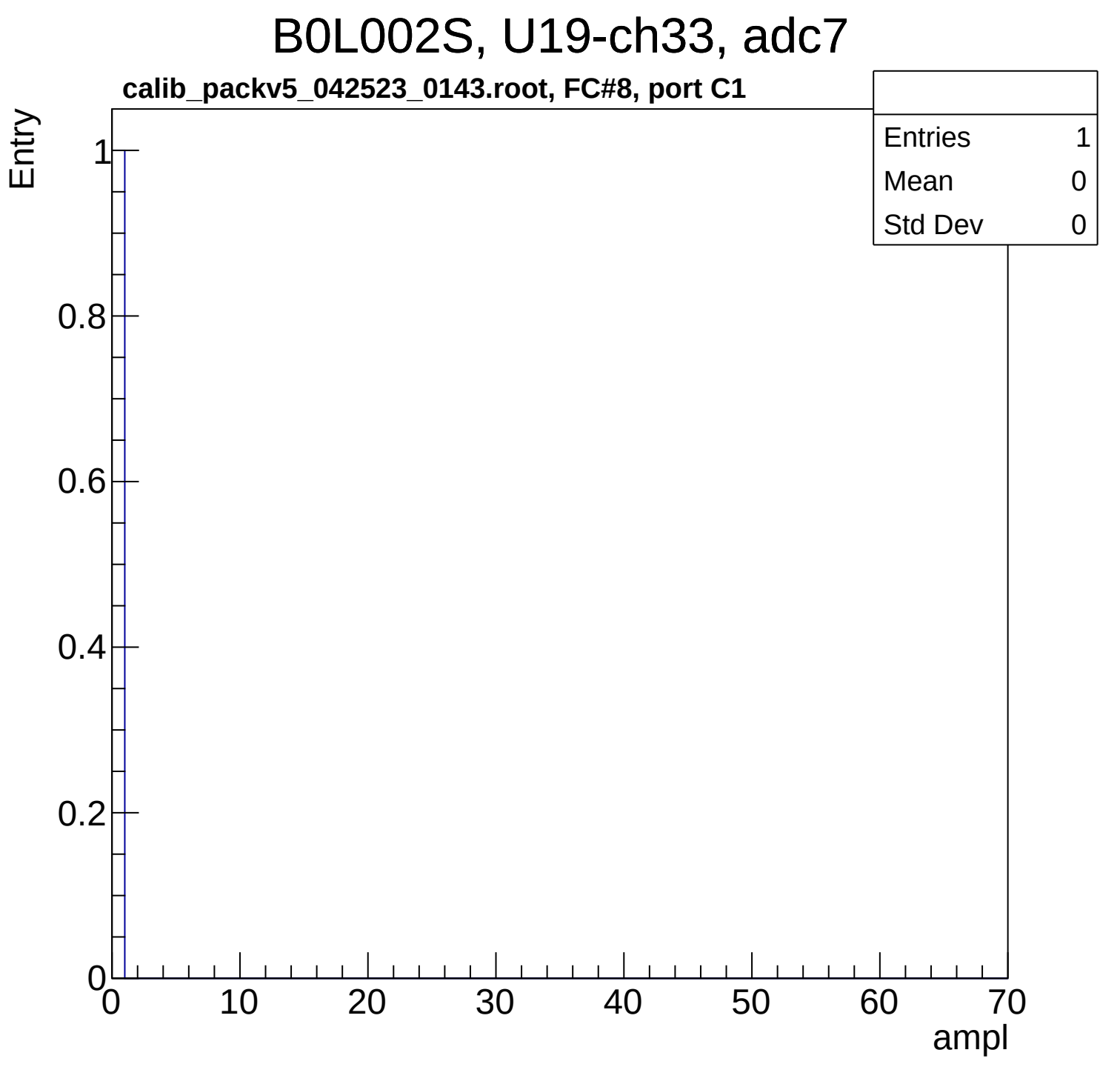

B0L002S, U19-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 43.44
Std Dev 2.897
Gaus mean : 43.9601
Gaus Width: 3.7251
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

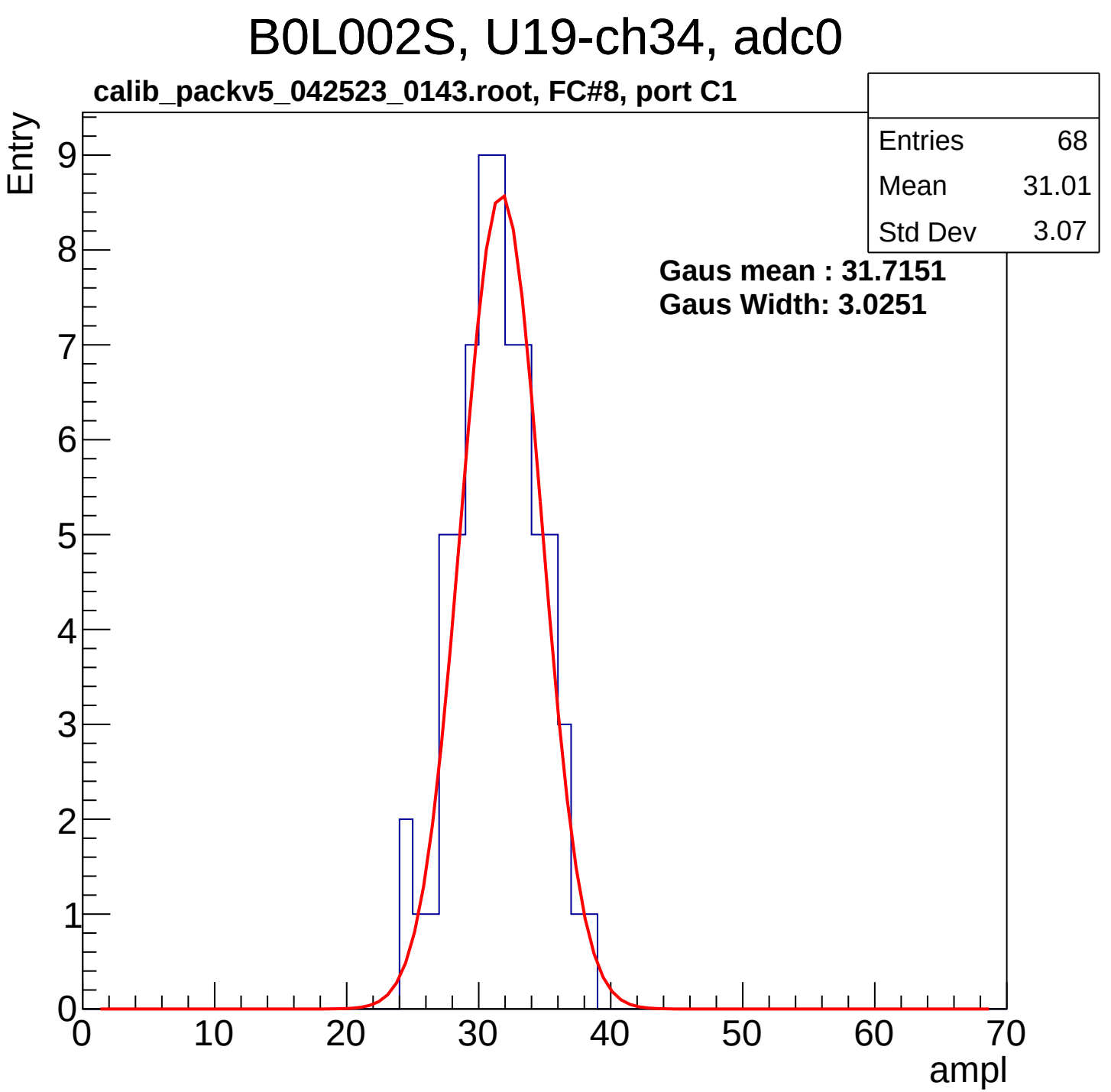

B0L002S, U19-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 50.67
Std Dev 3.703
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

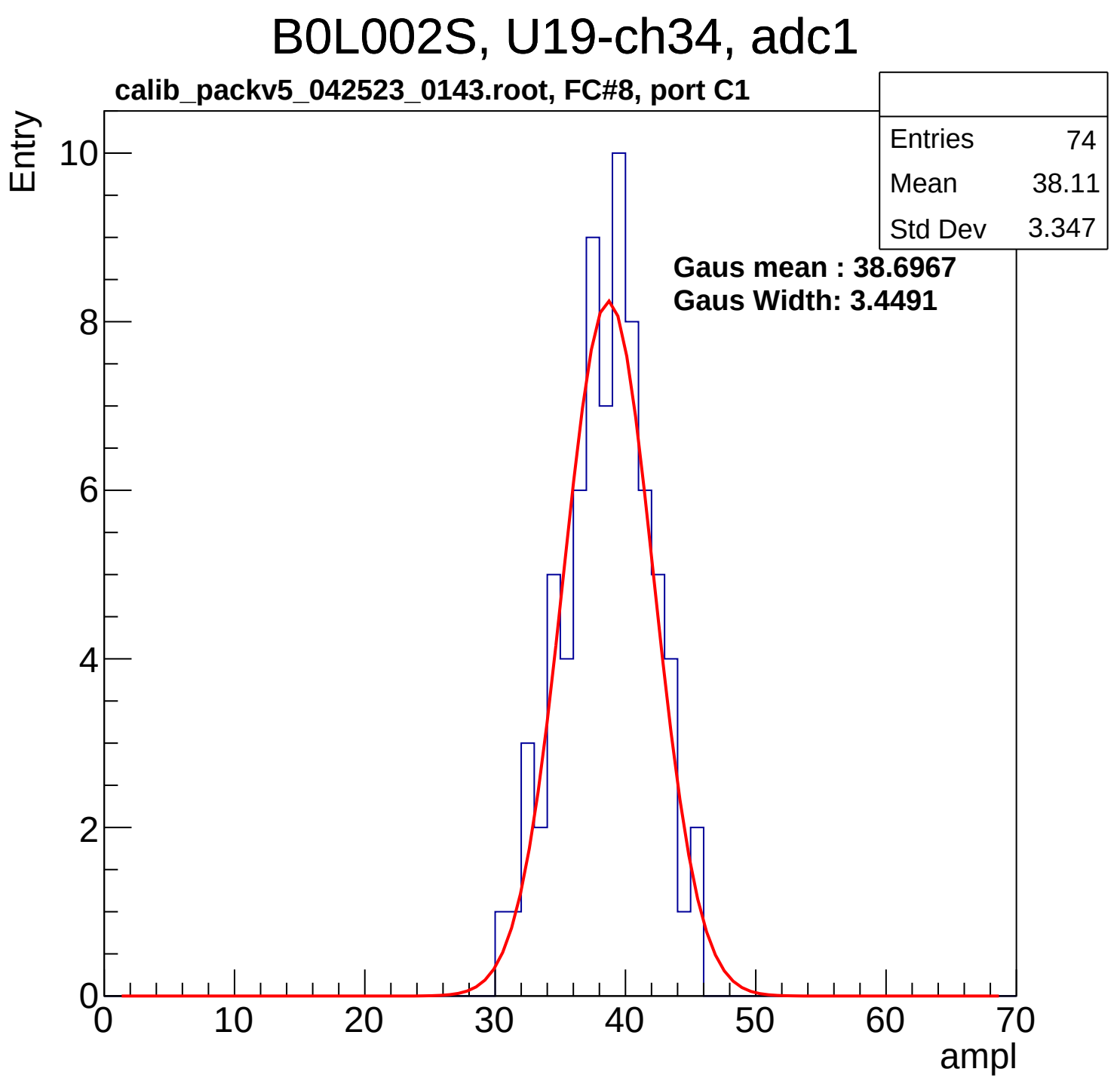

B0L002S, U19-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 57.25
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

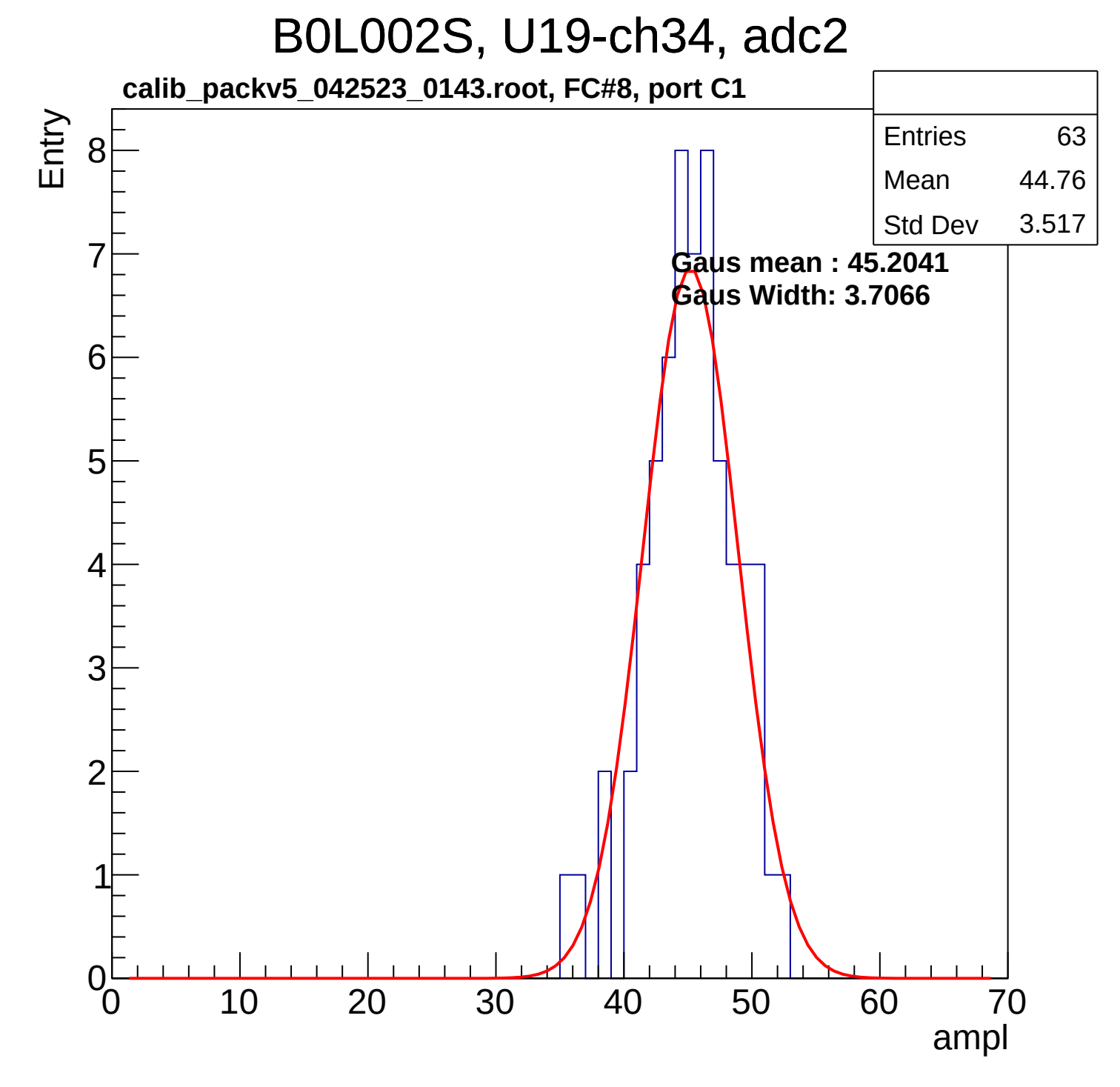

B0L002S, U19-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 61.57
Std Dev 1.346
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

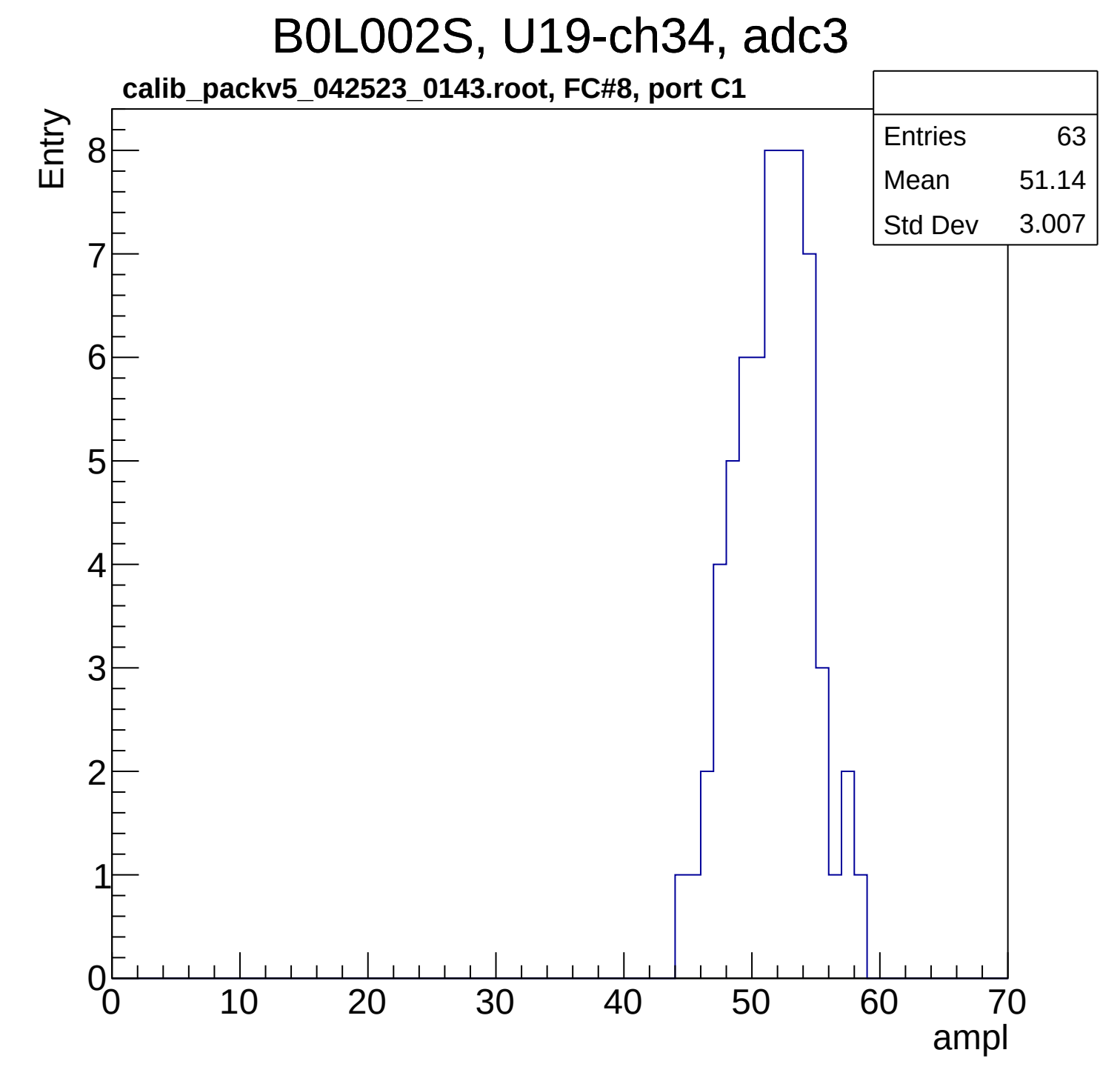

B0L002S, U19-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

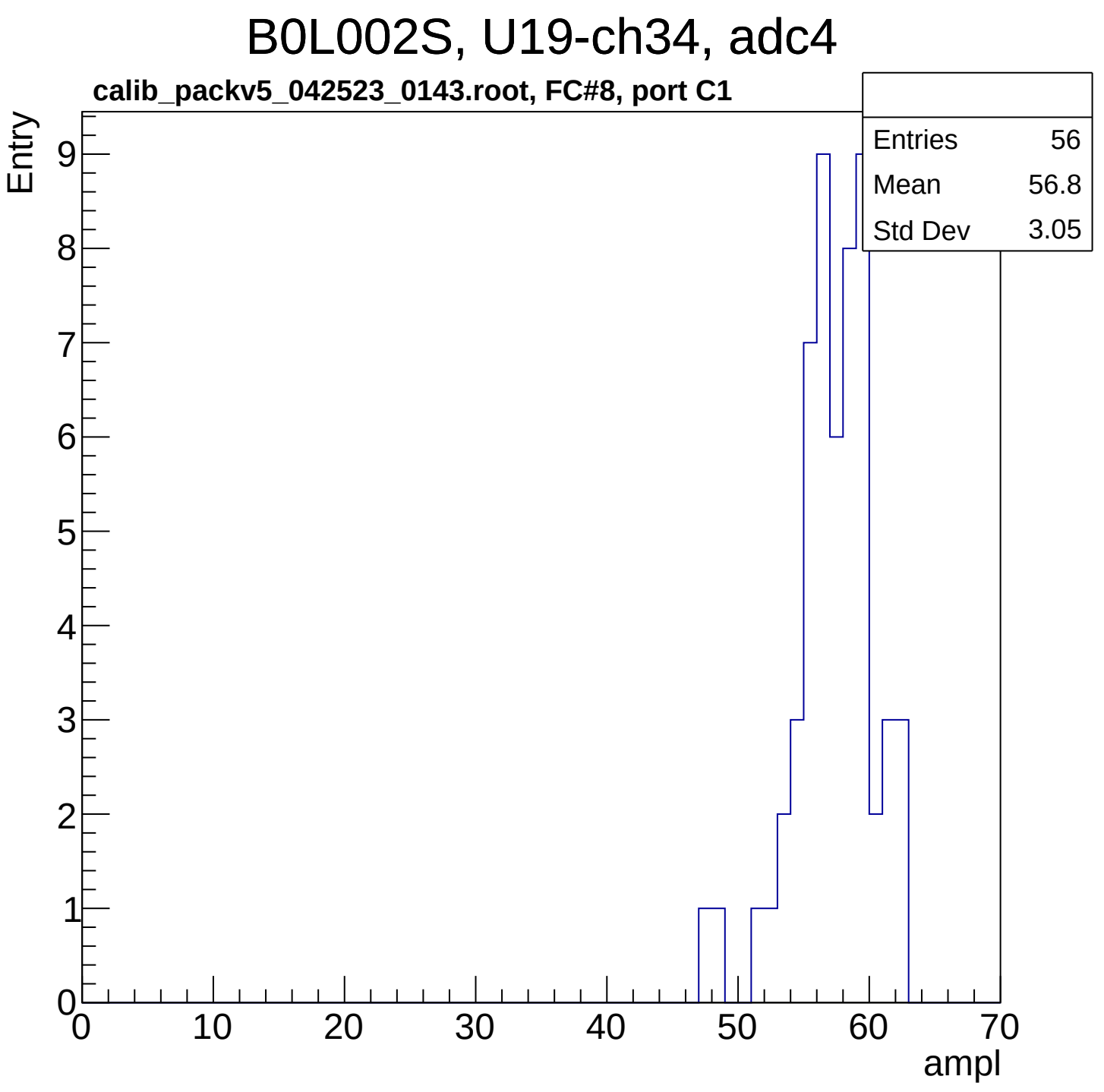

B0L002S, U19-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

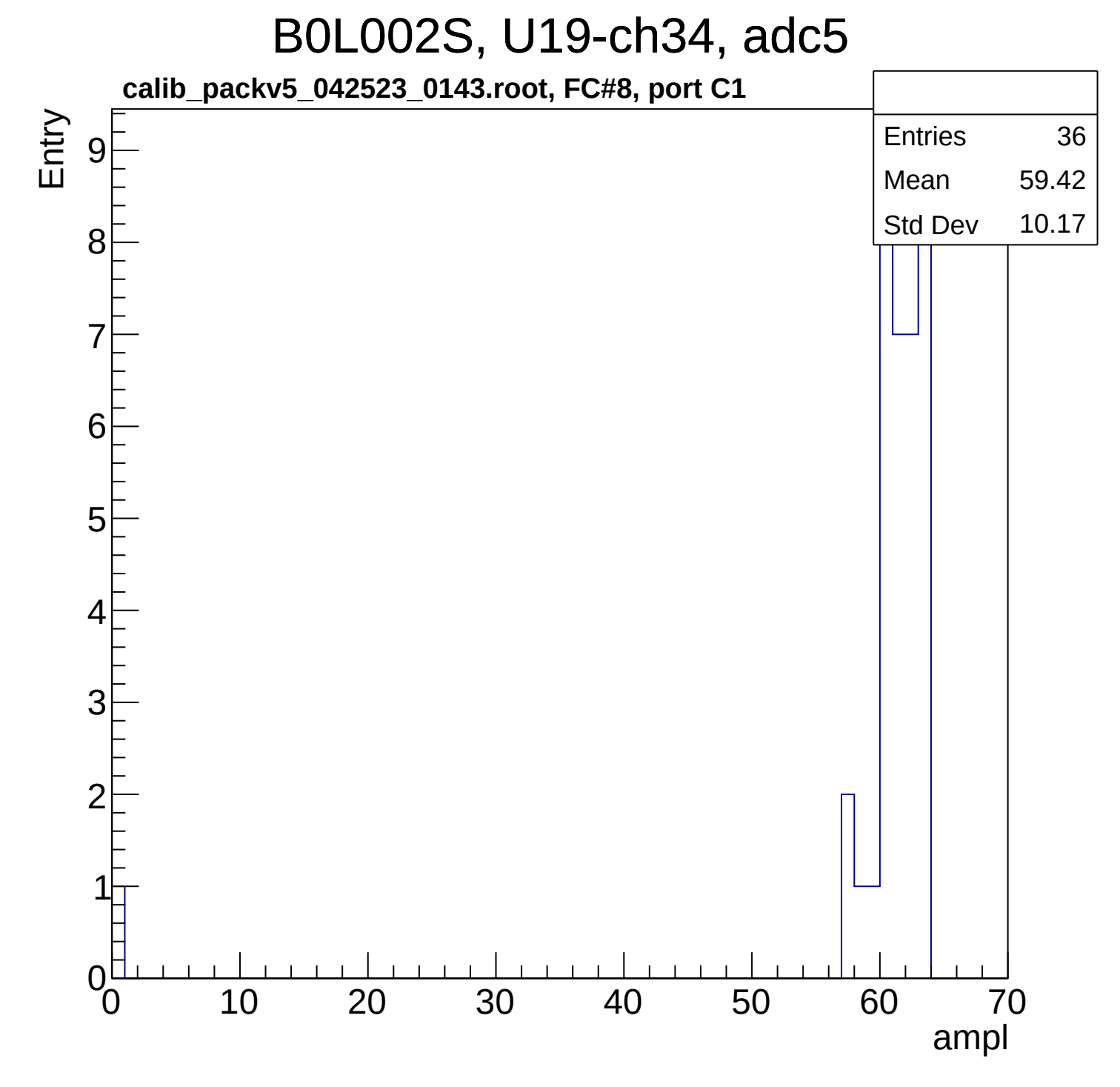

B0L002S, U19-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.01
Std Dev 3.07
Gaus mean : 31.7151
Gaus Width: 3.0251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

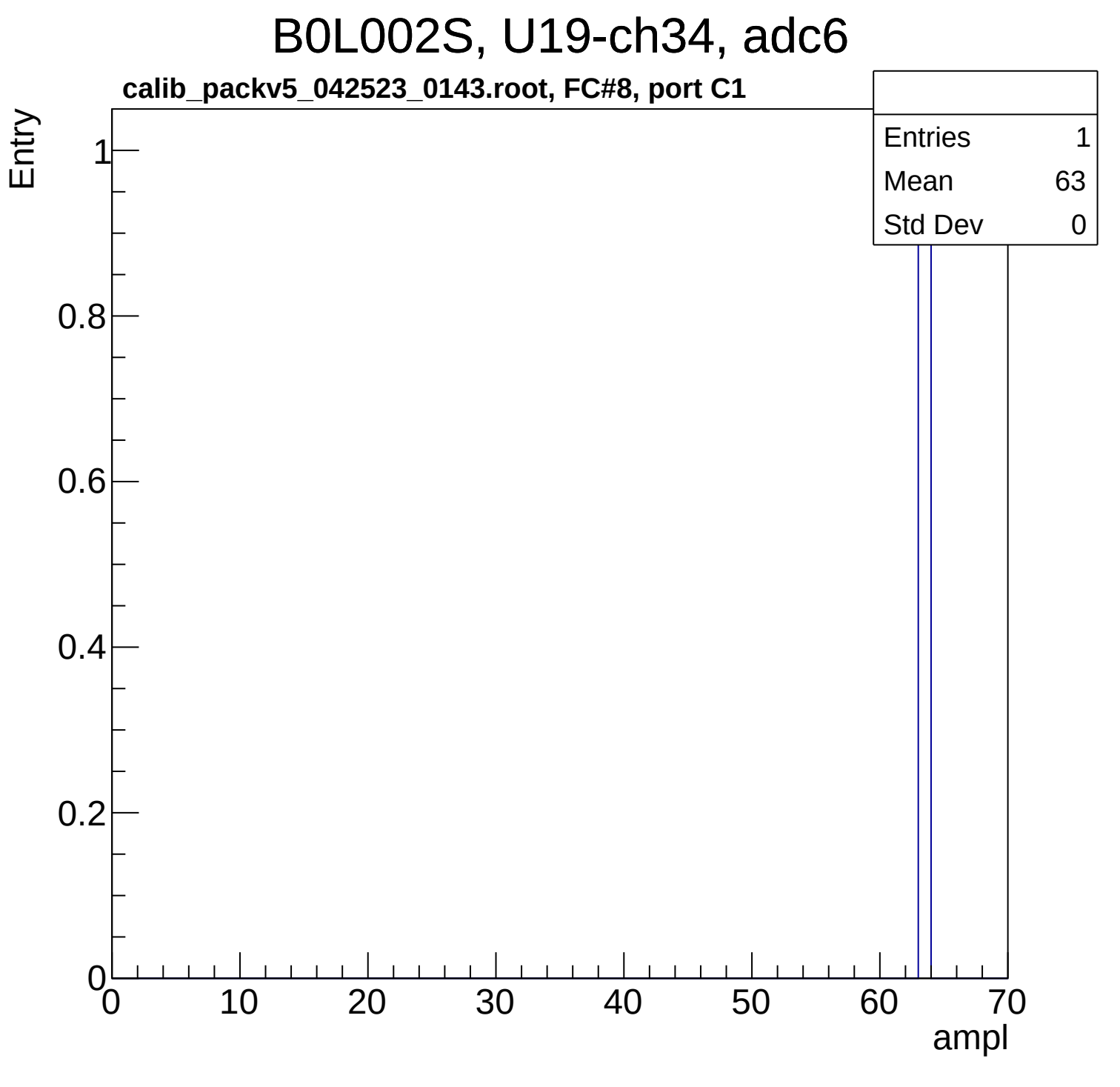

B0L002S, U19-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.11
Std Dev 3.347
Gaus mean : 38.6967
Gaus Width: 3.4491
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

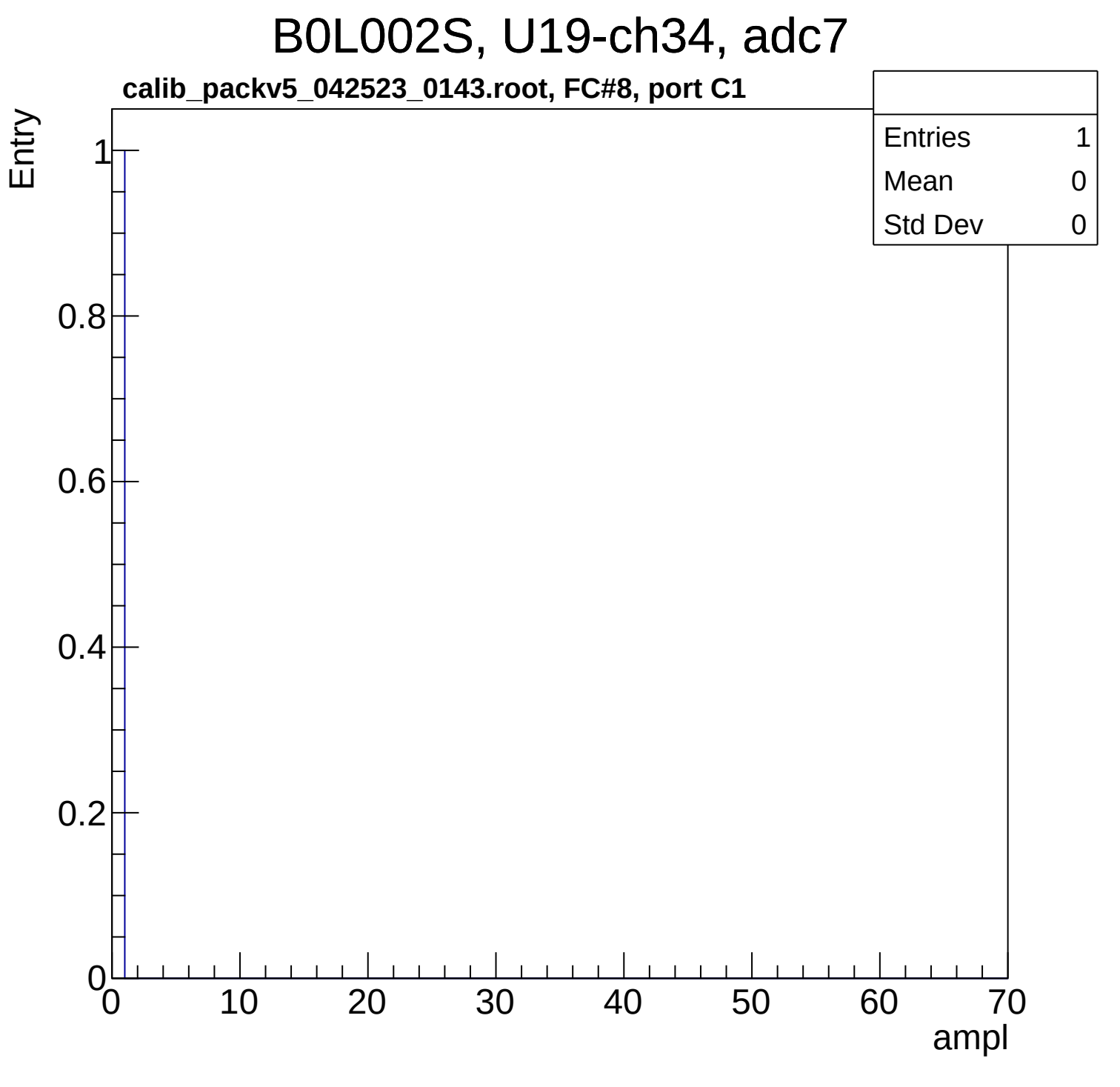

B0L002S, U19-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.76
Std Dev 3.517
Gaus mean : 45.2041
Gaus Width: 3.7066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

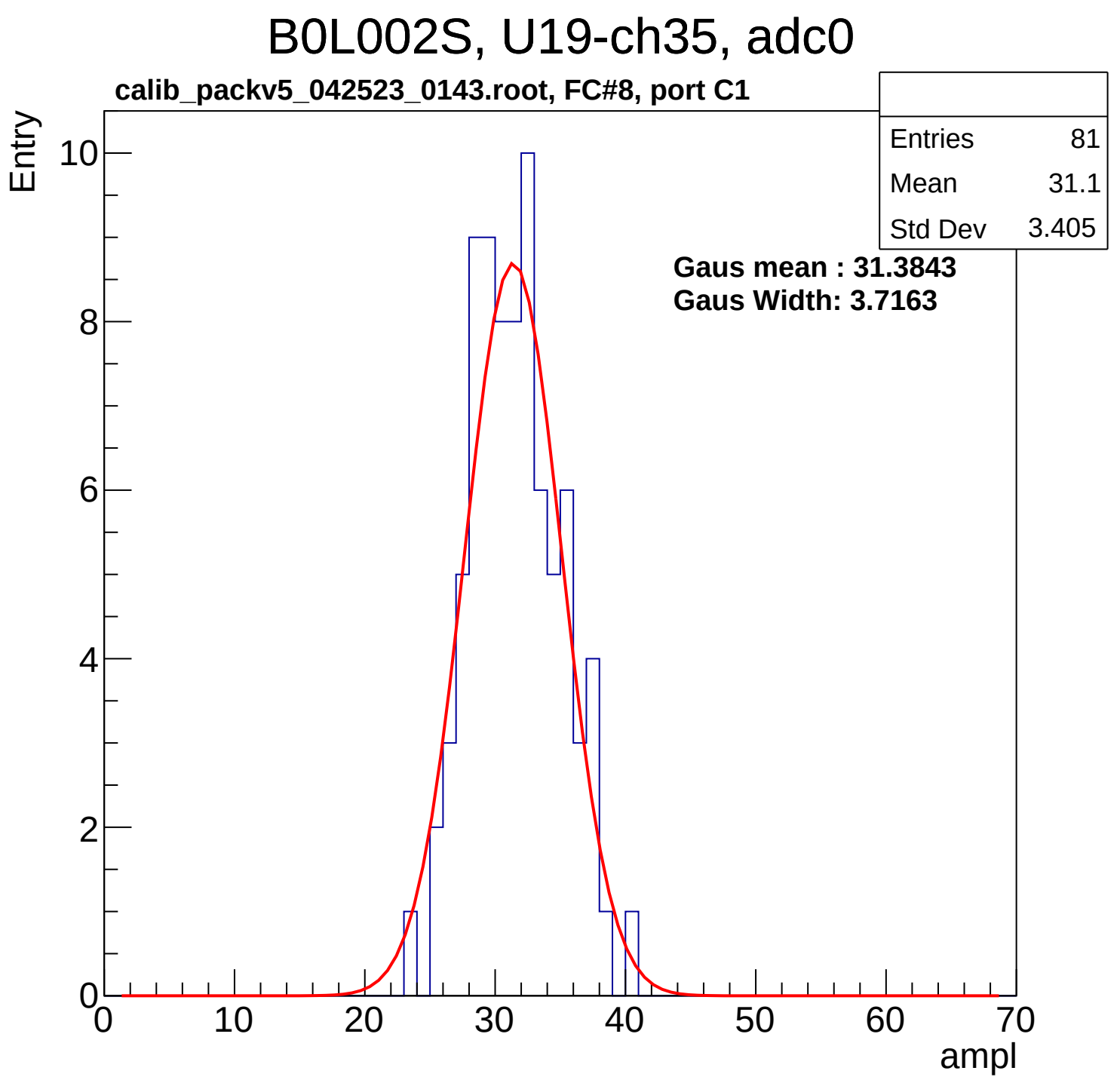

B0L002S, U19-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.14
Std Dev 3.007
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

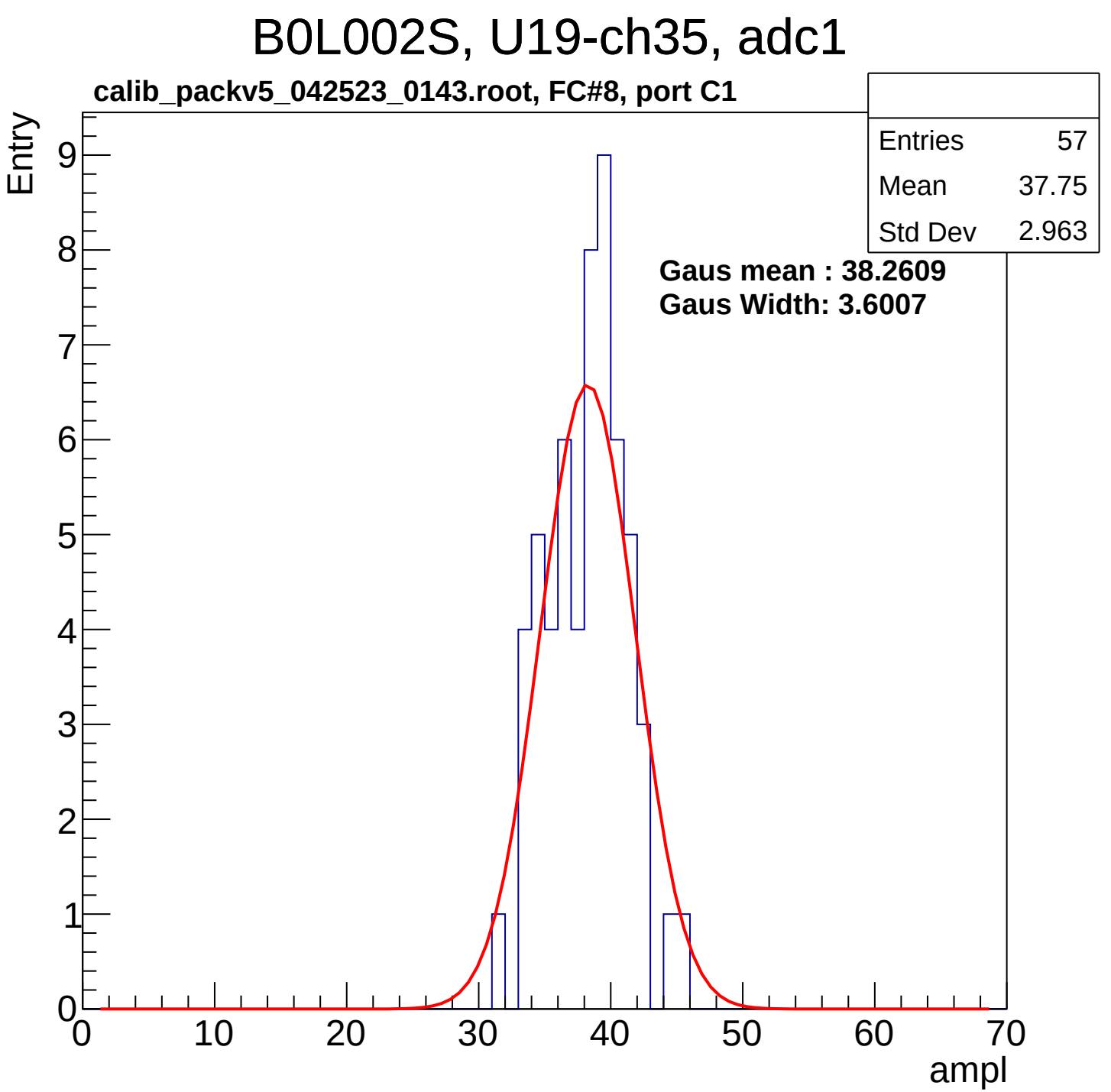

B0L002S, U19-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.8
Std Dev 3.05
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

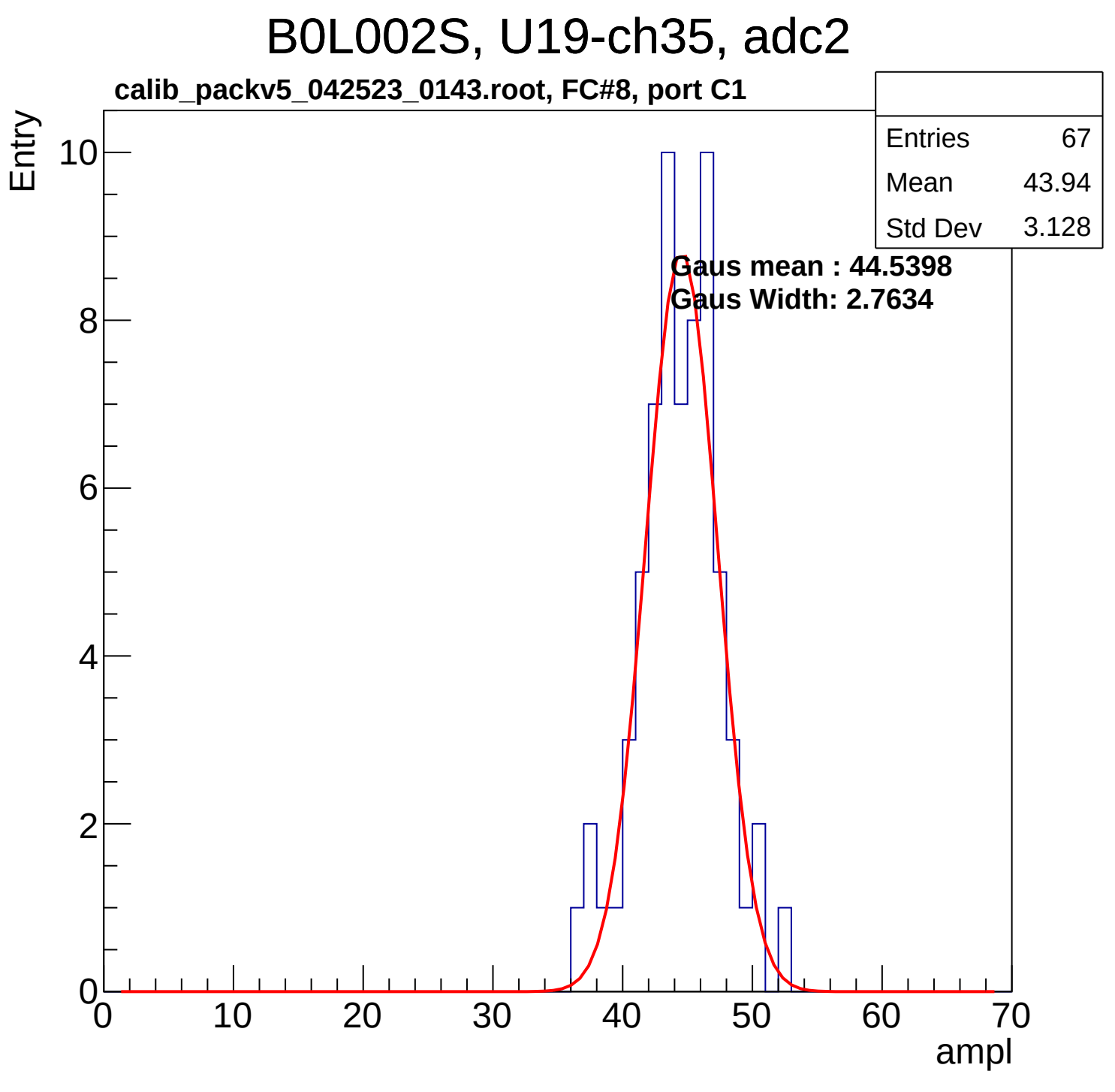

B0L002S, U19-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.42
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

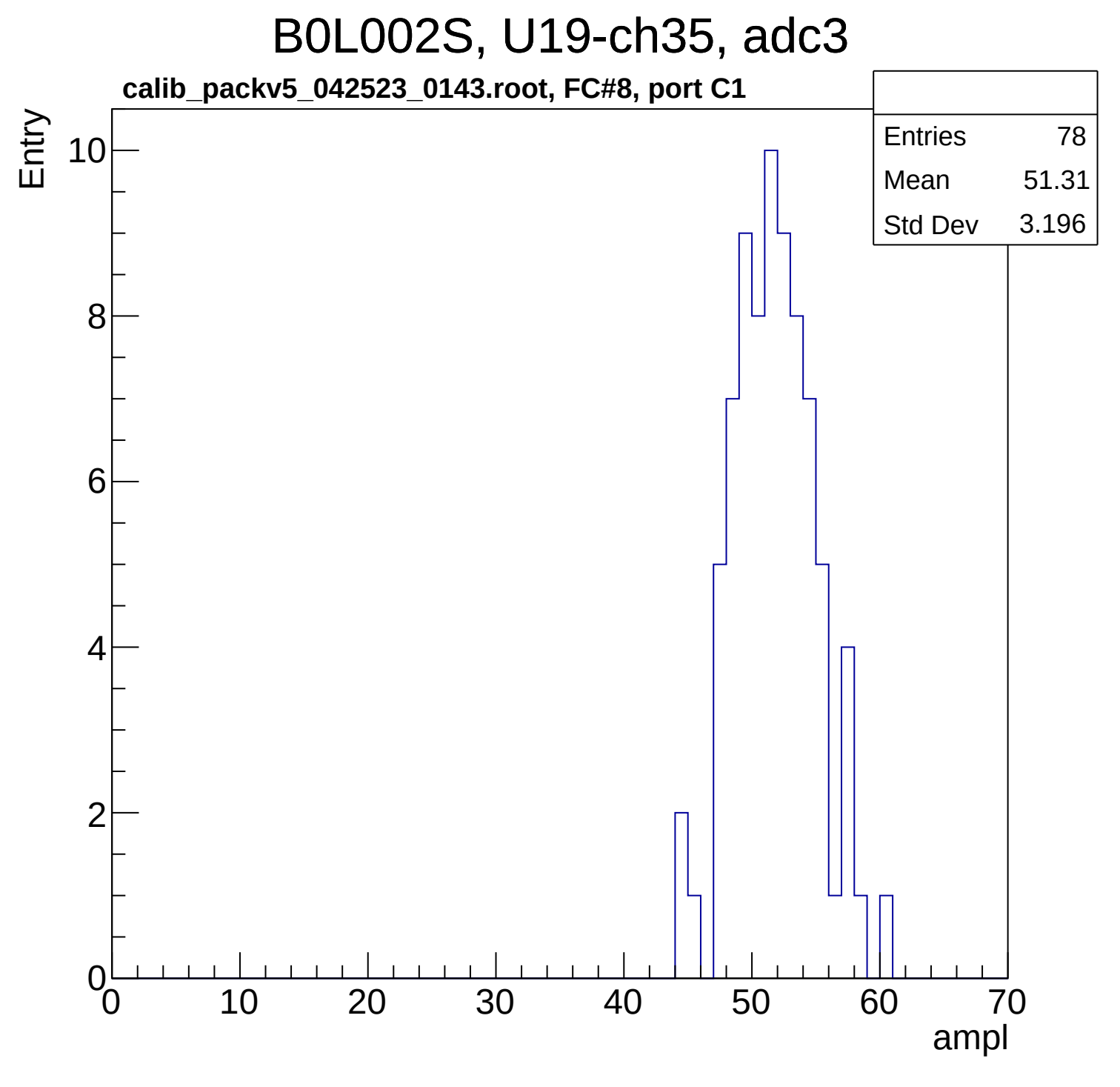

B0L002S, U19-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

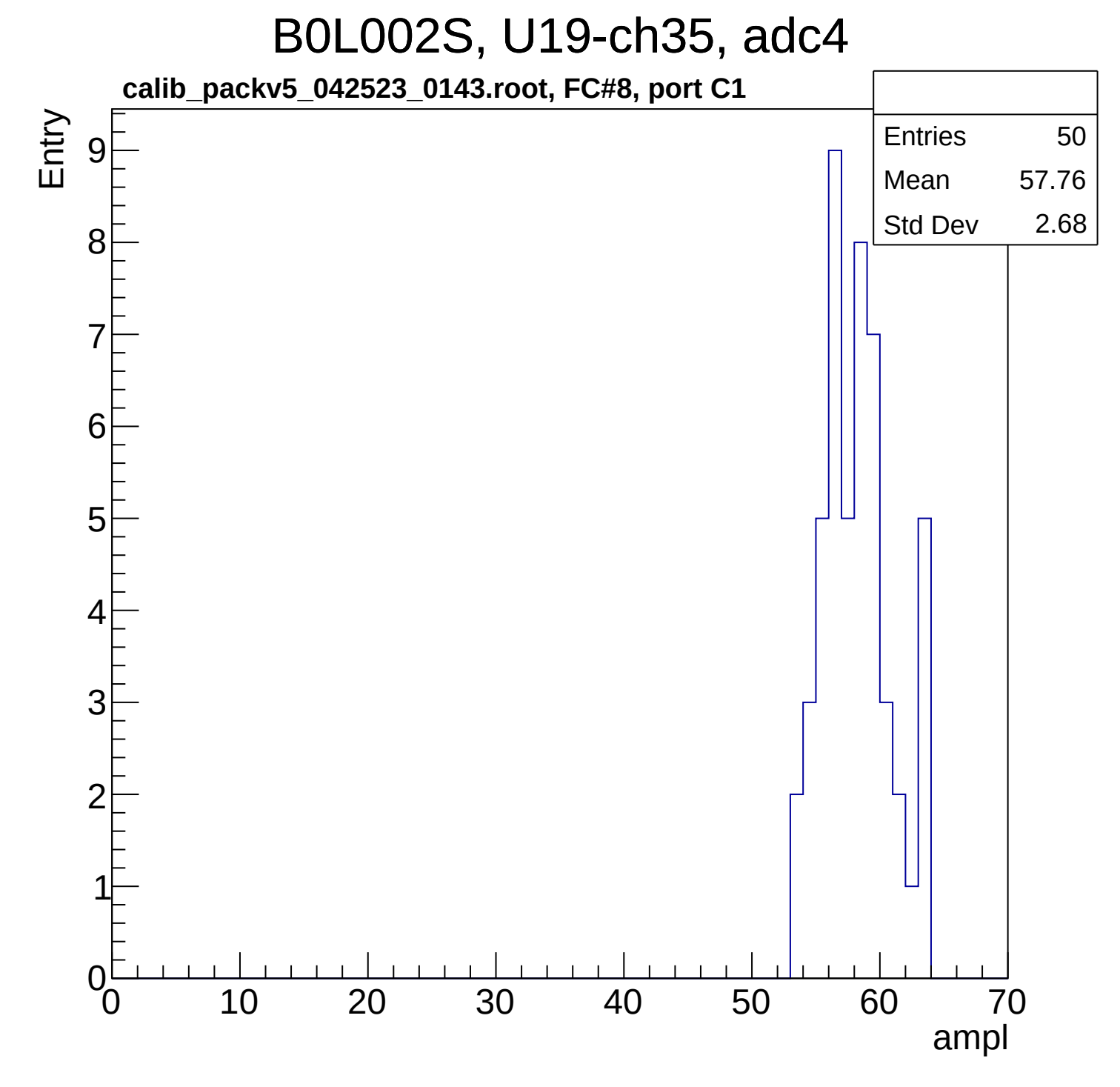

B0L002S, U19-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

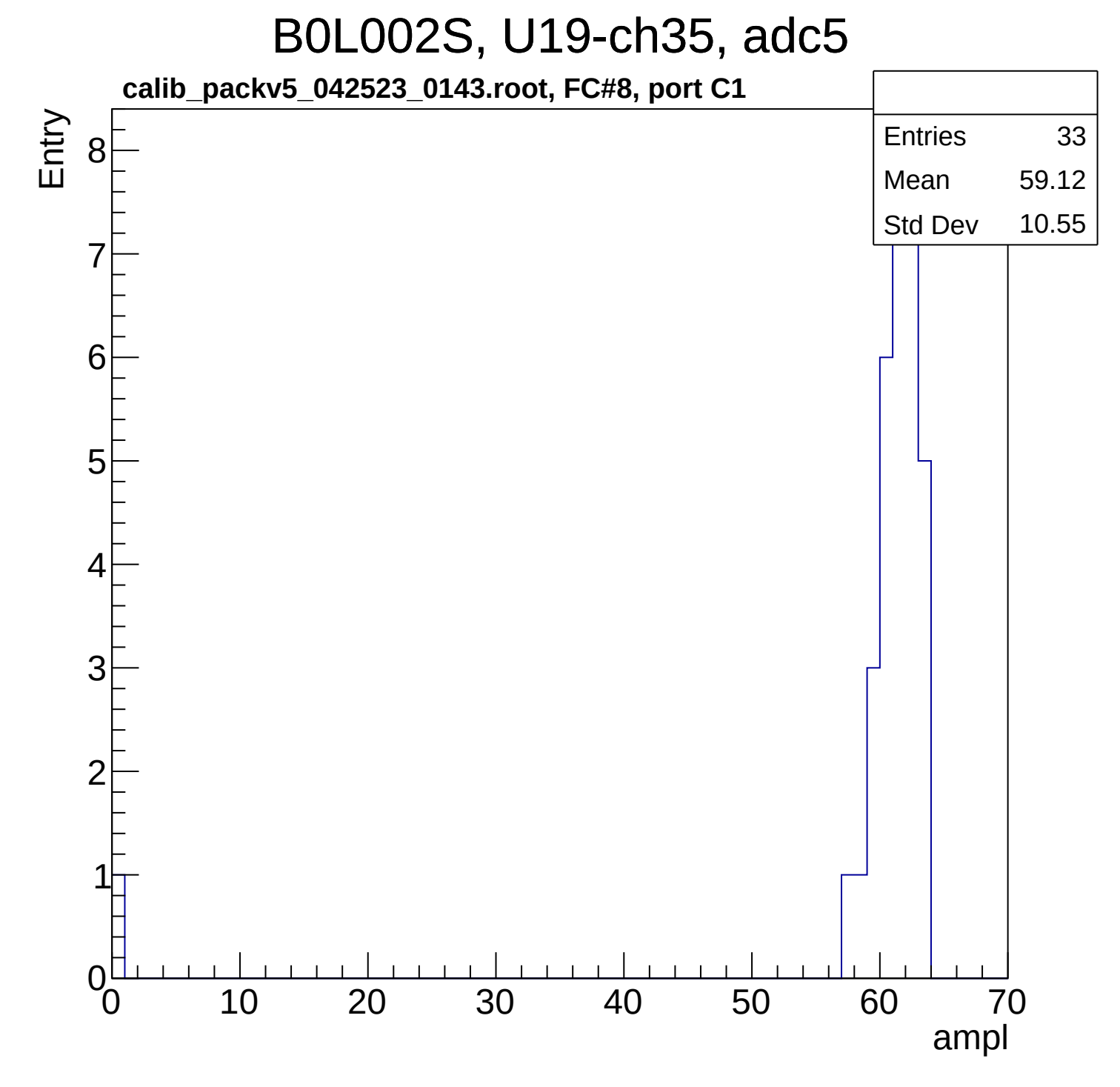

B0L002S, U19-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 31.1
Std Dev 3.405
Gaus mean : 31.3843
Gaus Width: 3.7163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

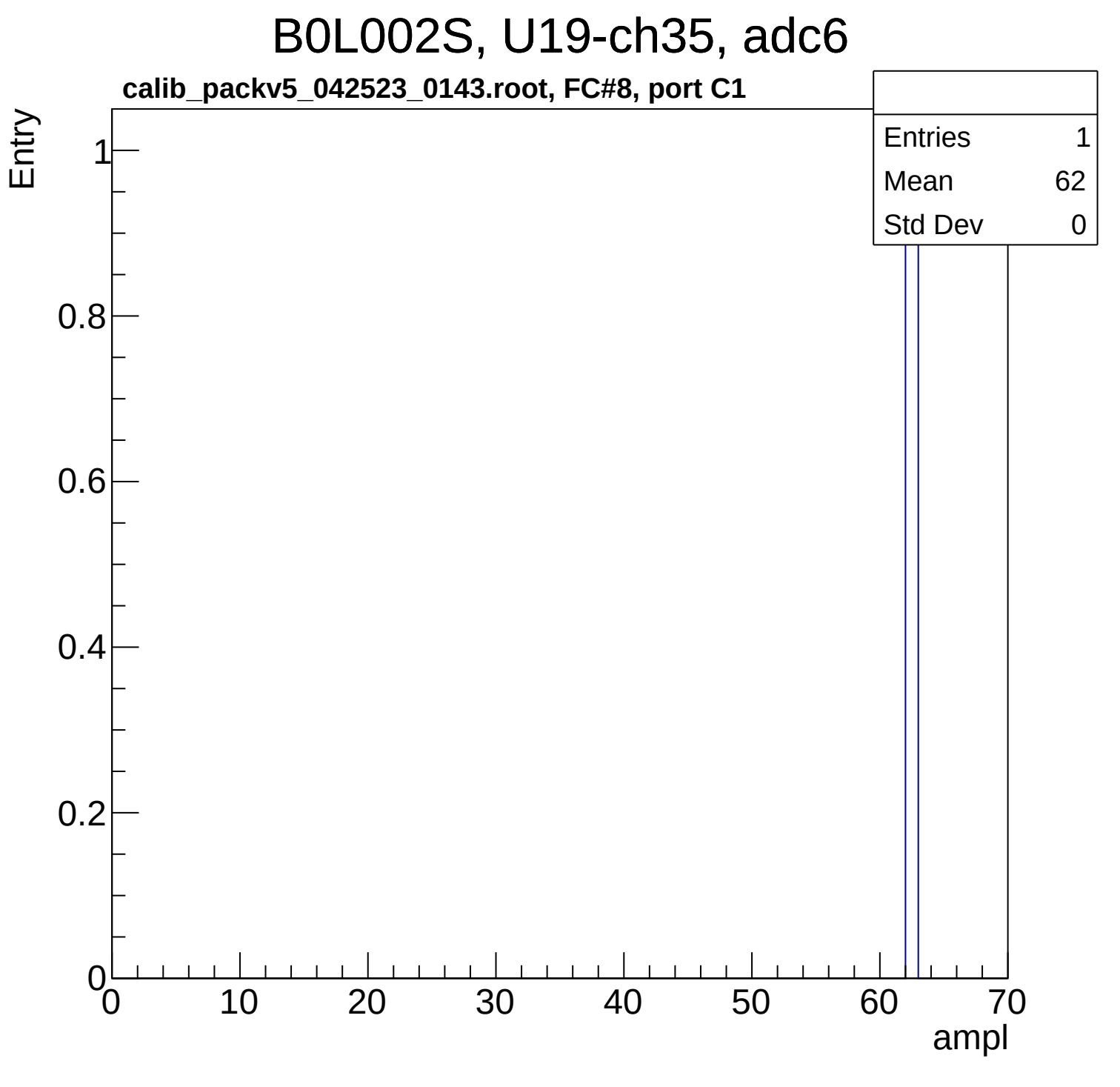

B0L002S, U19-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.75
Std Dev 2.963
Gaus mean : 38.2609
Gaus Width: 3.6007
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

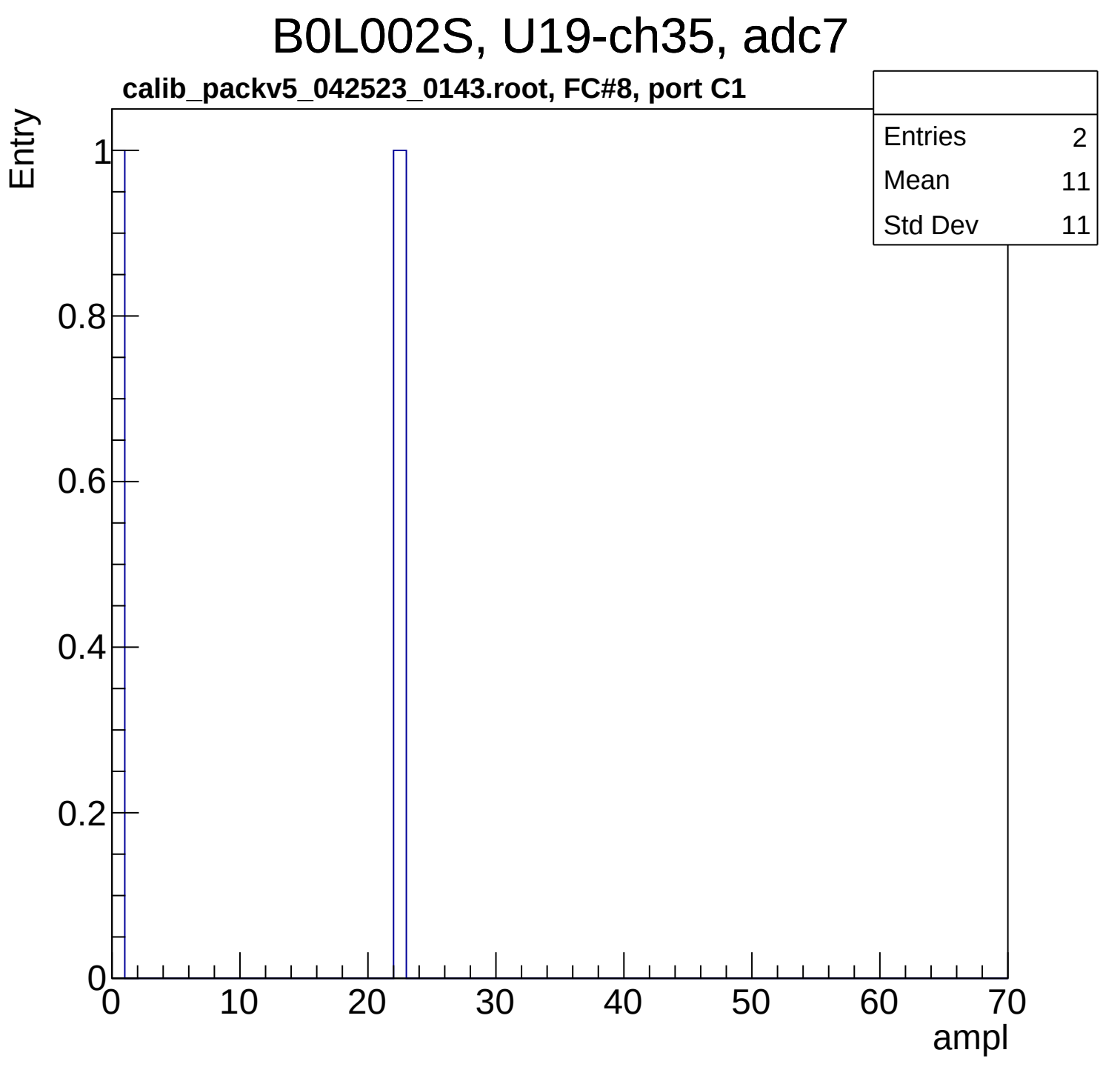

B0L002S, U19-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.94
Std Dev 3.128
Gaus mean : 44.5398
Gaus Width: 2.7634
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

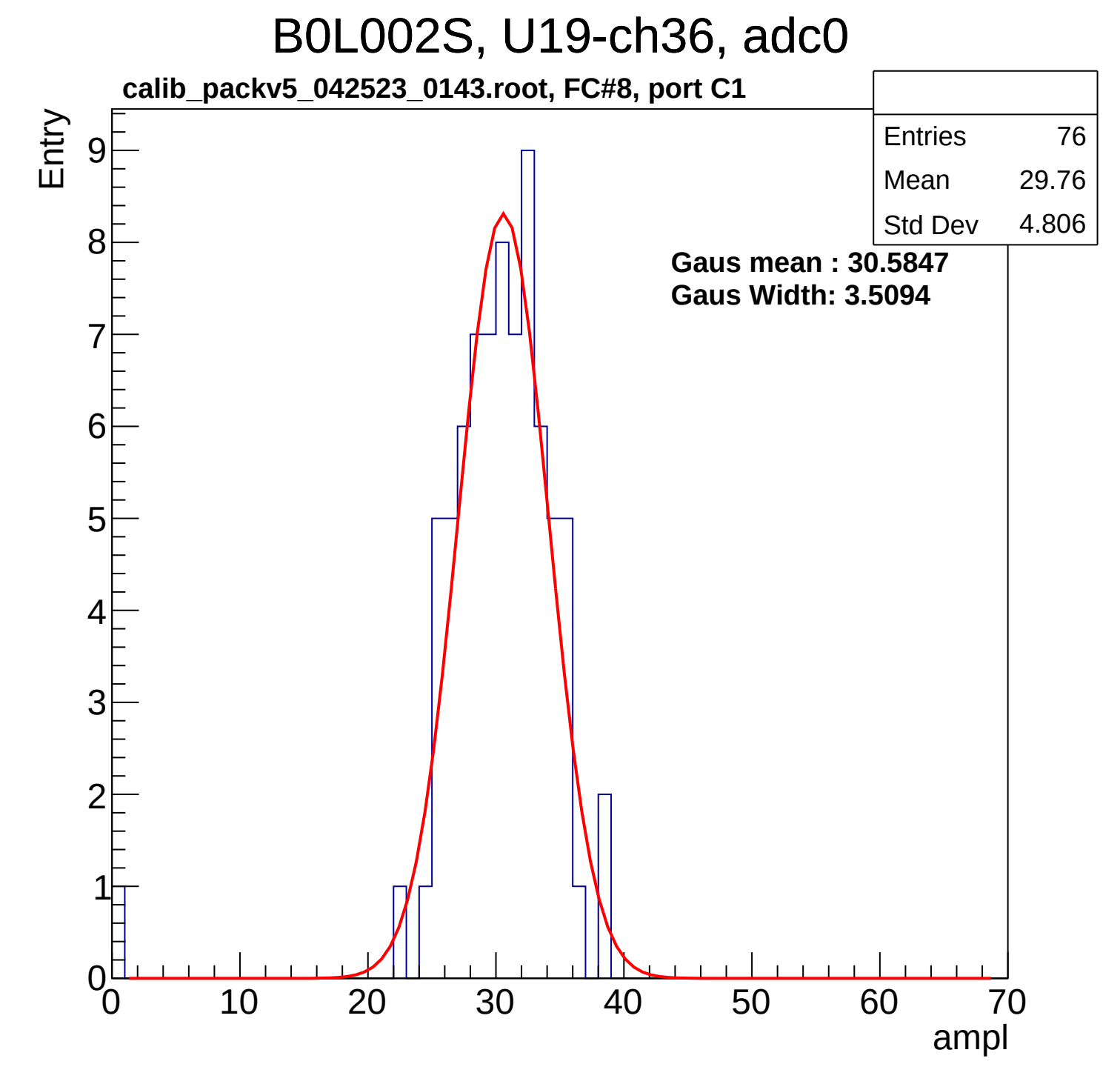

B0L002S, U19-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 51.31
Std Dev 3.196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

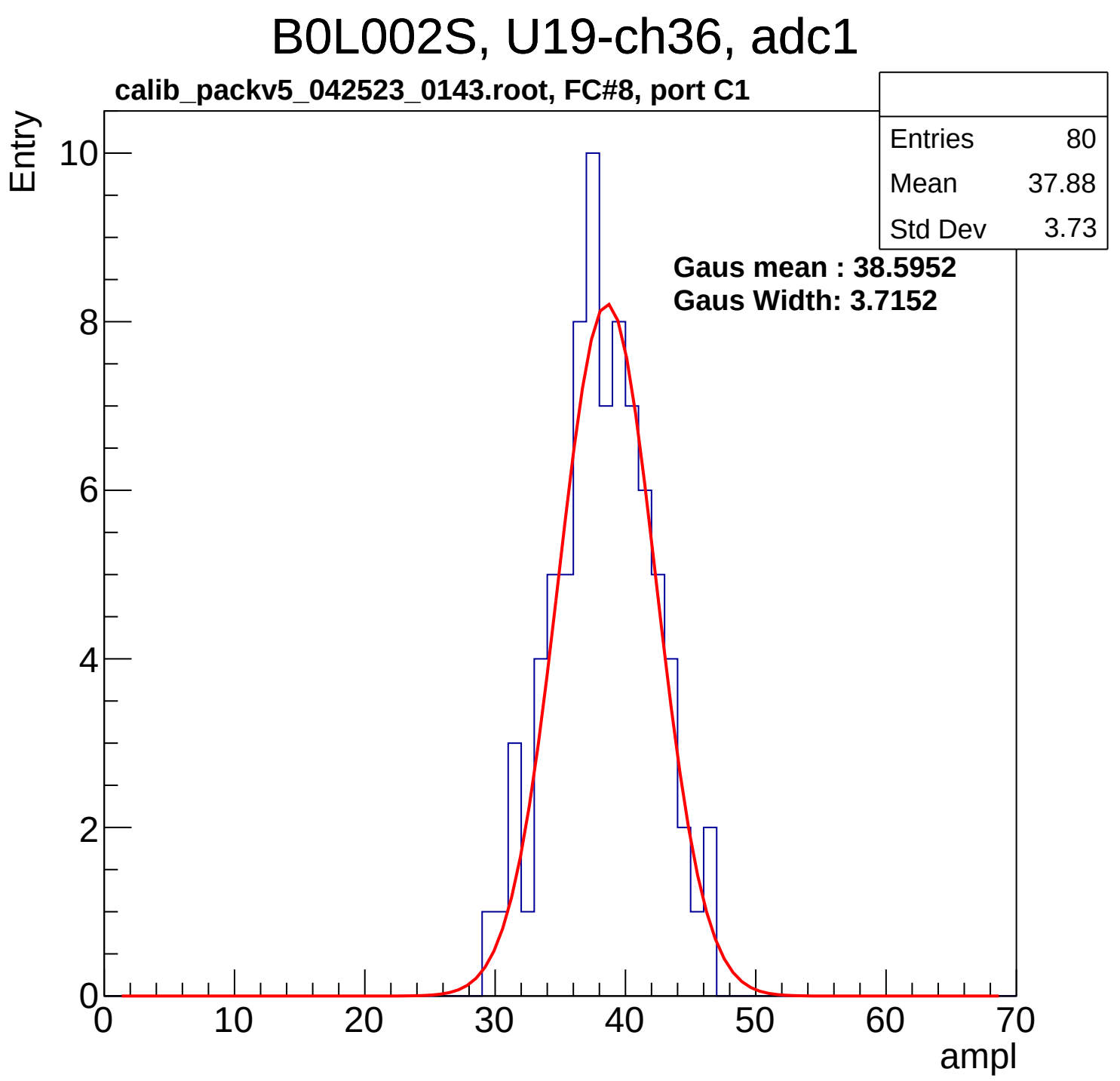

B0L002S, U19-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.76
Std Dev 2.68
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

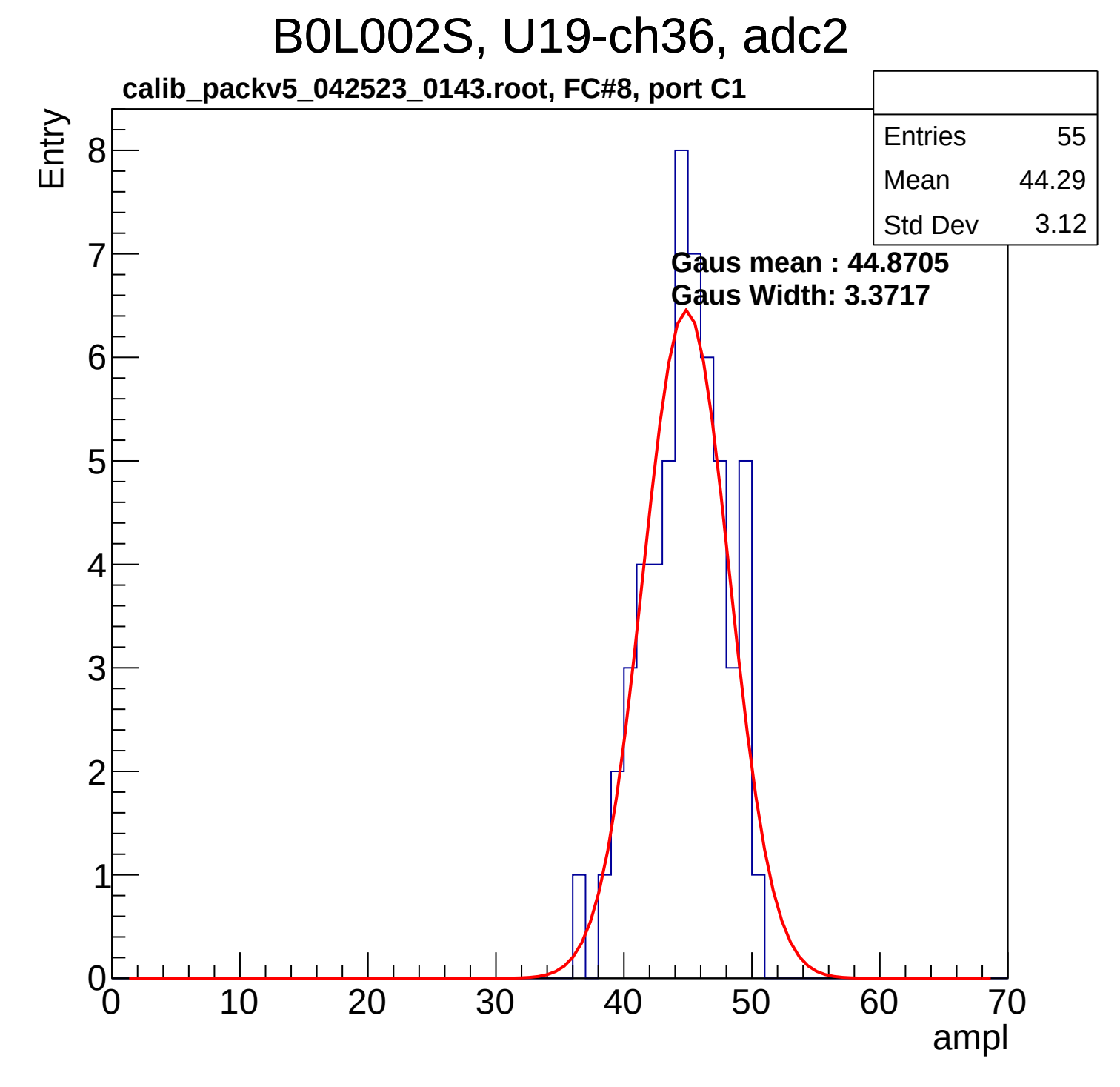

B0L002S, U19-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.12
Std Dev 10.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

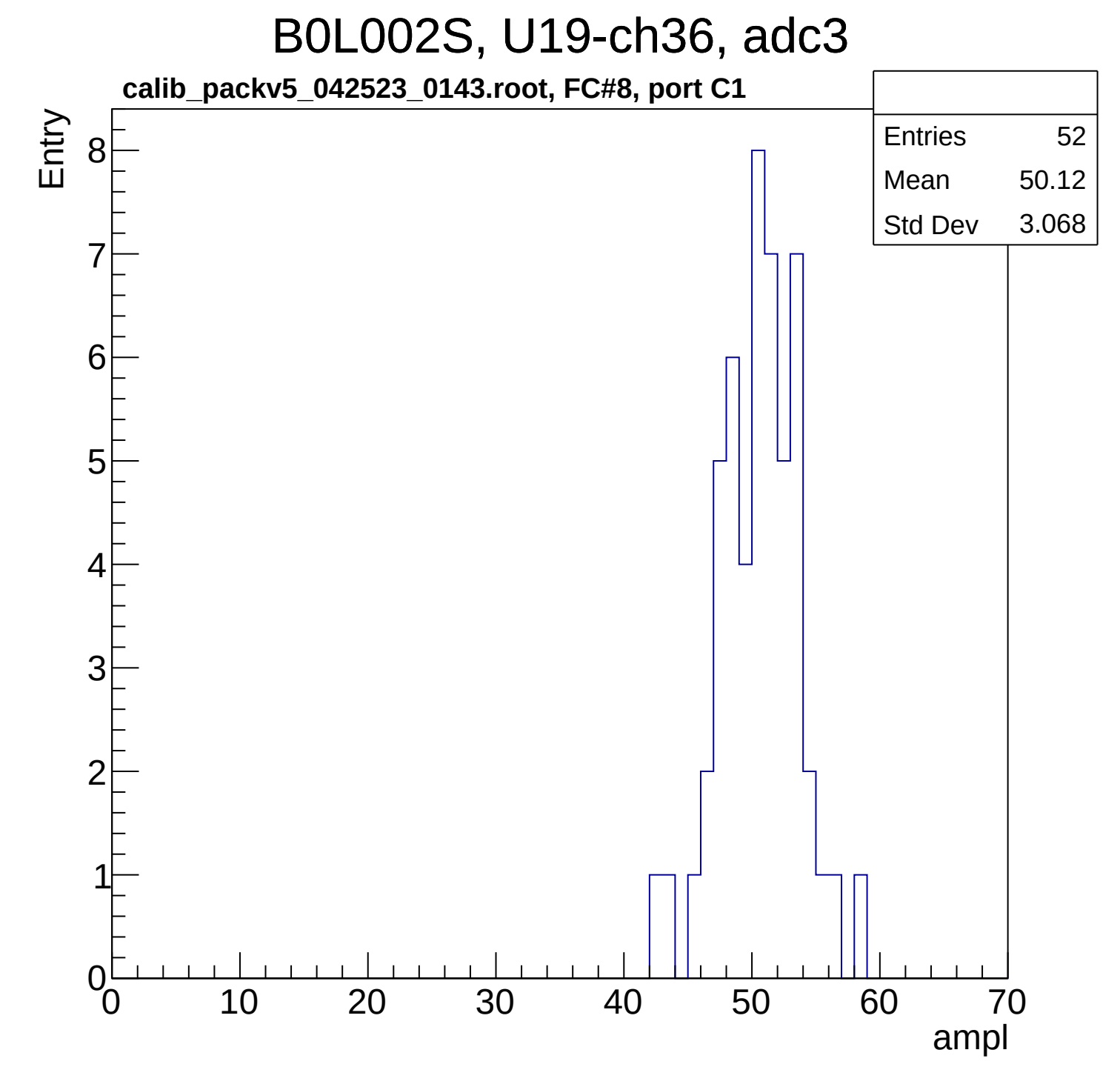

B0L002S, U19-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

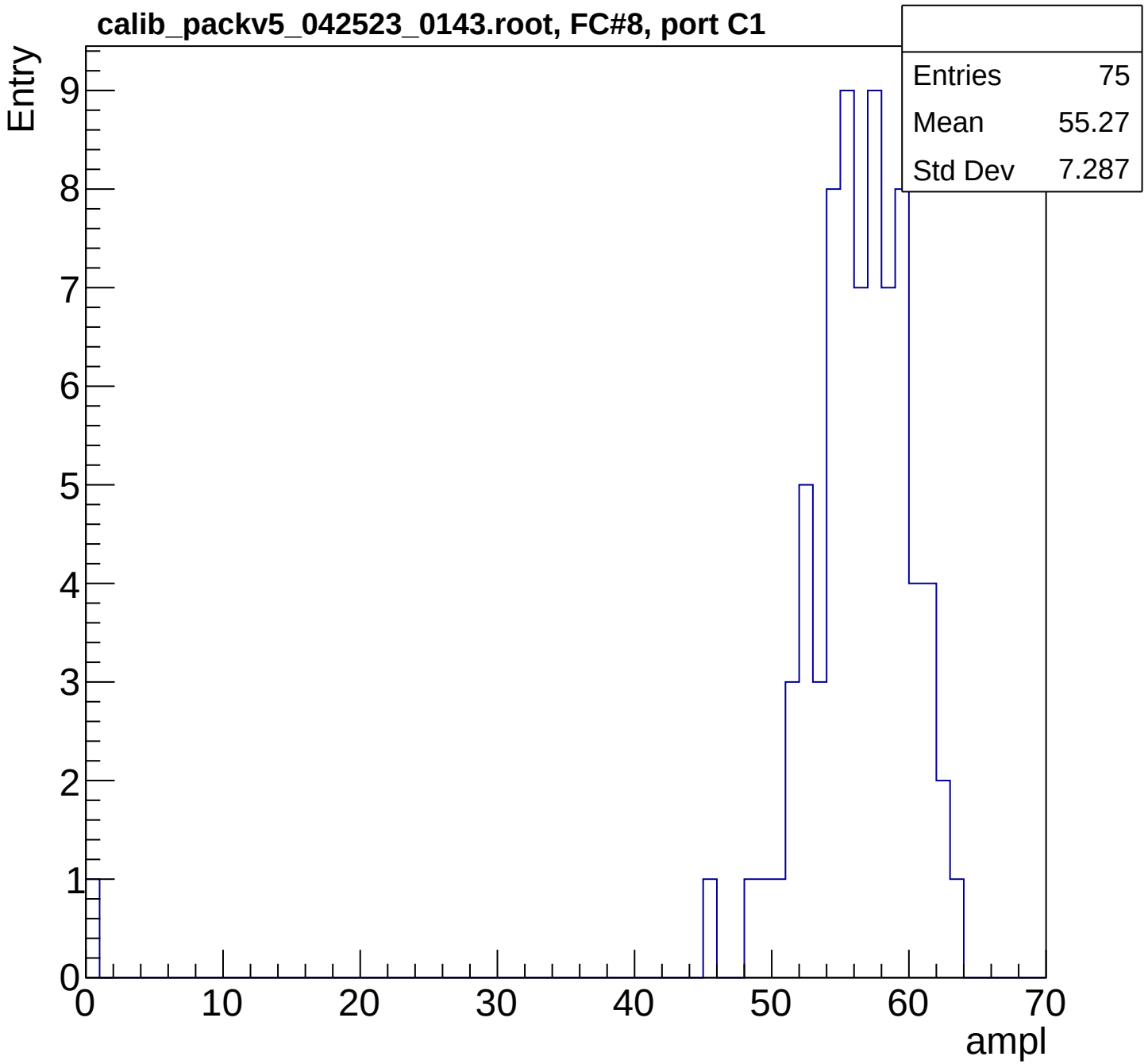

B0L002S, U19-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

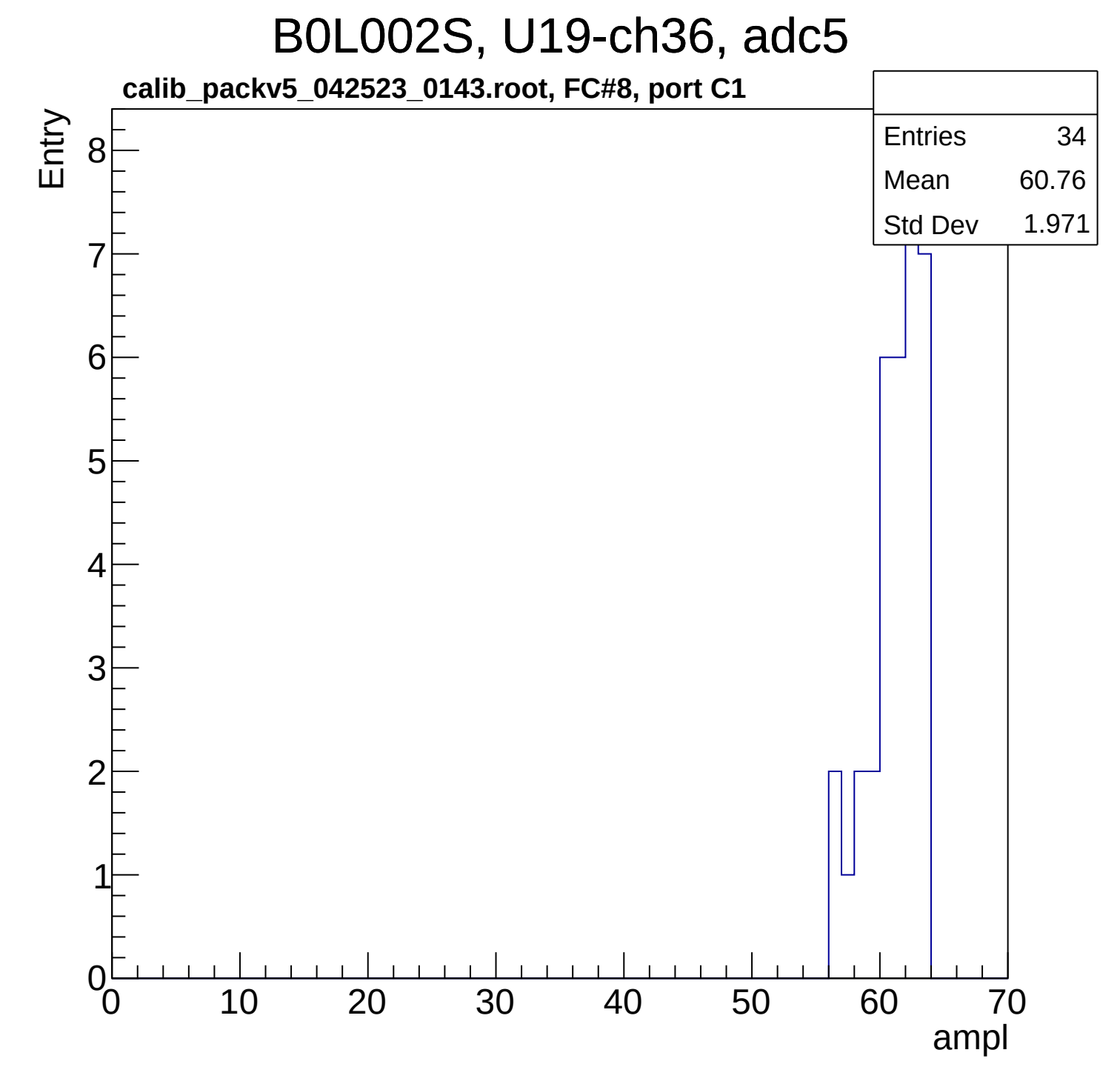

B0L002S, U19-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 29.76
Std Dev 4.806
Gaus mean : 30.5847
Gaus Width: 3.5094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

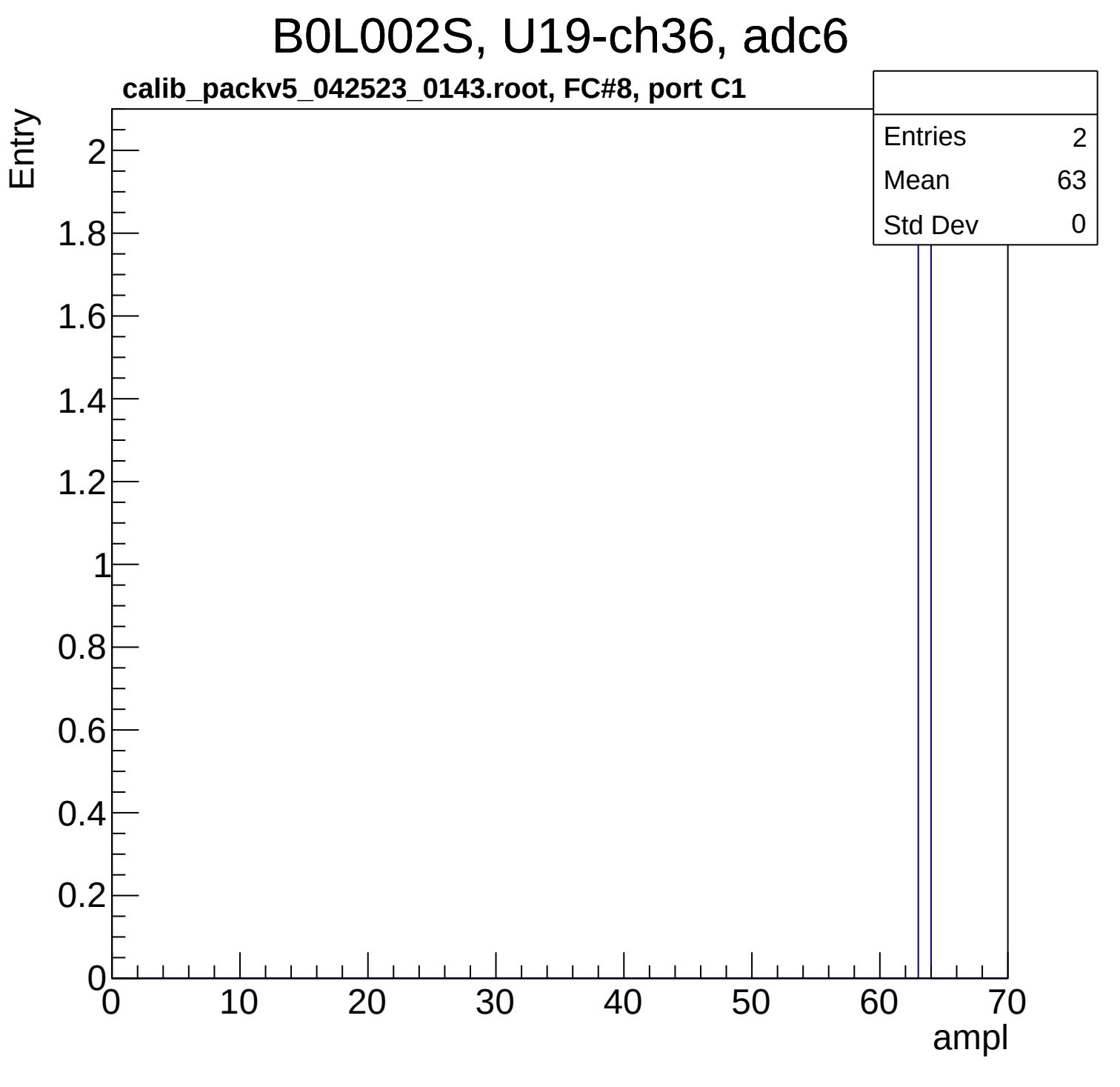

B0L002S, U19-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.88
Std Dev 3.73
Gaus mean : 38.5952
Gaus Width: 3.7152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

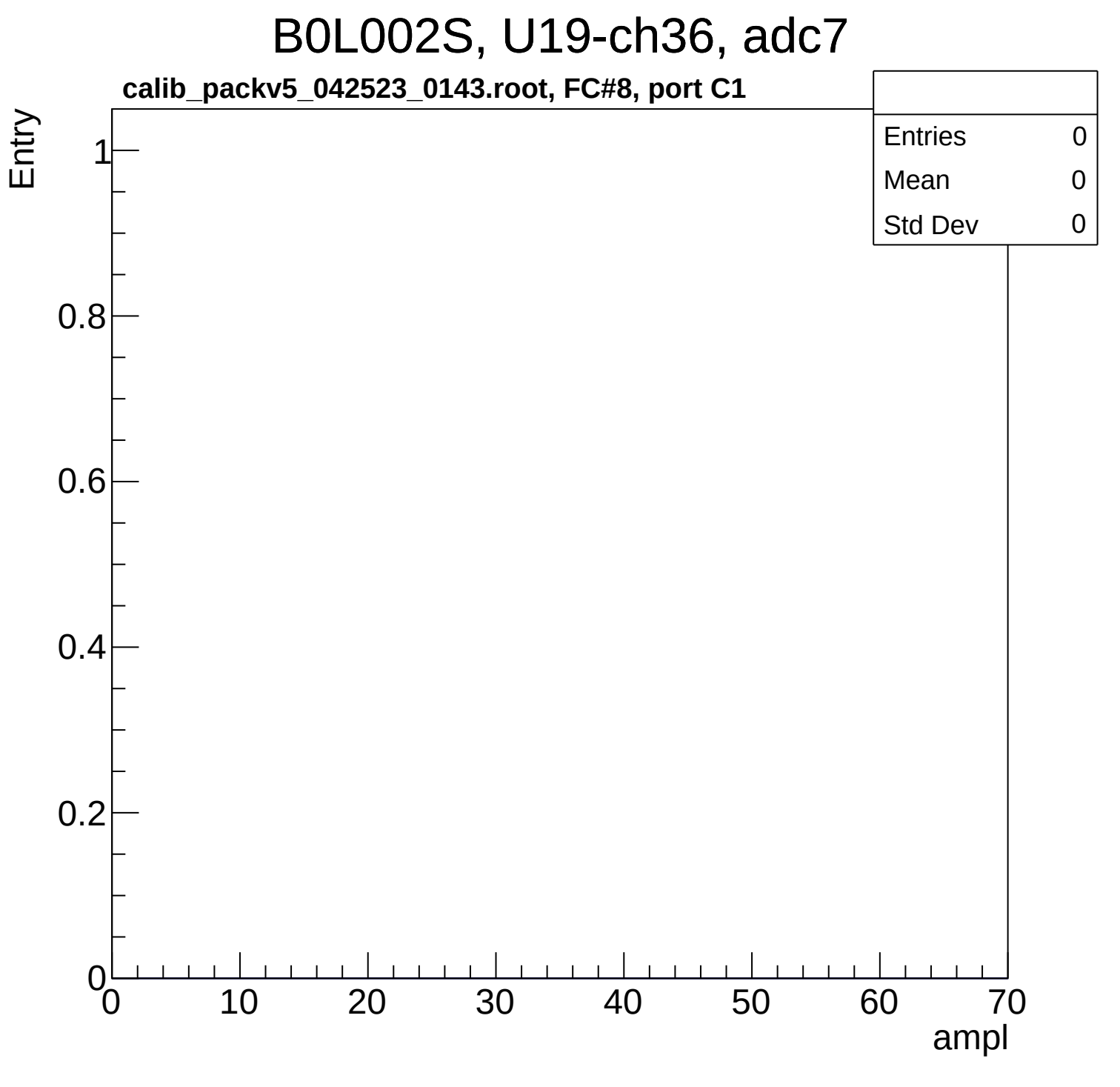

B0L002S, U19-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.29
Std Dev 3.12
Gaus mean : 44.8705
Gaus Width: 3.3717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

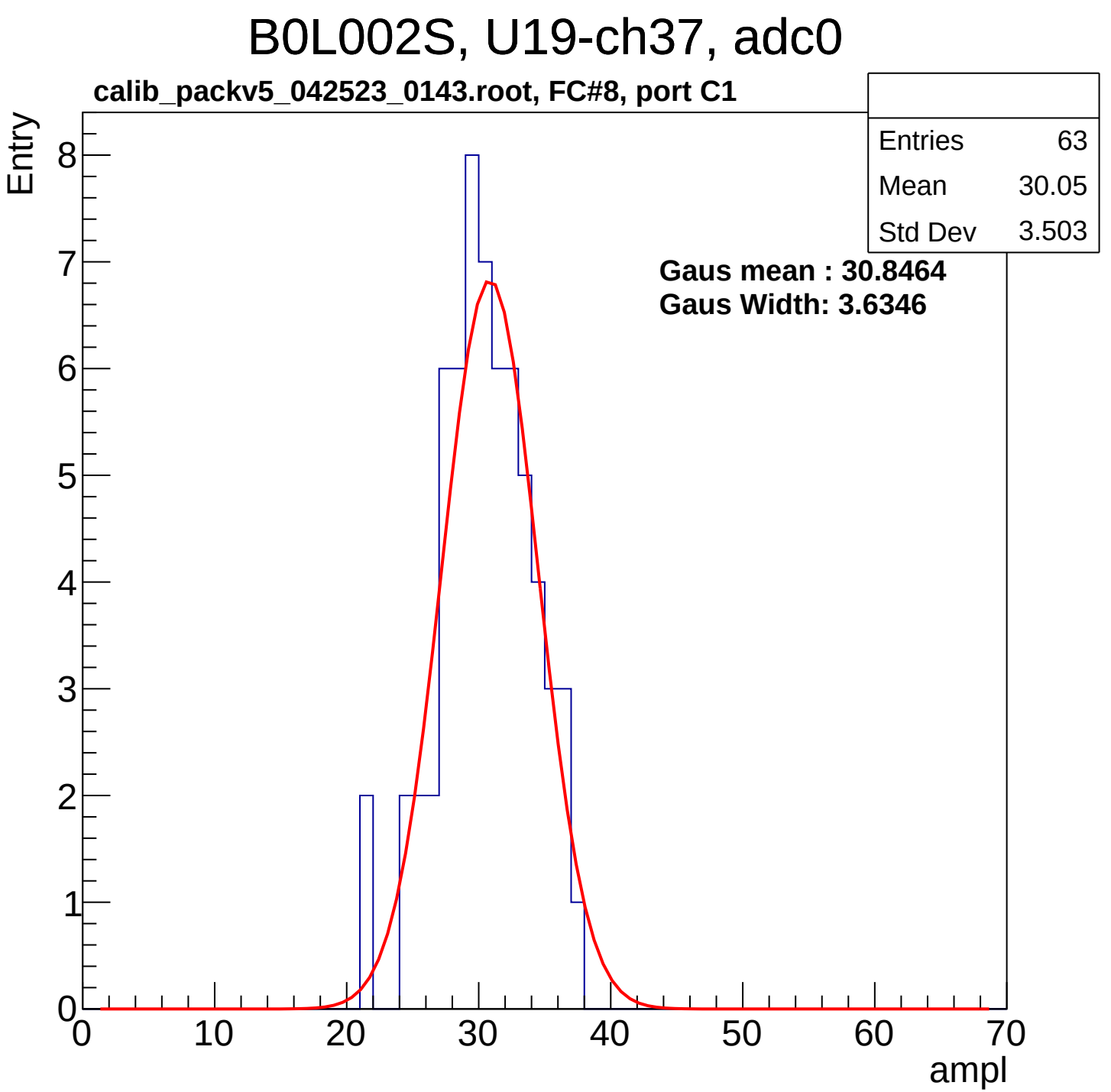

B0L002S, U19-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.12
Std Dev 3.068
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

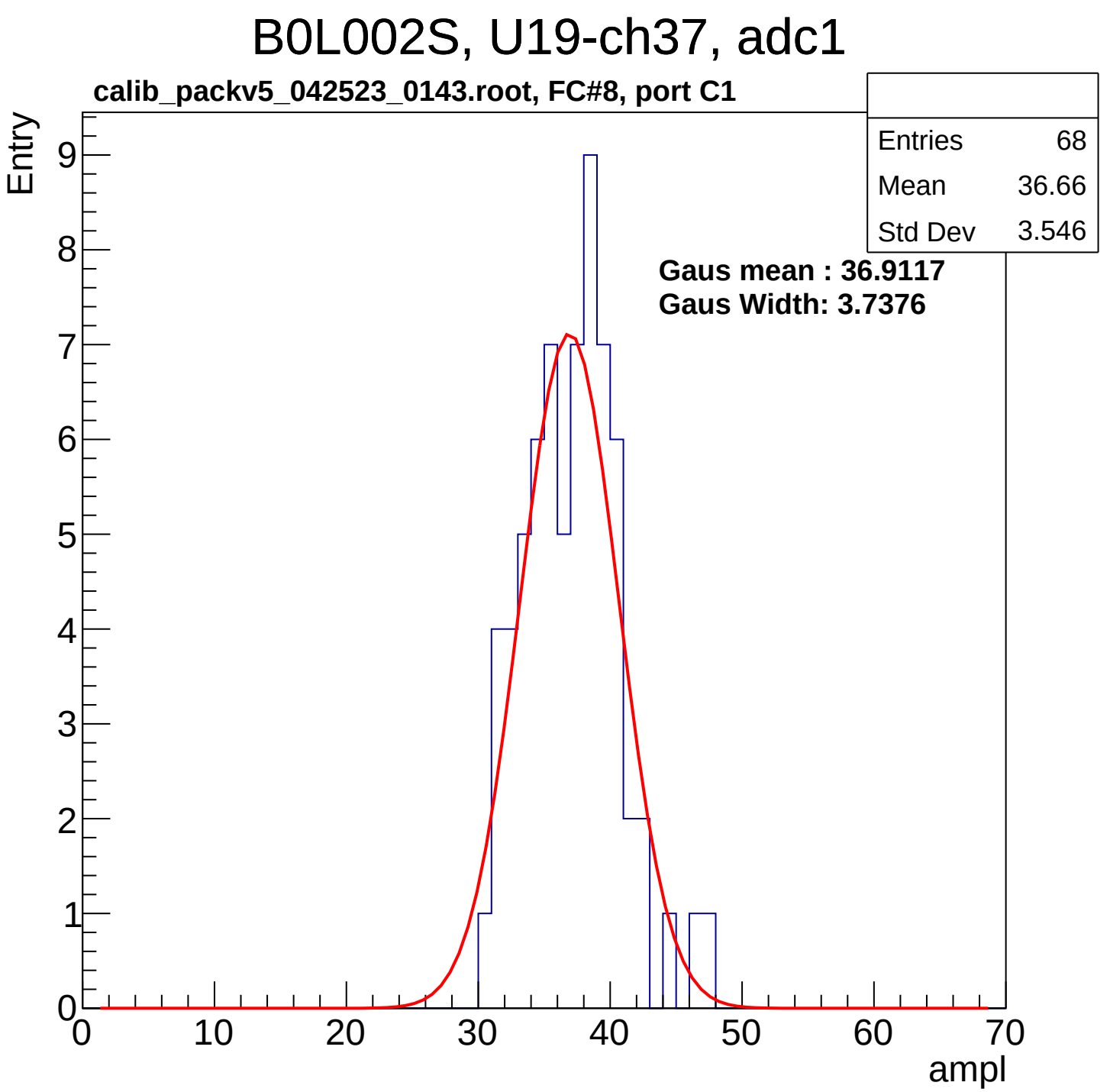

B0L002S, U19-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 55.27
Std Dev 7.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

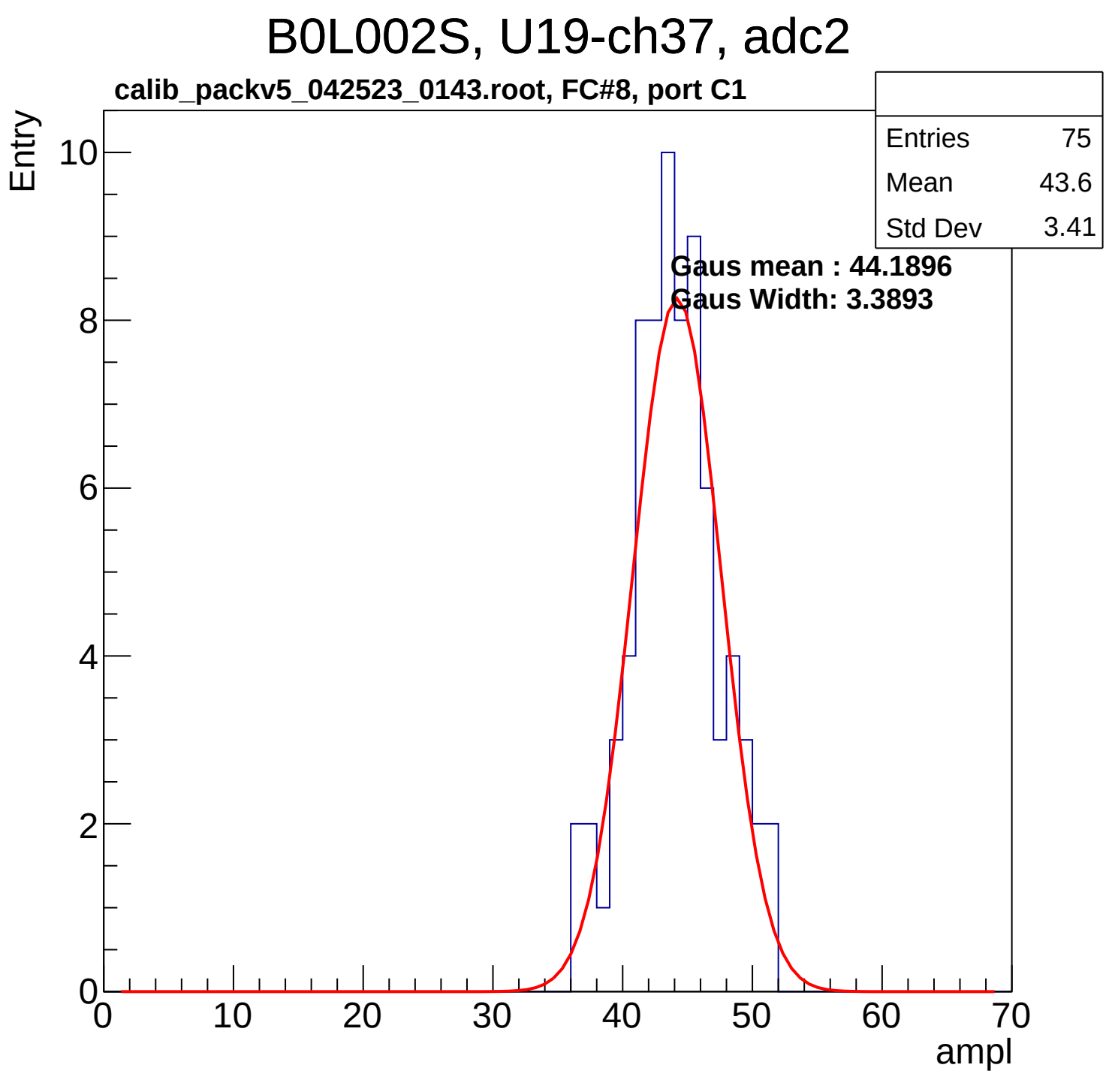

B0L002S, U19-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.76
Std Dev 1.971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

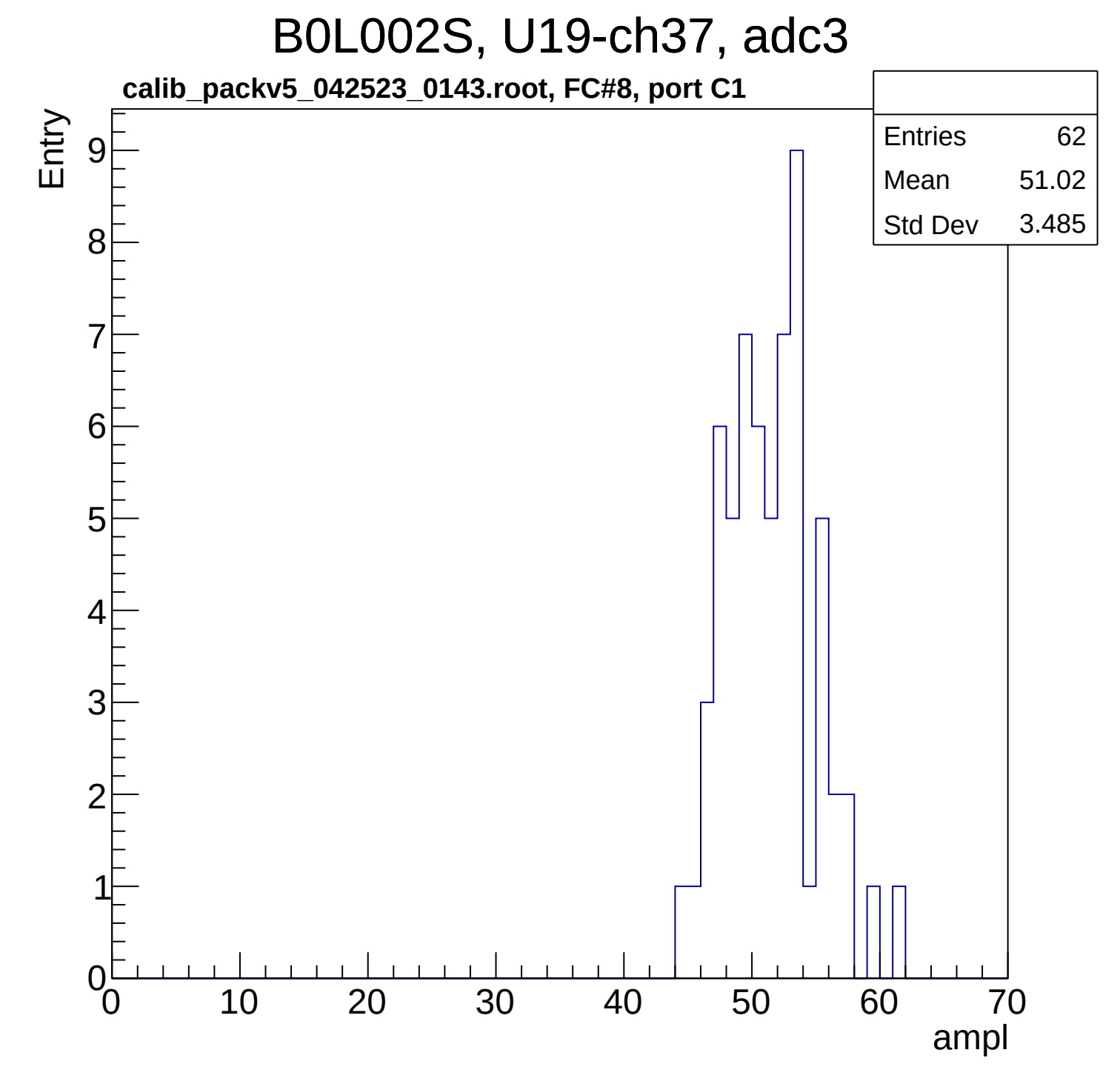

B0L002S, U19-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

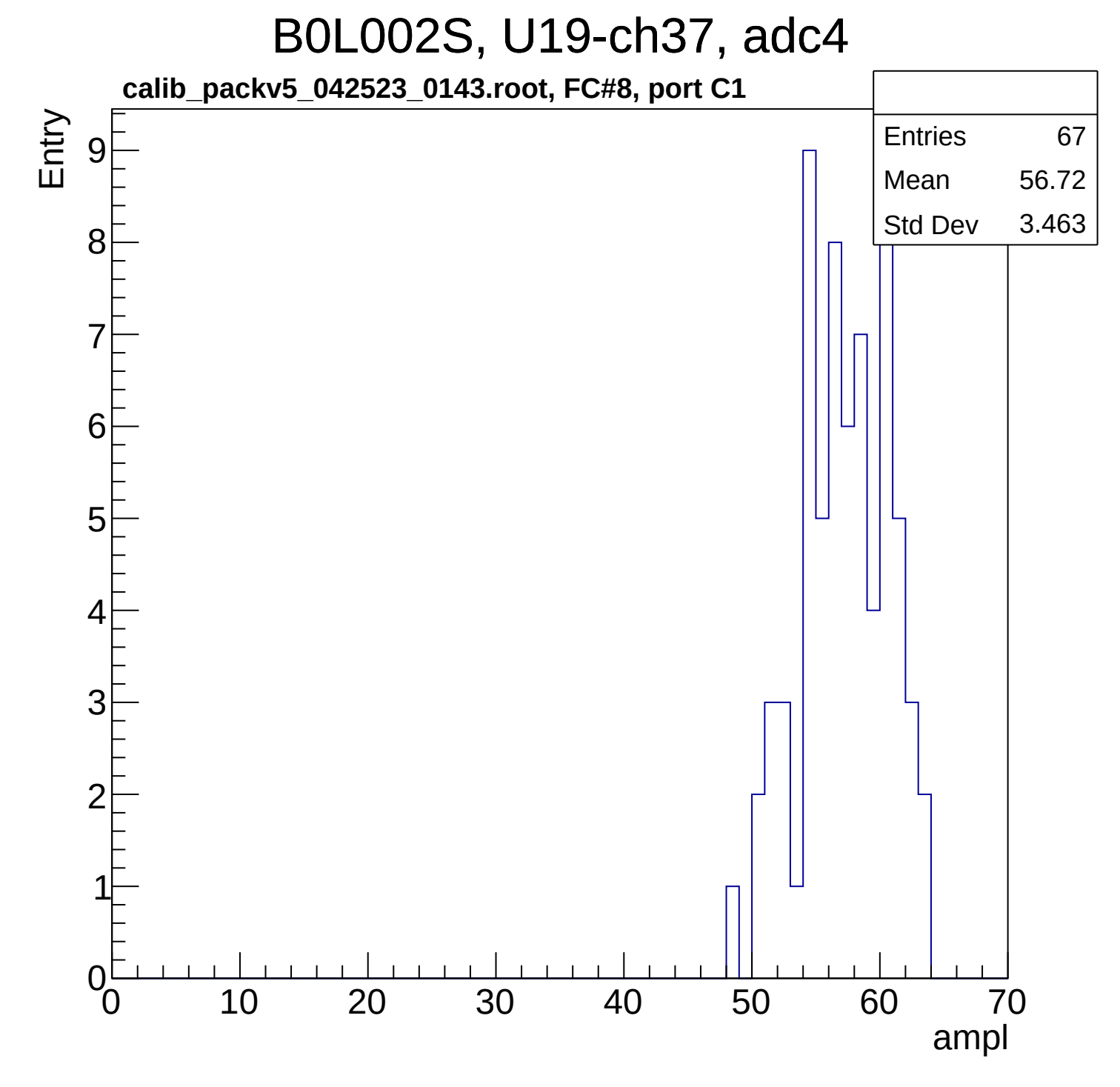

B0L002S, U19-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

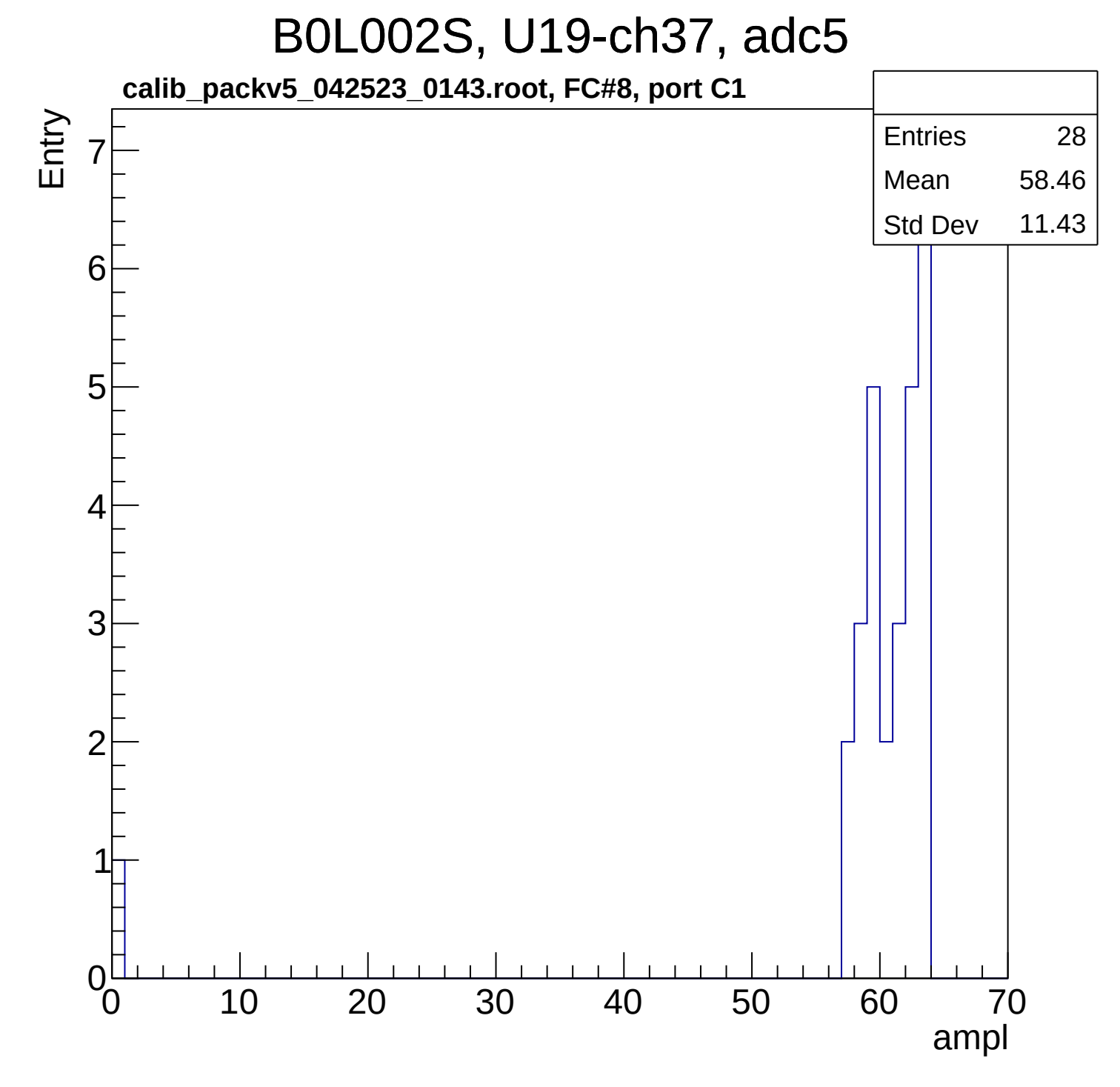

B0L002S, U19-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.05
Std Dev 3.503
Gaus mean : 30.8464
Gaus Width: 3.6346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

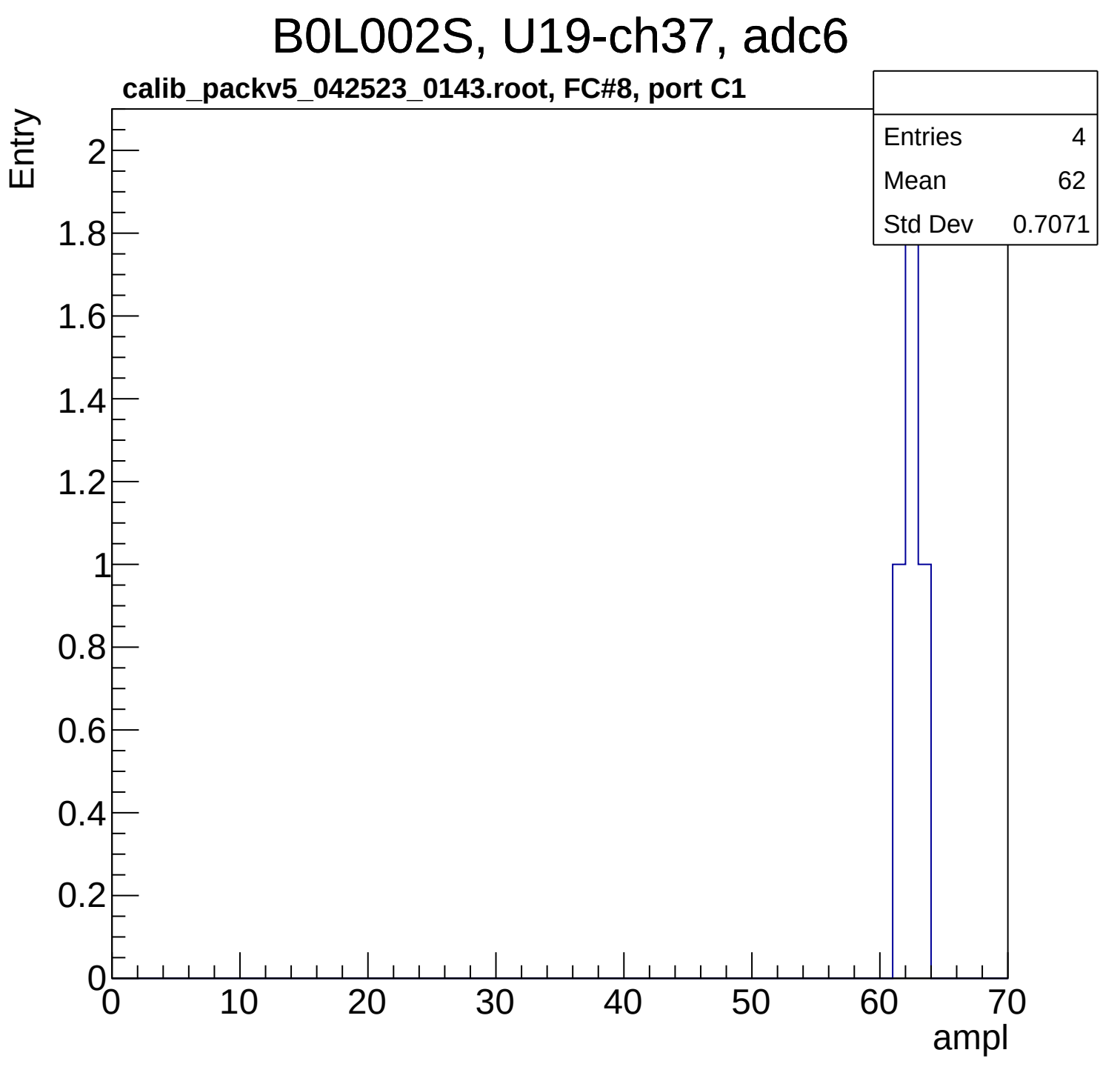

B0L002S, U19-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.66
Std Dev 3.546
Gaus mean : 36.9117
Gaus Width: 3.7376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

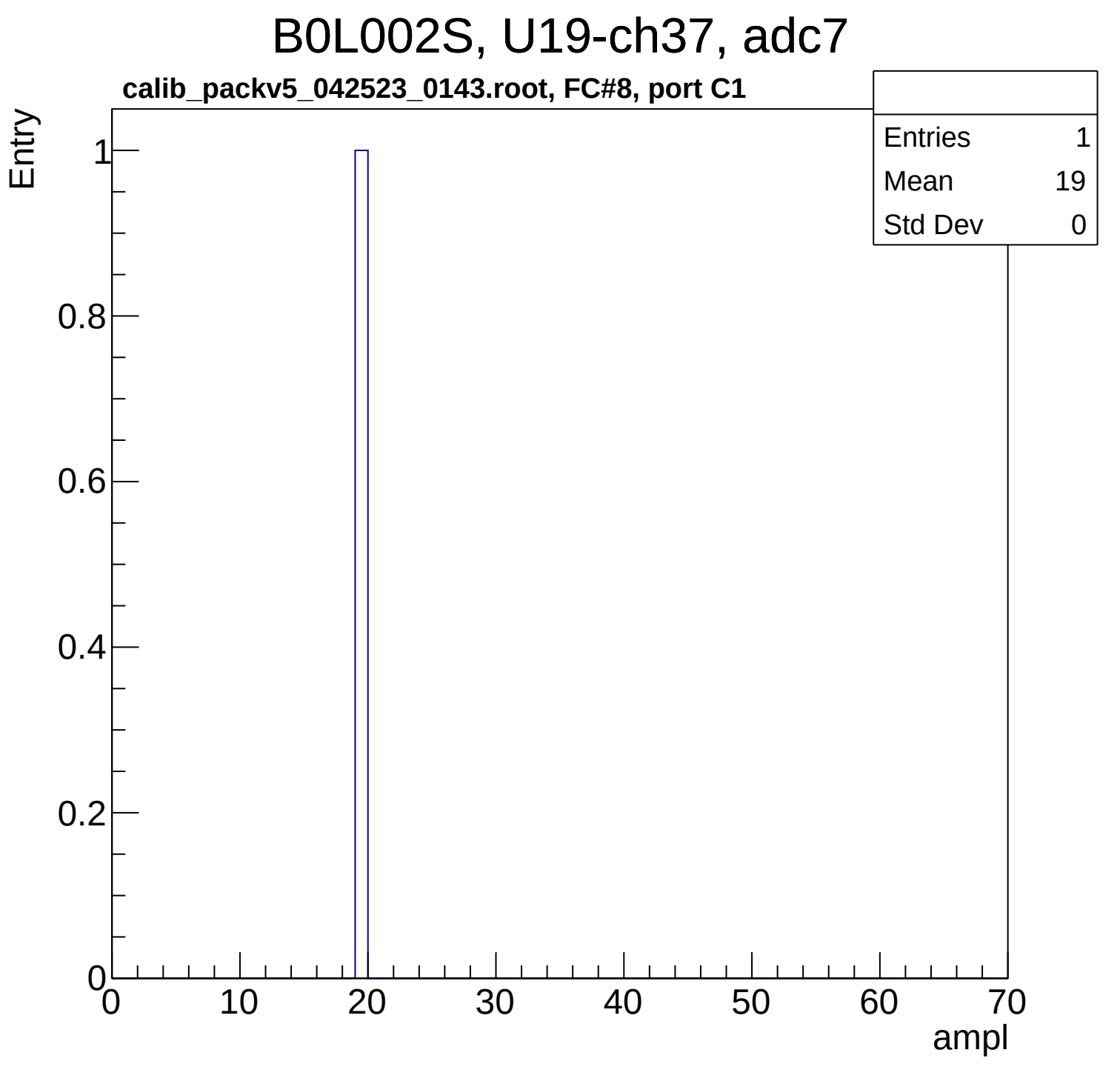

B0L002S, U19-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 43.6
Std Dev 3.41
Gaus mean : 44.1896
Gaus Width: 3.3893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

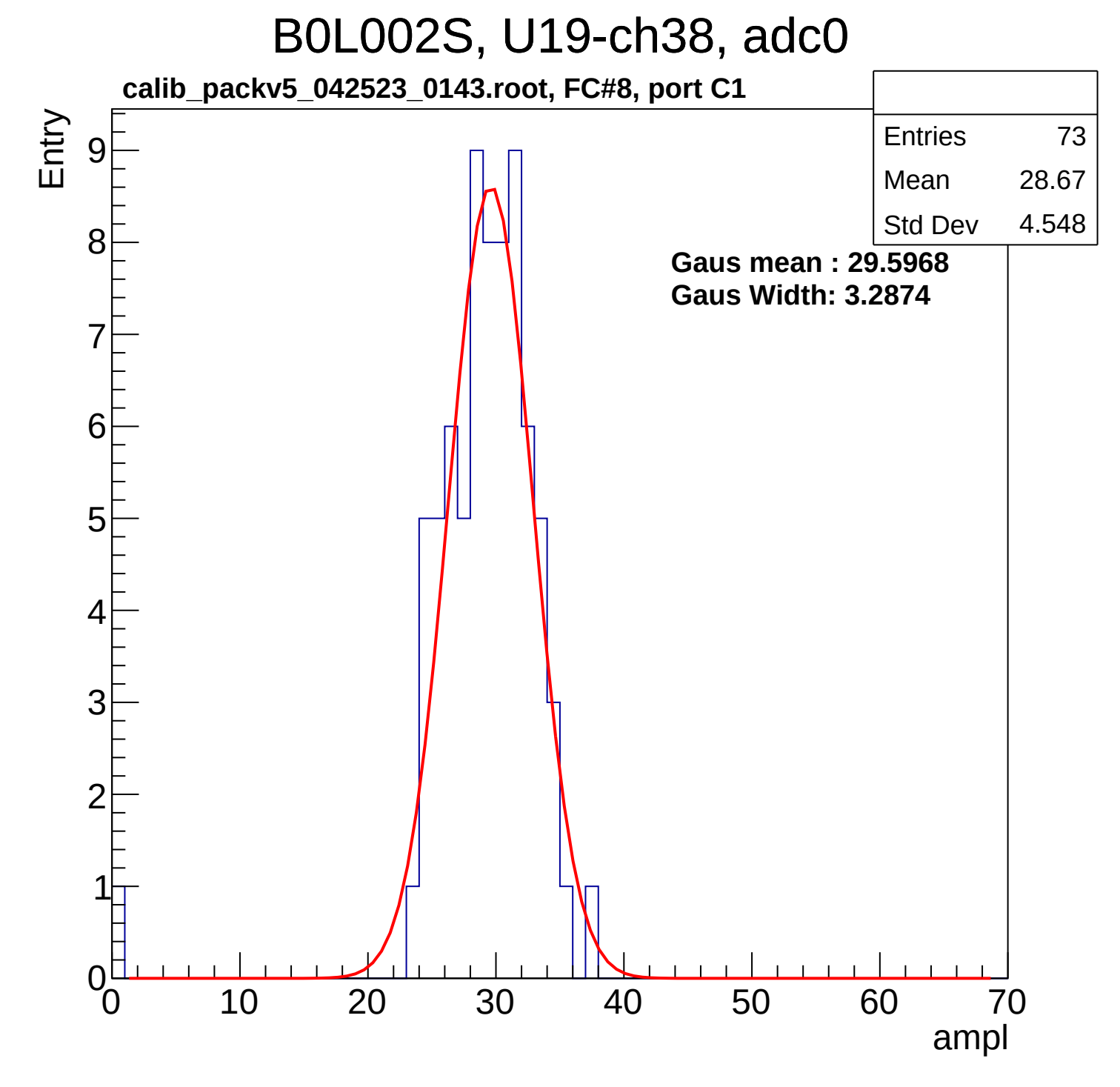

B0L002S, U19-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.02
Std Dev 3.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

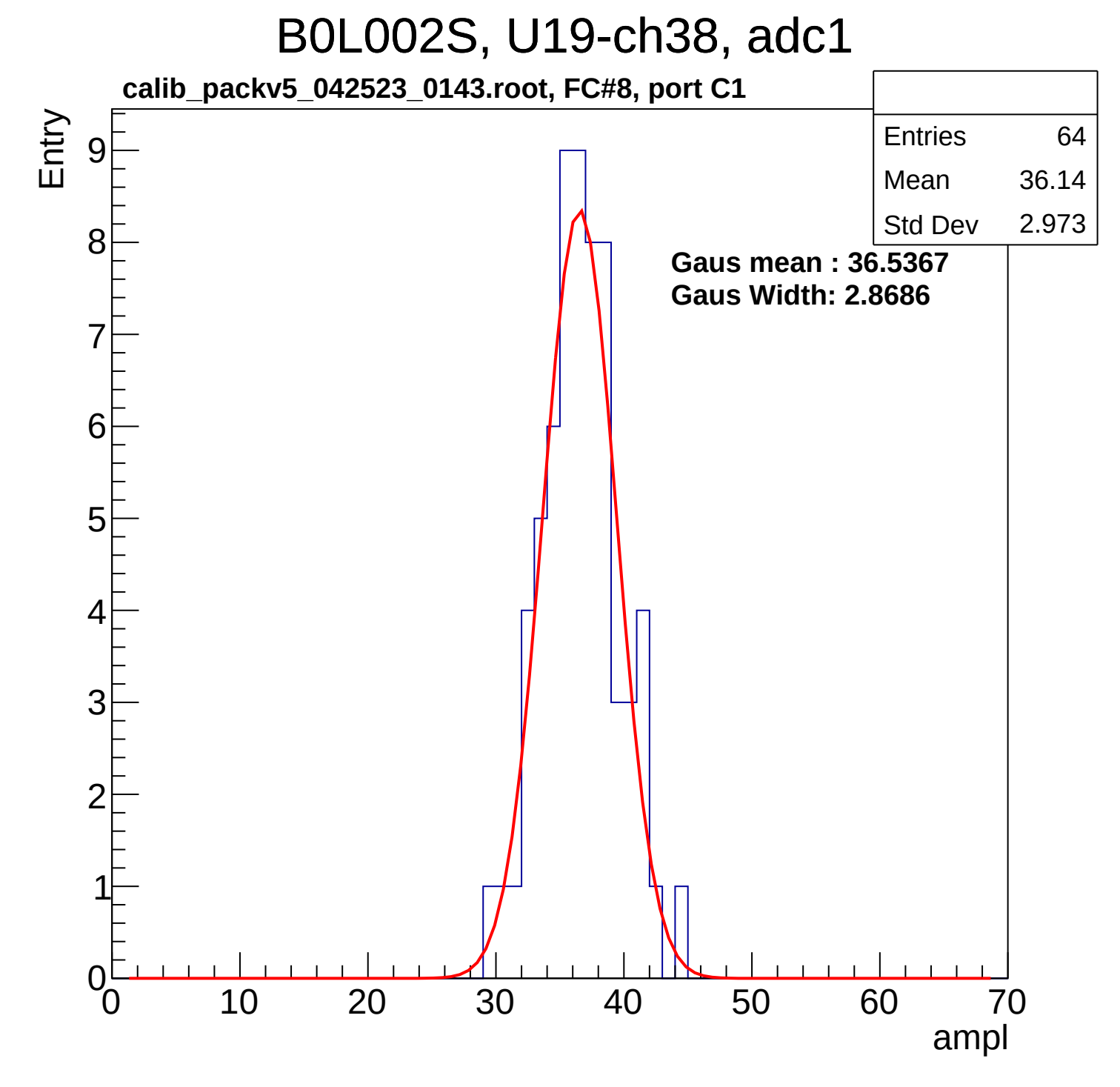

B0L002S, U19-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.72
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

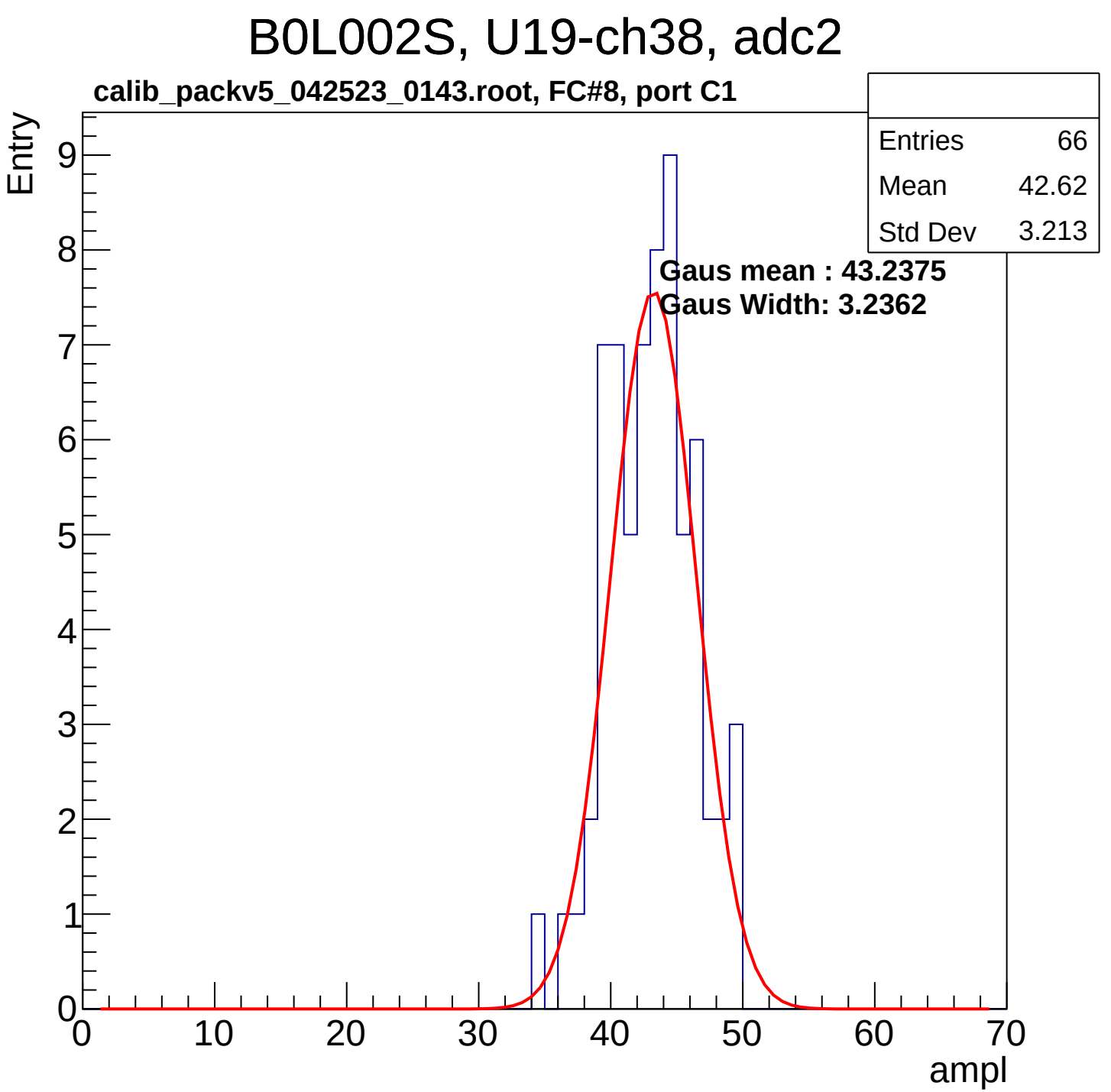

B0L002S, U19-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.46
Std Dev 11.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

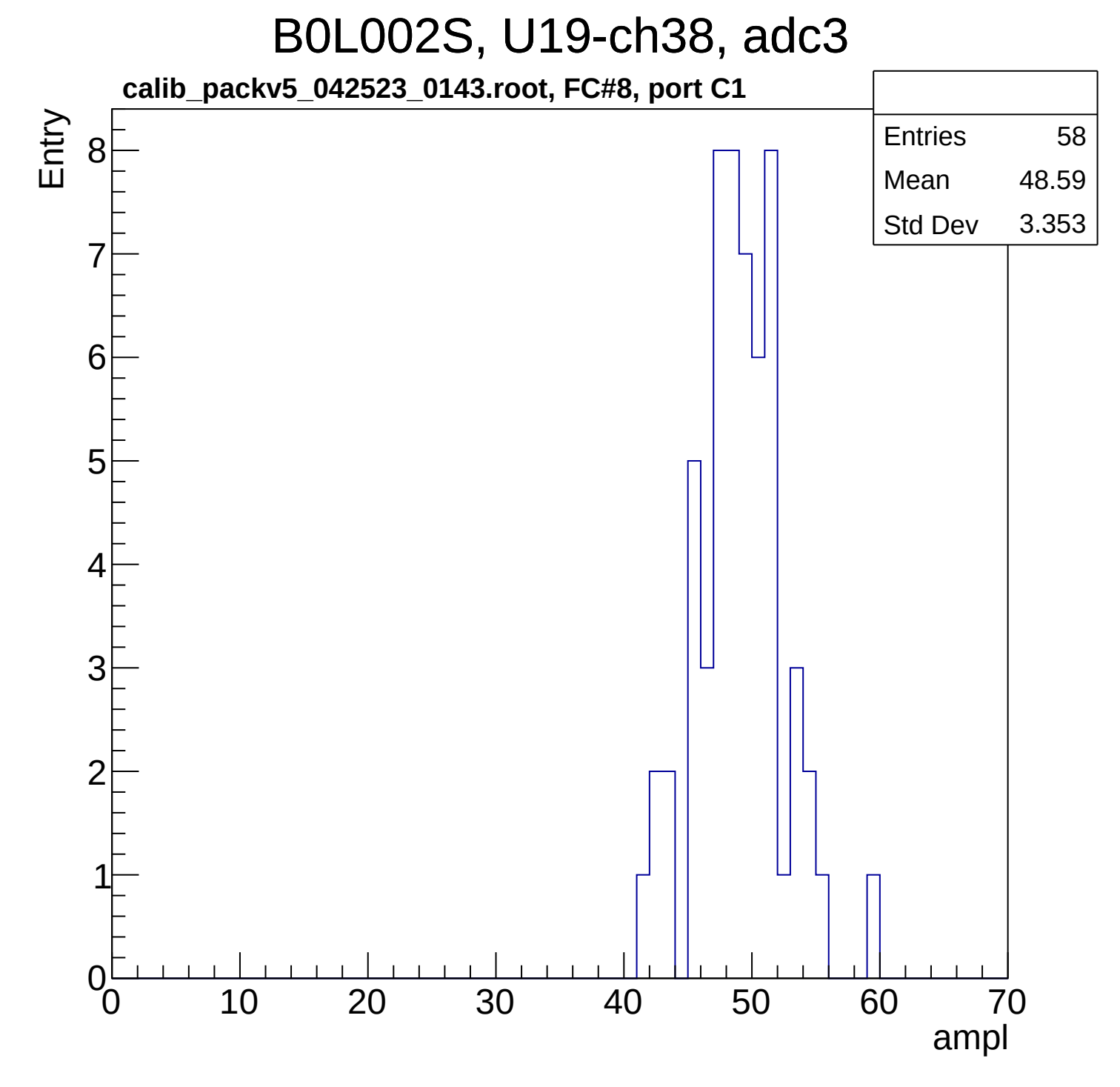

B0L002S, U19-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

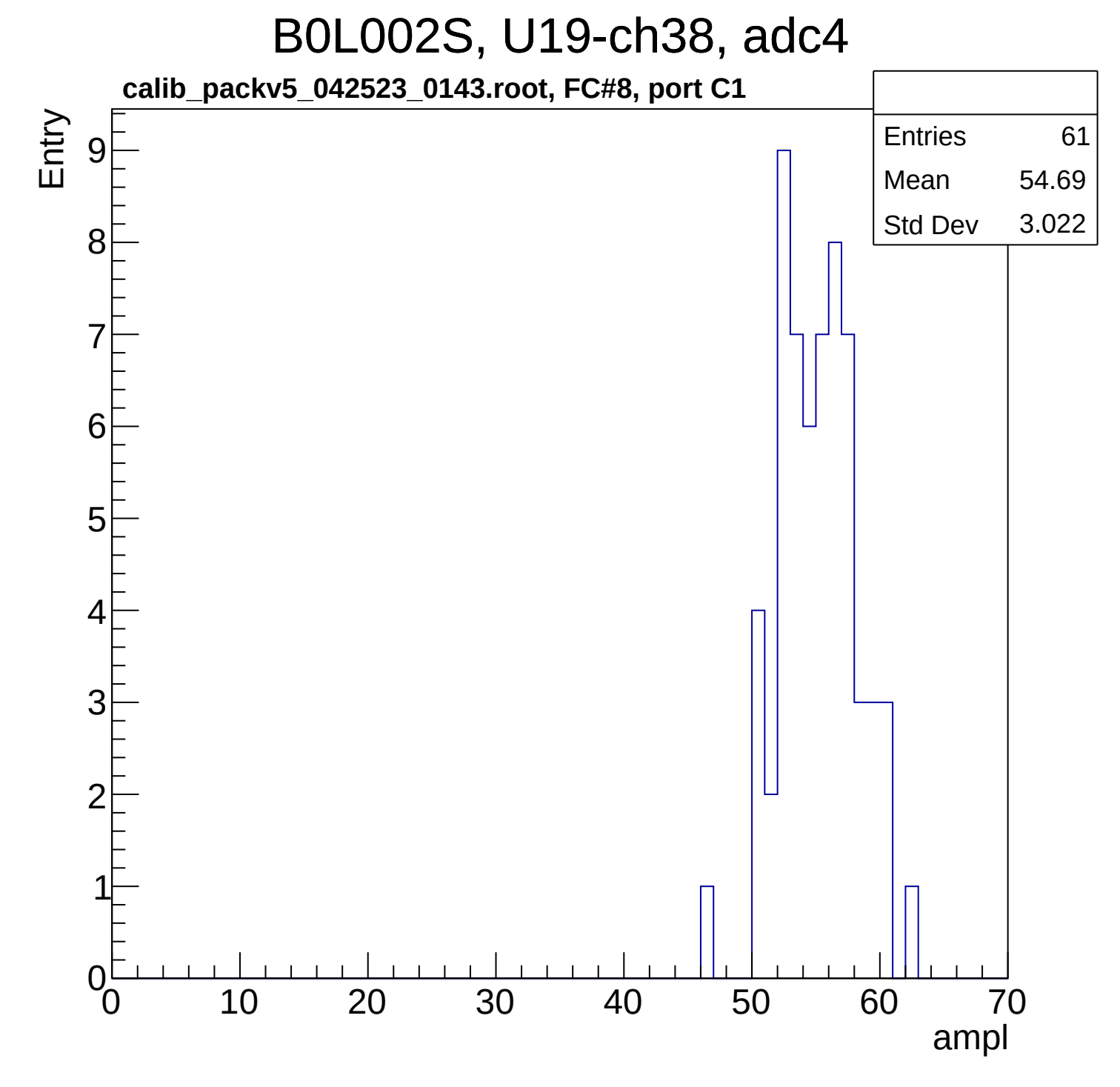

B0L002S, U19-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

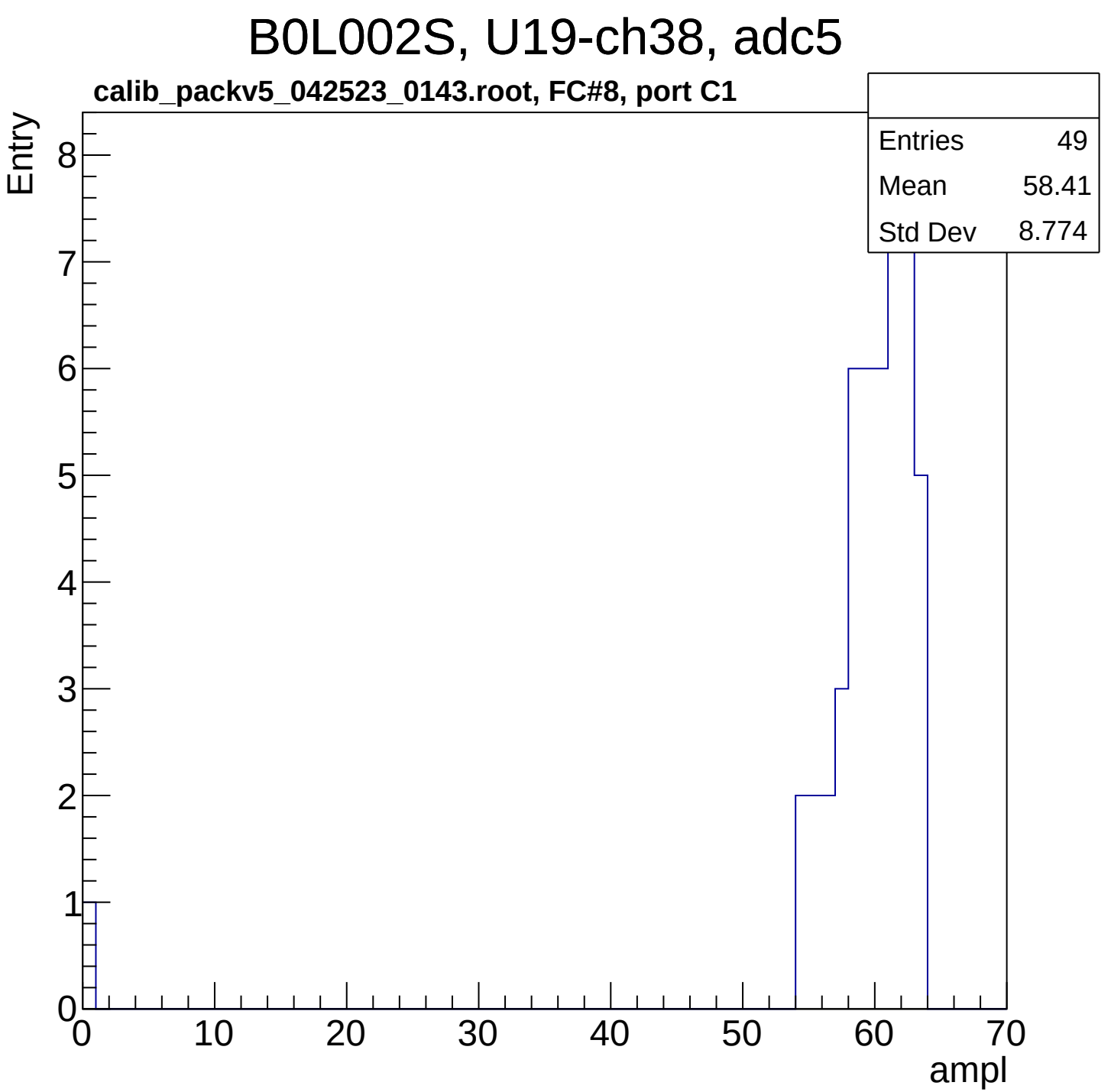

B0L002S, U19-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 28.67
Std Dev 4.548
Gaus mean : 29.5968
Gaus Width: 3.2874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

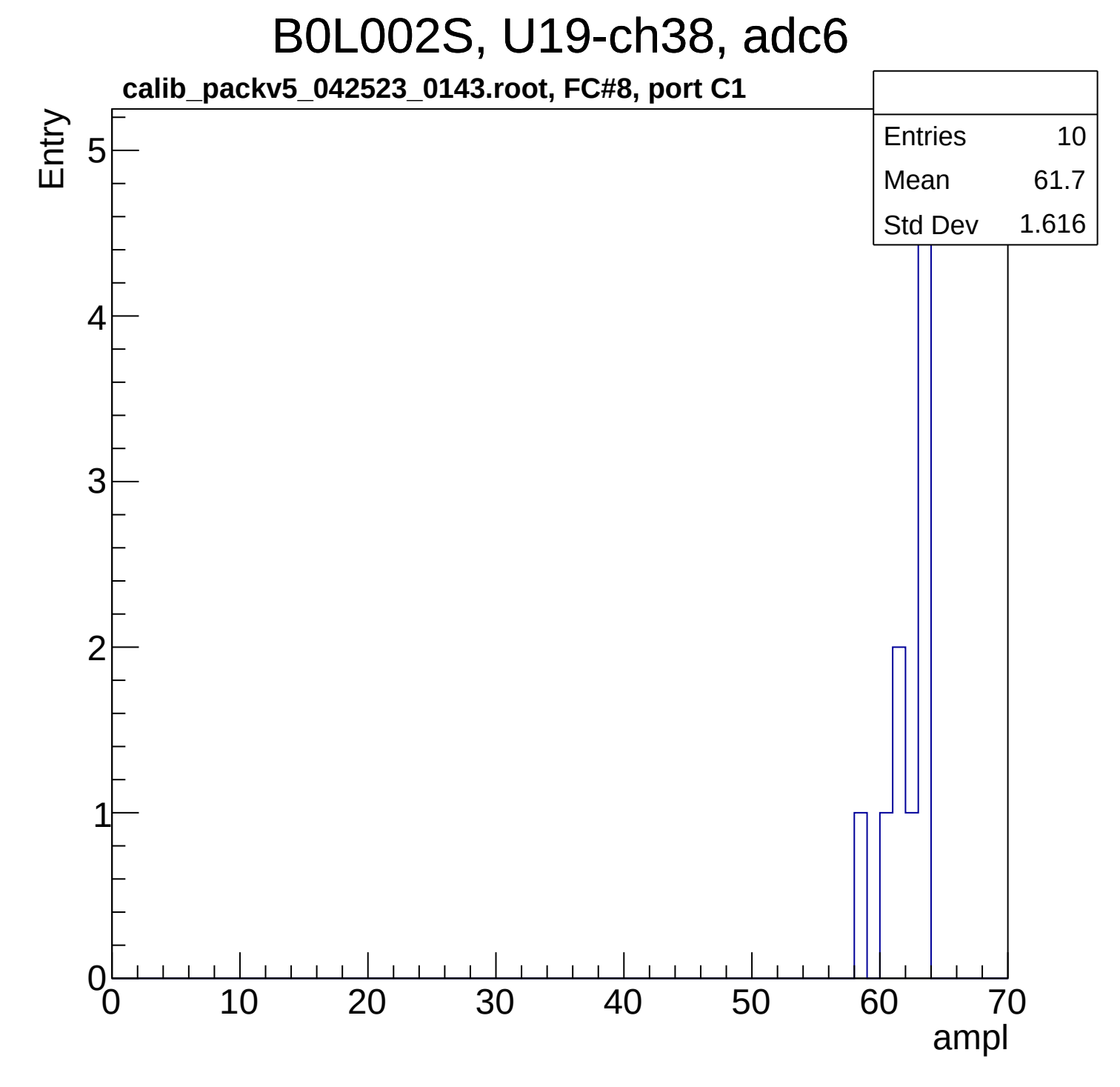

B0L002S, U19-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 36.14
Std Dev 2.973
Gaus mean : 36.5367
Gaus Width: 2.8686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

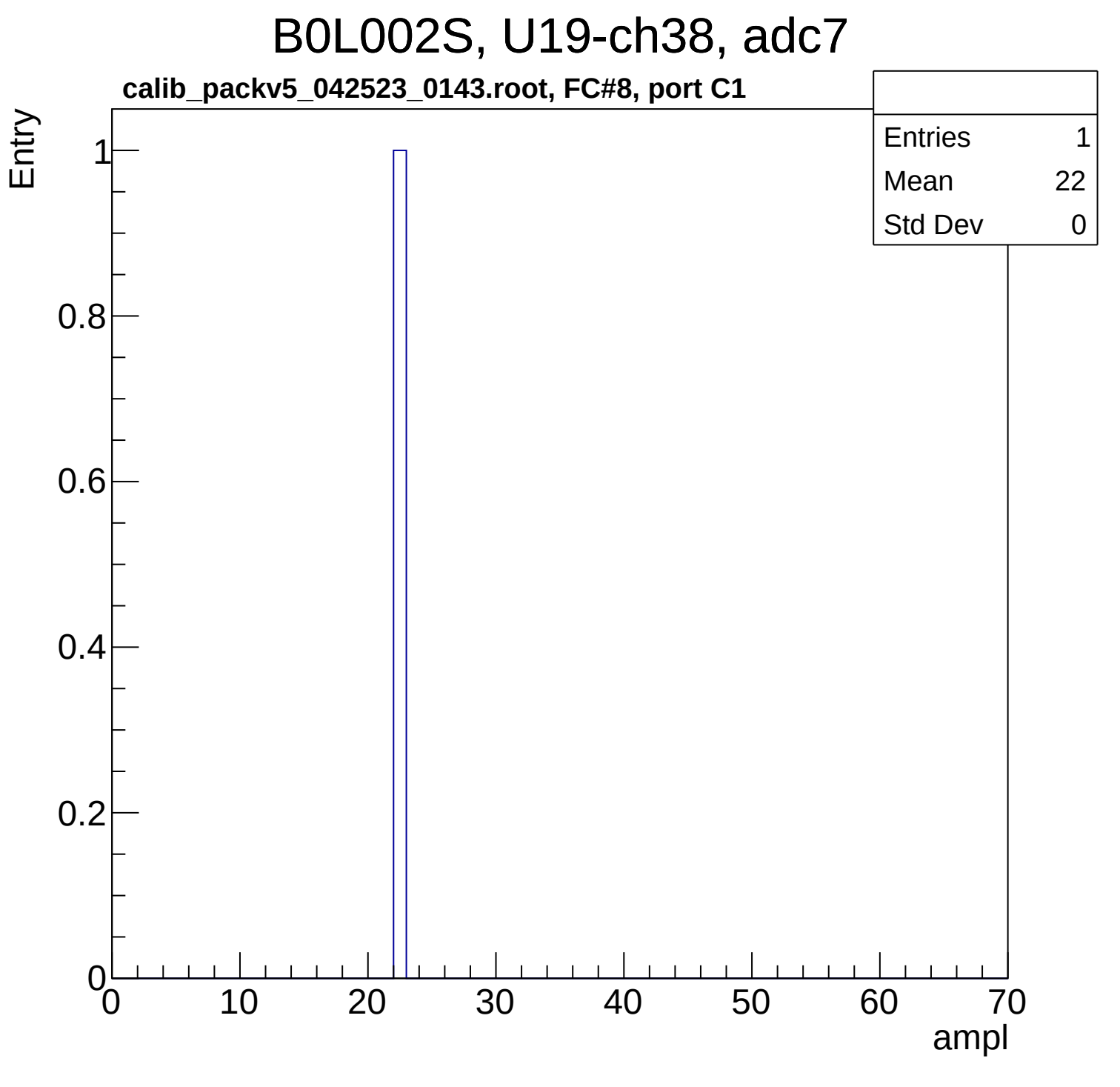

B0L002S, U19-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.62
Std Dev 3.213
Gaus mean : 43.2375
Gaus Width: 3.2362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

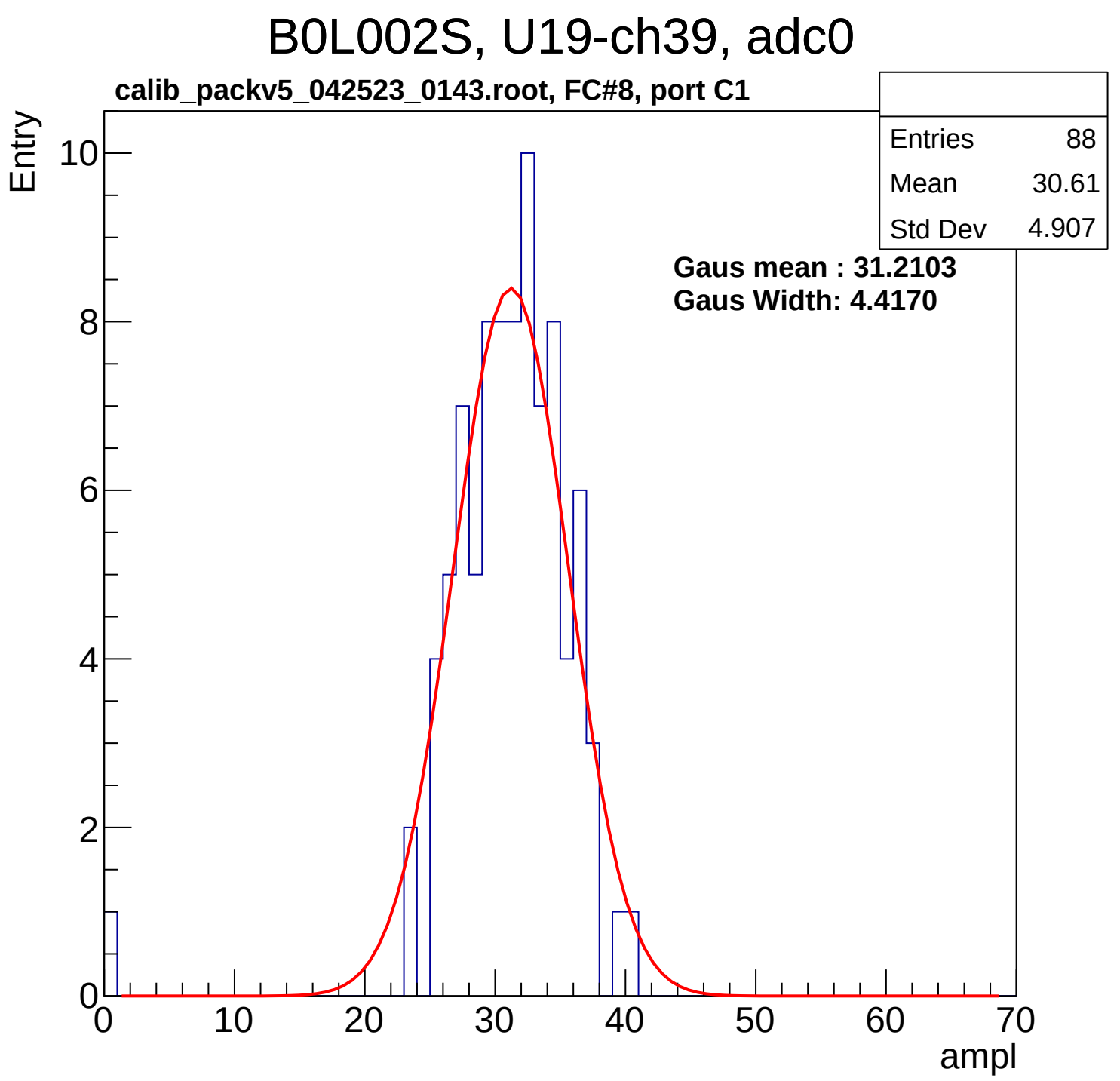

B0L002S, U19-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 48.59
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

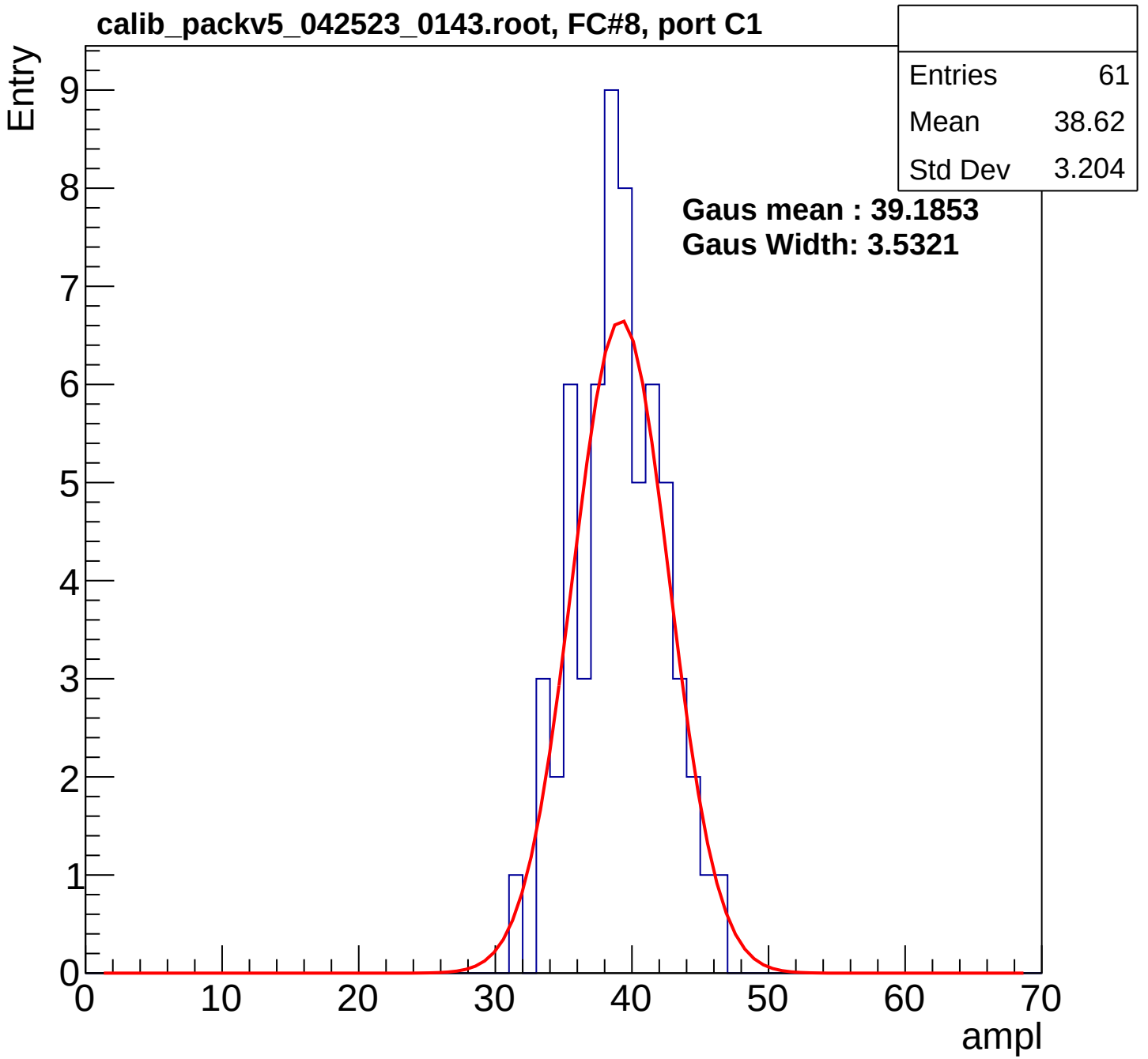

B0L002S, U19-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 54.69
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

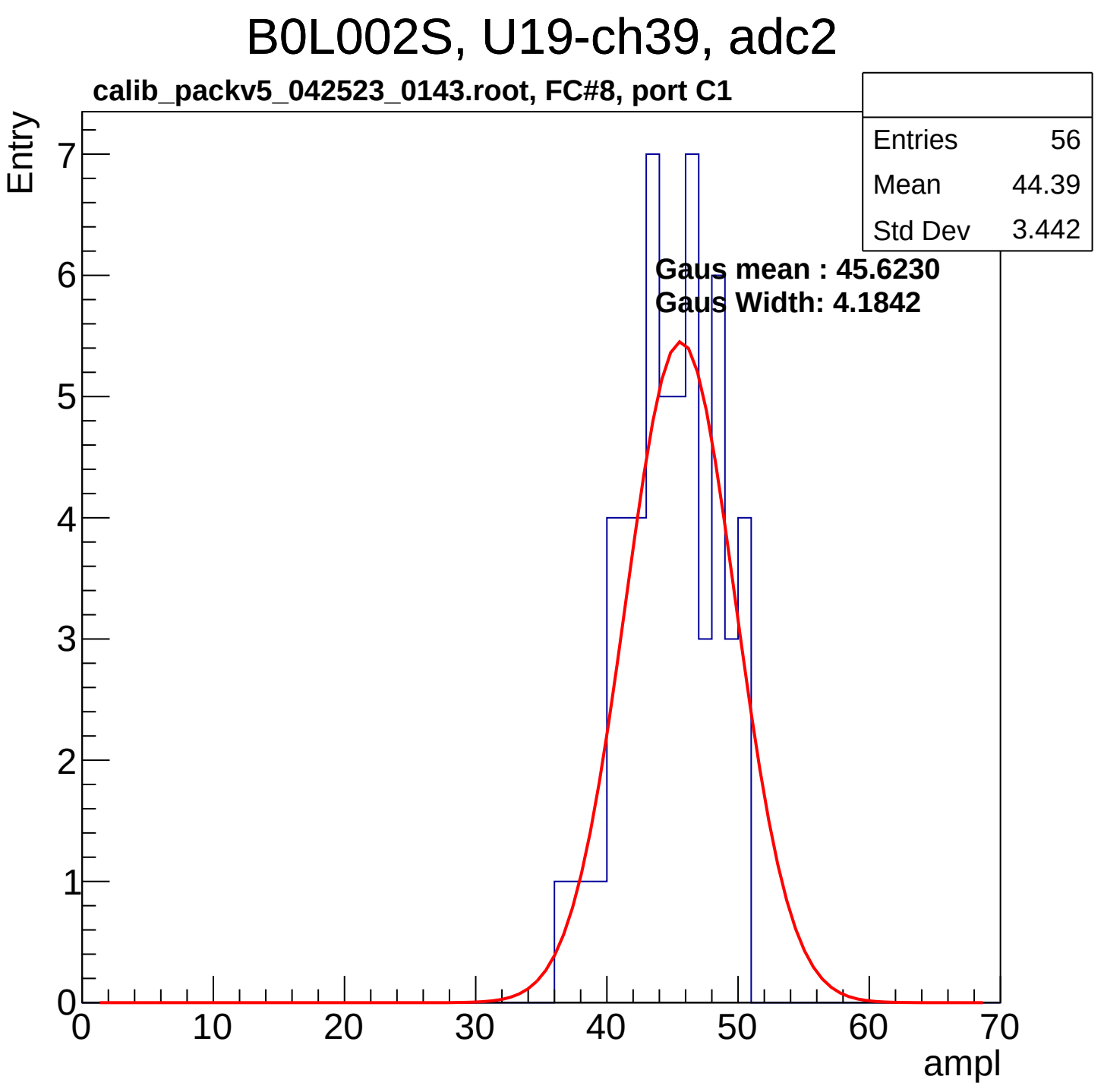

B0L002S, U19-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.41
Std Dev 8.774
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

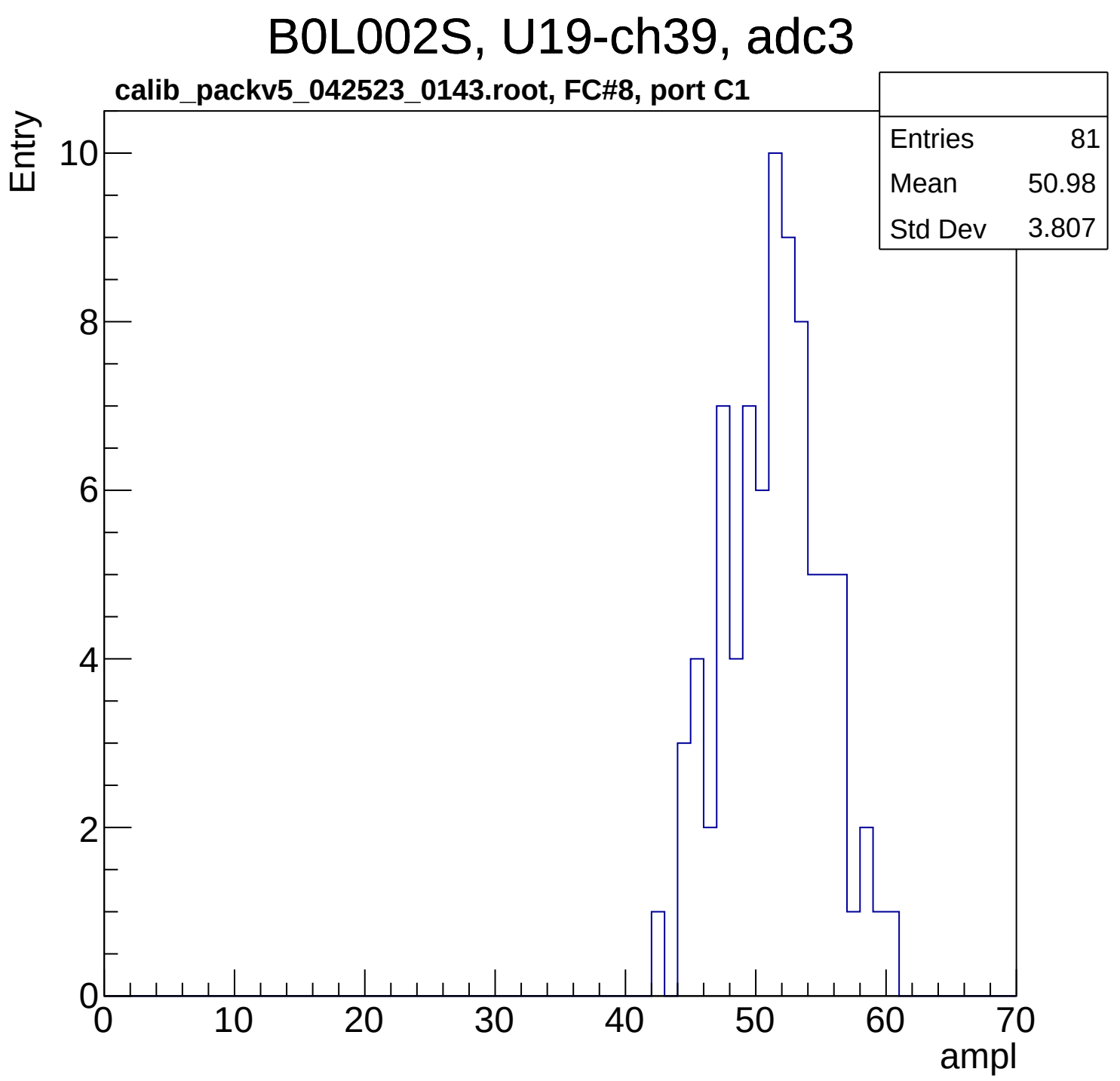

B0L002S, U19-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.7
Std Dev 1.616
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

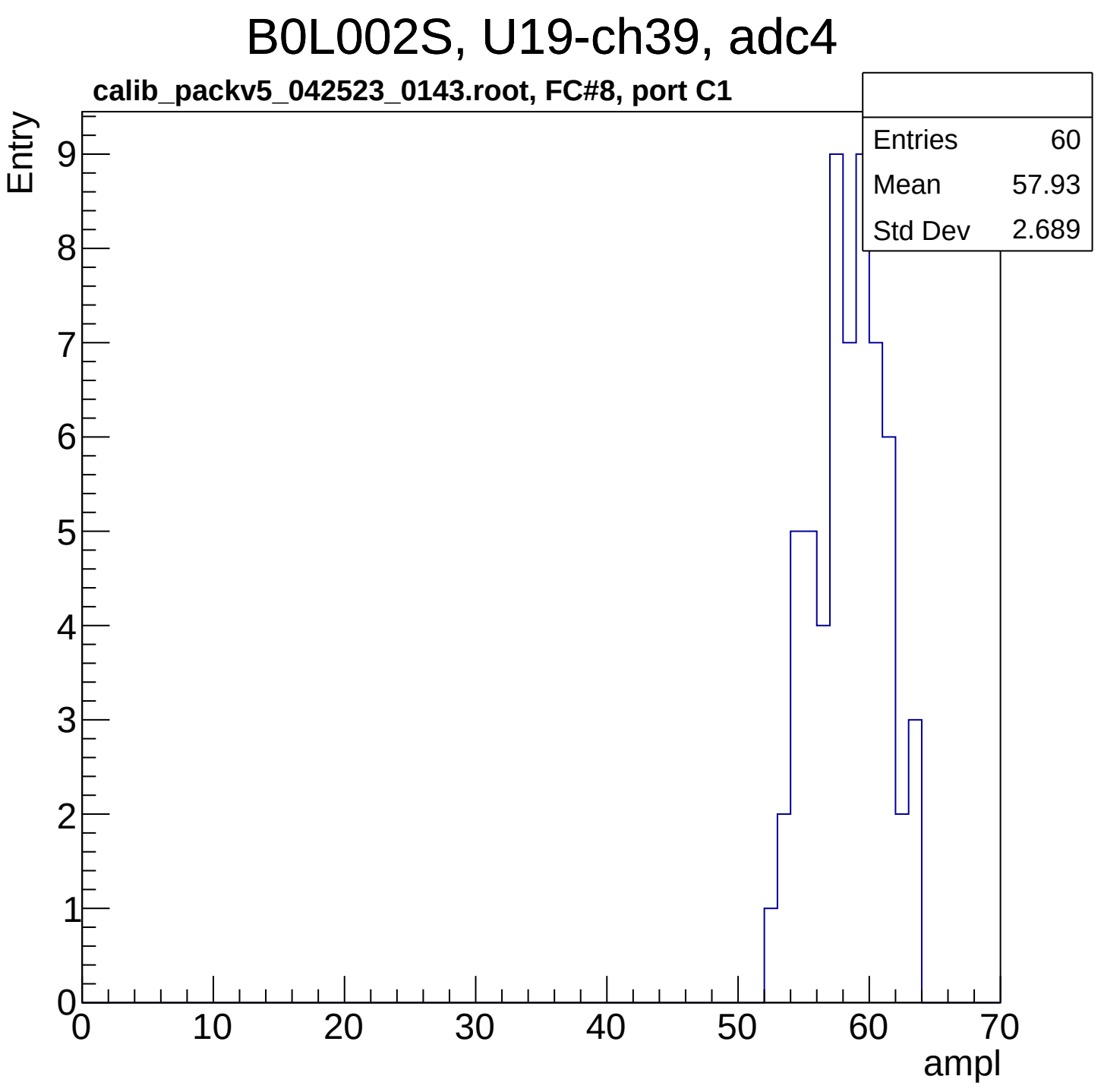

B0L002S, U19-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

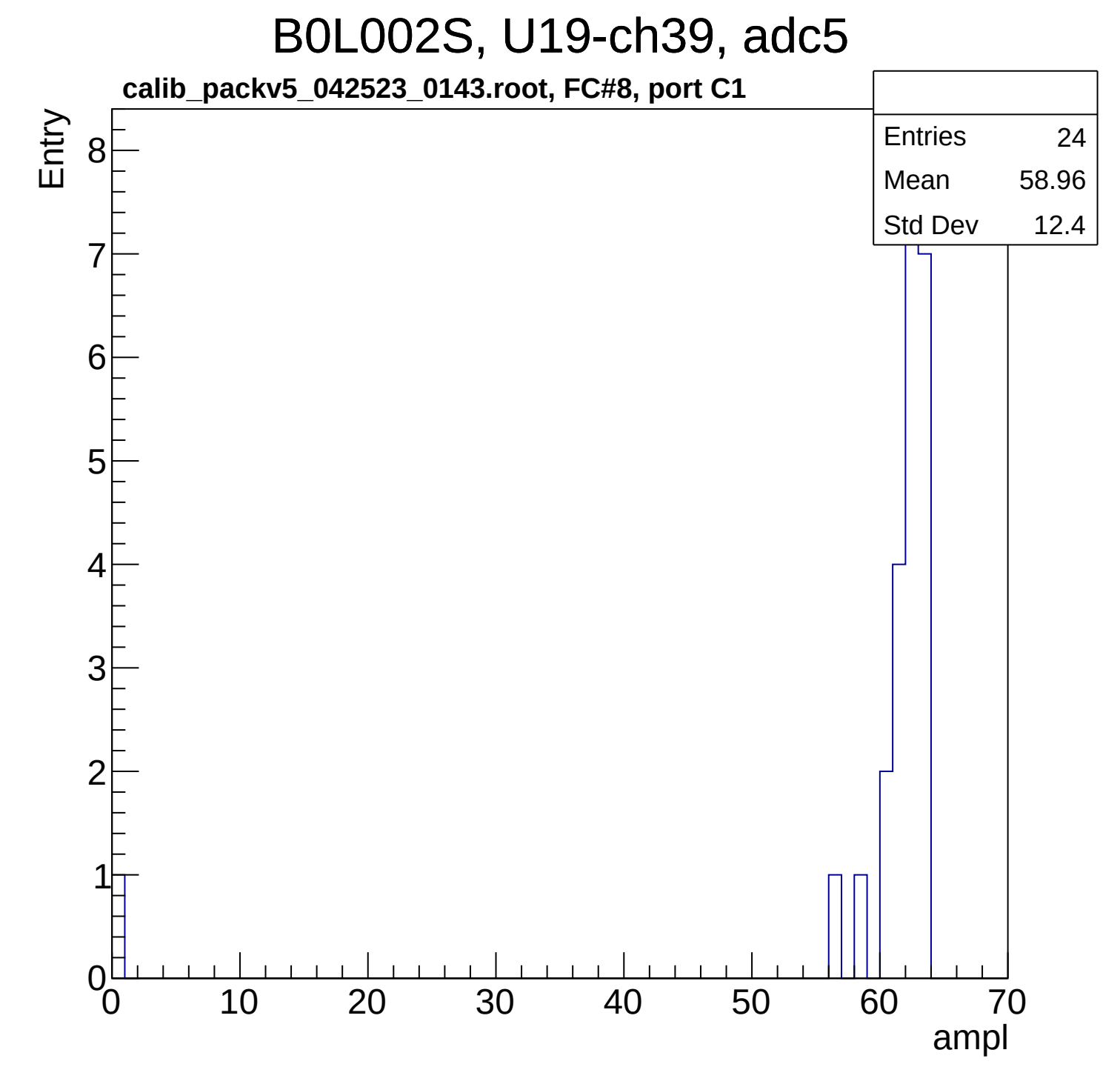

B0L002S, U19-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 30.61
Std Dev 4.907
Gaus mean : 31.2103
Gaus Width: 4.4170
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

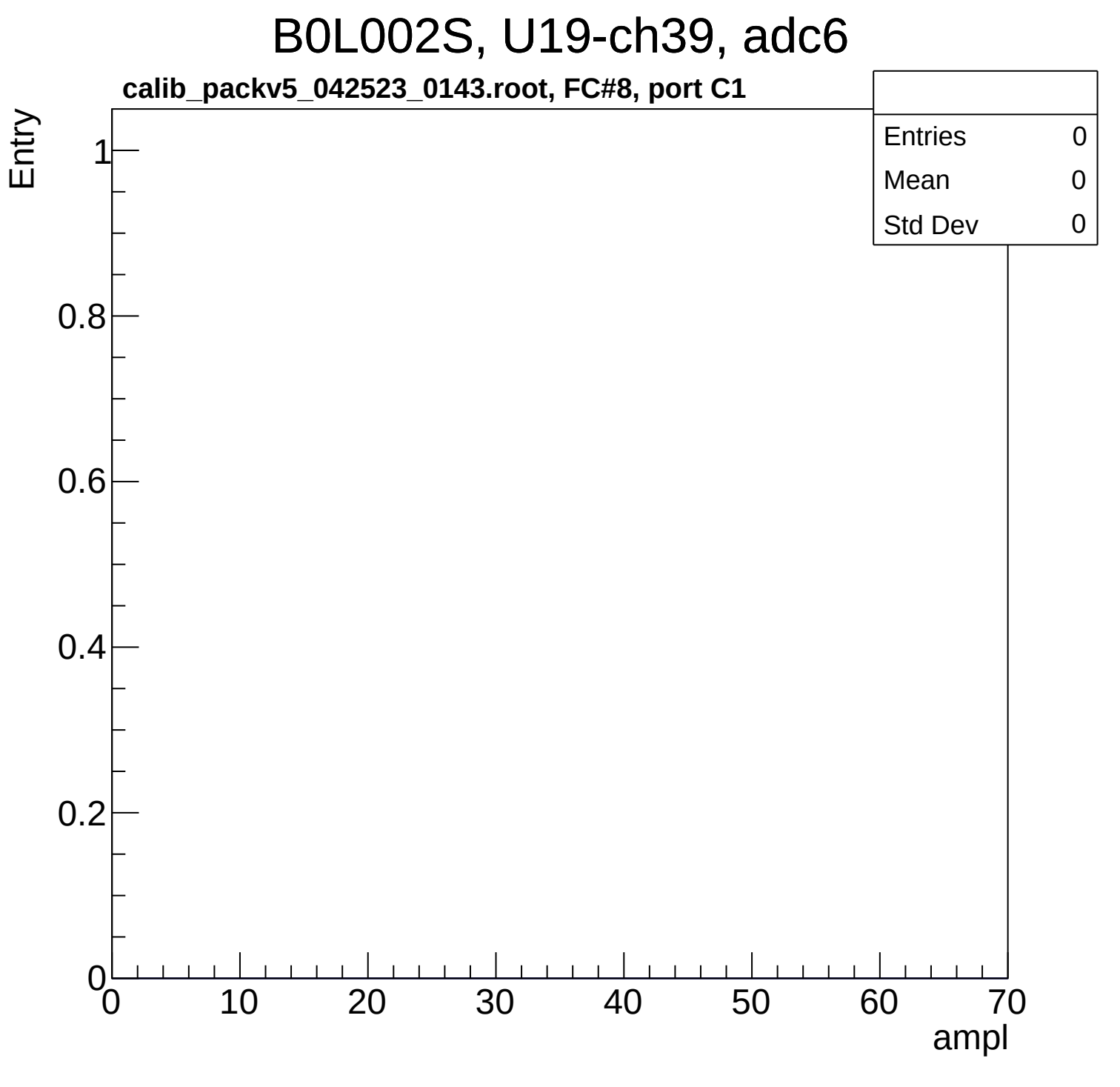

B0L002S, U19-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.62
Std Dev 3.204
Gaus mean : 39.1853
Gaus Width: 3.5321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

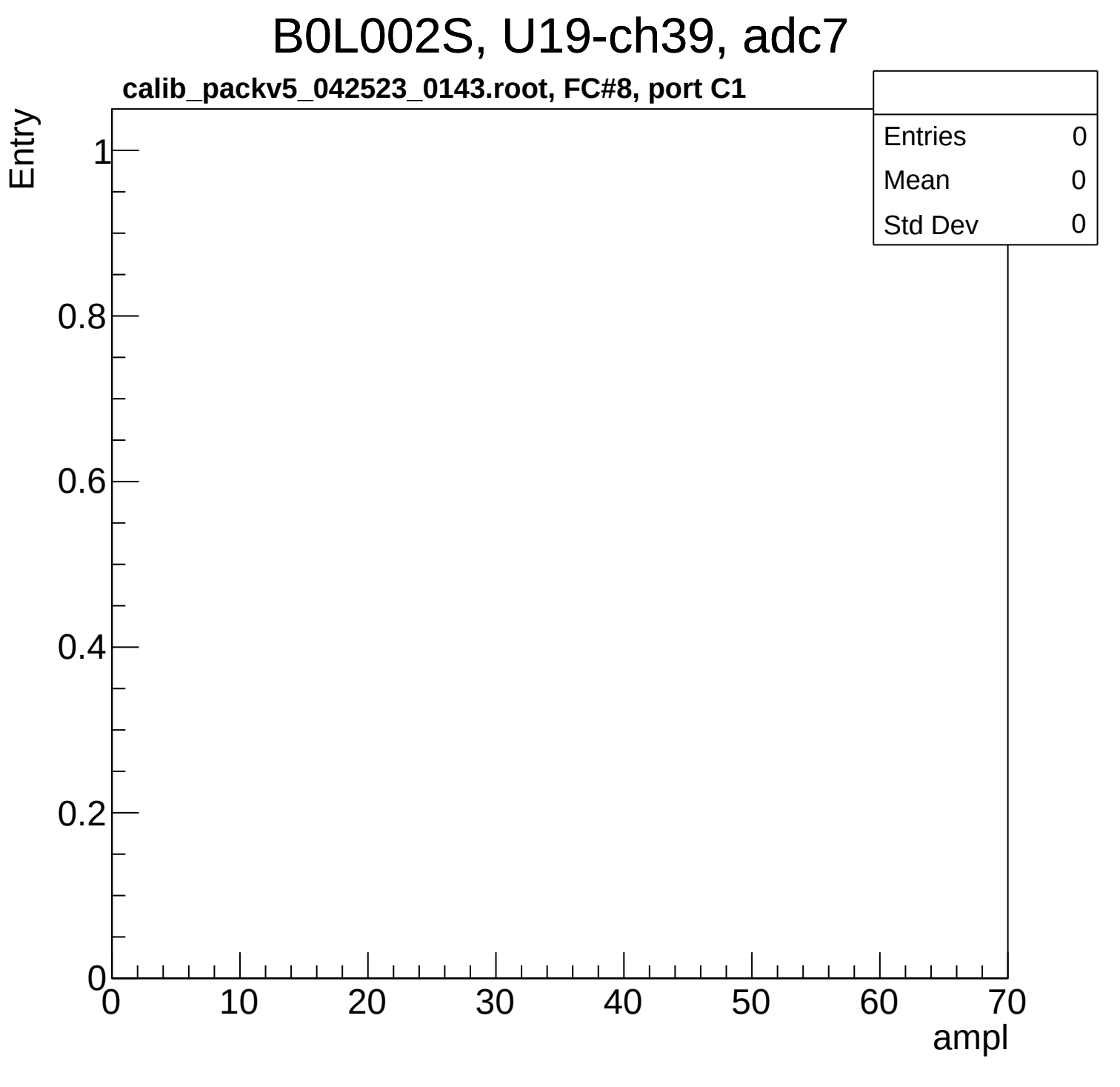

B0L002S, U19-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.39
Std Dev 3.442
Gaus mean : 45.6230
Gaus Width: 4.1842
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

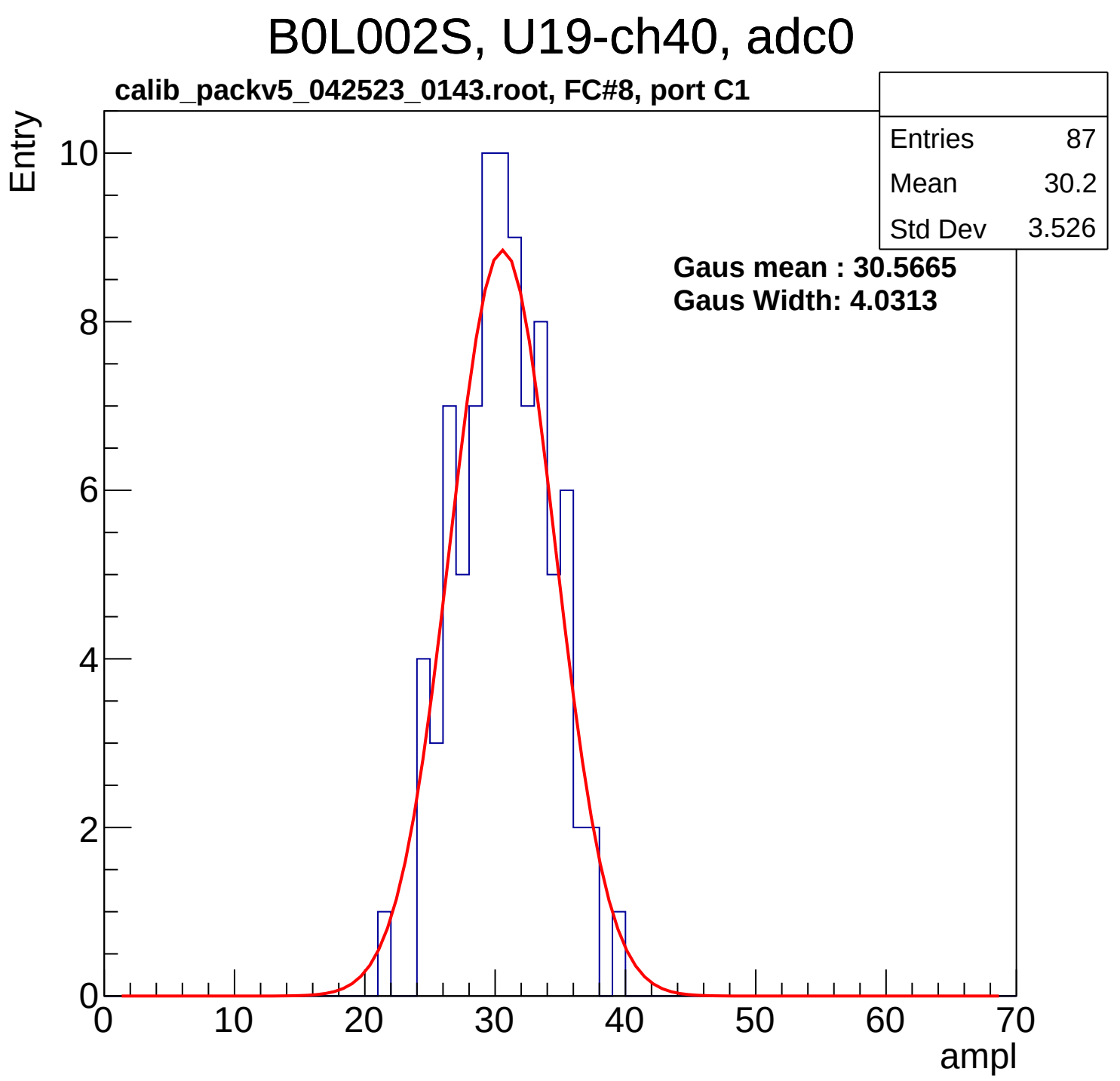

B0L002S, U19-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 50.98
Std Dev 3.807
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

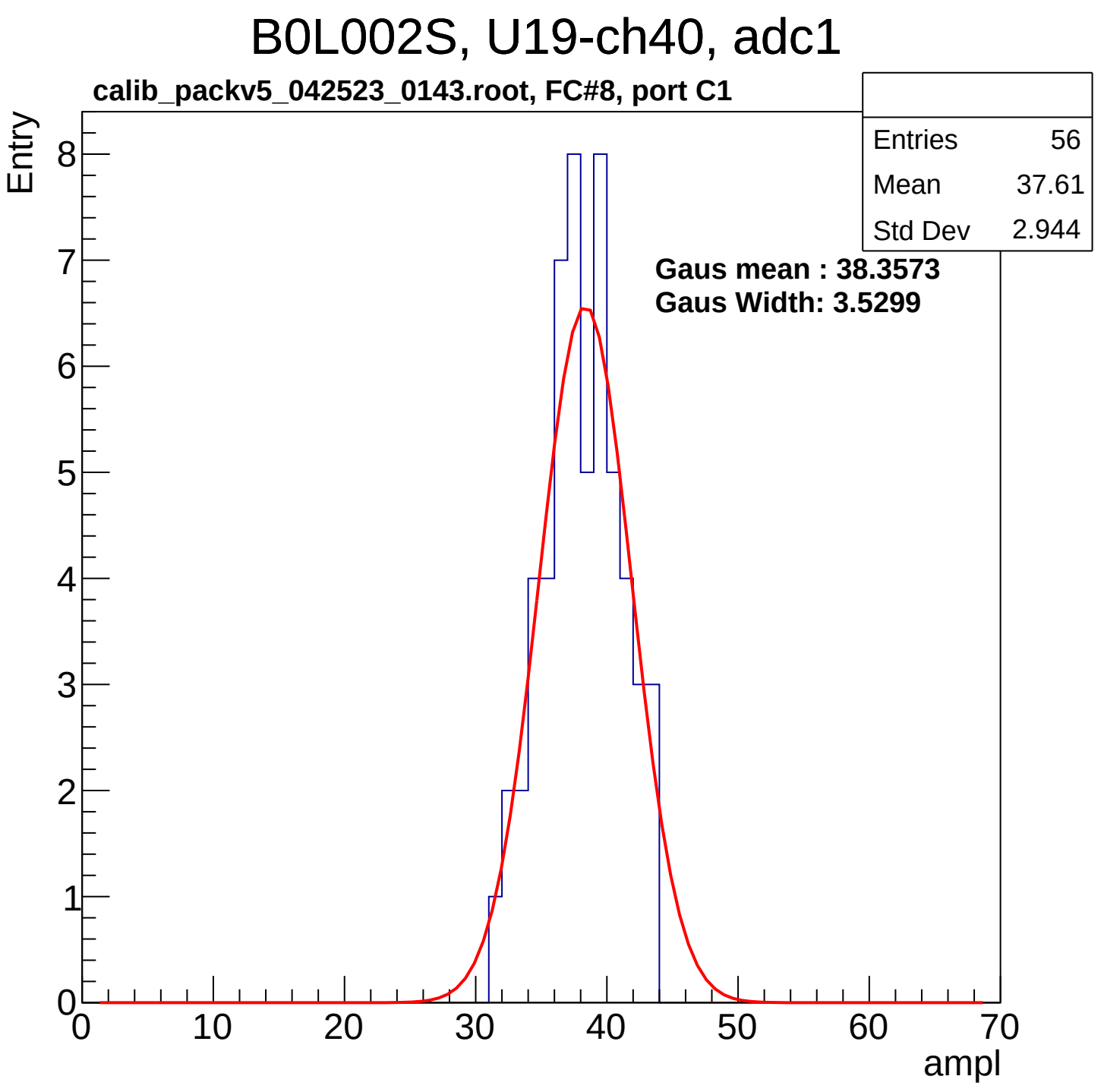

B0L002S, U19-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.93
Std Dev 2.689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

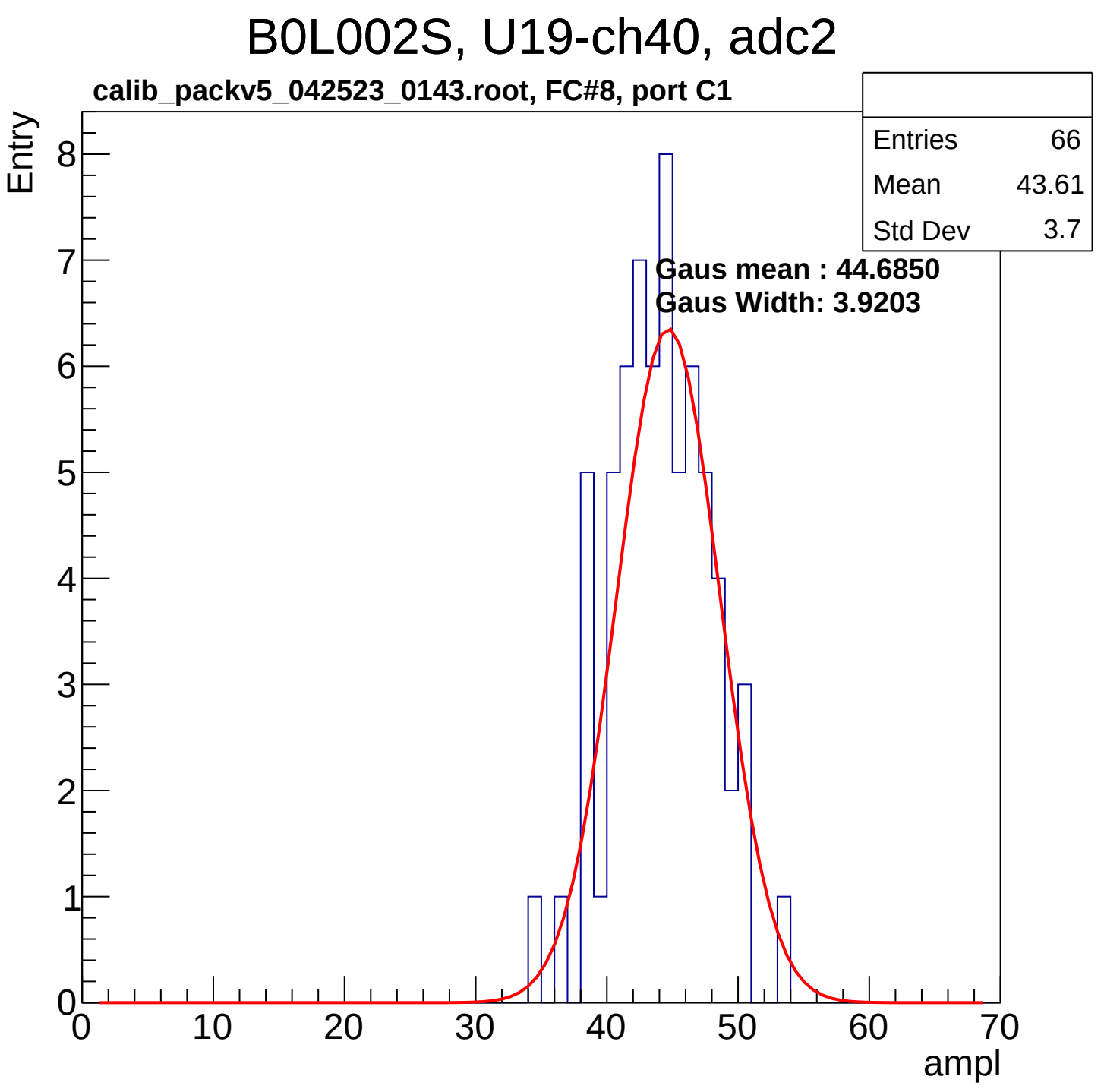

B0L002S, U19-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.96
Std Dev 12.4
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

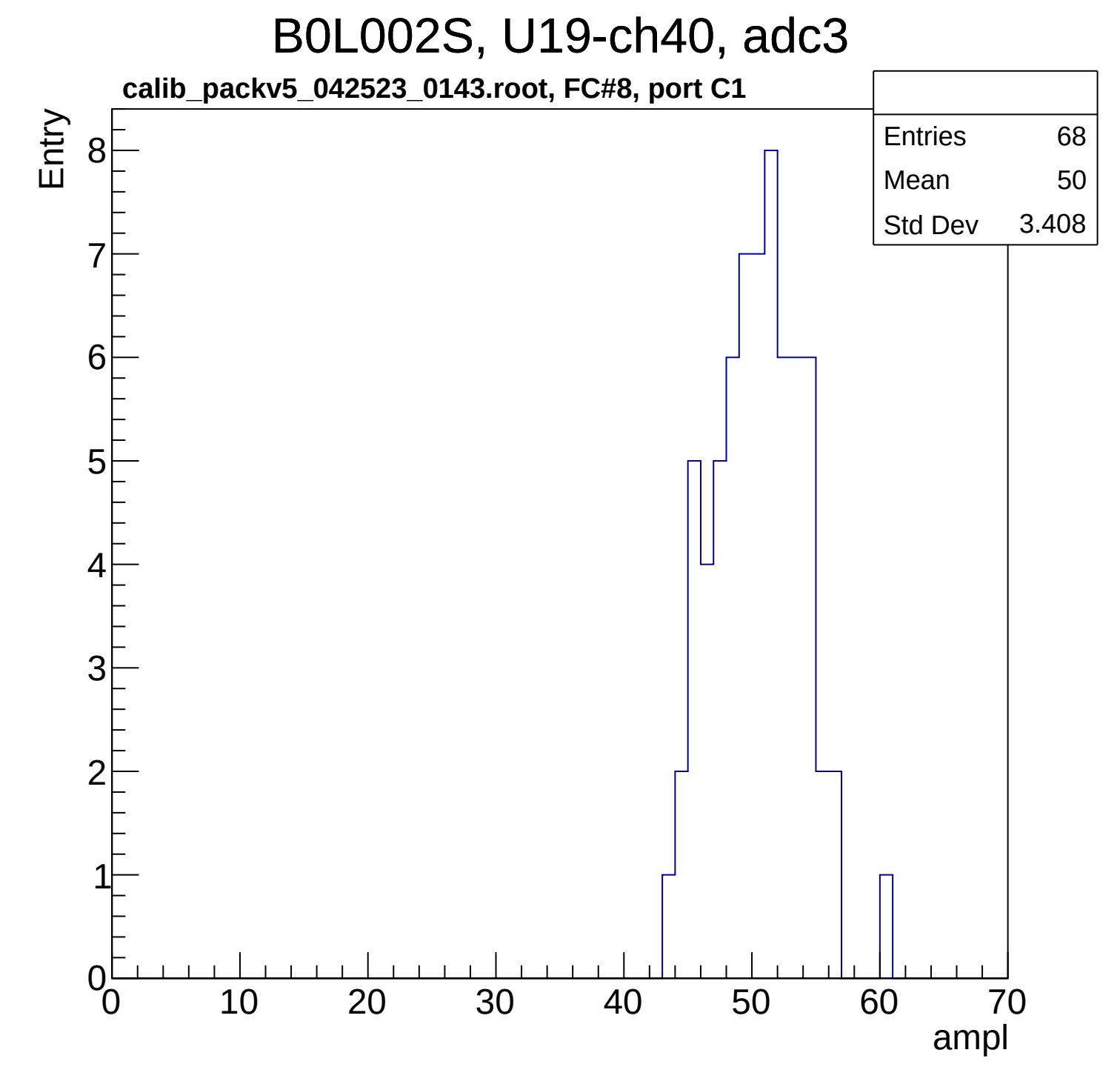

B0L002S, U19-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

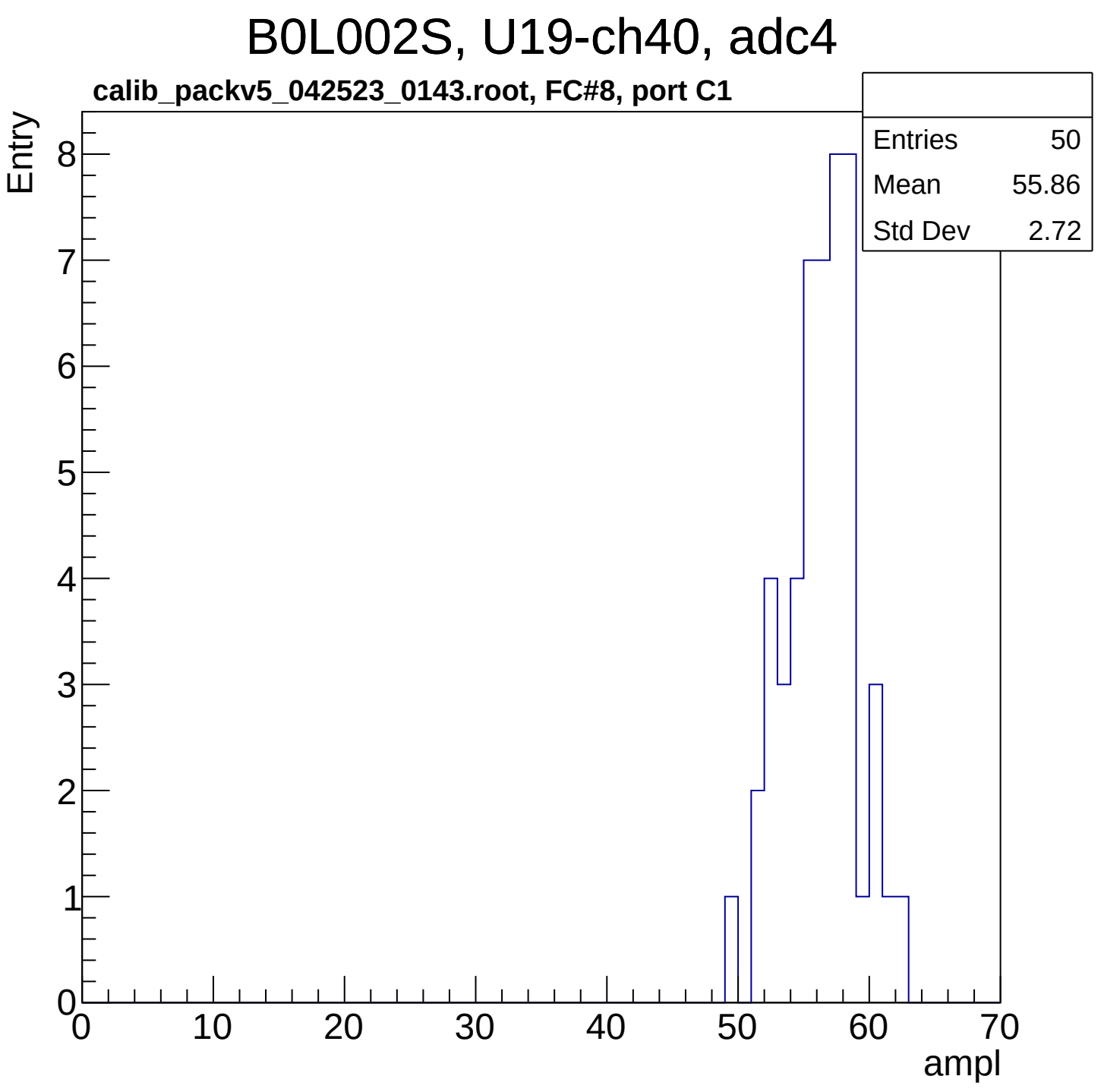

B0L002S, U19-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

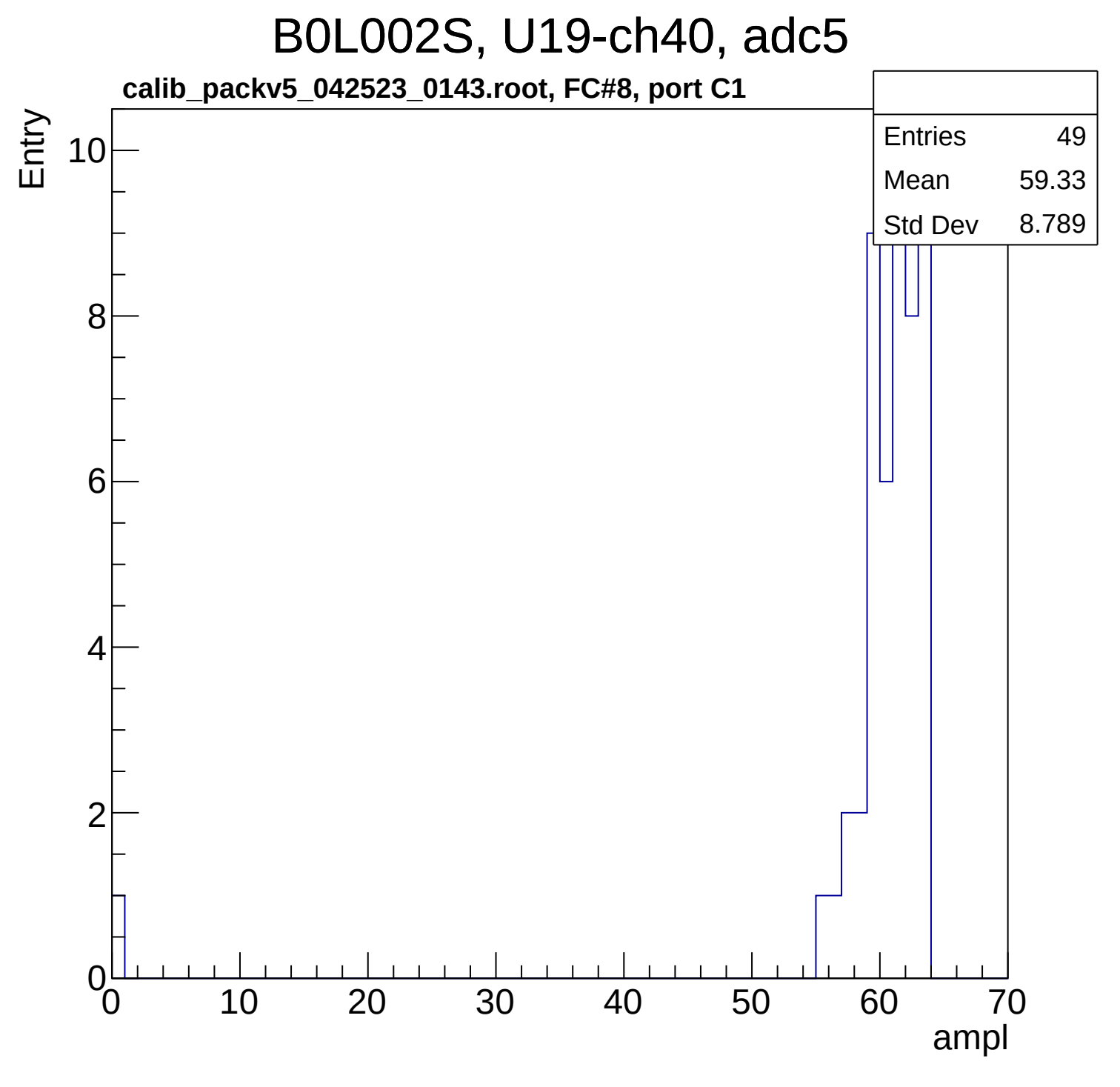

B0L002S, U19-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 30.2
Std Dev 3.526
Gaus mean : 30.5665
Gaus Width: 4.0313
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

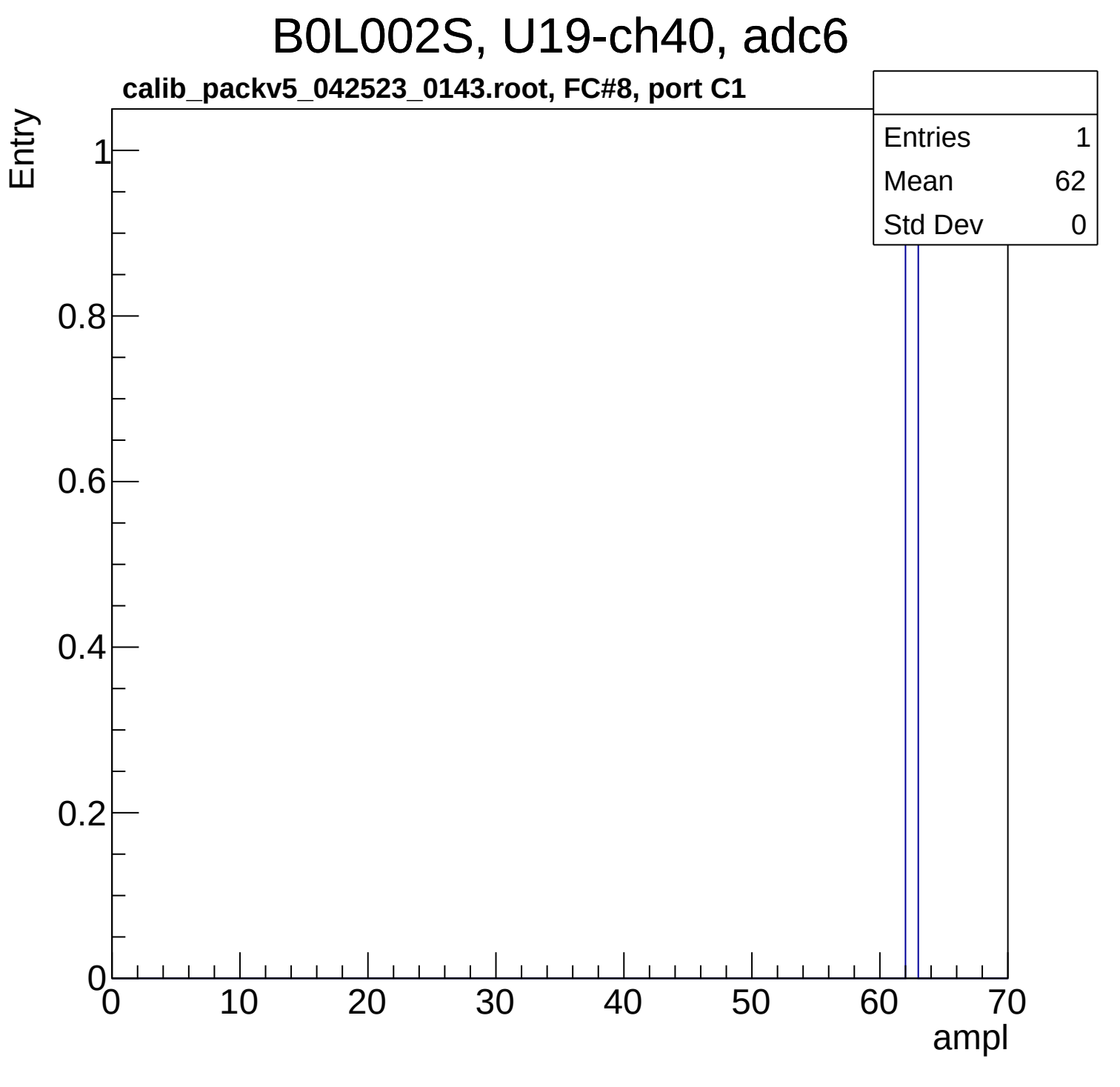

B0L002S, U19-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 37.61
Std Dev 2.944
Gaus mean : 38.3573
Gaus Width: 3.5299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

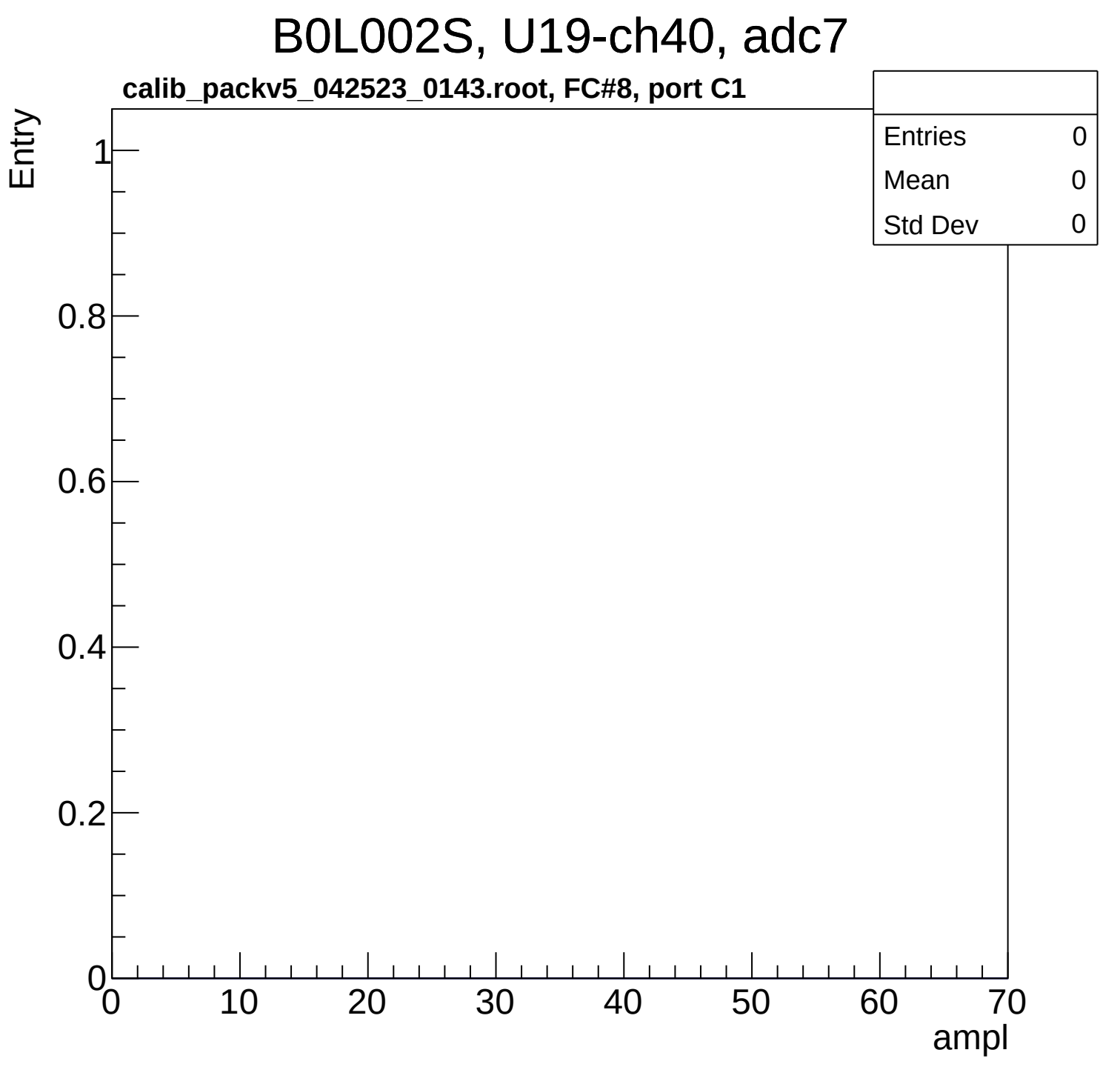

B0L002S, U19-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.61
Std Dev 3.7
Gaus mean : 44.6850
Gaus Width: 3.9203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

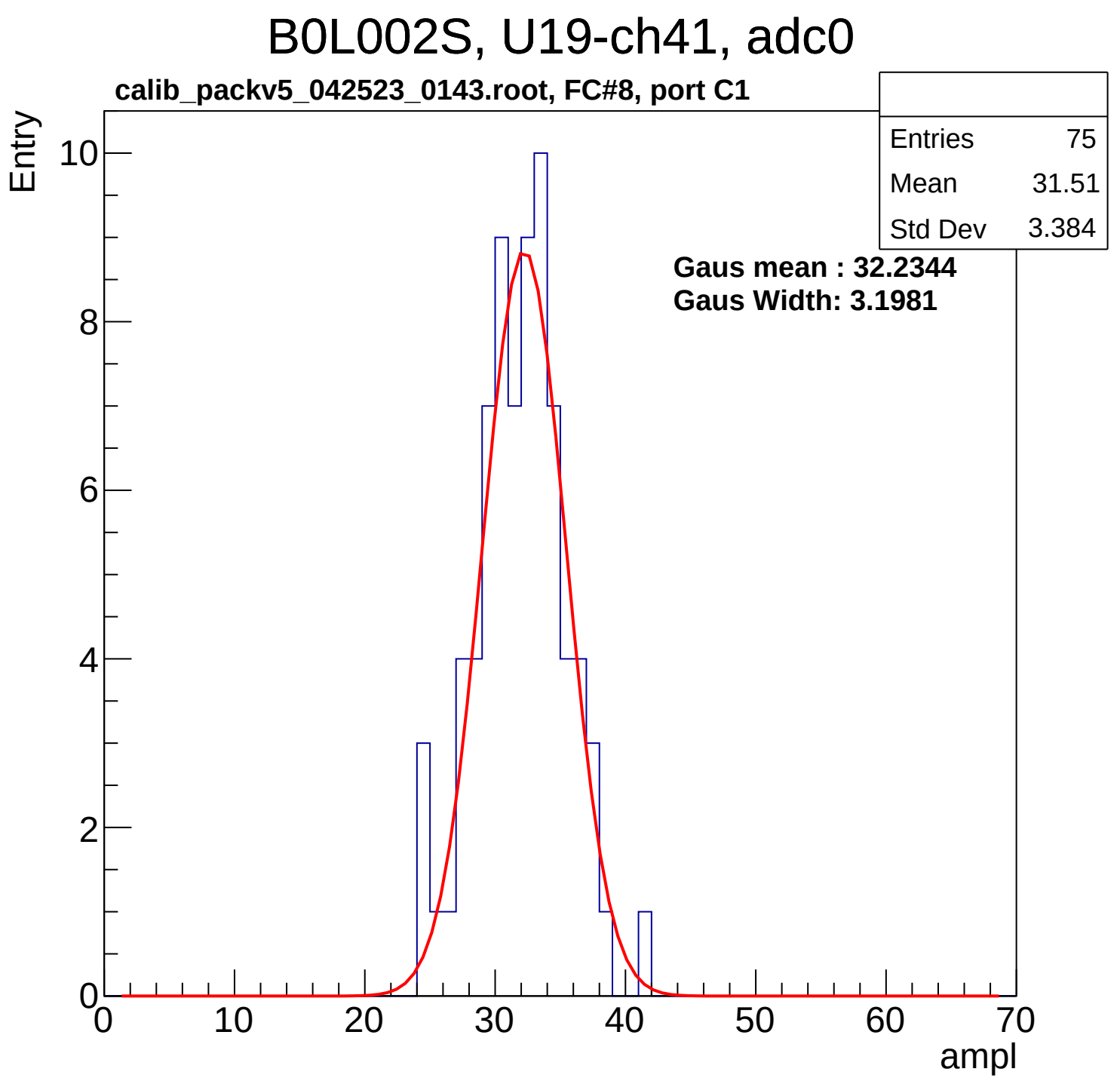

B0L002S, U19-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50
Std Dev 3.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

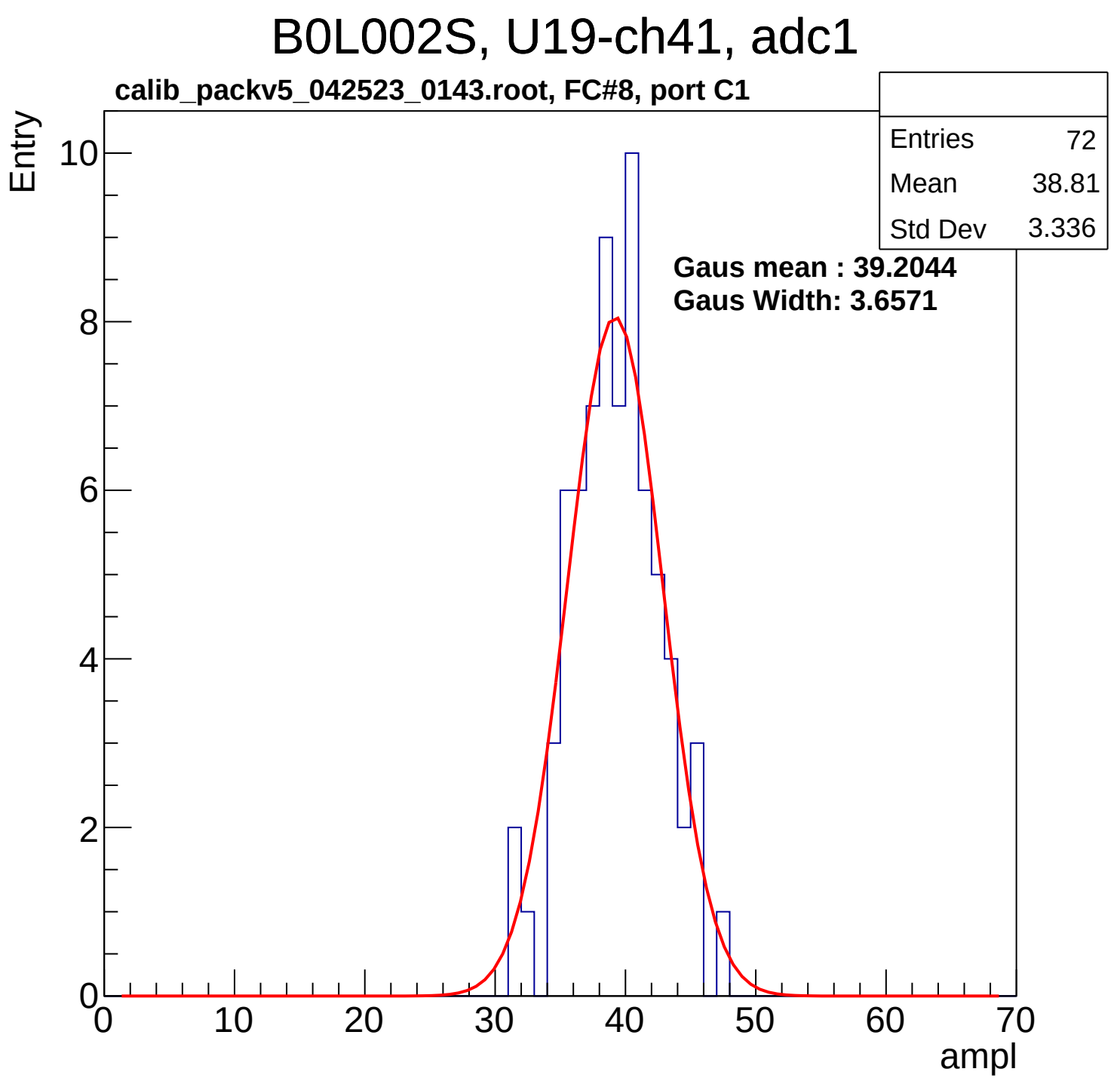

B0L002S, U19-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 55.86
Std Dev 2.72
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

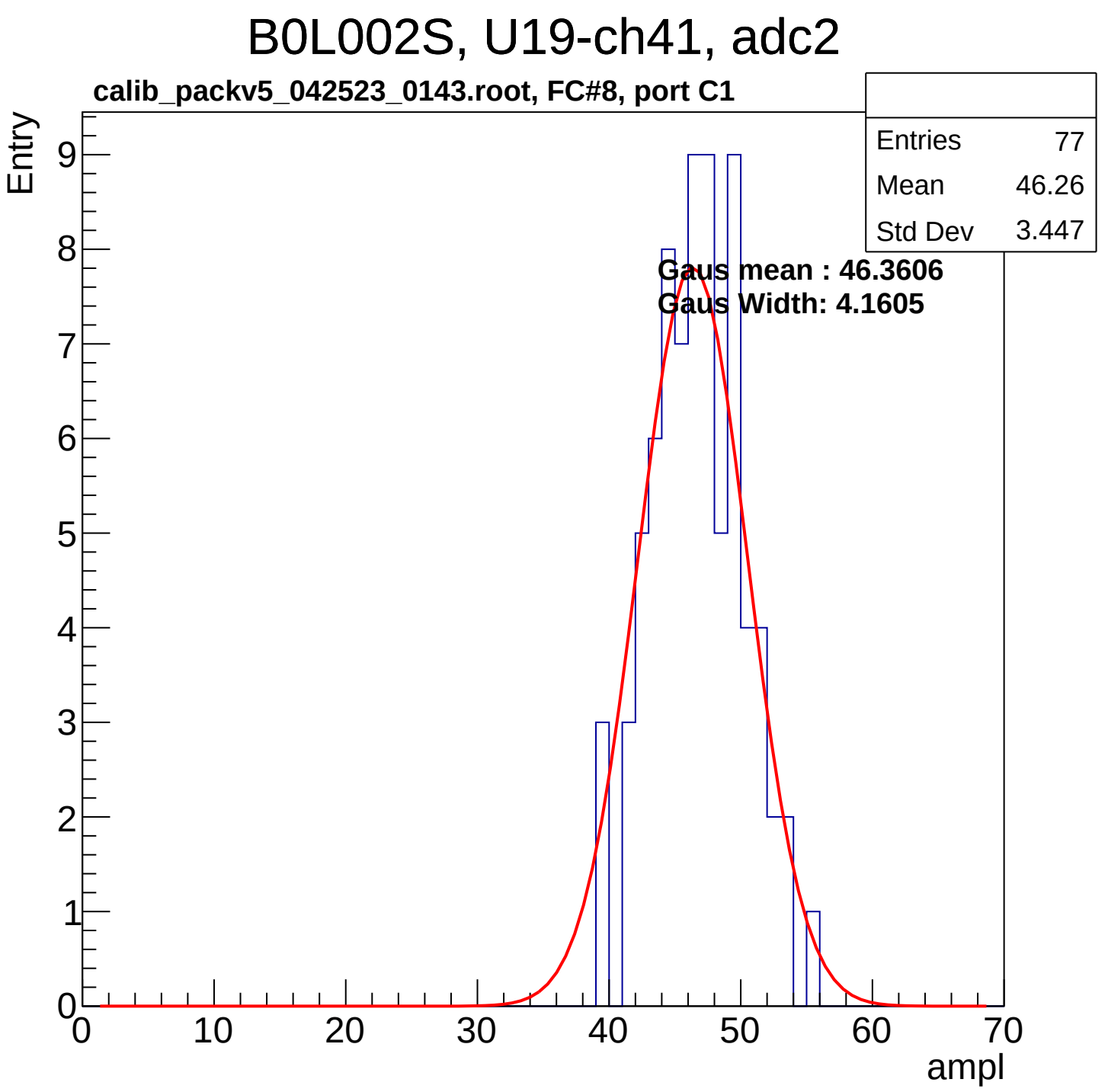

B0L002S, U19-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.33
Std Dev 8.789
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

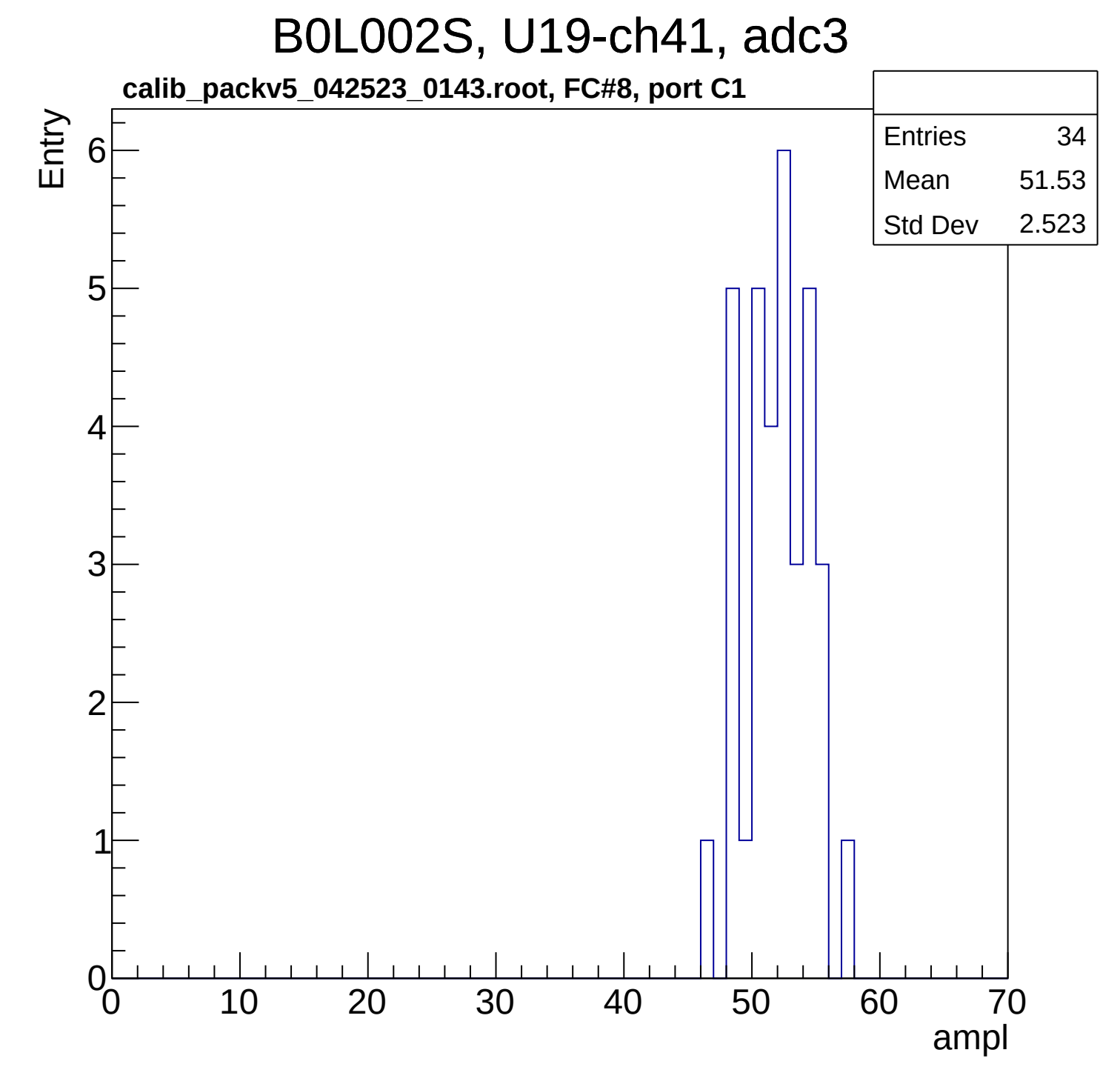

B0L002S, U19-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

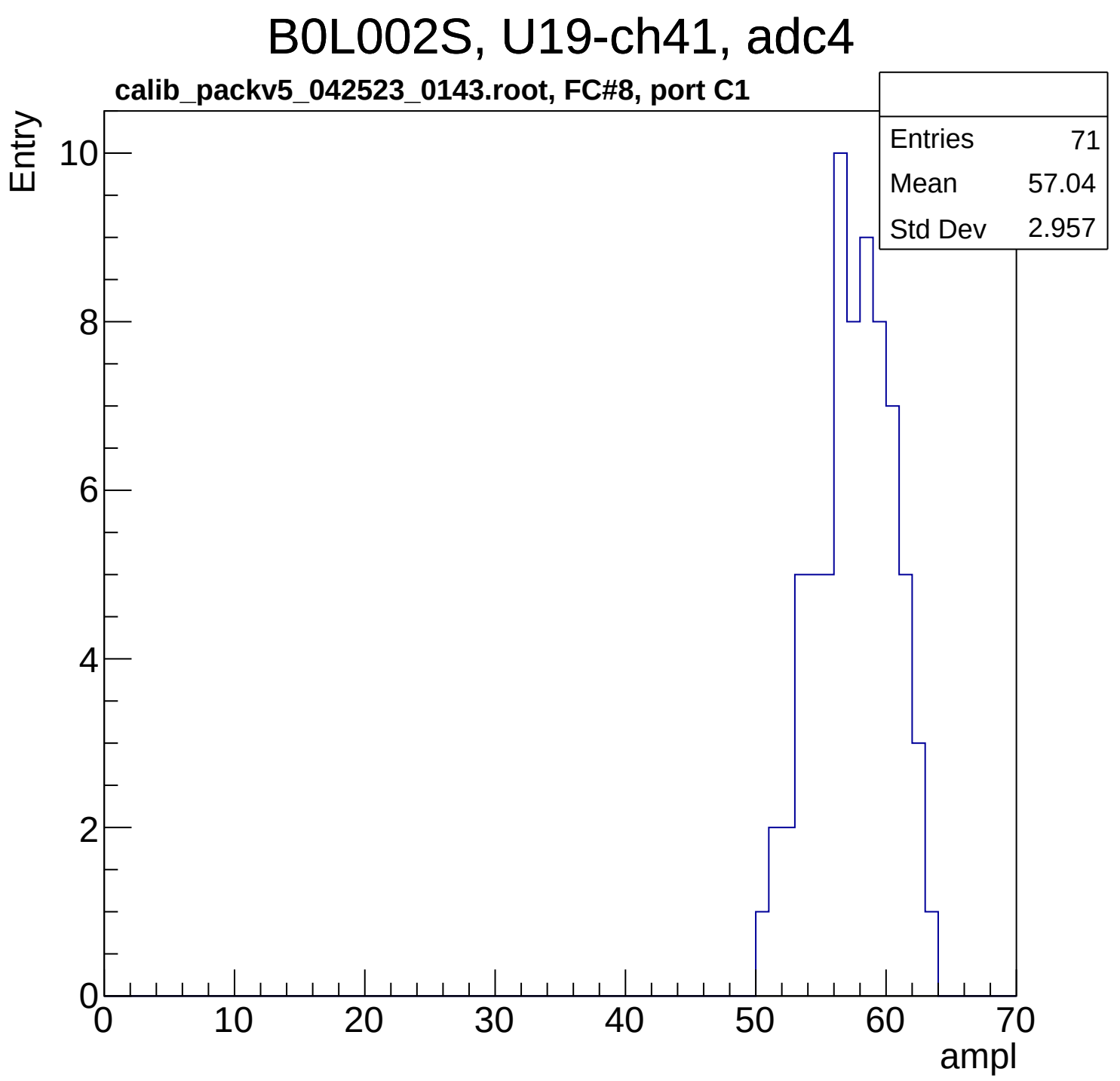

B0L002S, U19-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

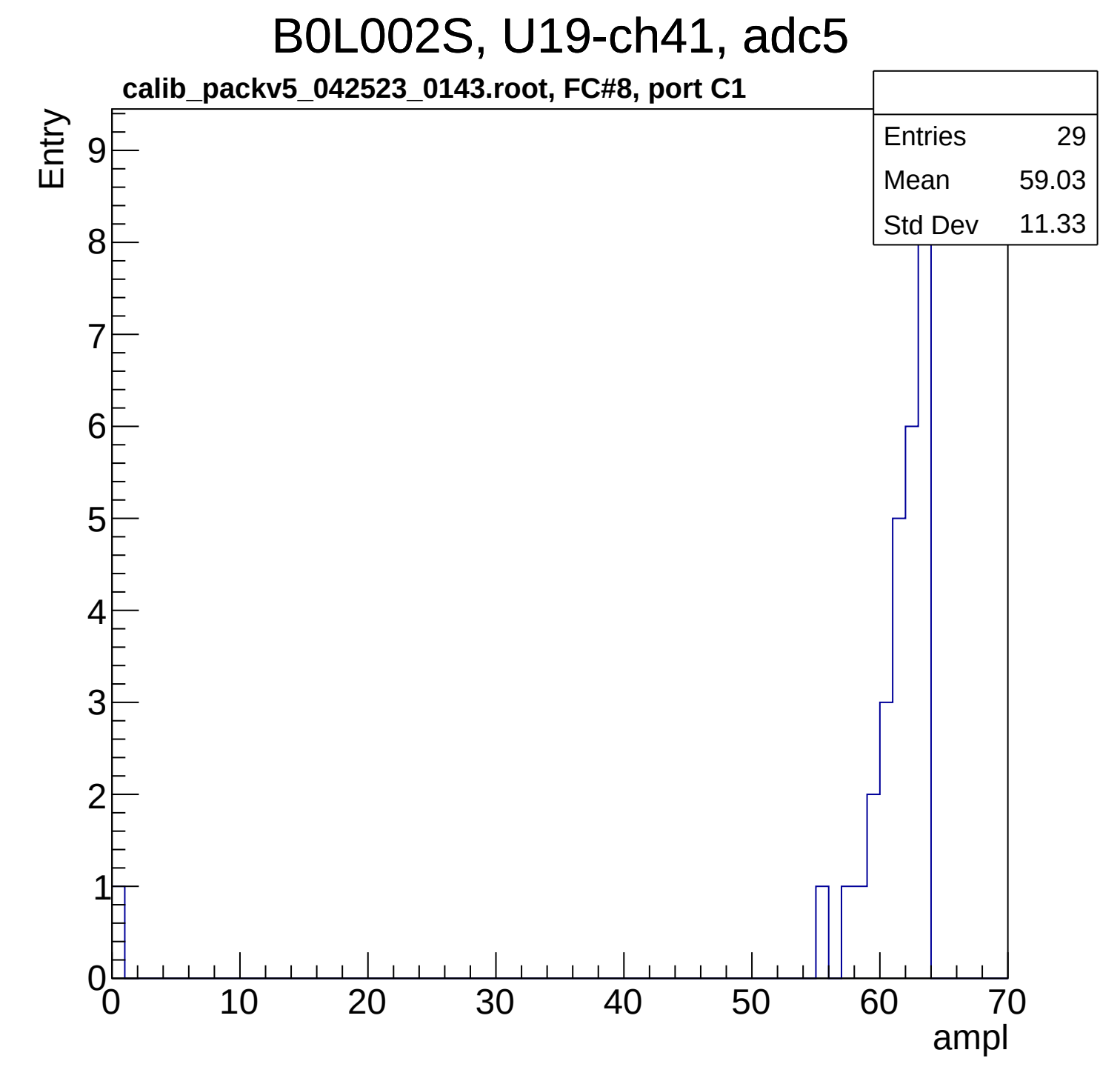

B0L002S, U19-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.51
Std Dev 3.384
Gaus mean : 32.2344
Gaus Width: 3.1981
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

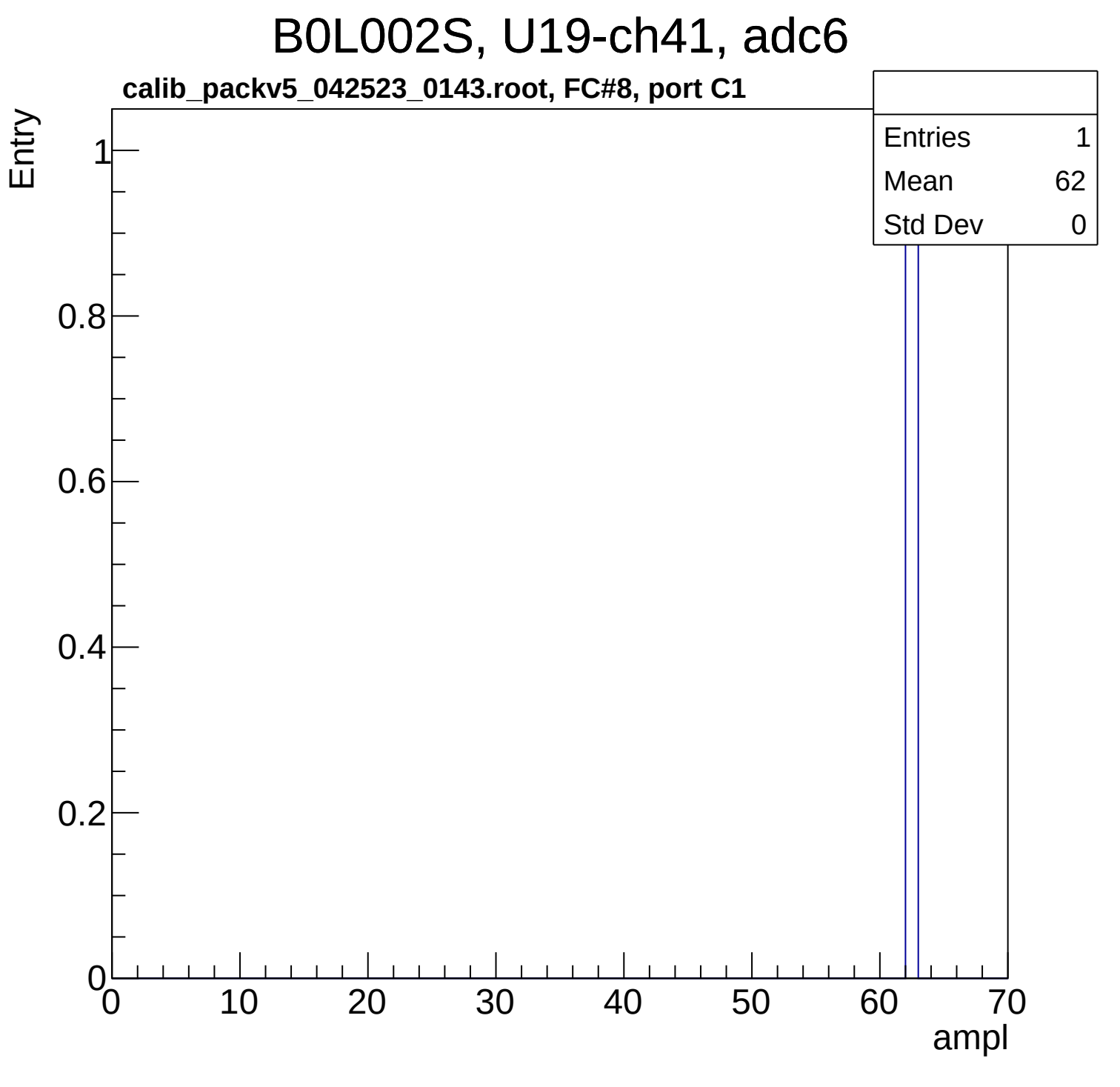

B0L002S, U19-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.81
Std Dev 3.336
Gaus mean : 39.2044
Gaus Width: 3.6571
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

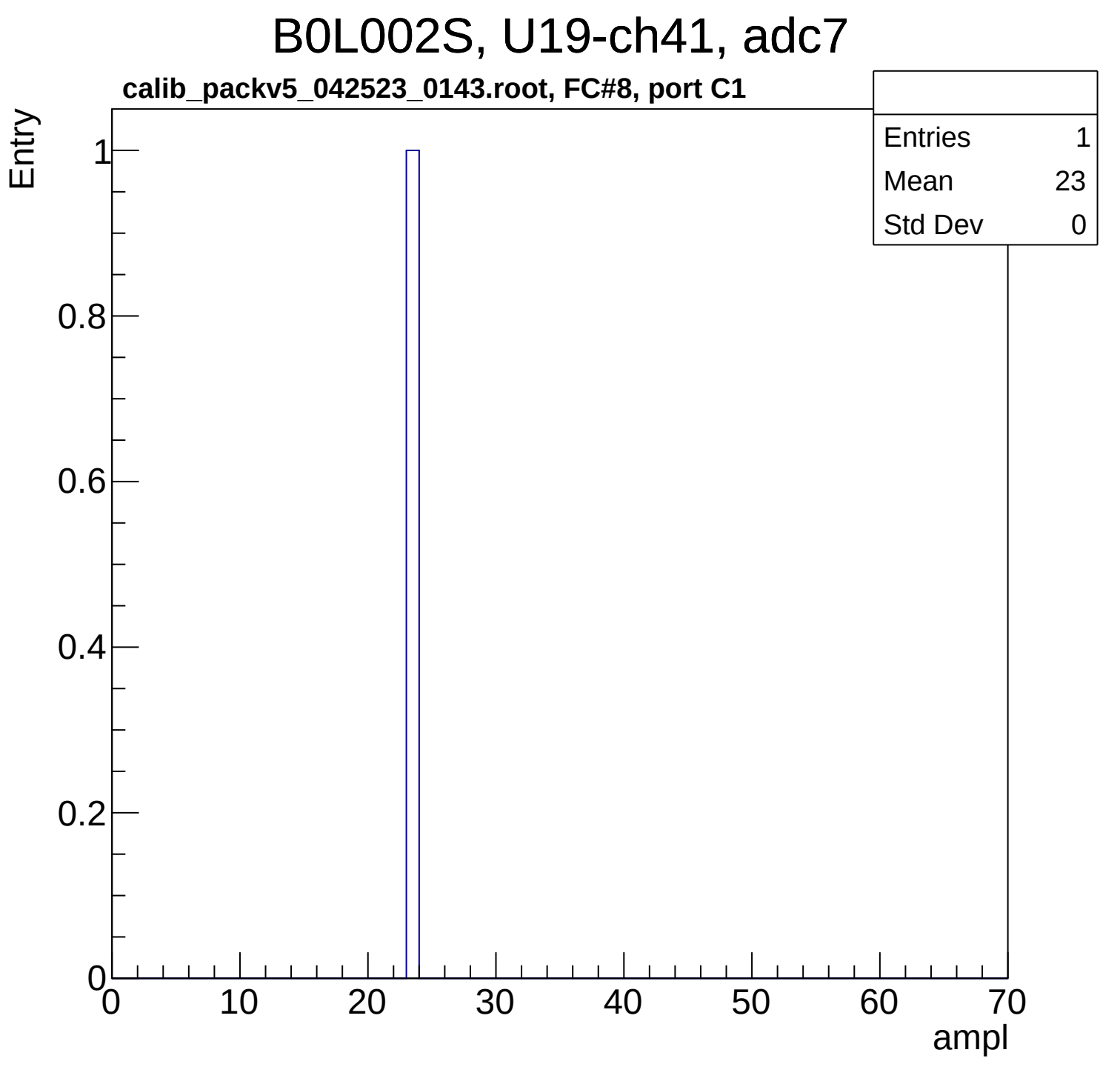

B0L002S, U19-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 46.26
Std Dev 3.447
Gaus mean : 46.3606
Gaus Width: 4.1605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

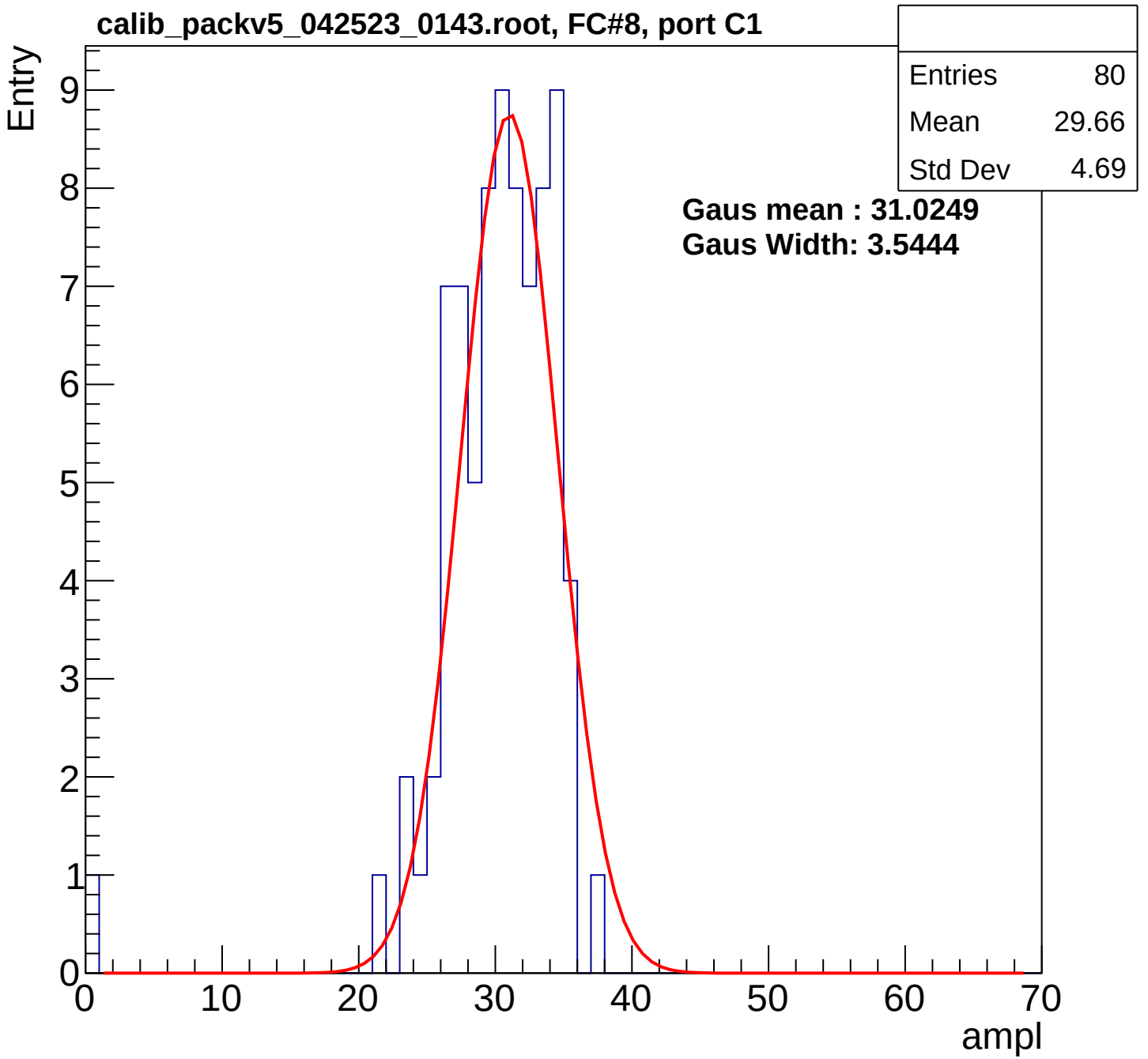

B0L002S, U19-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 51.53
Std Dev 2.523
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

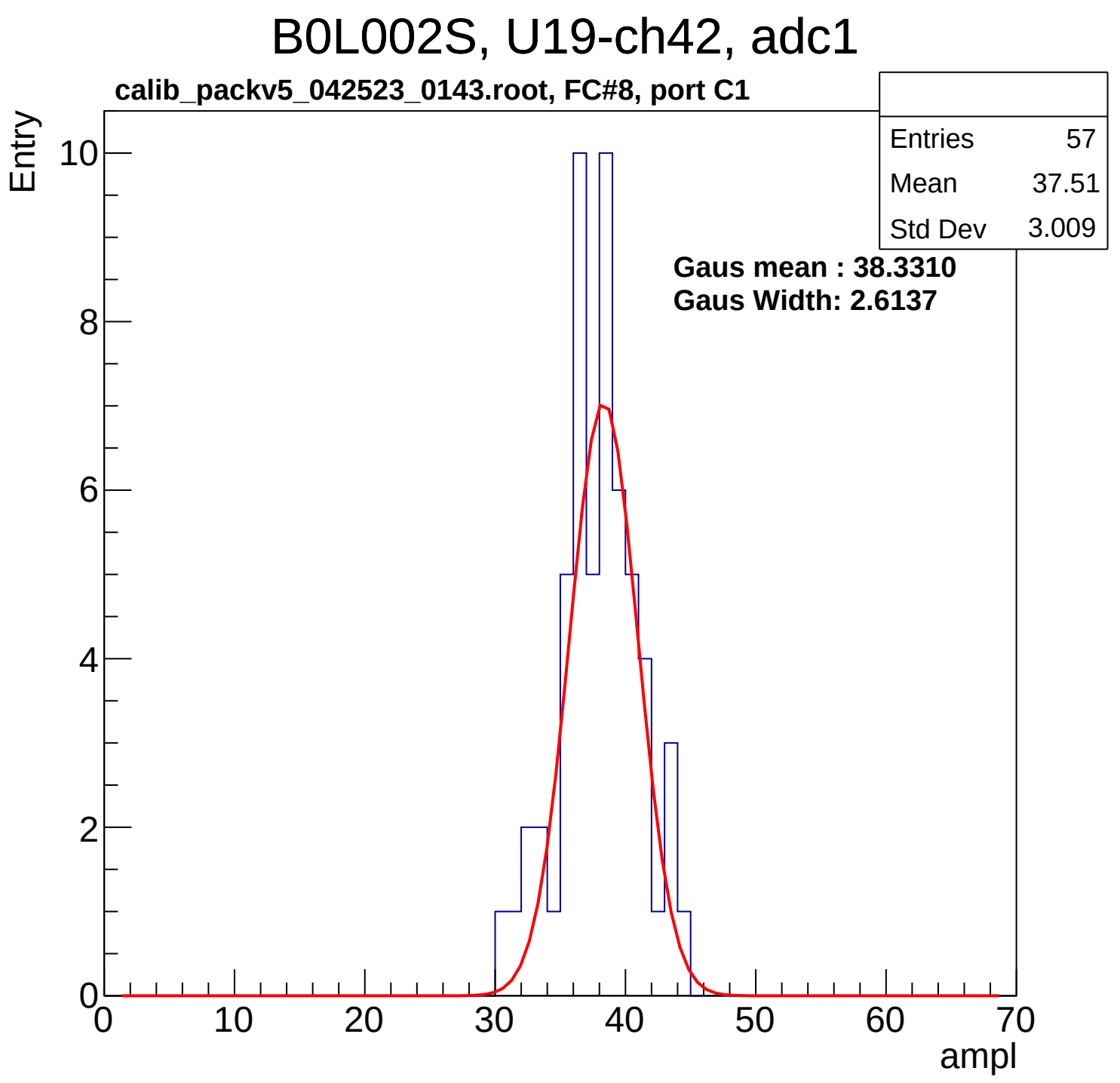

B0L002S, U19-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 57.04
Std Dev 2.957
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

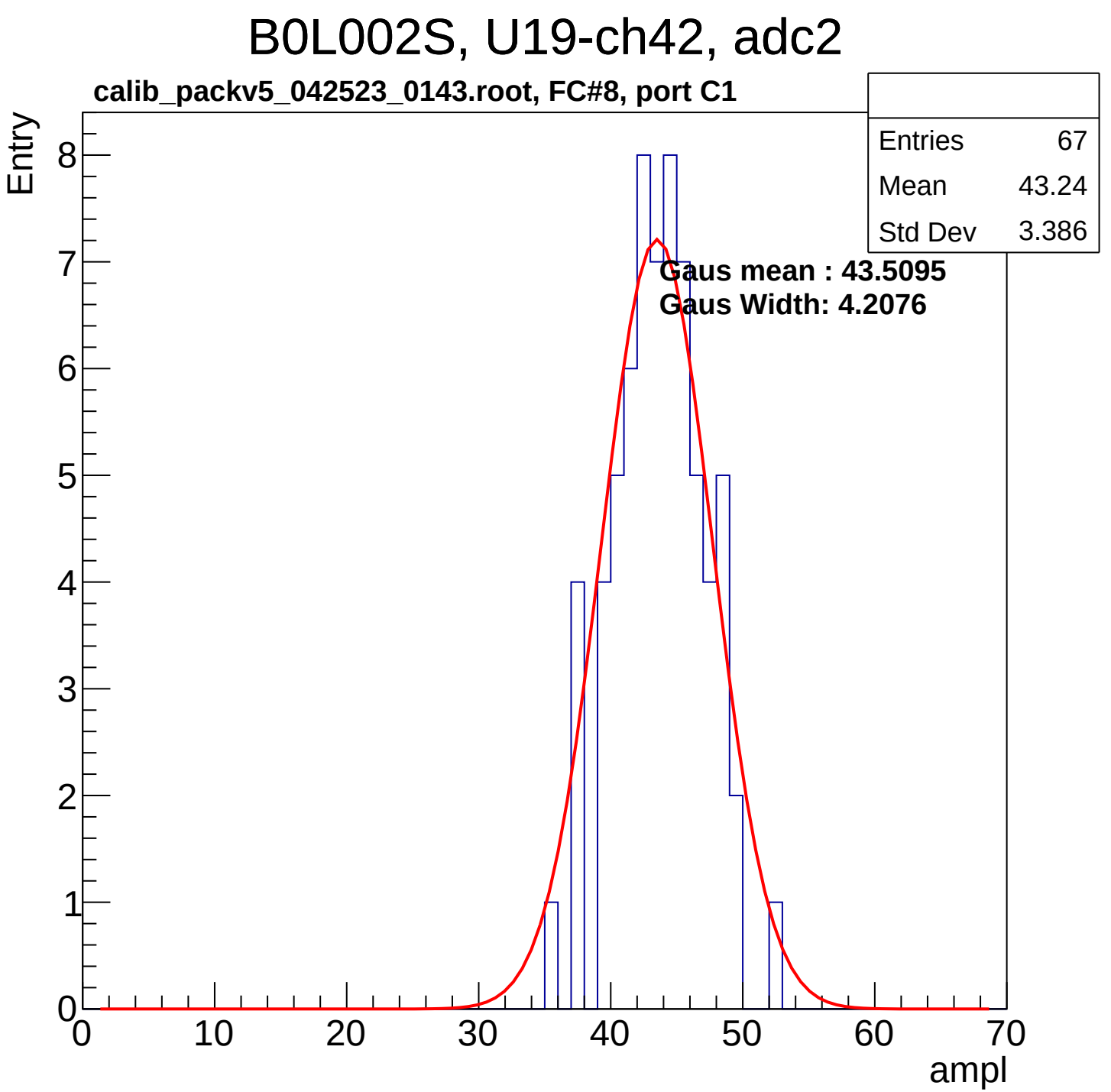

B0L002S, U19-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.03
Std Dev 11.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

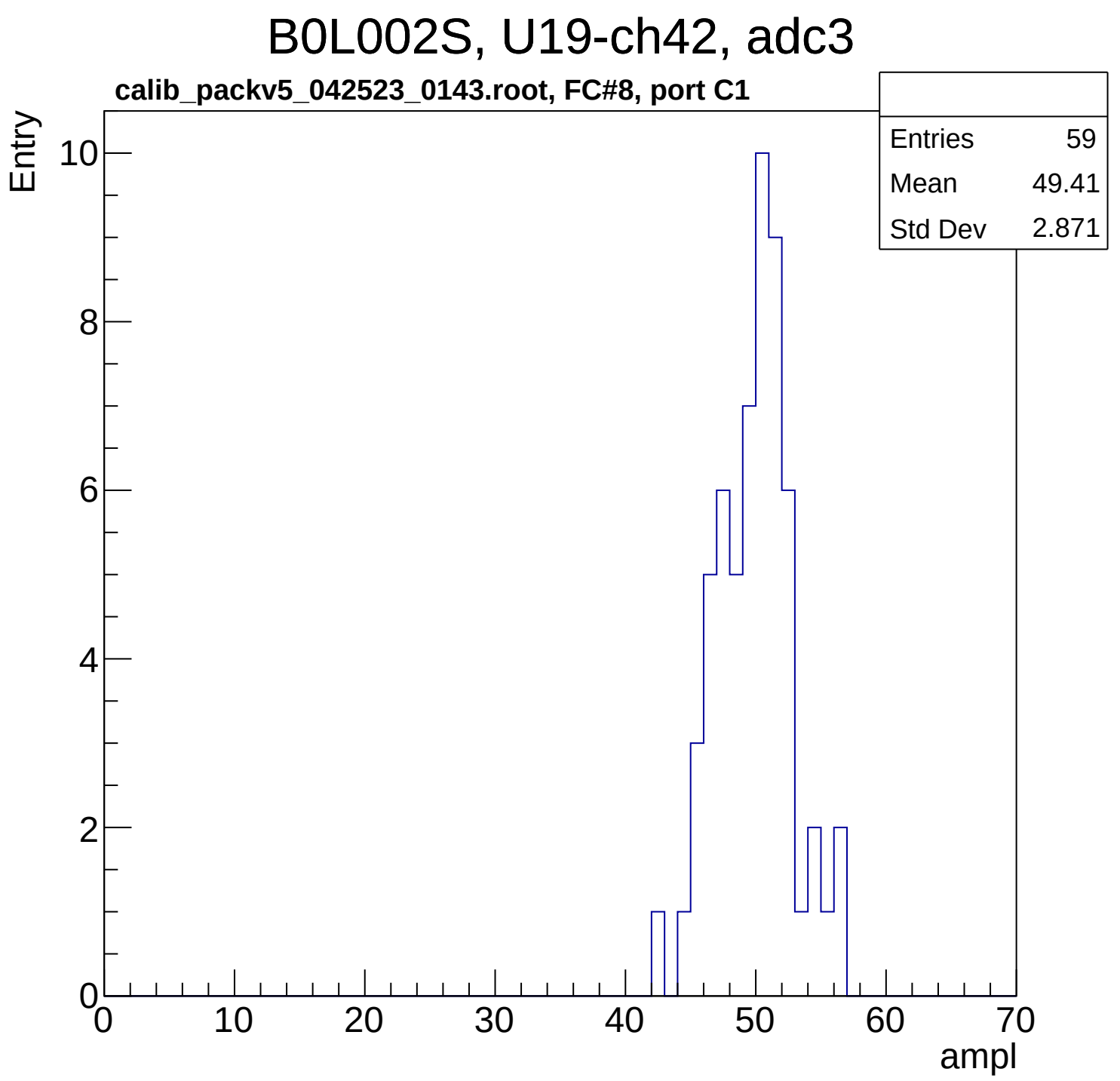

B0L002S, U19-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

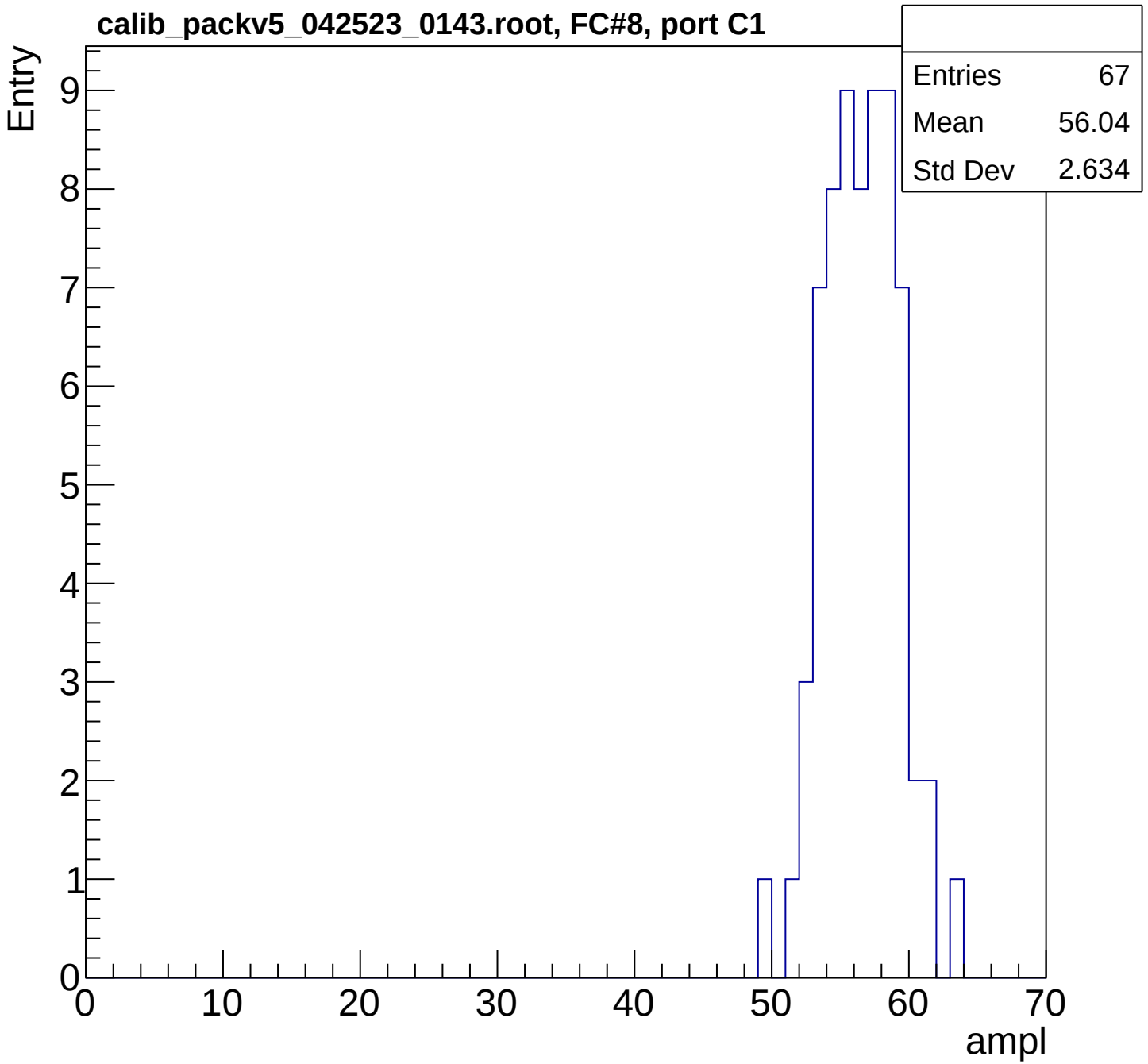

B0L002S, U19-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

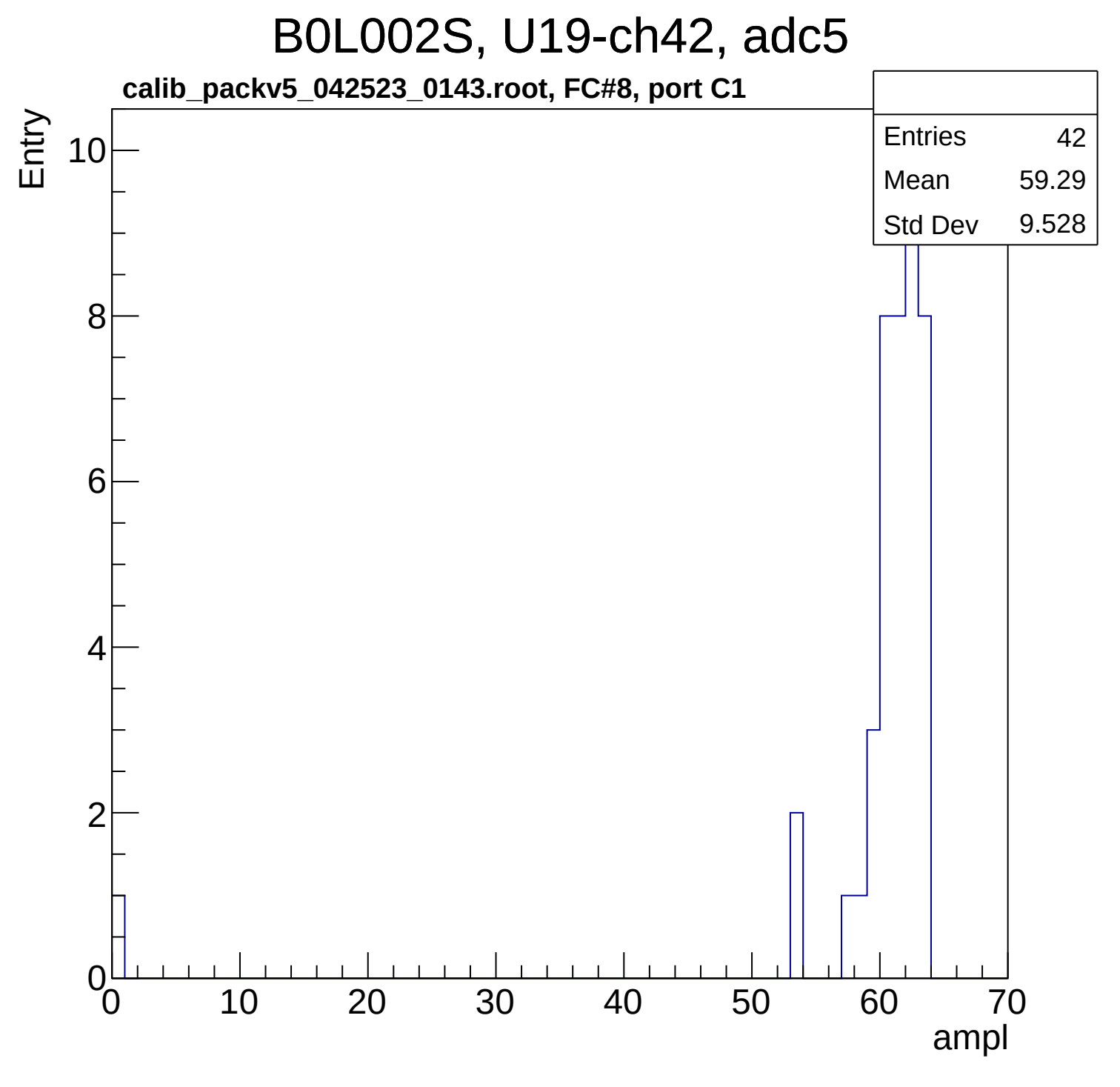

B0L002S, U19-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 29.66
Std Dev 4.69
Gaus mean : 31.0249
Gaus Width: 3.5444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

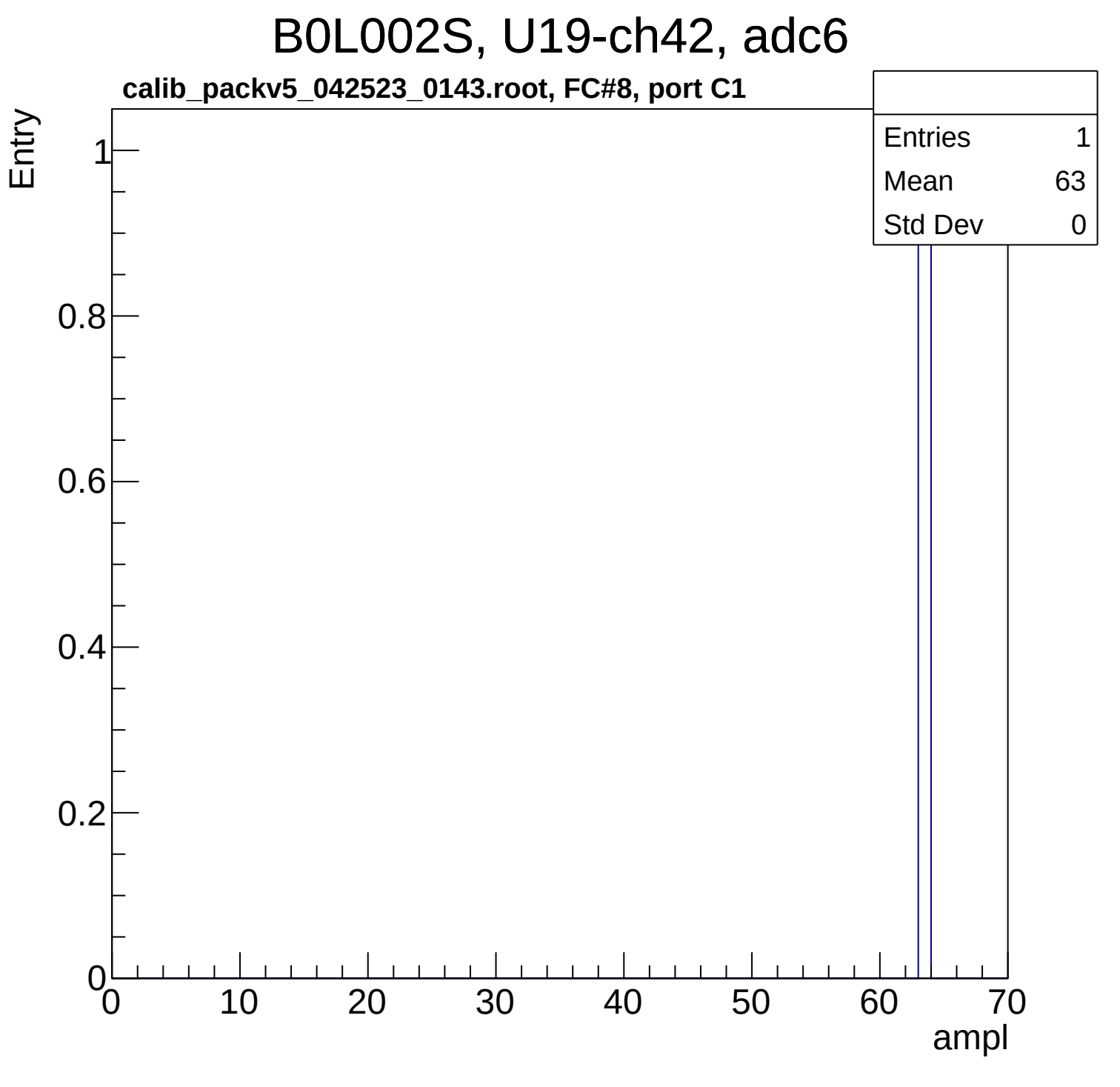

B0L002S, U19-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.51
Std Dev 3.009
Gaus mean : 38.3310
Gaus Width: 2.6137
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

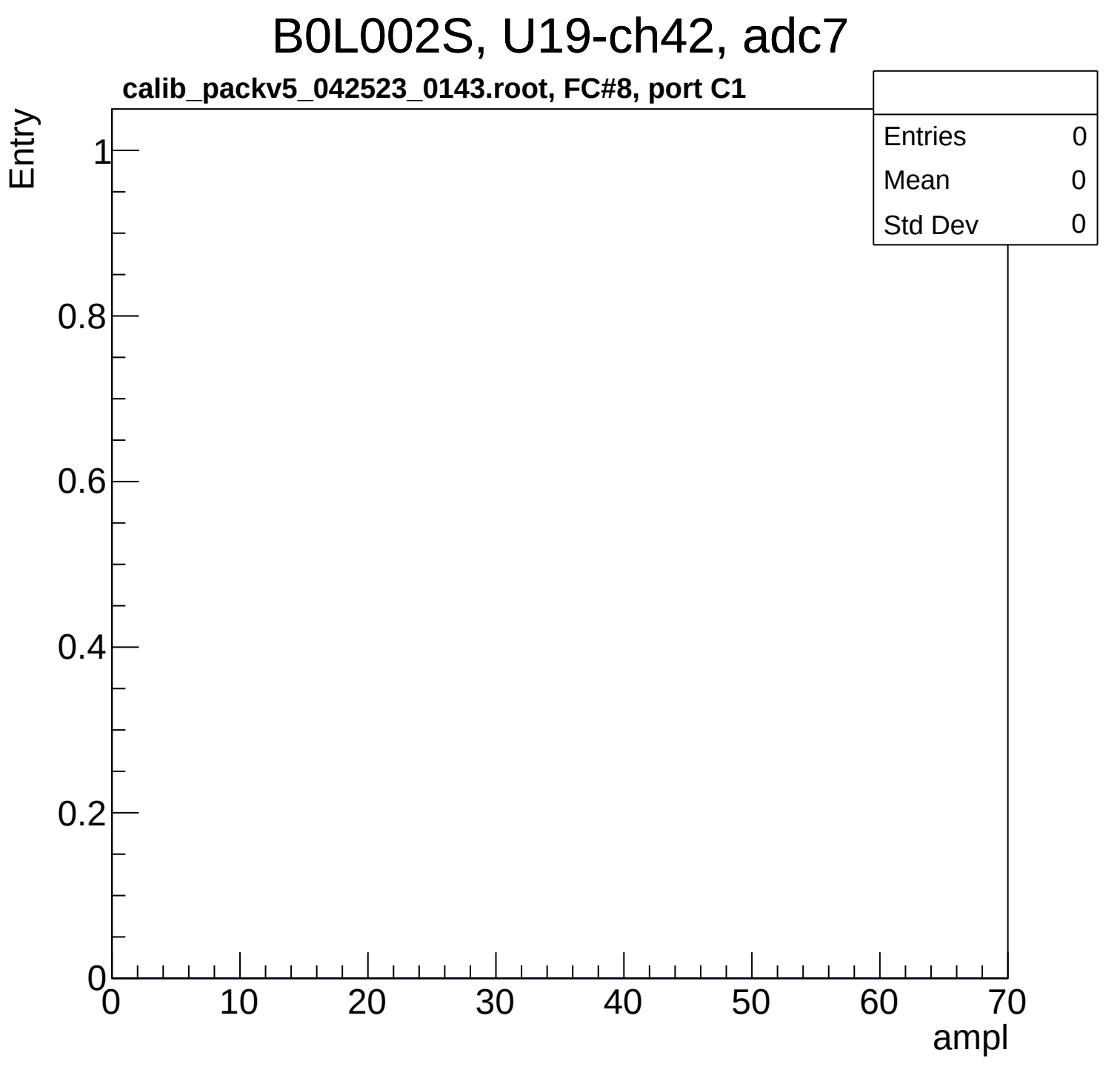

B0L002S, U19-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.24
Std Dev 3.386
Gaus mean : 43.5095
Gaus Width: 4.2076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

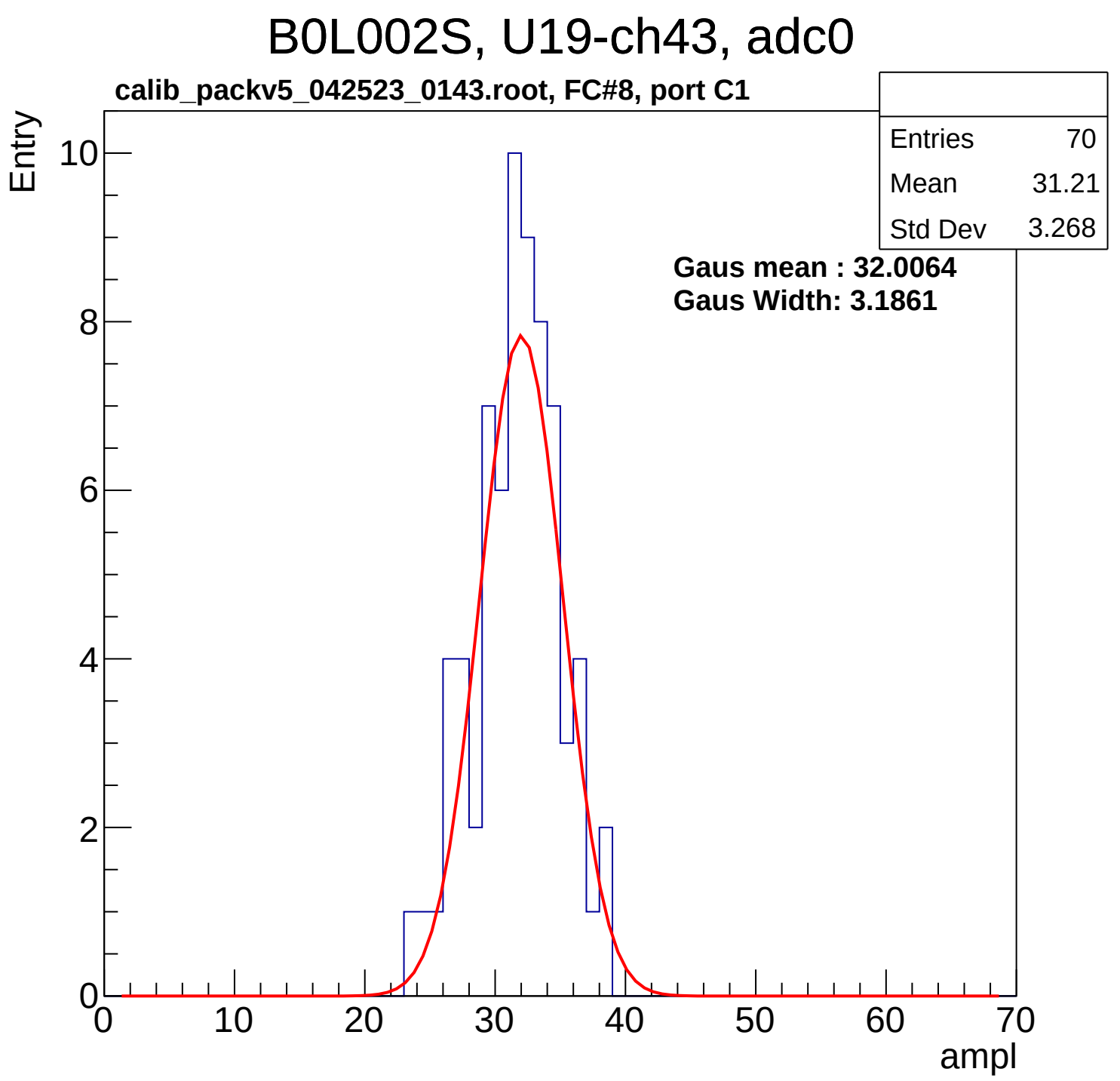

B0L002S, U19-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 49.41
Std Dev 2.871
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

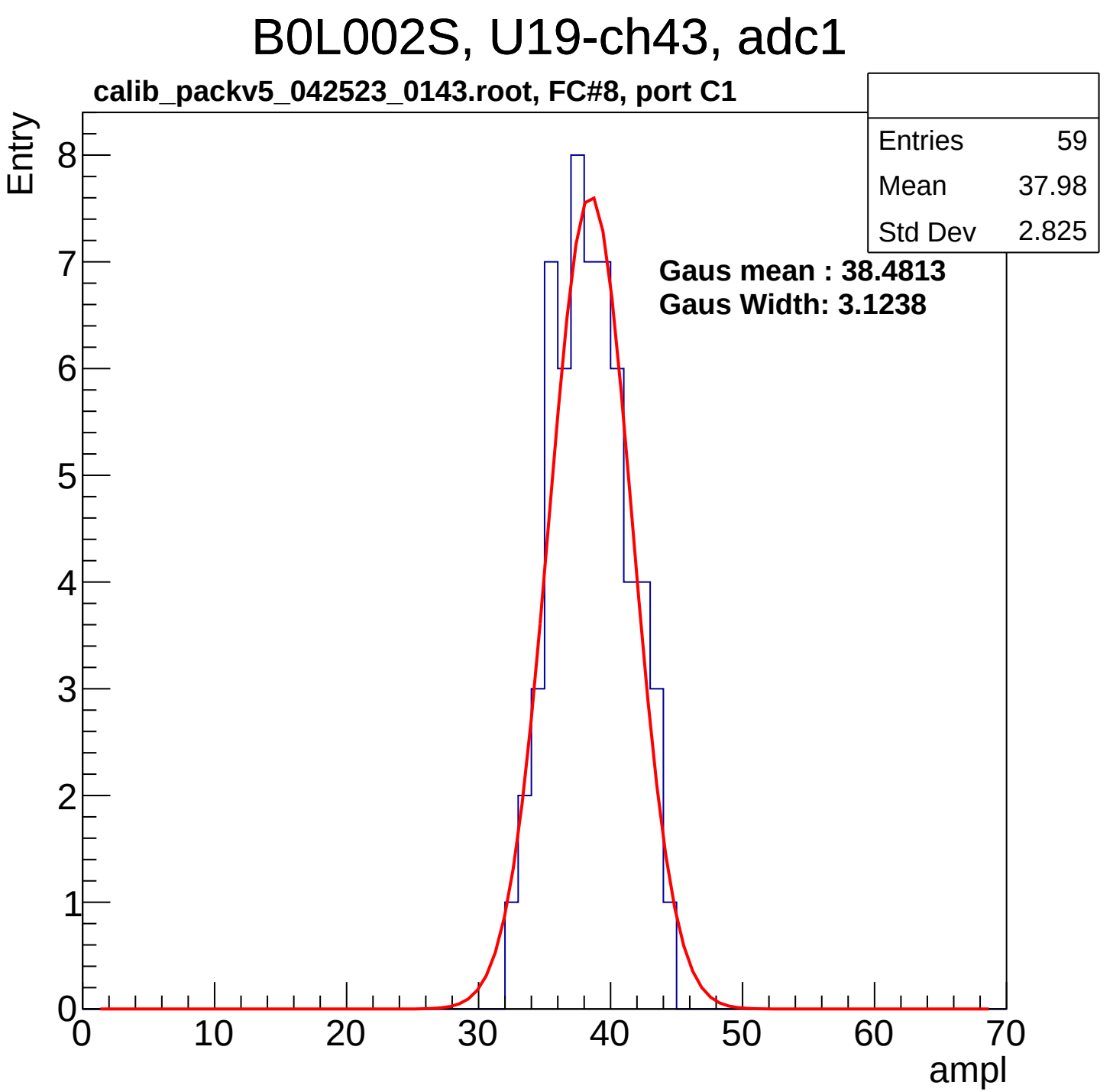

B0L002S, U19-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.04
Std Dev 2.634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

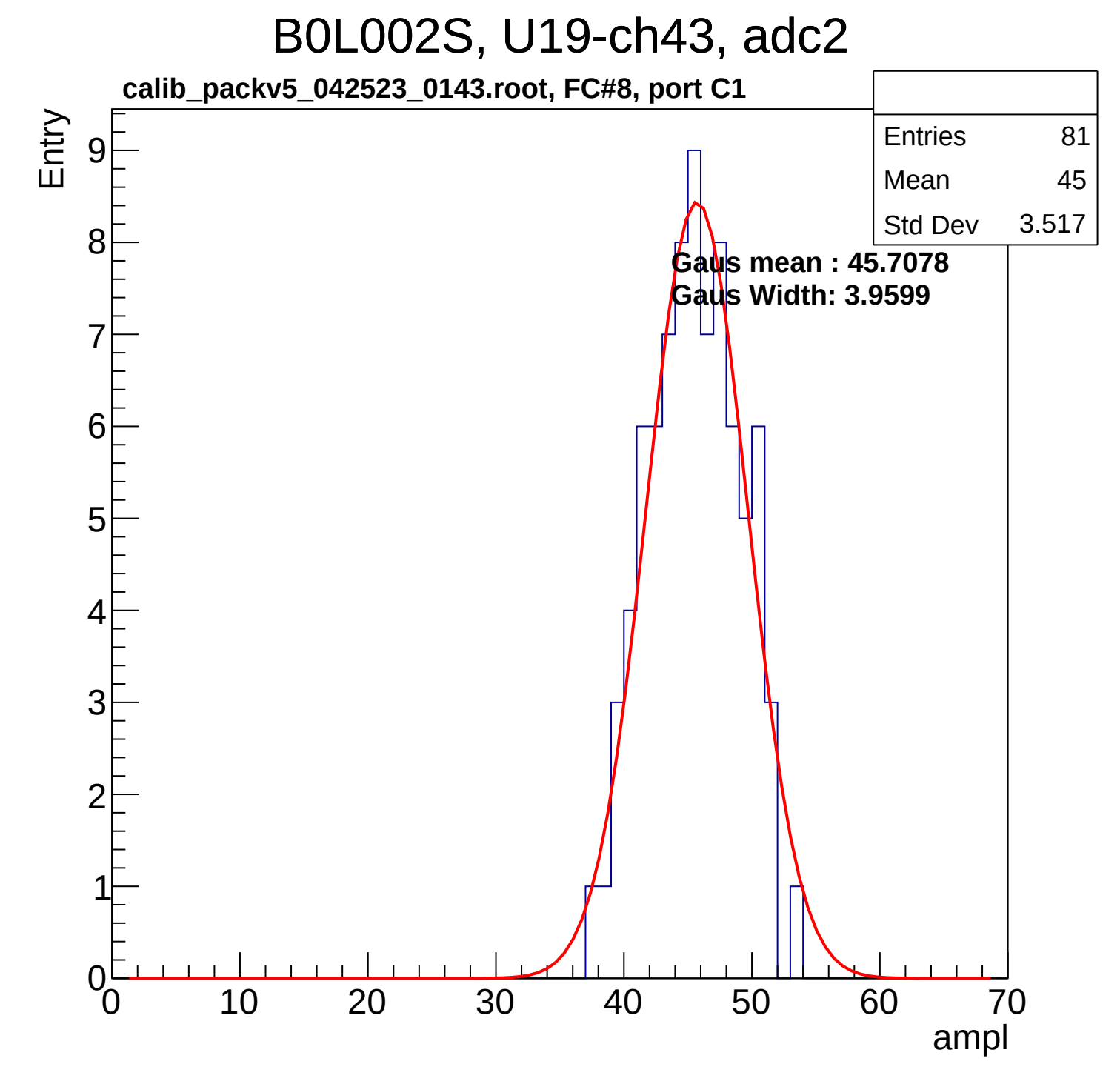

B0L002S, U19-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.29
Std Dev 9.528
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

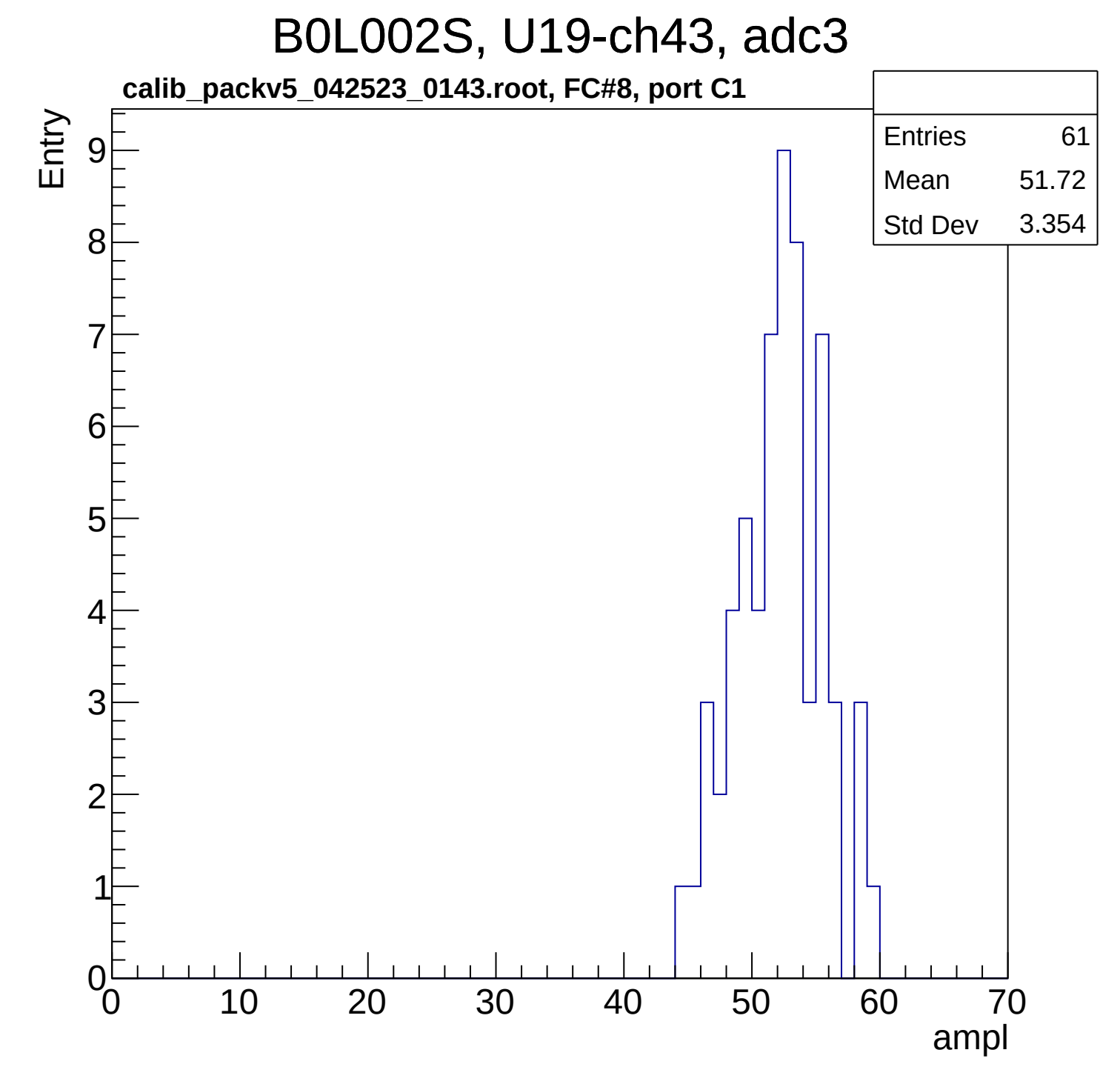

B0L002S, U19-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

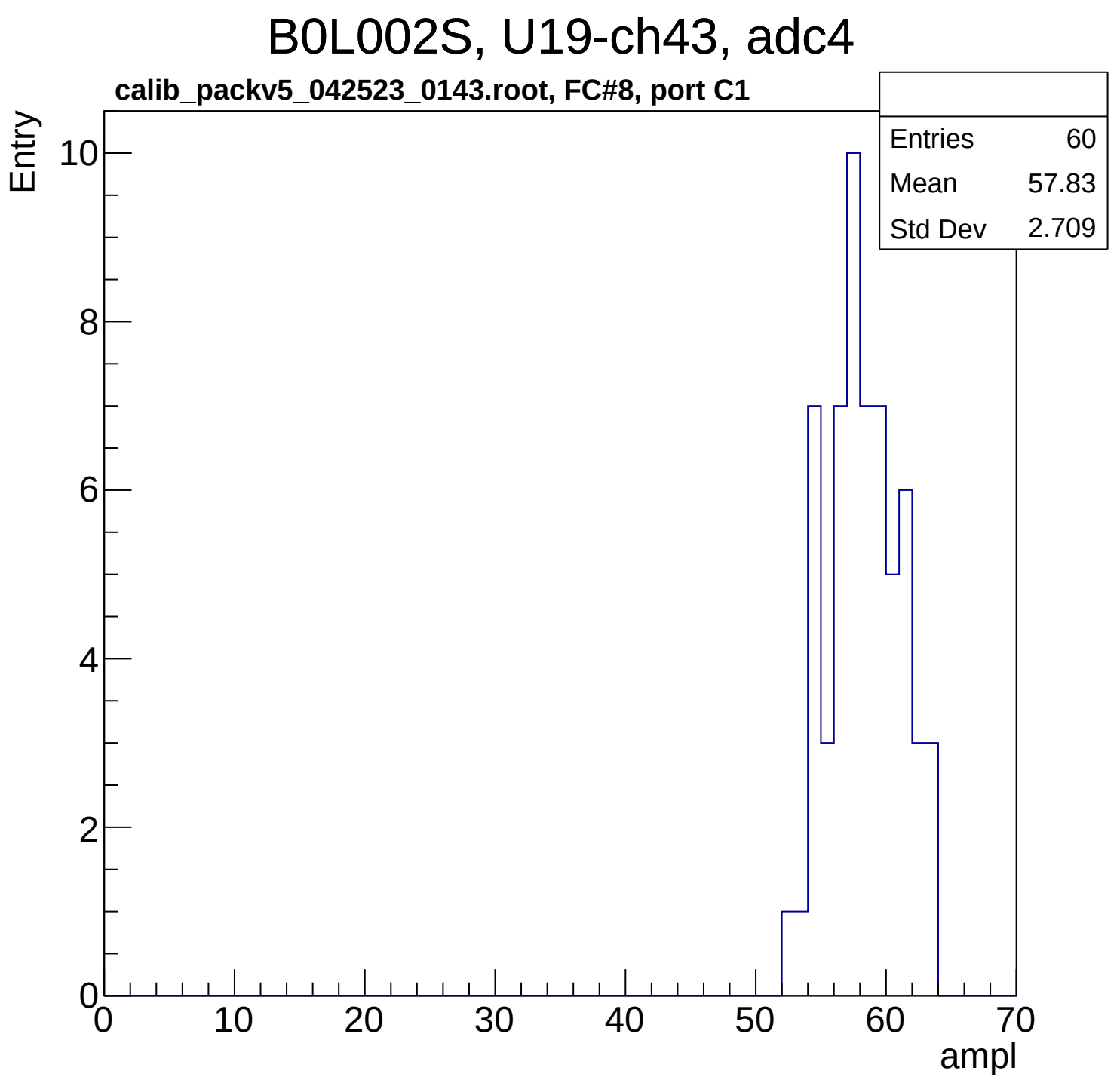

B0L002S, U19-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

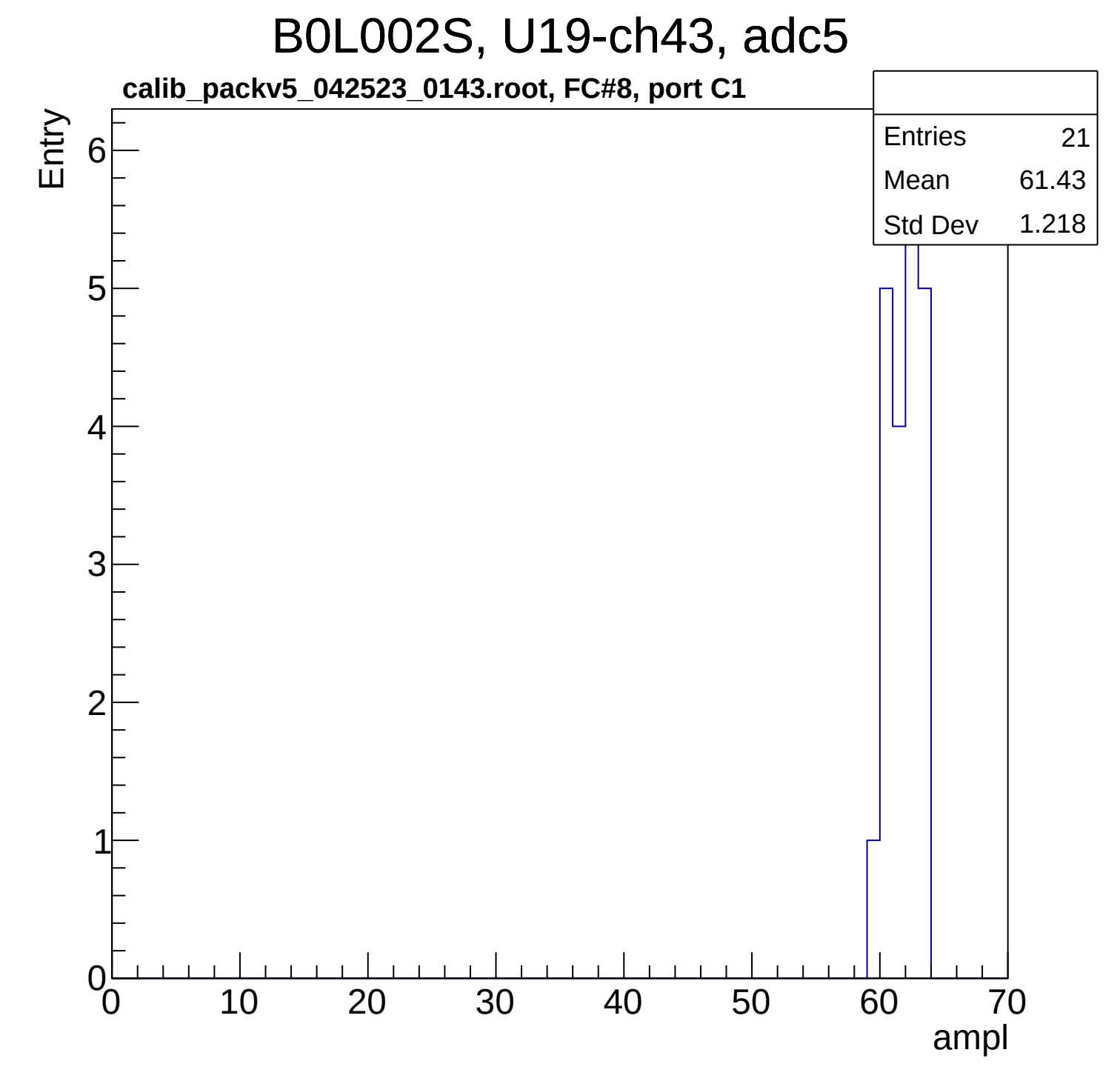

B0L002S, U19-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.21
Std Dev 3.268
Gaus mean : 32.0064
Gaus Width: 3.1861
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

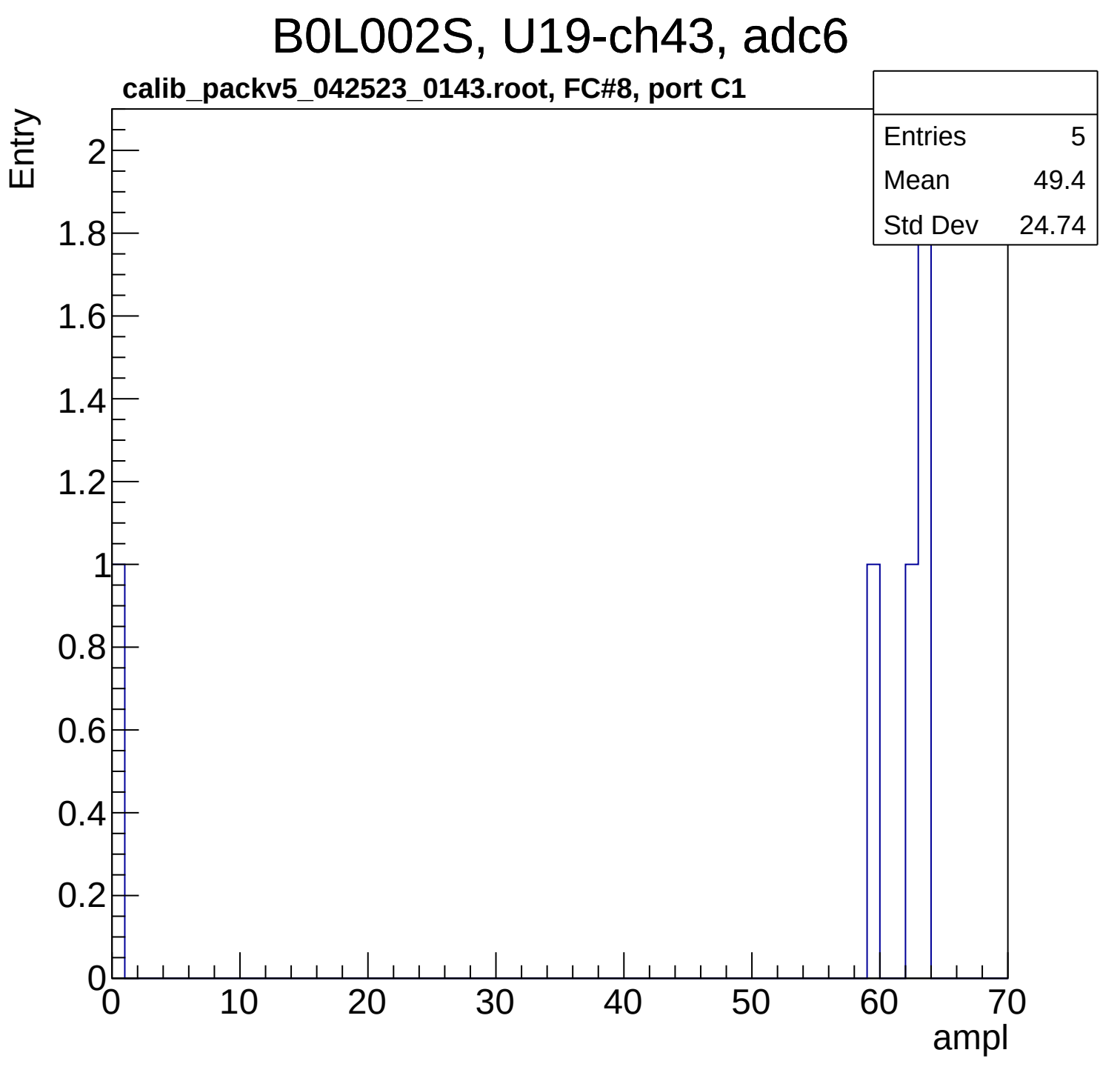

B0L002S, U19-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.98
Std Dev 2.825
Gaus mean : 38.4813
Gaus Width: 3.1238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

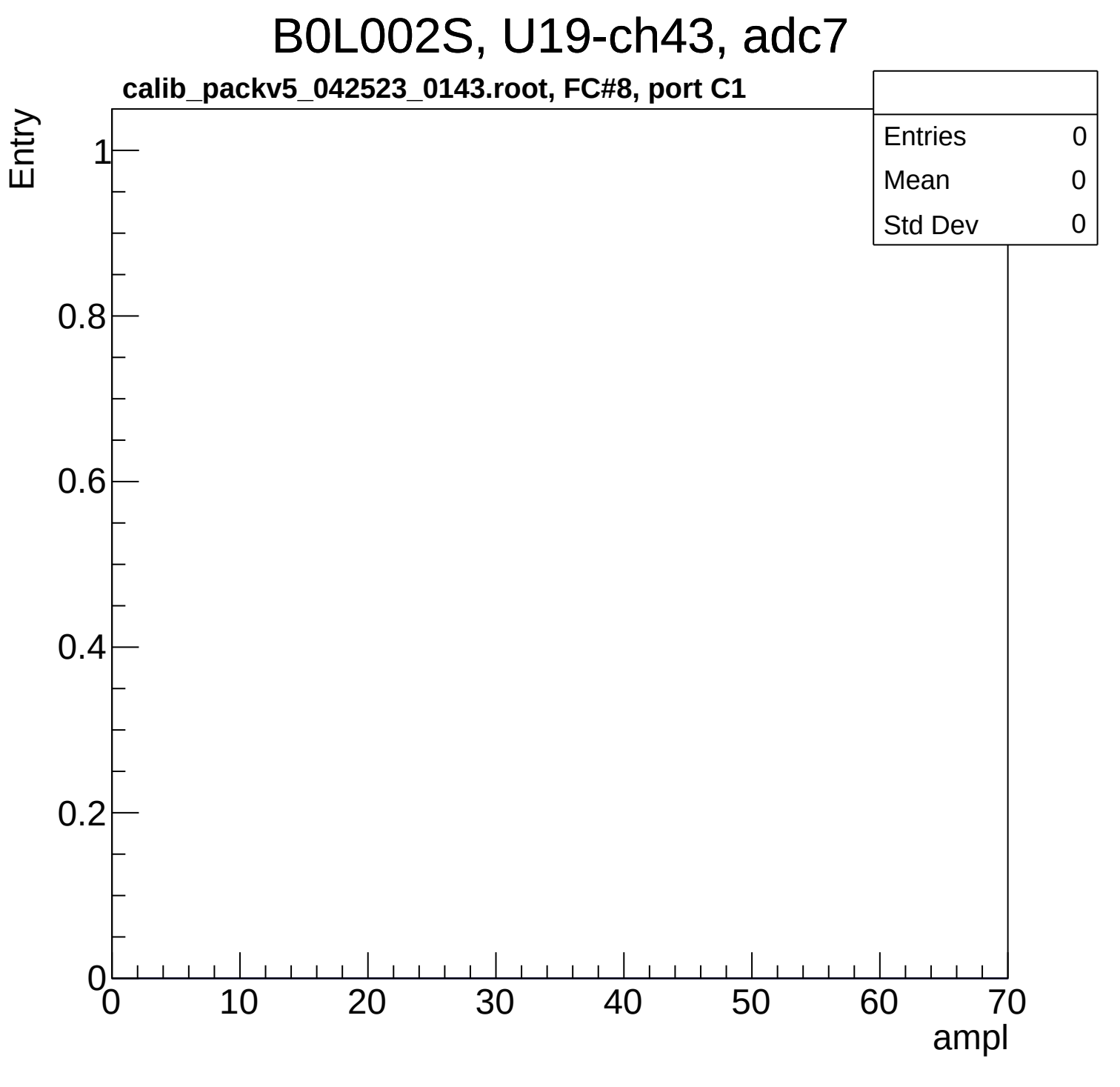

B0L002S, U19-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 45
Std Dev 3.517
Gaus mean : 45.7078
Gaus Width: 3.9599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

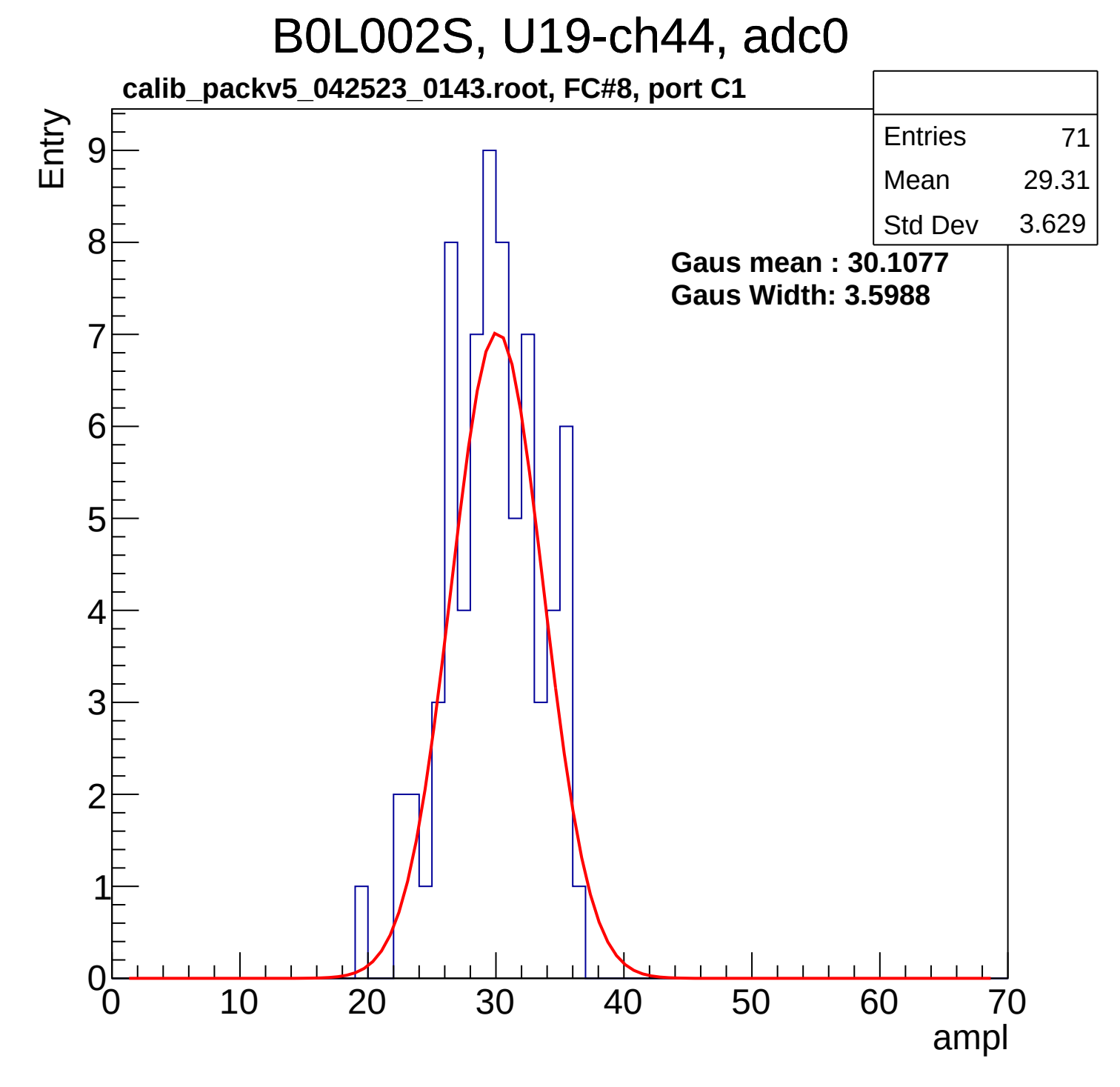

B0L002S, U19-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.72
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

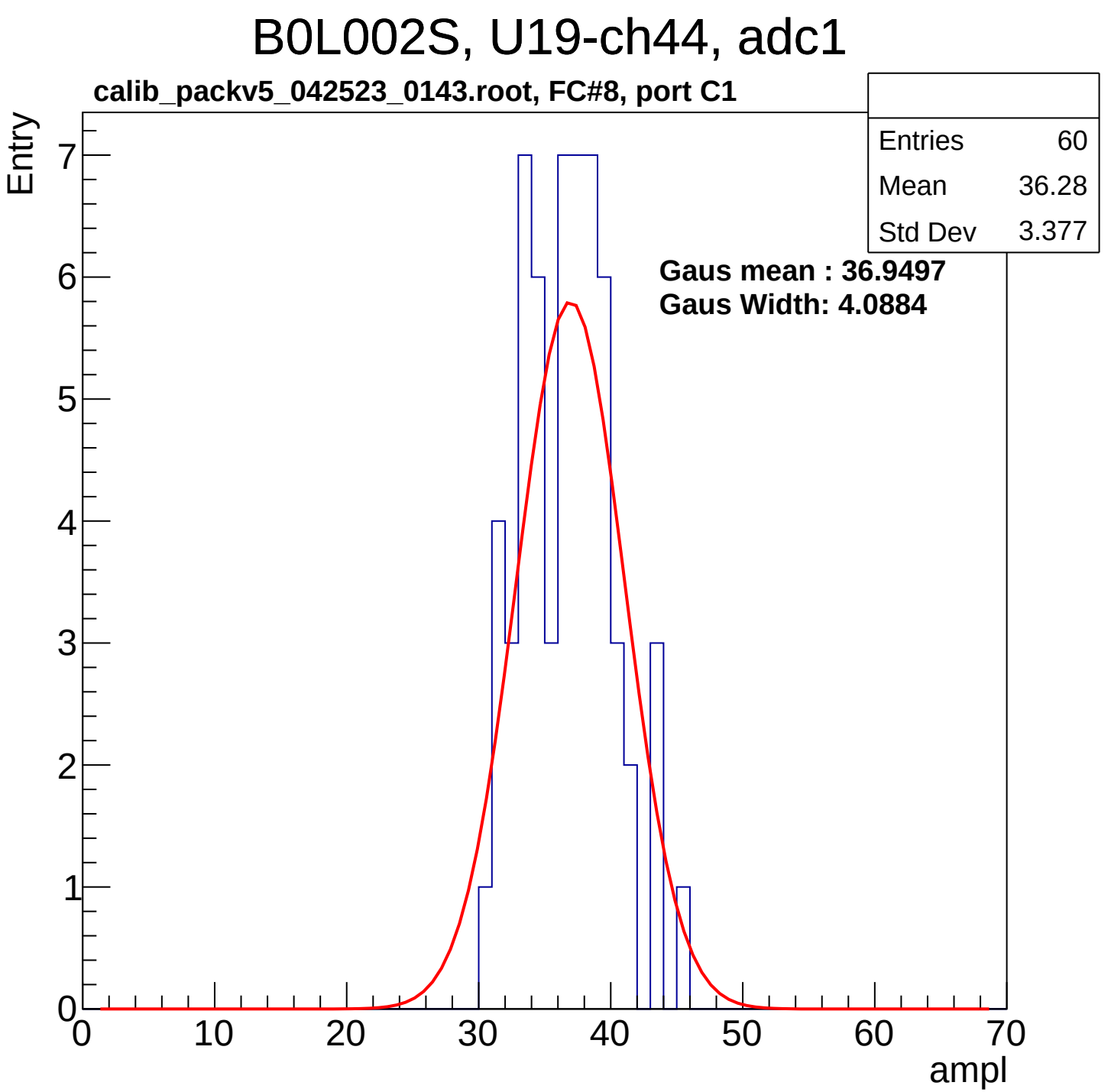

B0L002S, U19-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.83
Std Dev 2.709
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

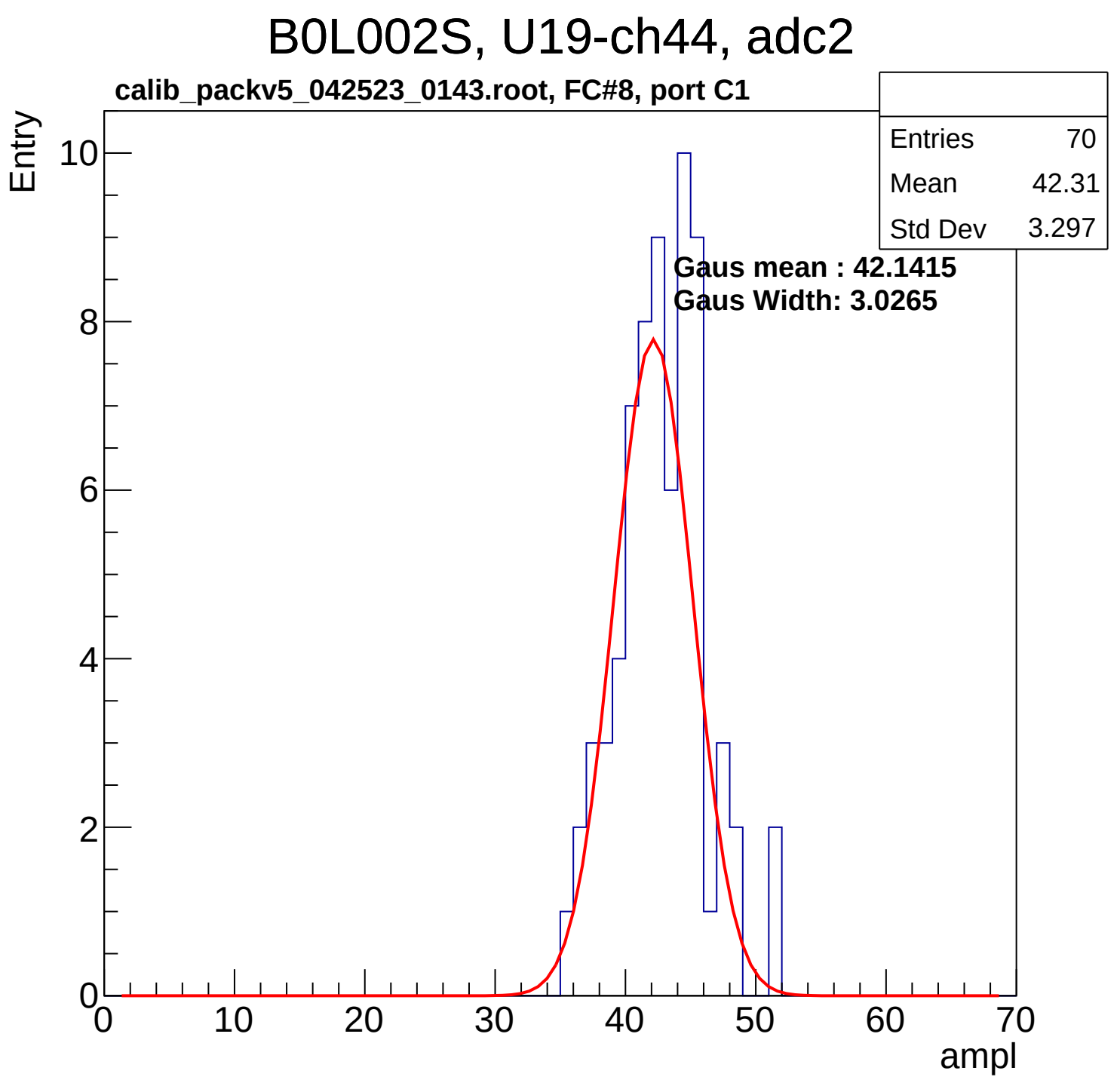

B0L002S, U19-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.43
Std Dev 1.218
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

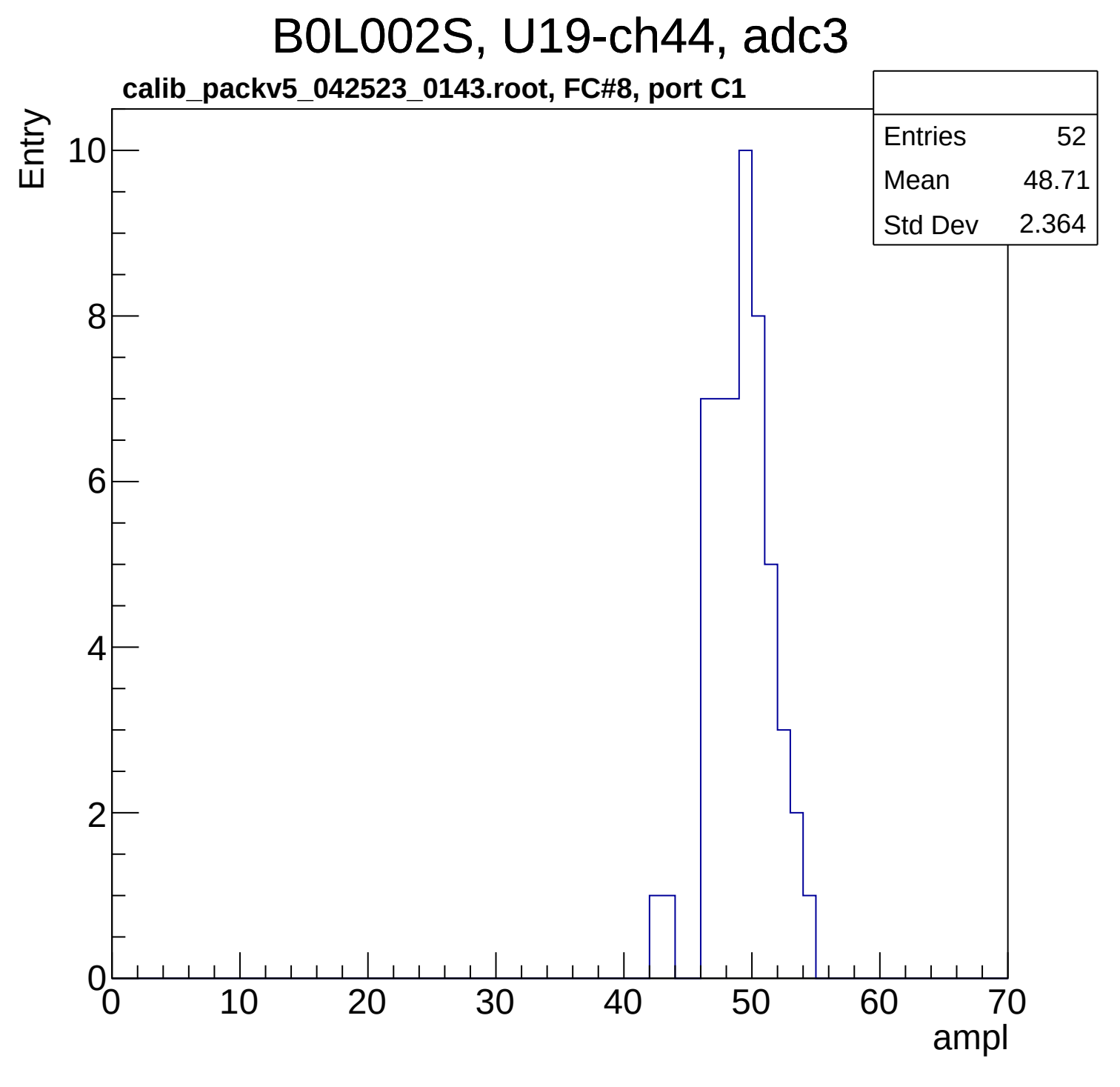

B0L002S, U19-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49.4
Std Dev 24.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

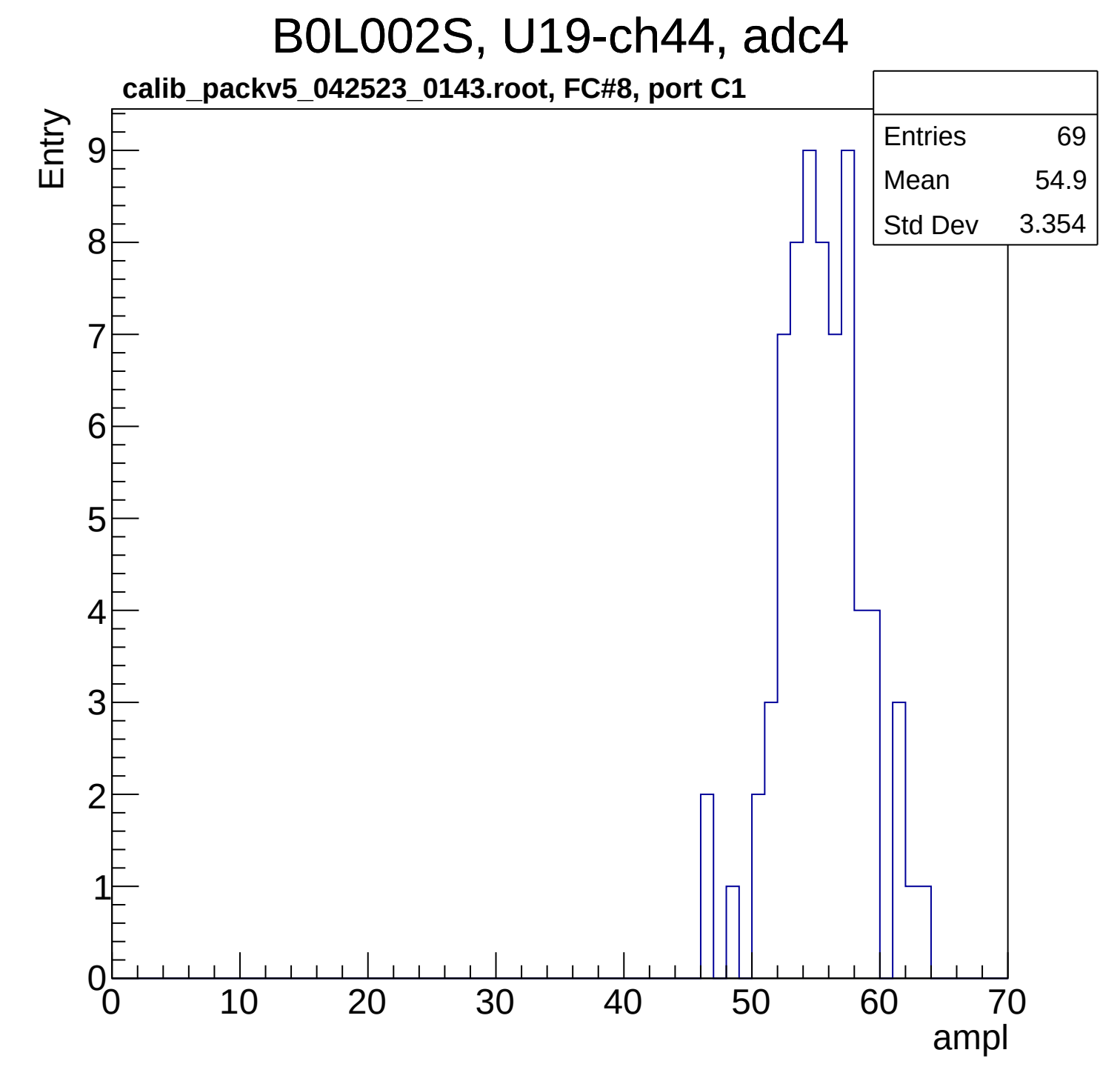

B0L002S, U19-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

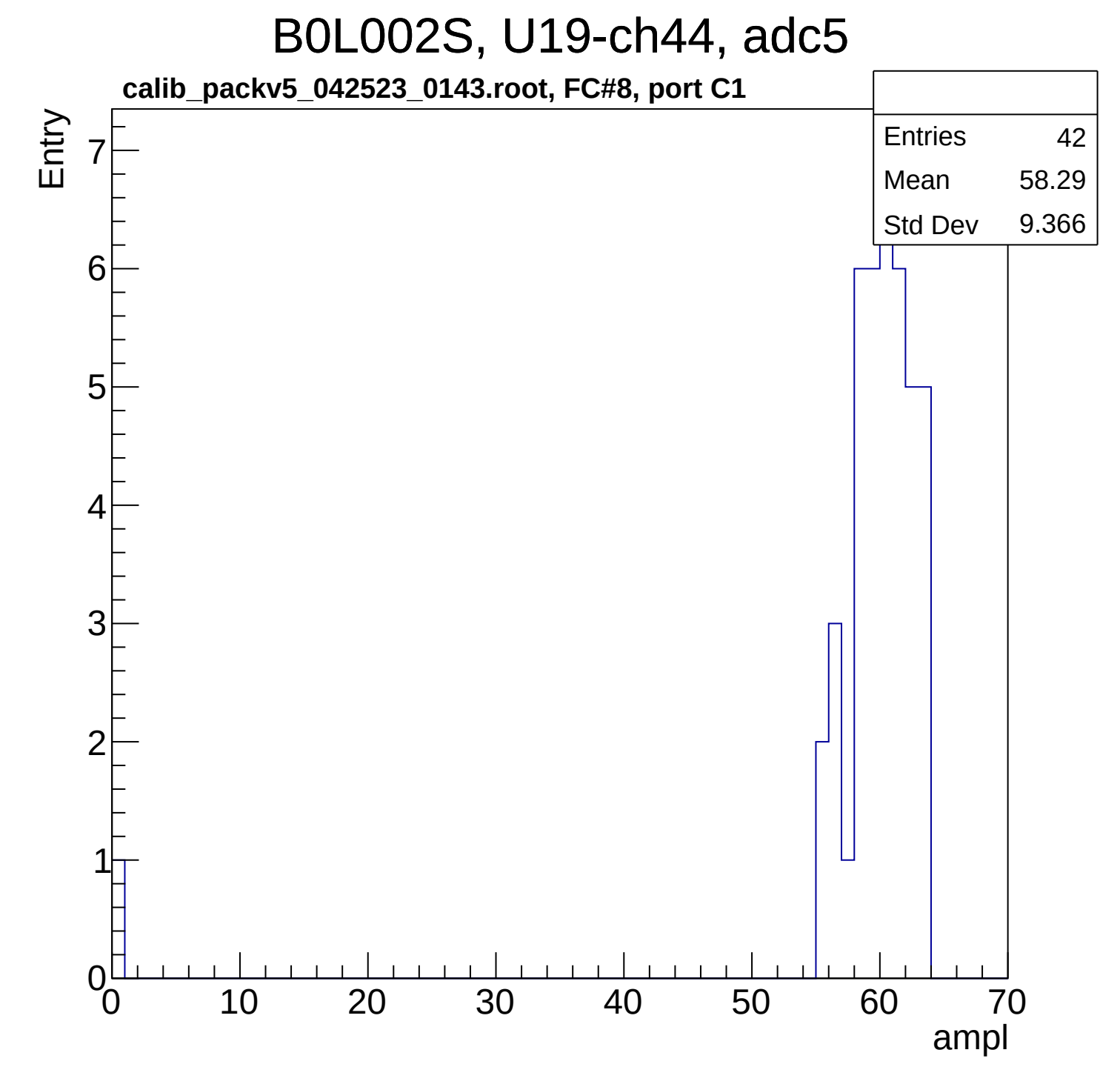

B0L002S, U19-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.31
Std Dev 3.629
Gaus mean : 30.1077
Gaus Width: 3.5988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

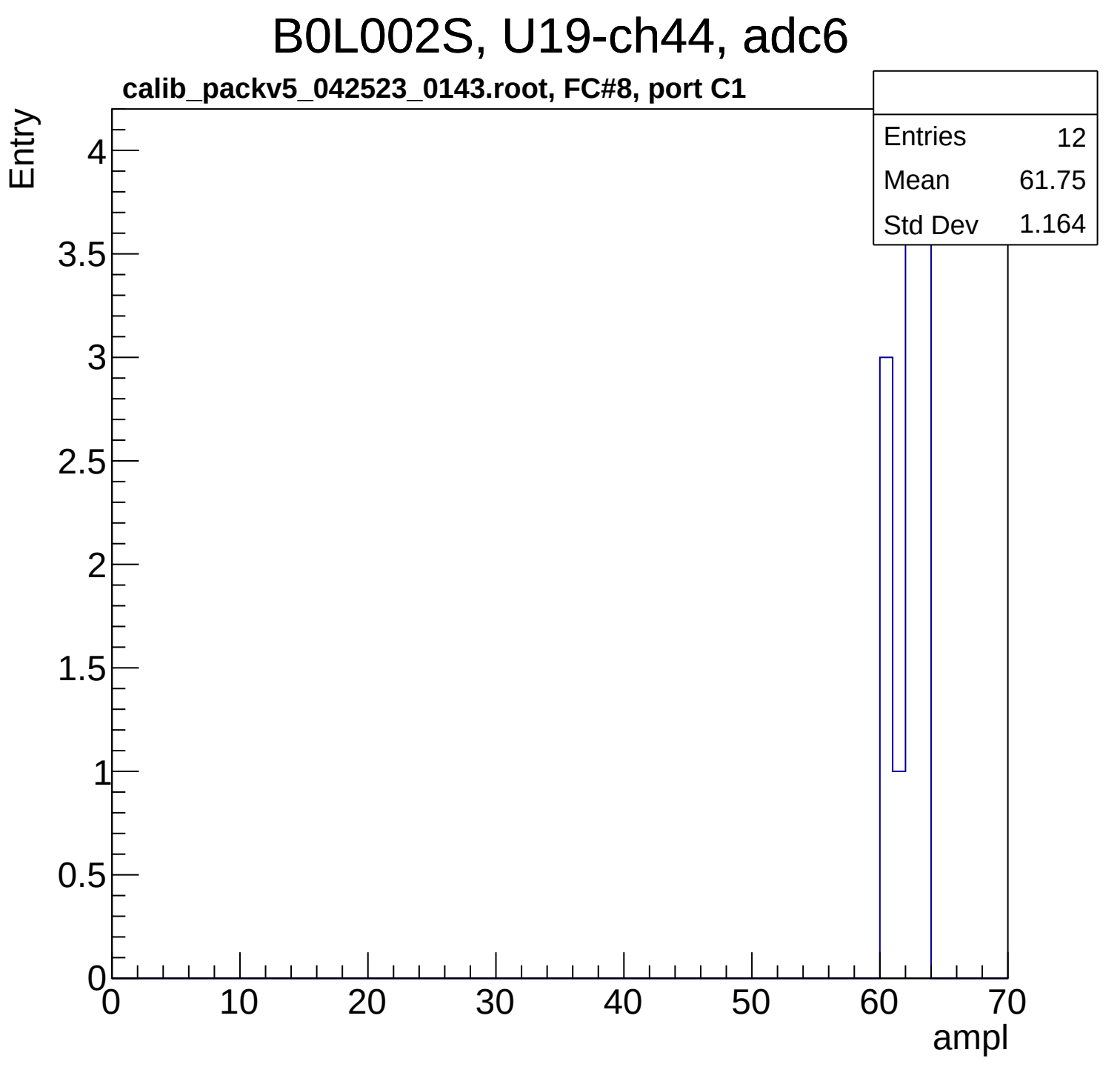

B0L002S, U19-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.28
Std Dev 3.377
Gaus mean : 36.9497
Gaus Width: 4.0884
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

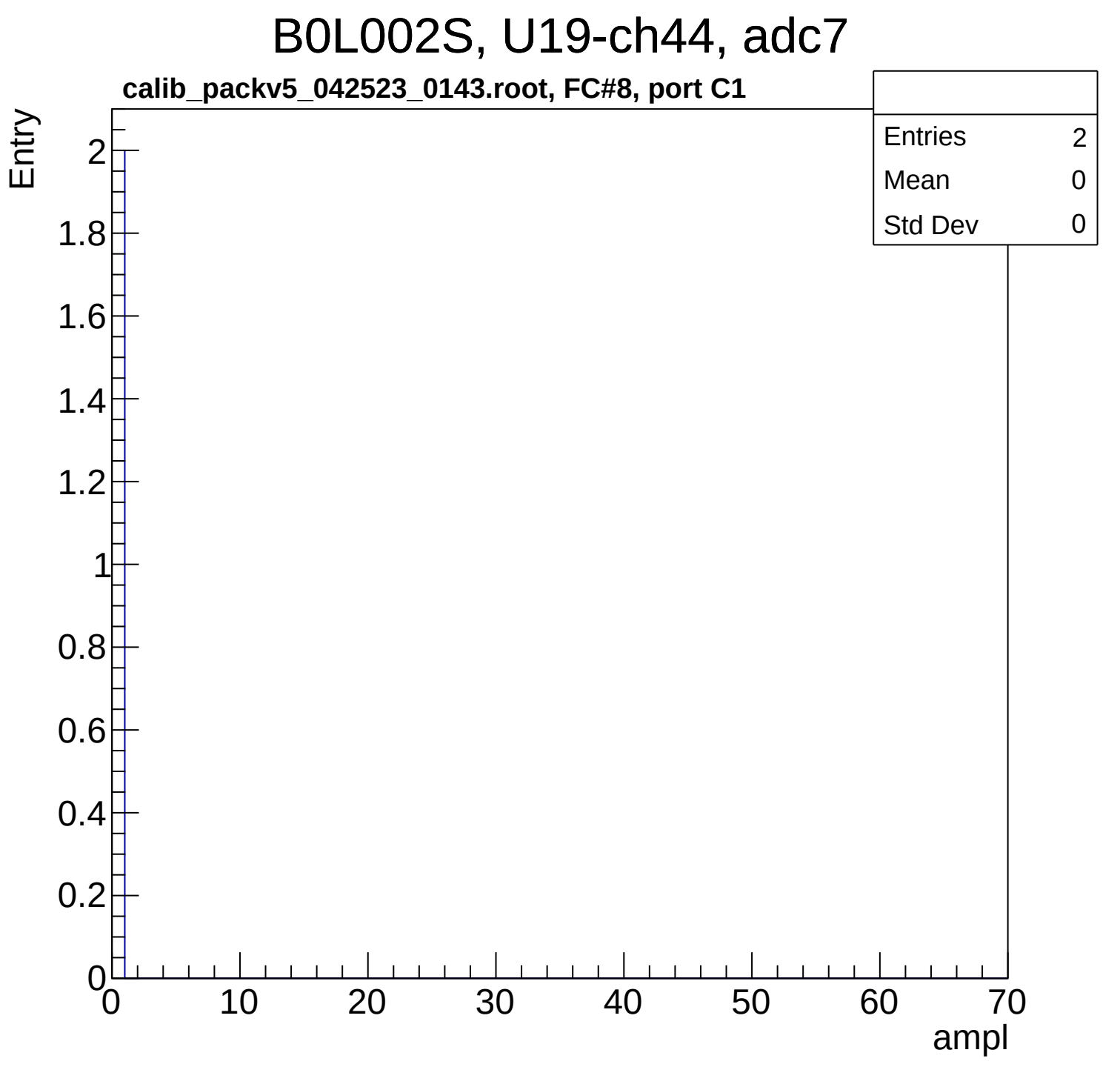

B0L002S, U19-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 42.31
Std Dev 3.297
Gaus mean : 42.1415
Gaus Width: 3.0265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

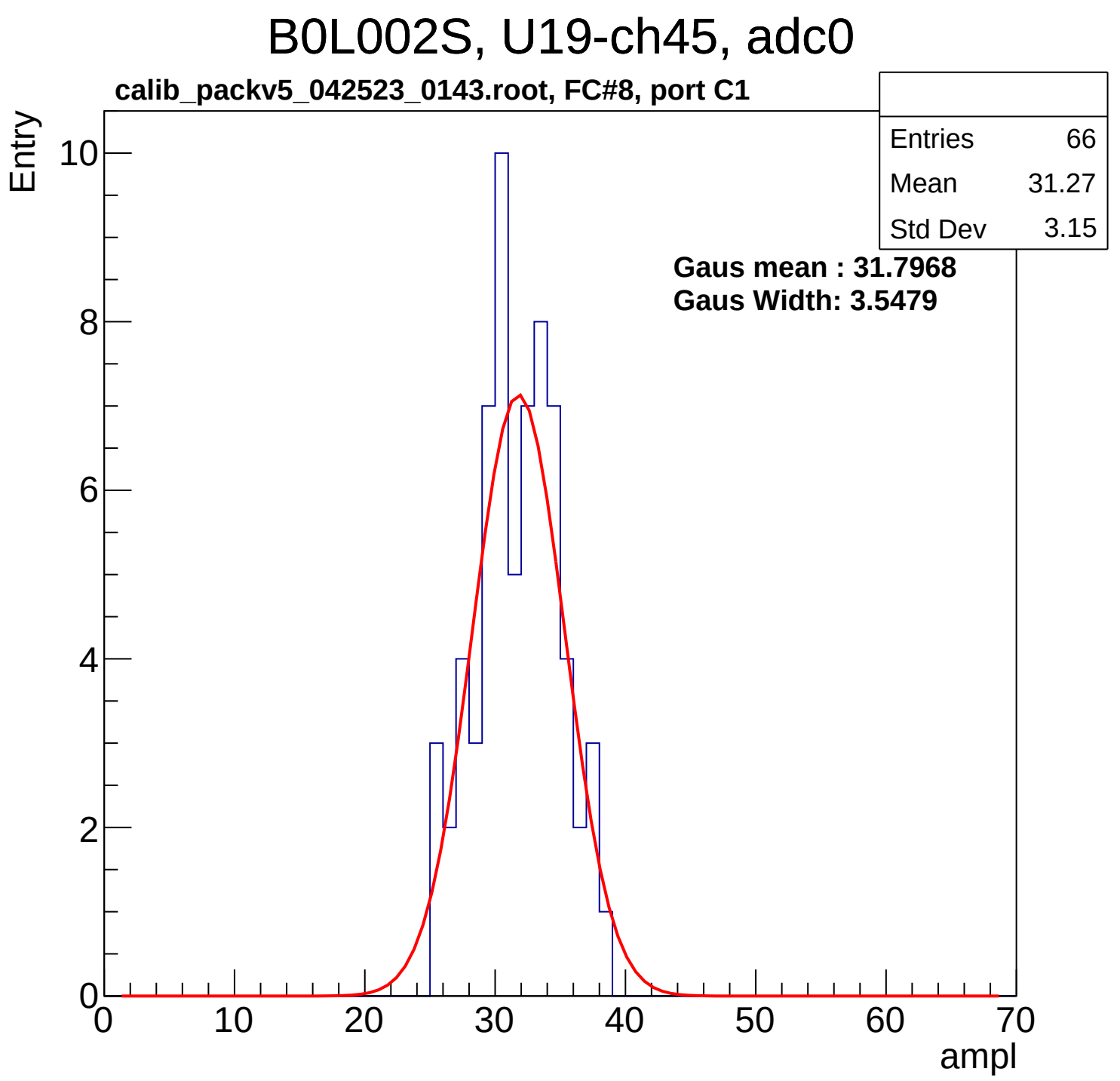

B0L002S, U19-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 48.71
Std Dev 2.364
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

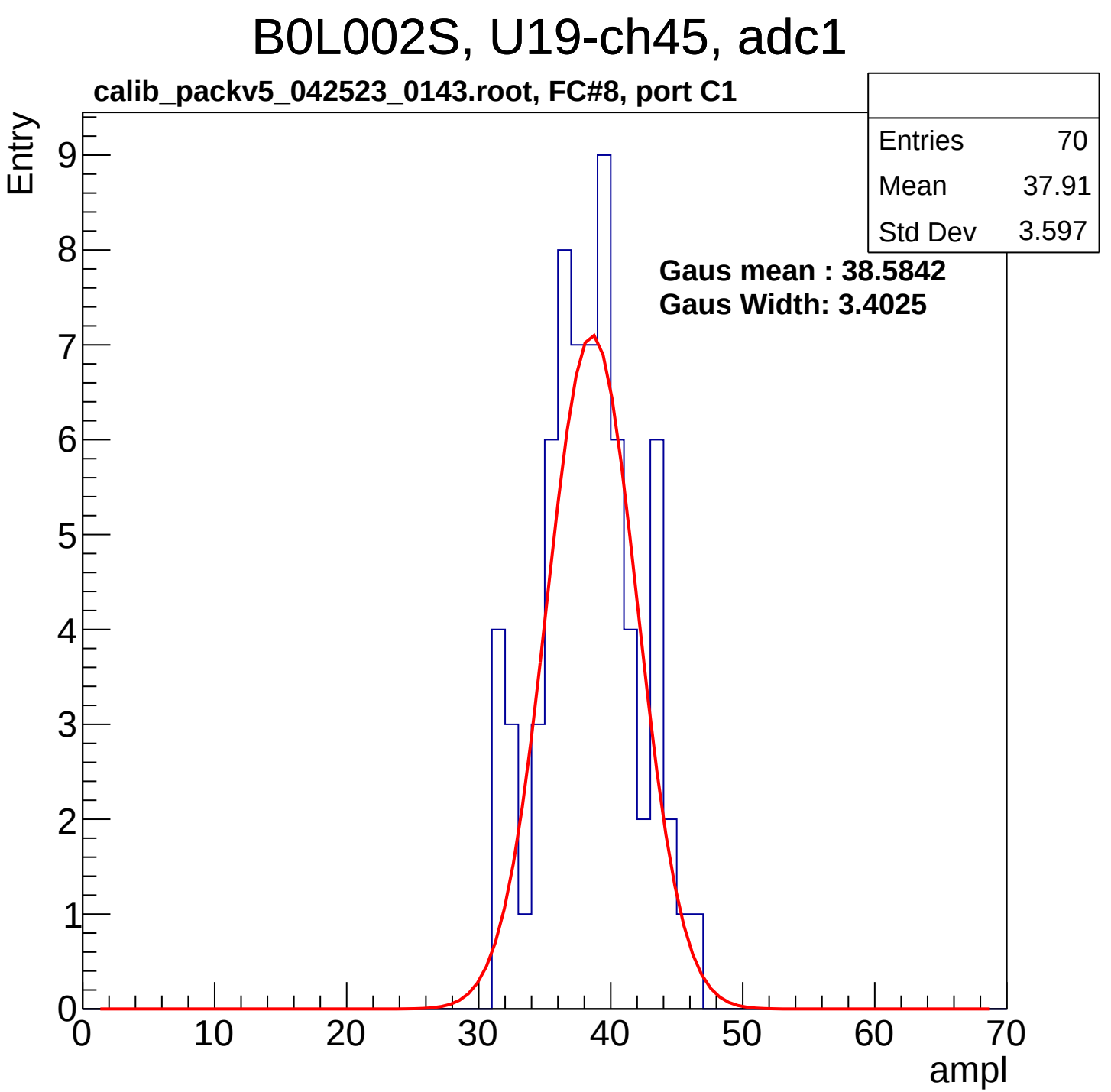

B0L002S, U19-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 54.9
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

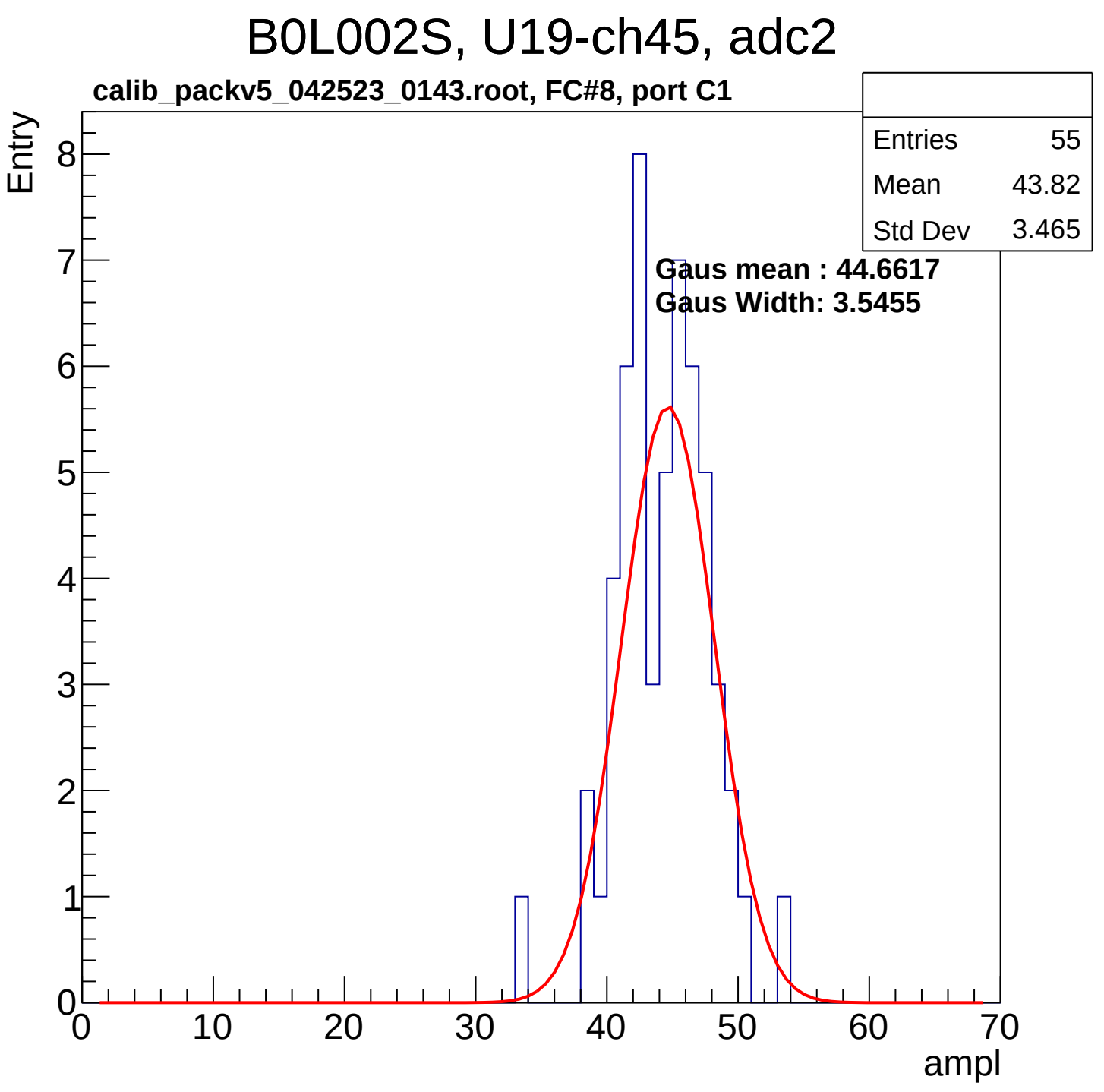

B0L002S, U19-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.29
Std Dev 9.366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

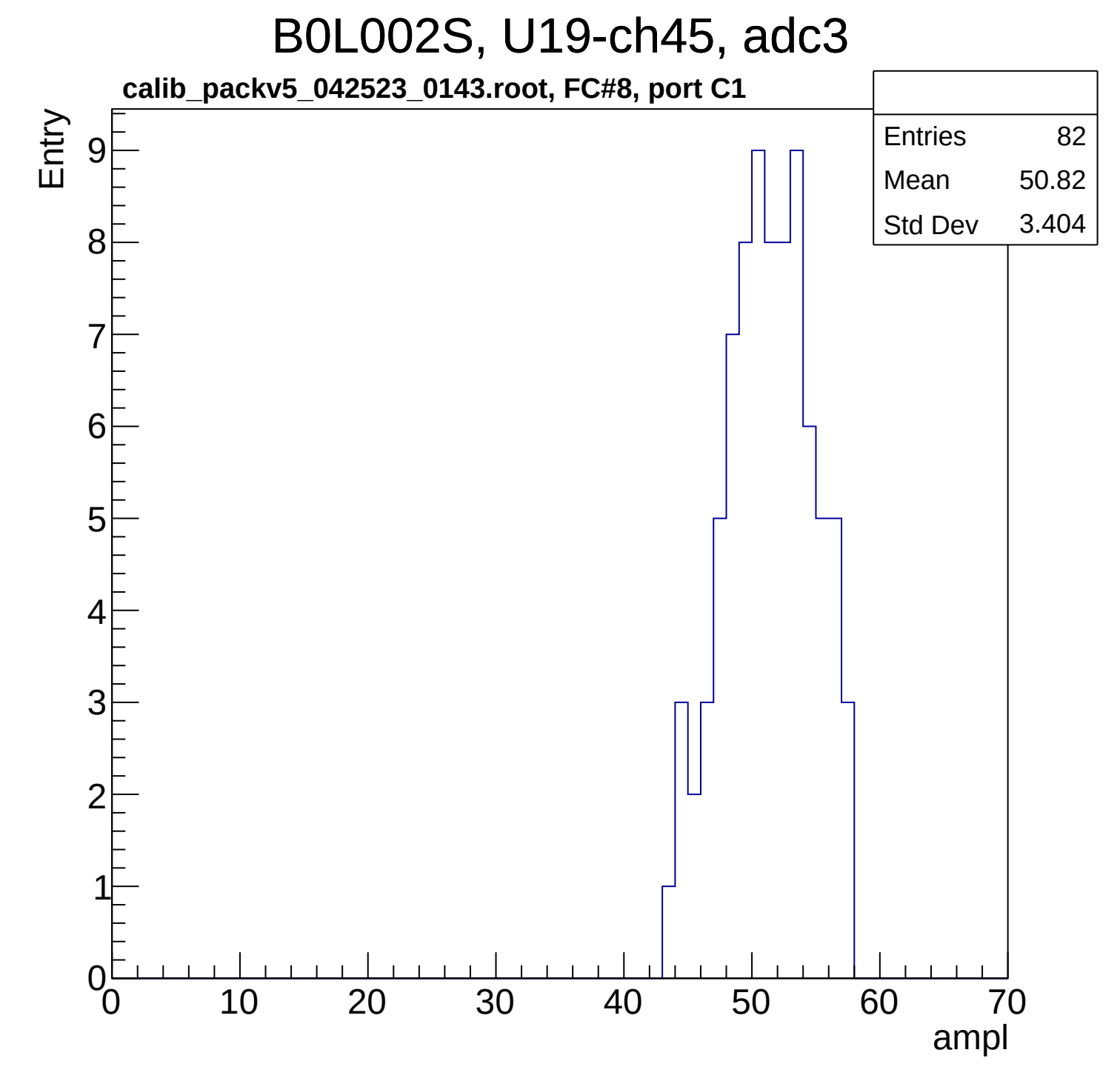

B0L002S, U19-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 61.75
Std Dev 1.164
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

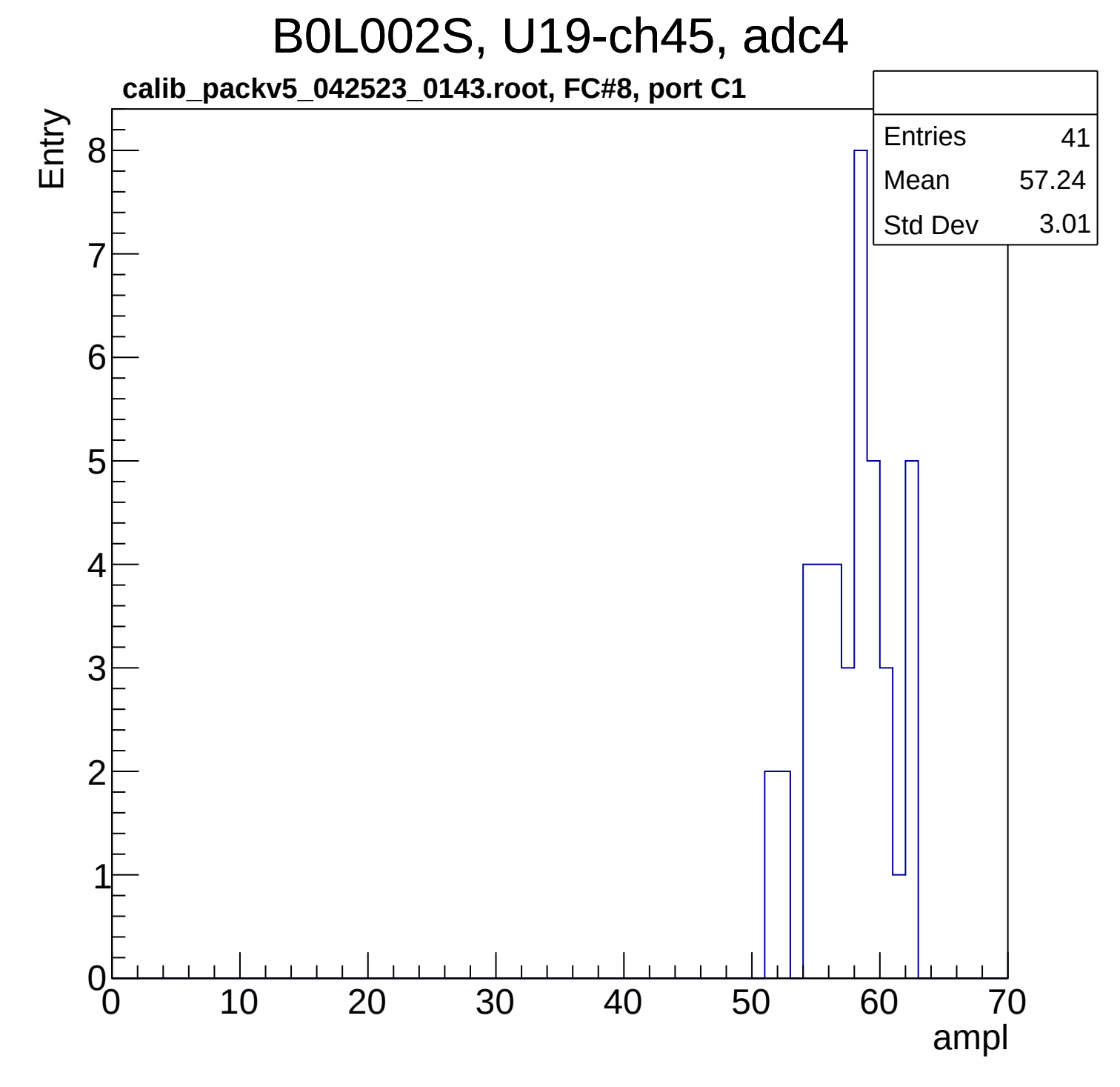

B0L002S, U19-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

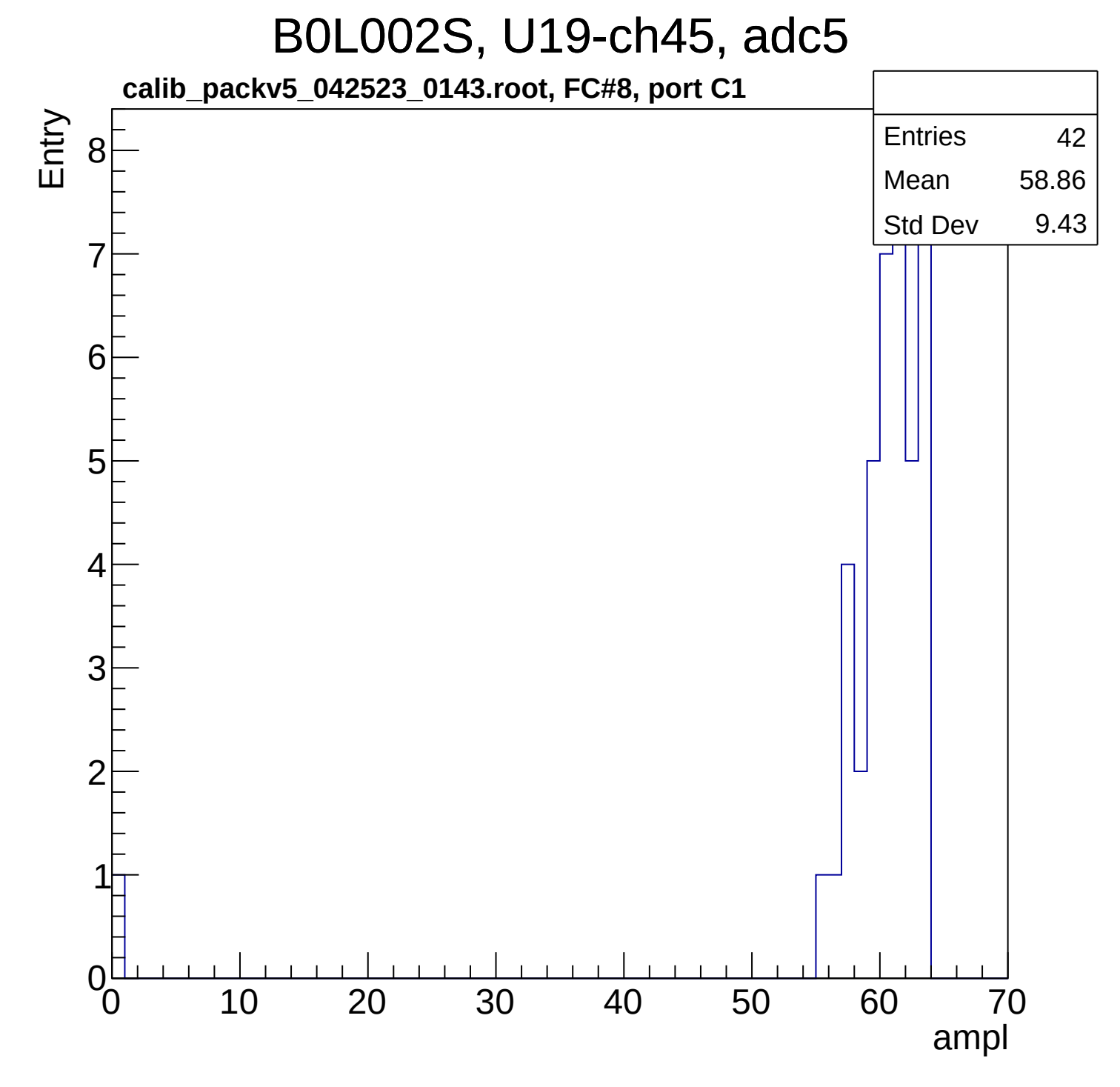

B0L002S, U19-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.27
Std Dev 3.15
Gaus mean : 31.7968
Gaus Width: 3.5479
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

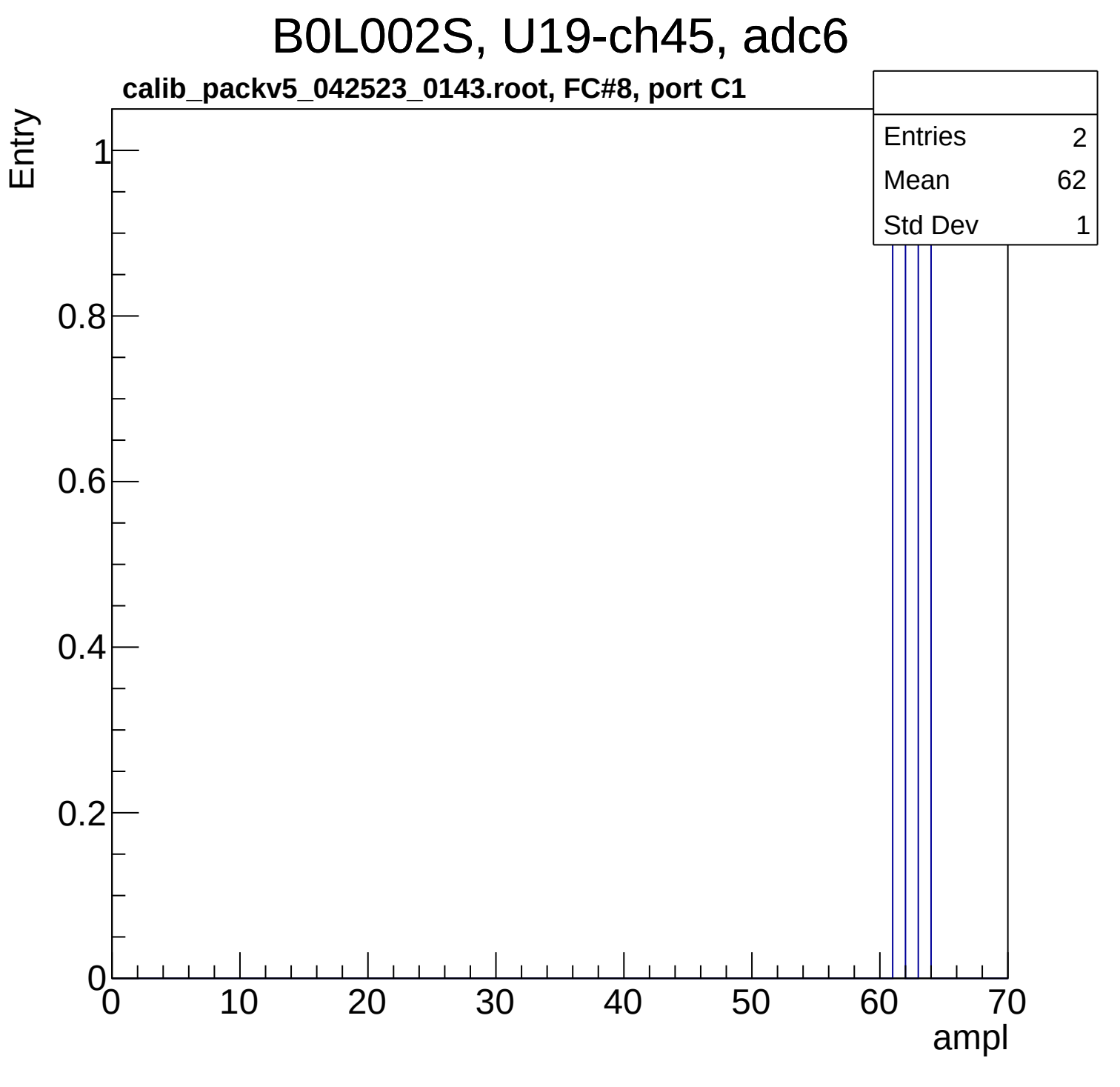

B0L002S, U19-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.91
Std Dev 3.597
Gaus mean : 38.5842
Gaus Width: 3.4025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

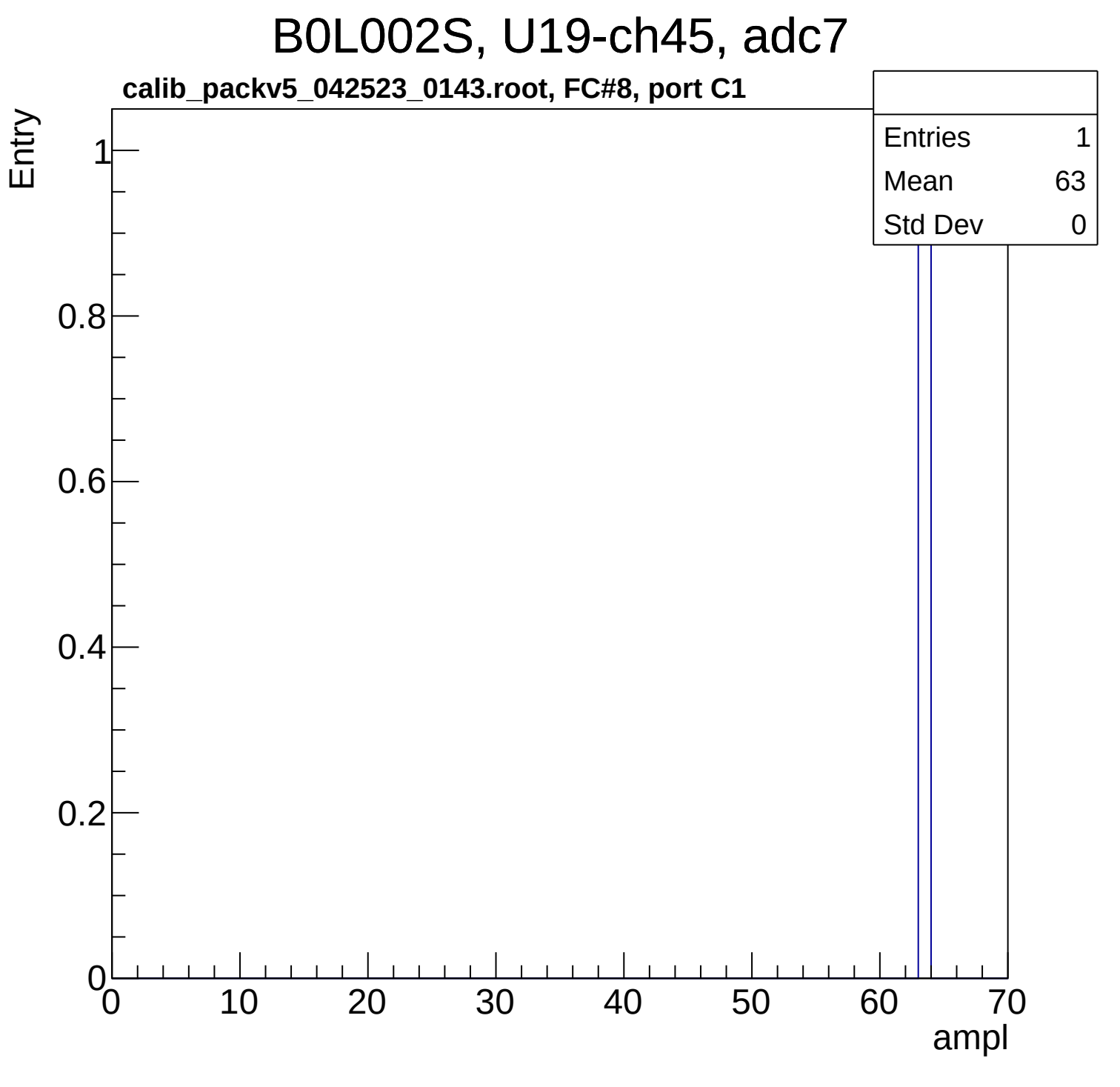

B0L002S, U19-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 43.82
Std Dev 3.465
Gaus mean : 44.6617
Gaus Width: 3.5455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

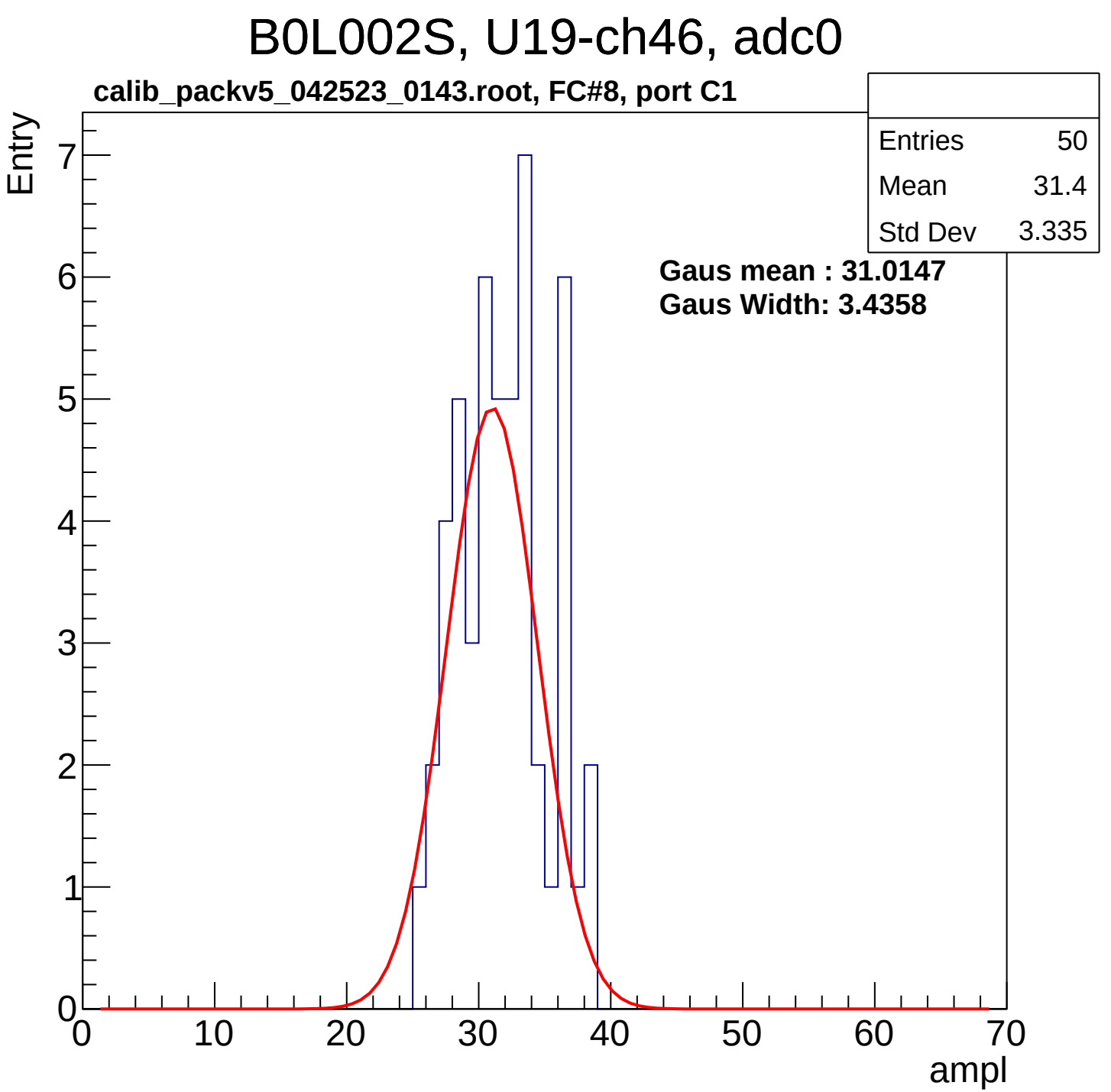

B0L002S, U19-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 50.82
Std Dev 3.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

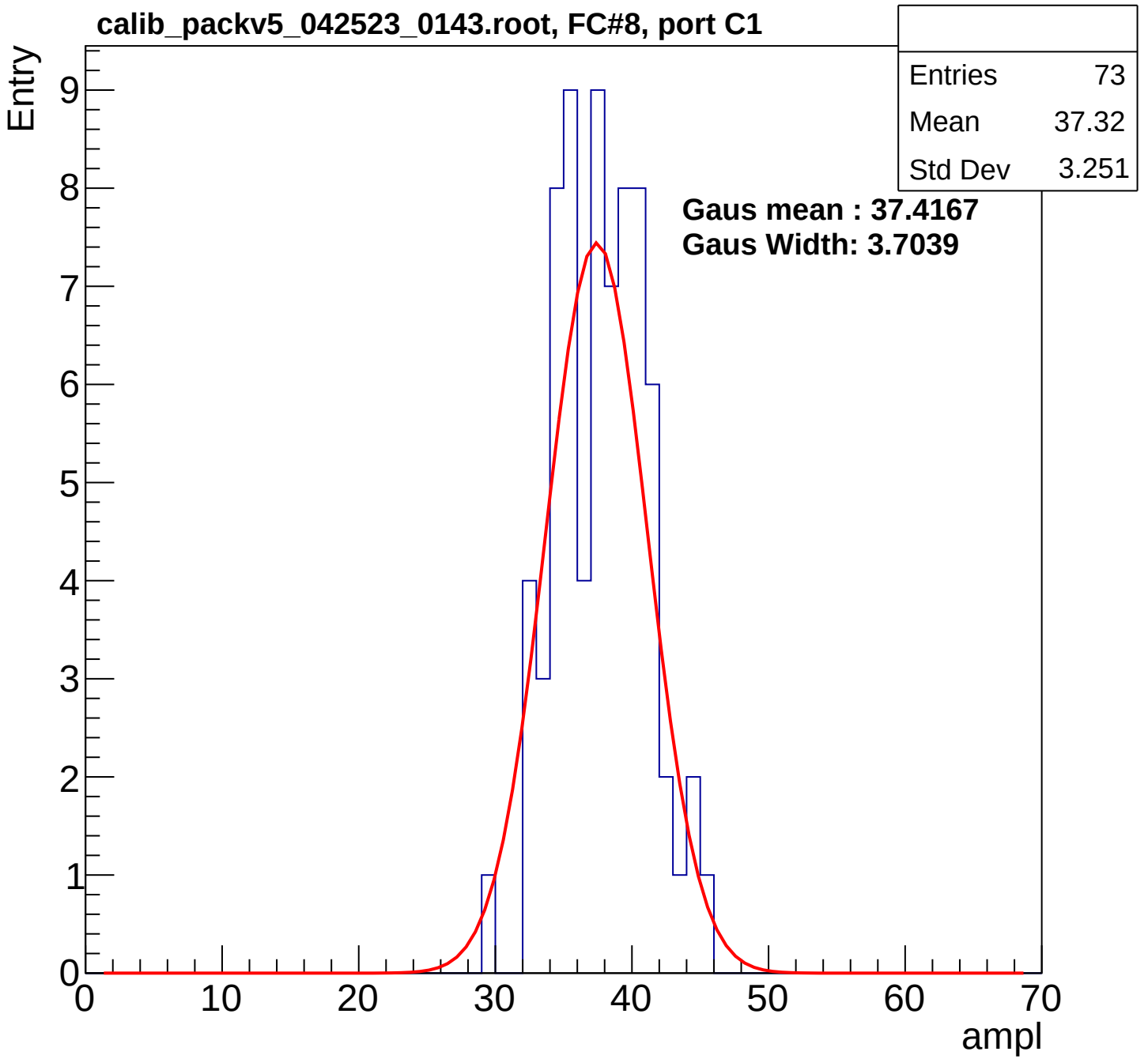

B0L002S, U19-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 57.24
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

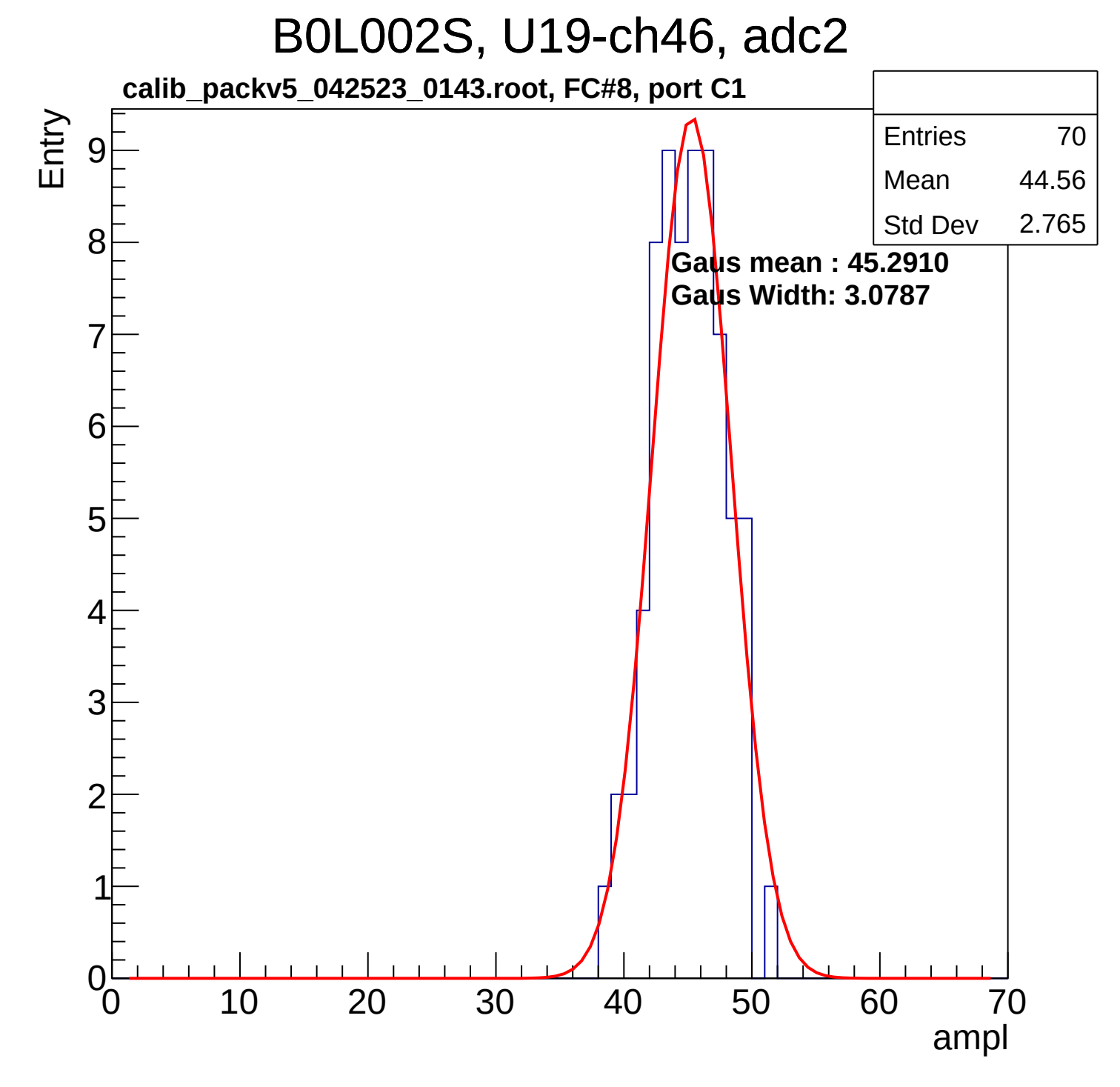

B0L002S, U19-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.86
Std Dev 9.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

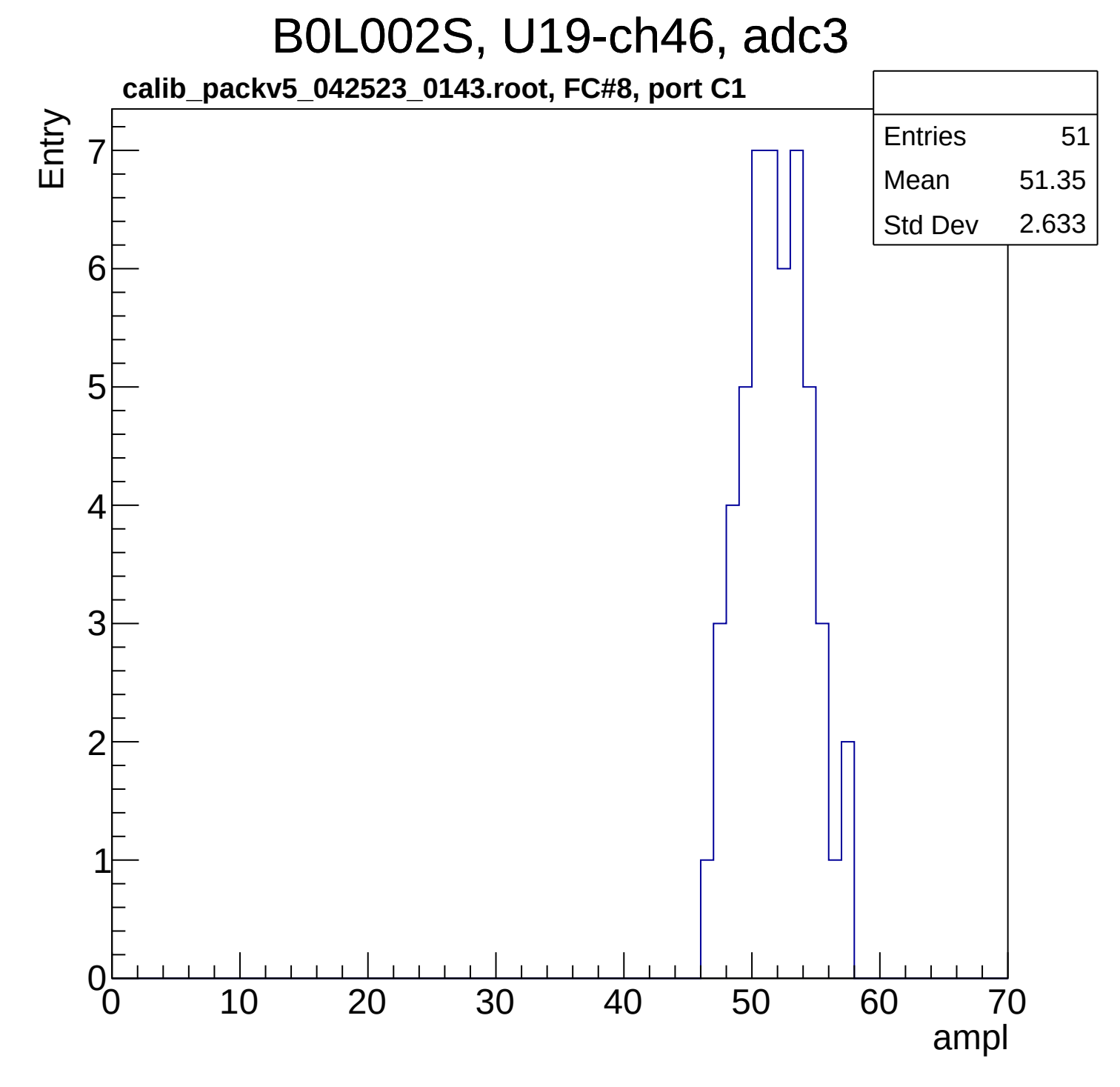

B0L002S, U19-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

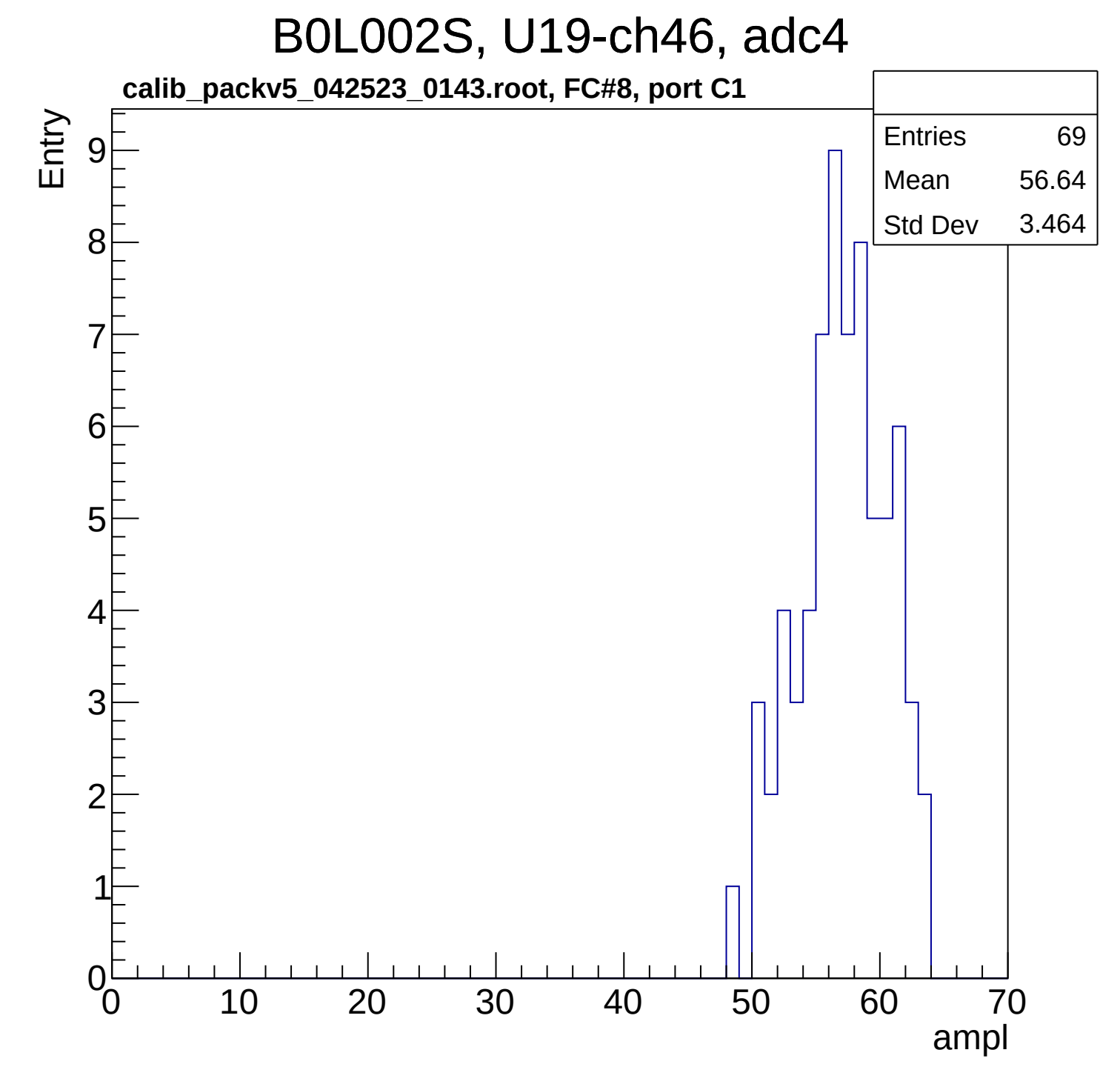

B0L002S, U19-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

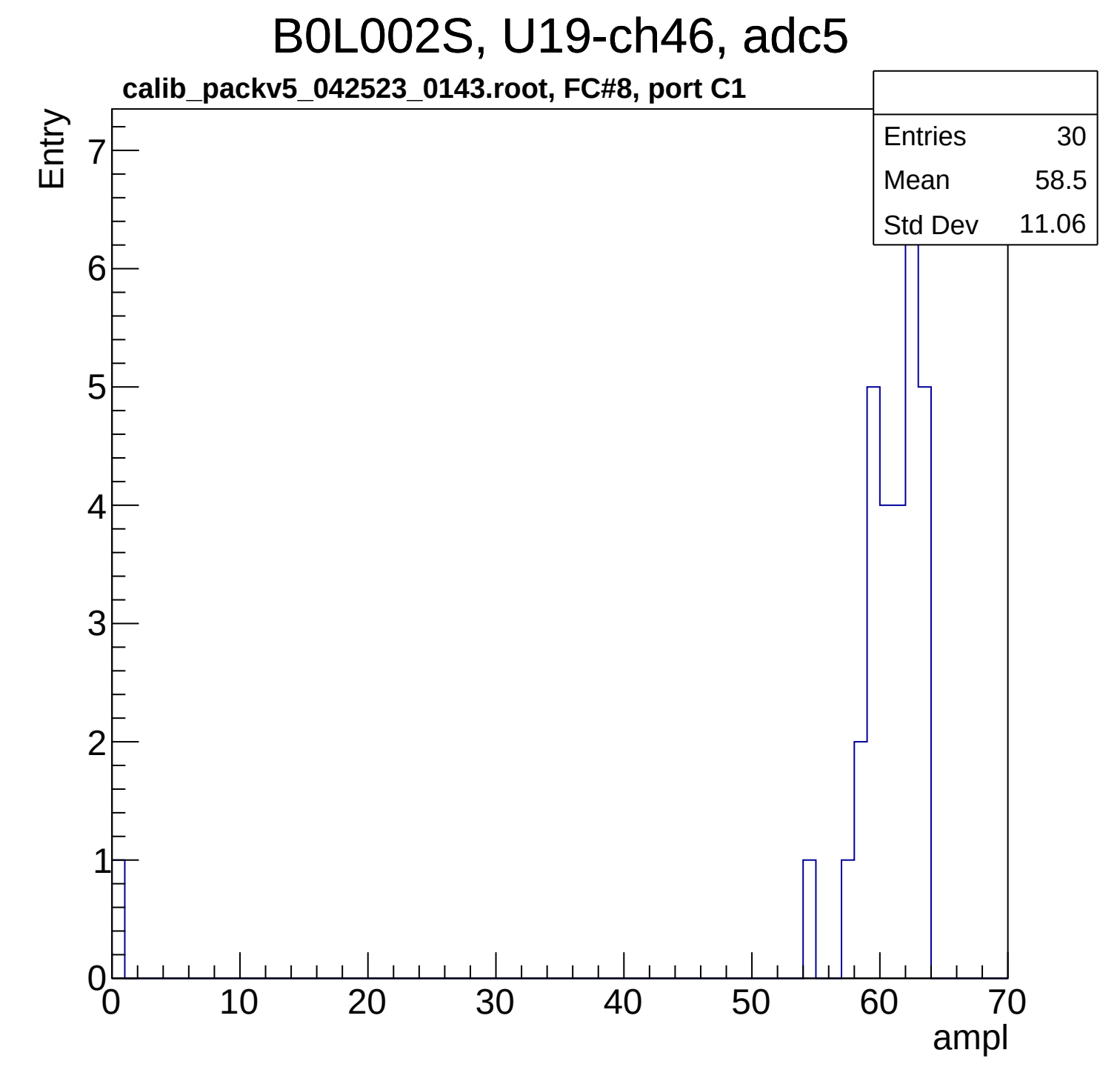

B0L002S, U19-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 31.4
Std Dev 3.335
Gaus mean : 31.0147
Gaus Width: 3.4358
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

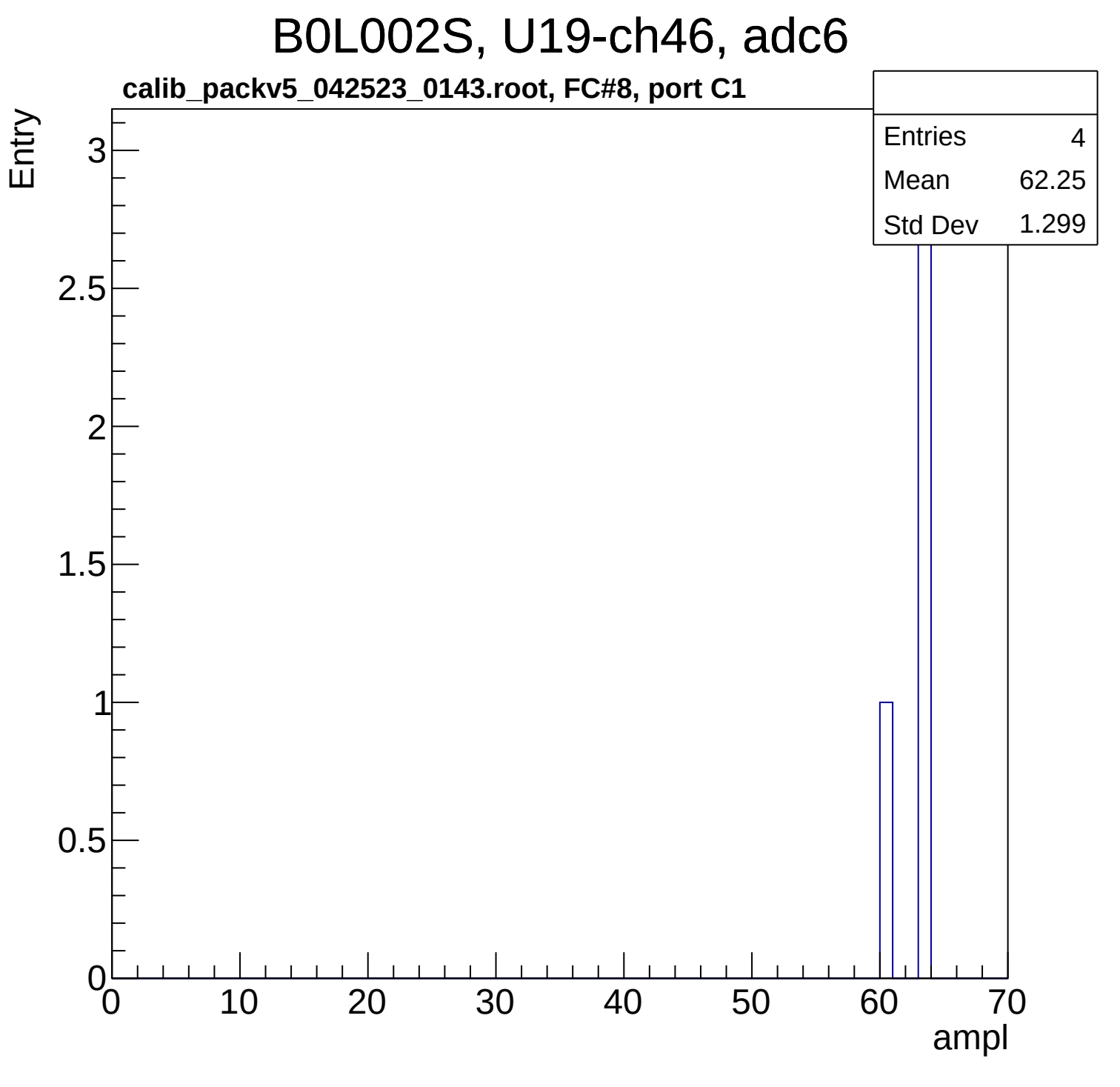

B0L002S, U19-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.32
Std Dev 3.251
Gaus mean : 37.4167
Gaus Width: 3.7039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

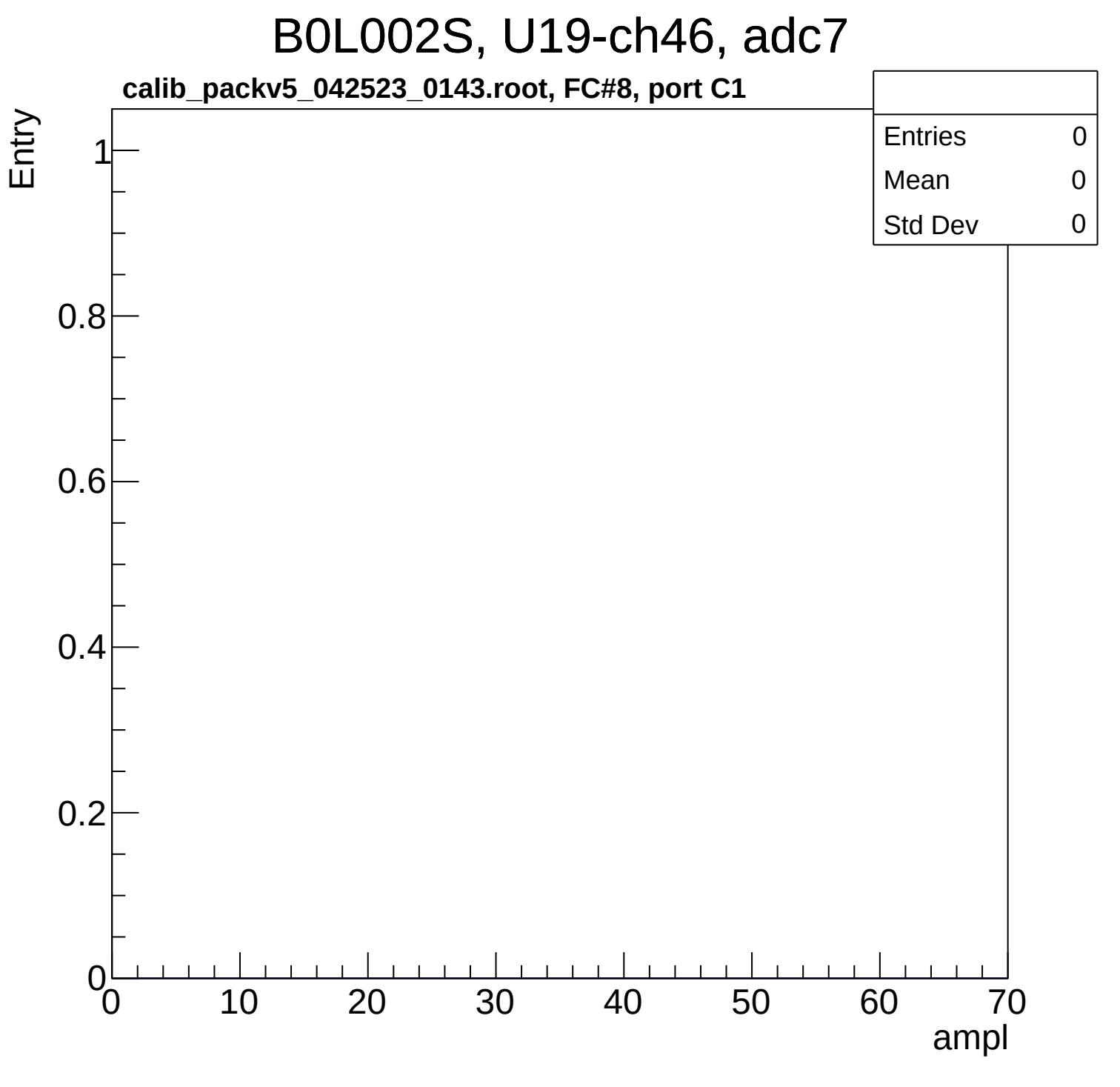

B0L002S, U19-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.56
Std Dev 2.765
Gaus mean : 45.2910
Gaus Width: 3.0787
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

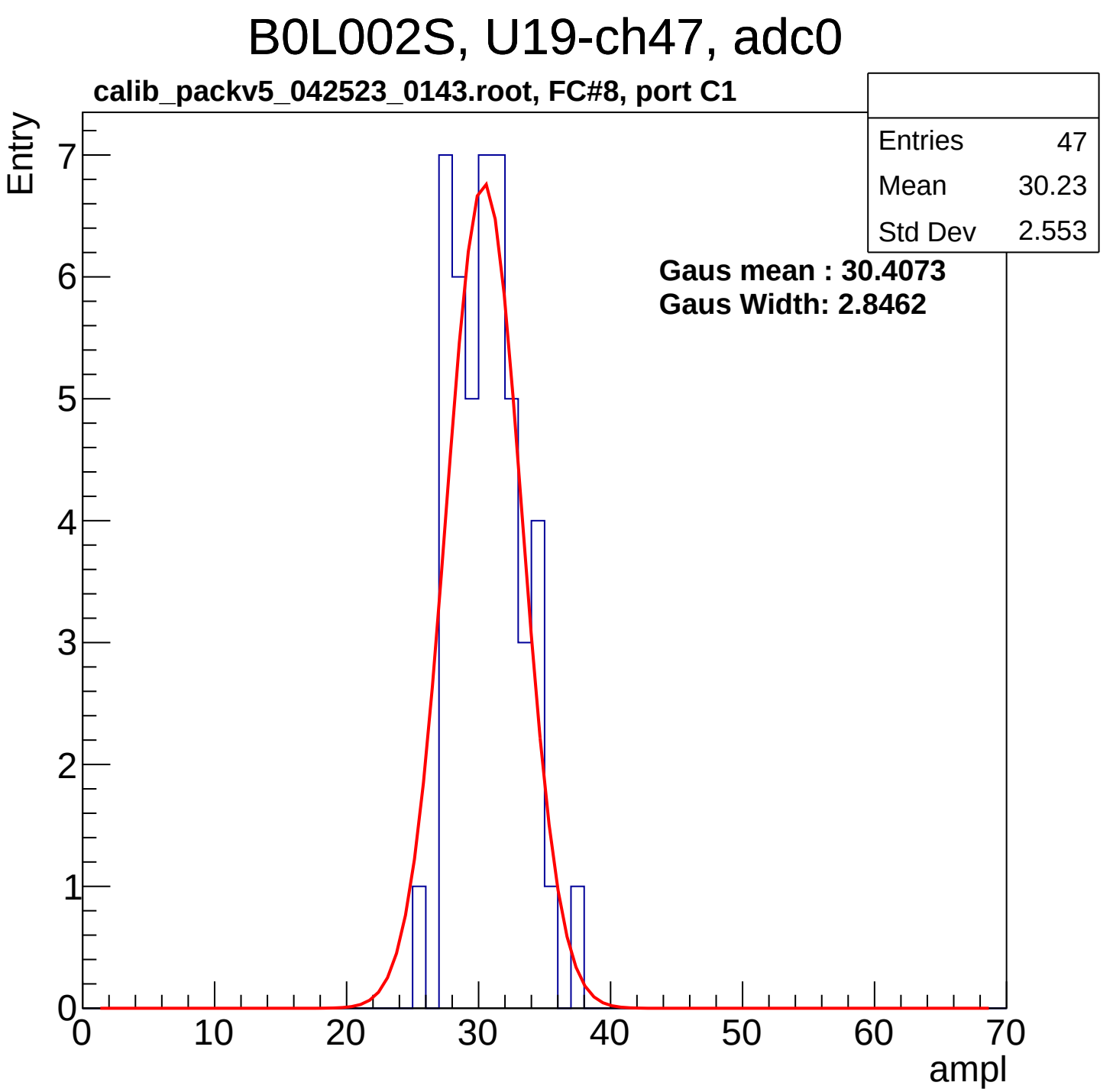

B0L002S, U19-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 51.35
Std Dev 2.633
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

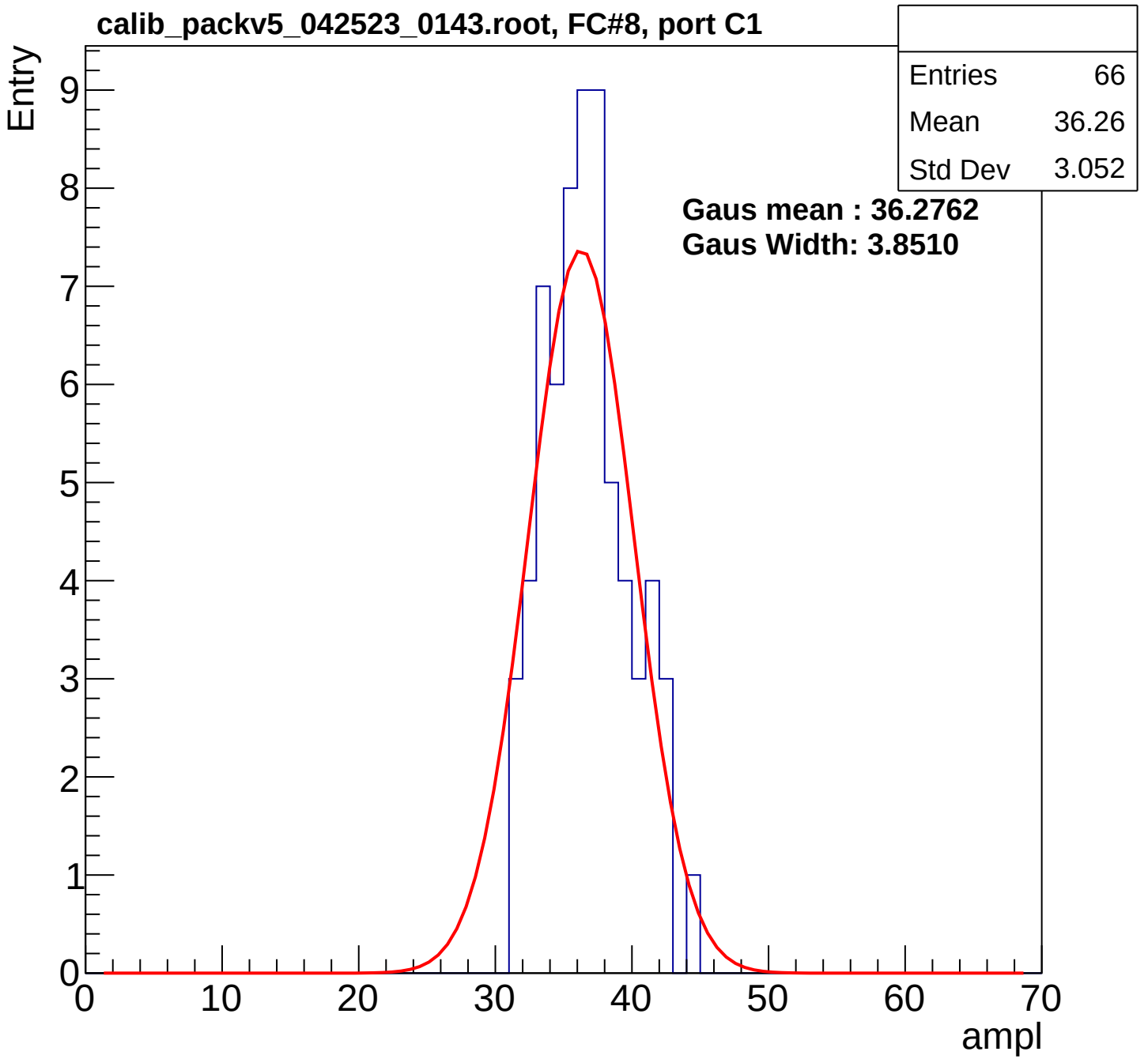

B0L002S, U19-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 56.64
Std Dev 3.464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

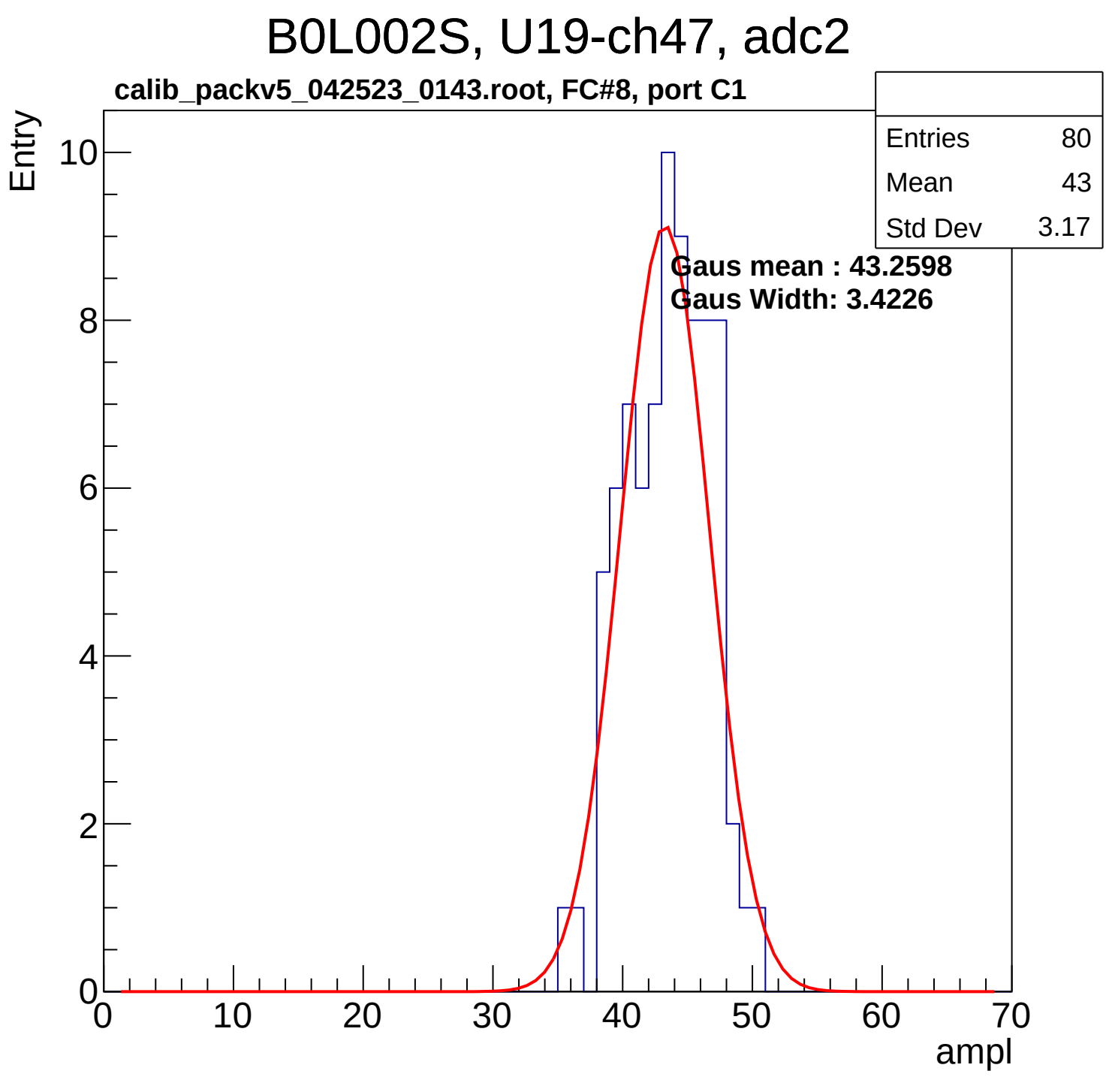

B0L002S, U19-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.5
Std Dev 11.06
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

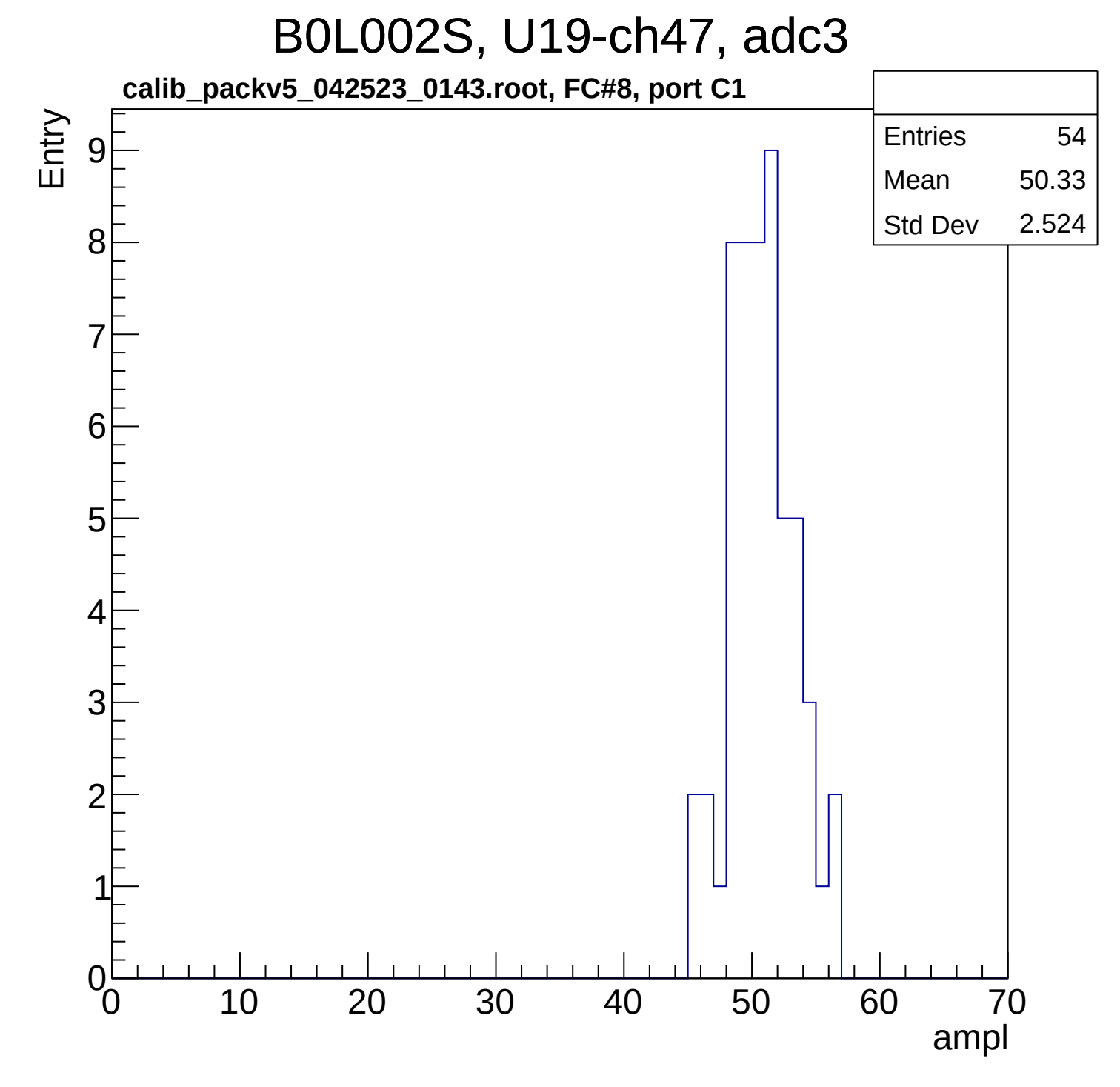

B0L002S, U19-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

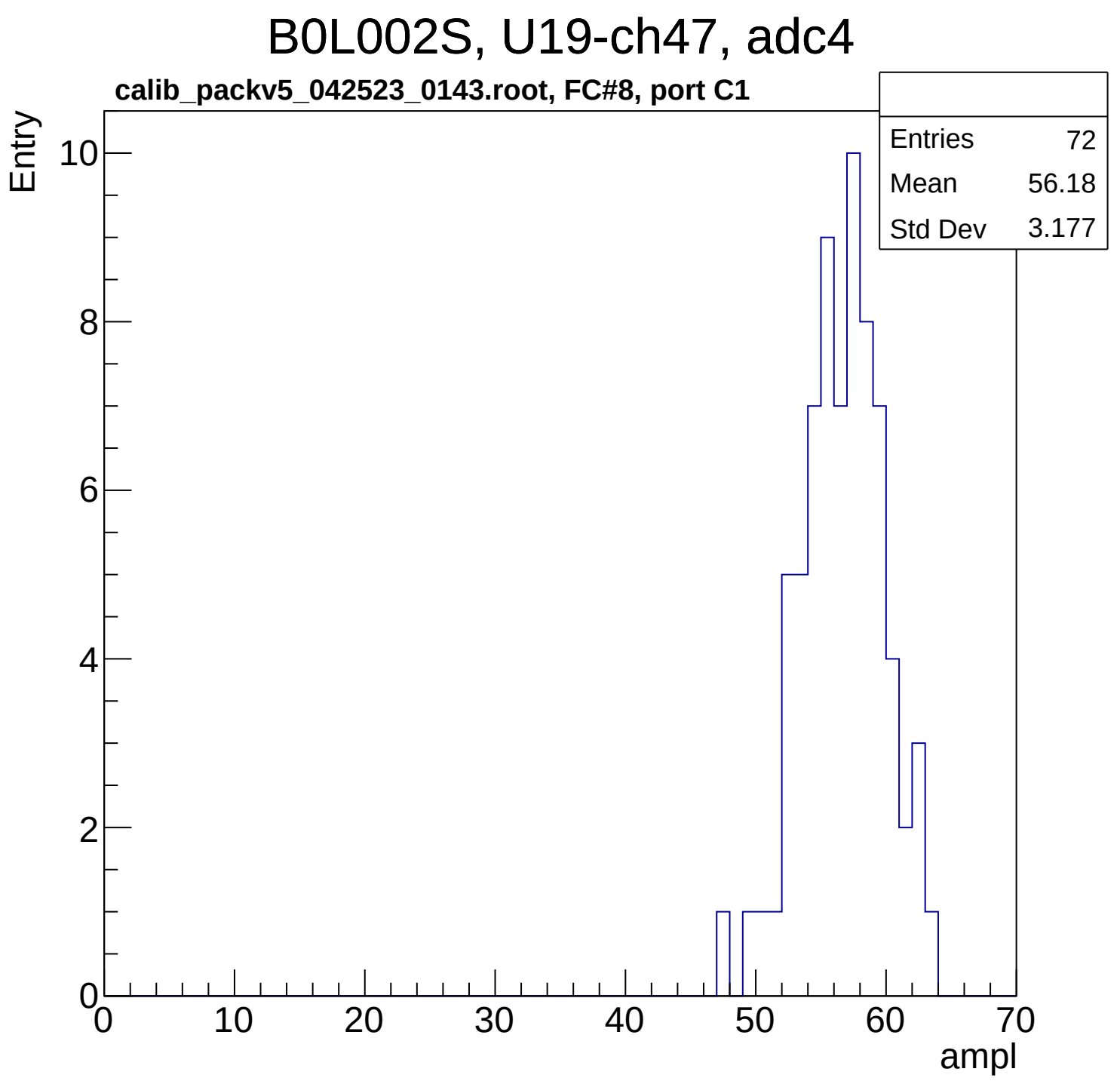

B0L002S, U19-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

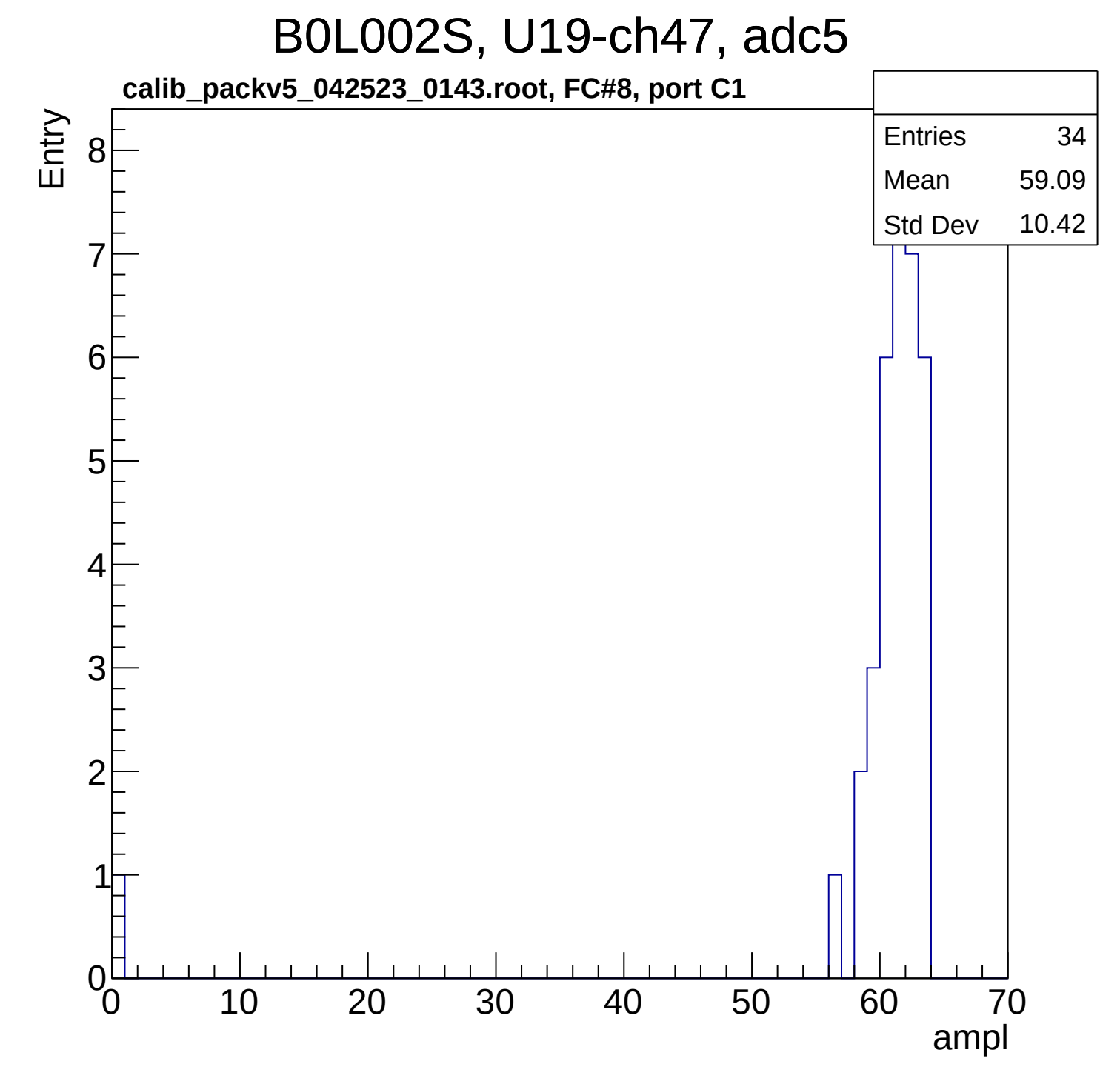

B0L002S, U19-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 30.23
Std Dev 2.553
Gaus mean : 30.4073
Gaus Width: 2.8462
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

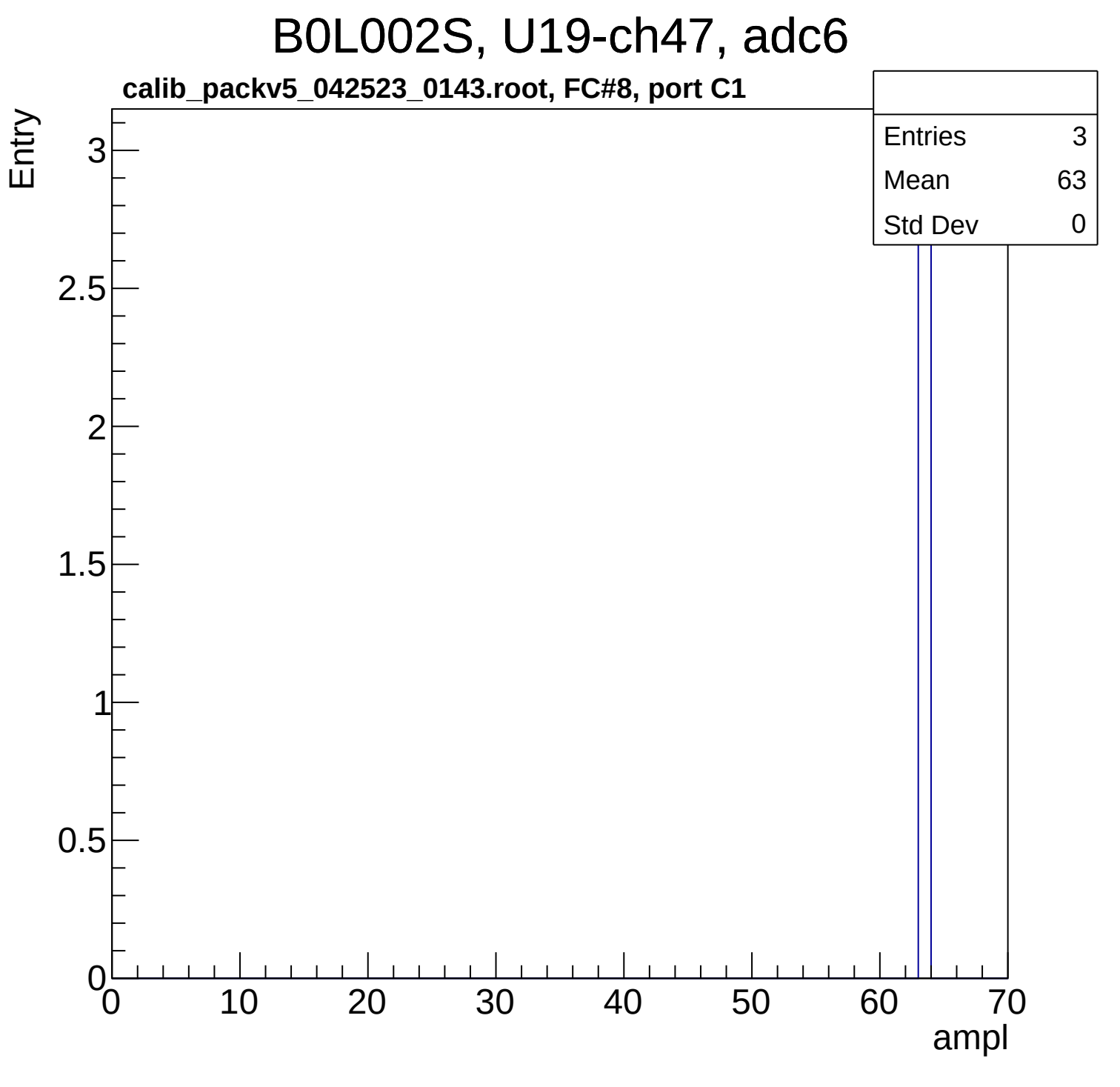

B0L002S, U19-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.26
Std Dev 3.052
Gaus mean : 36.2762
Gaus Width: 3.8510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

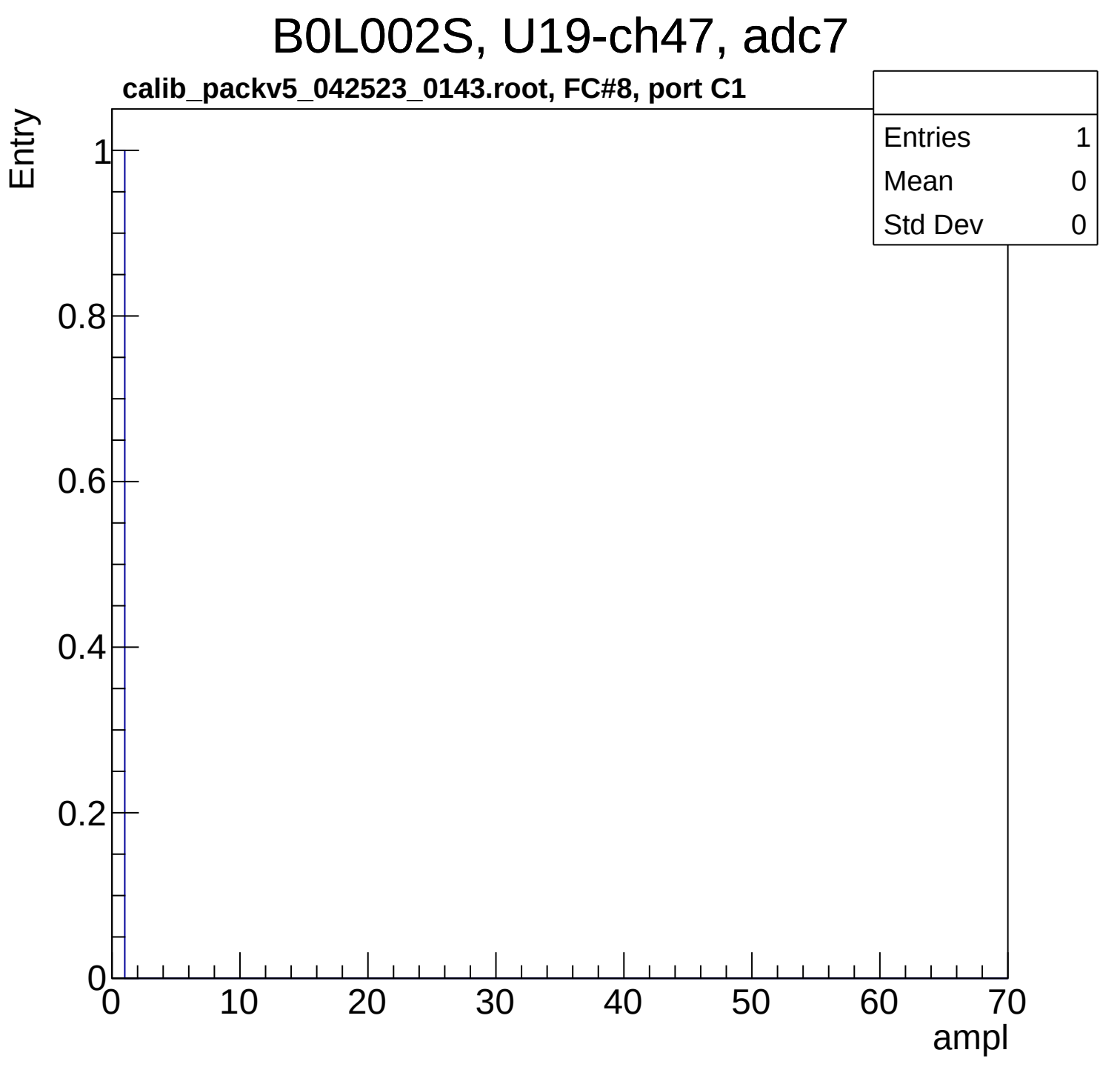

B0L002S, U19-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43
Std Dev 3.17
Gaus mean : 43.2598
Gaus Width: 3.4226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

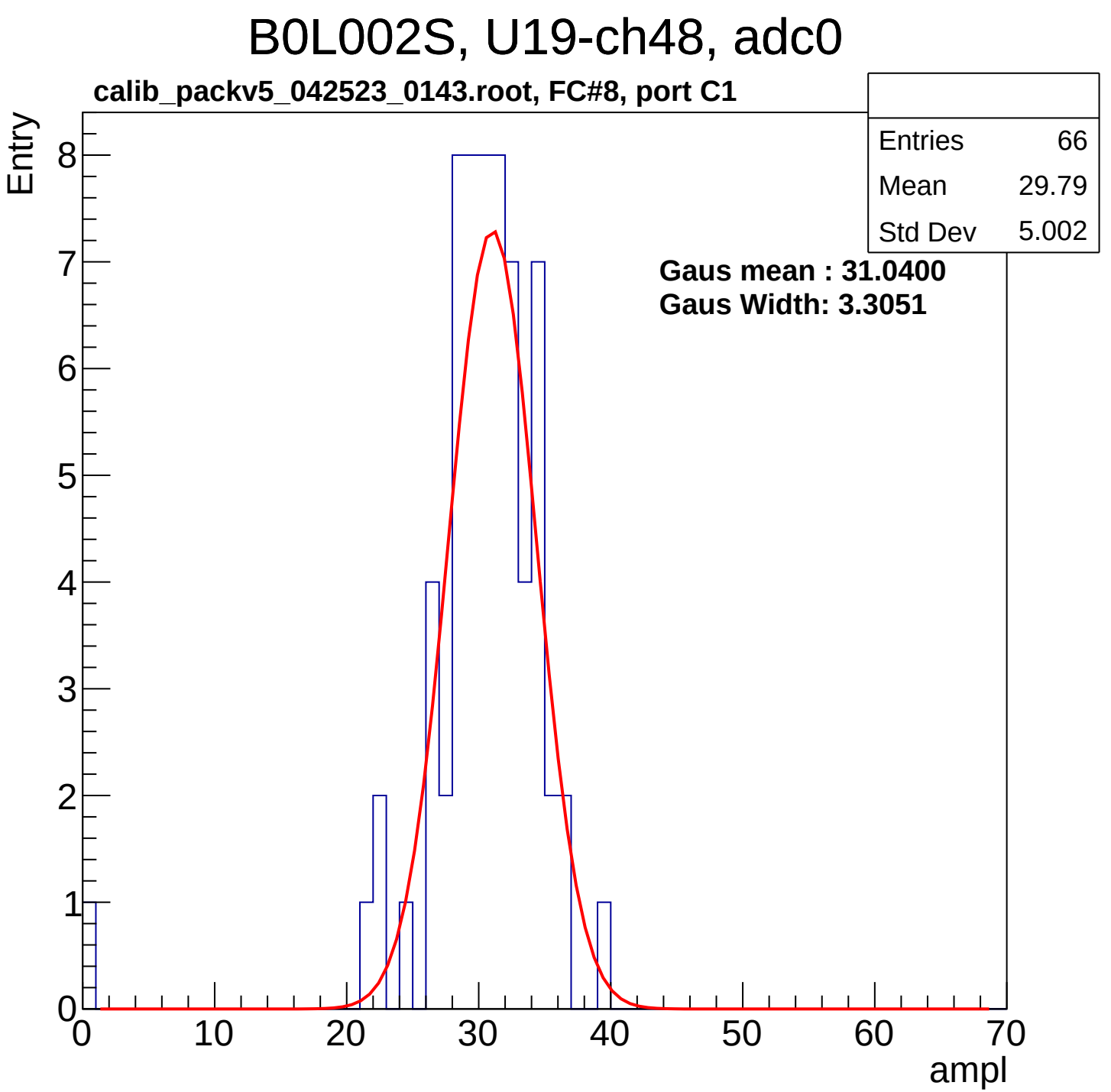

B0L002S, U19-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.33
Std Dev 2.524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

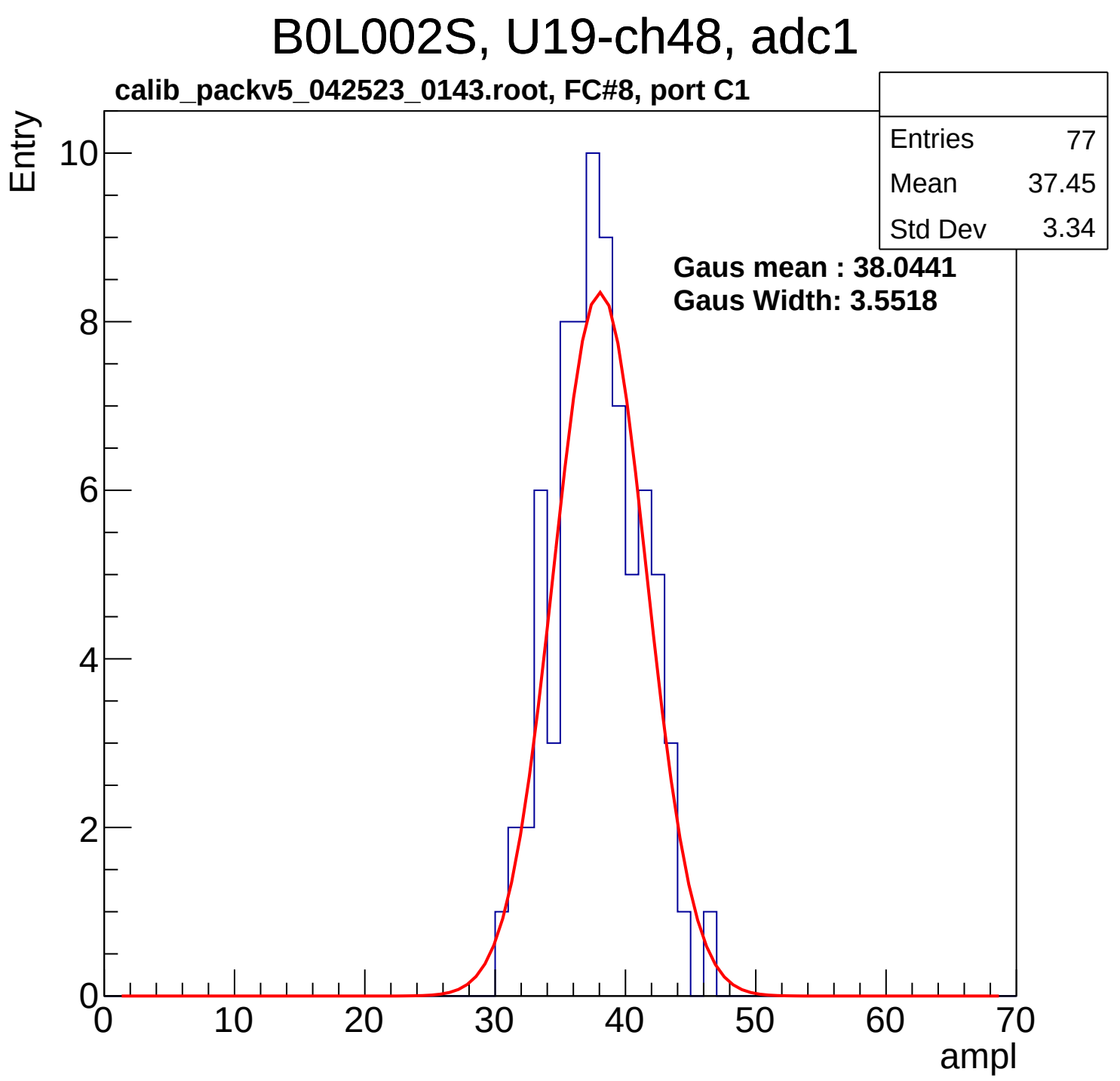

B0L002S, U19-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 56.18
Std Dev 3.177
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

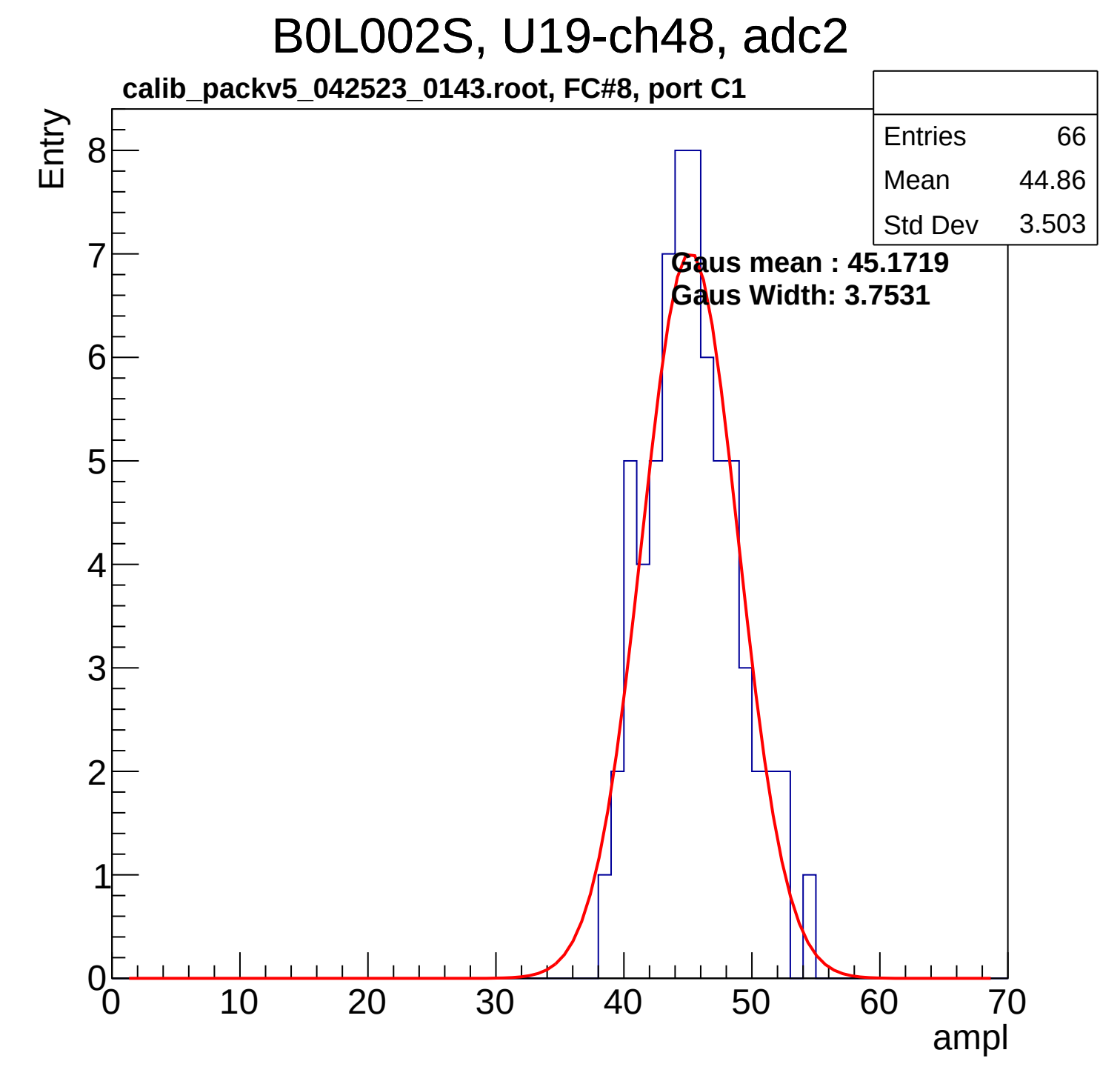

B0L002S, U19-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.09
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

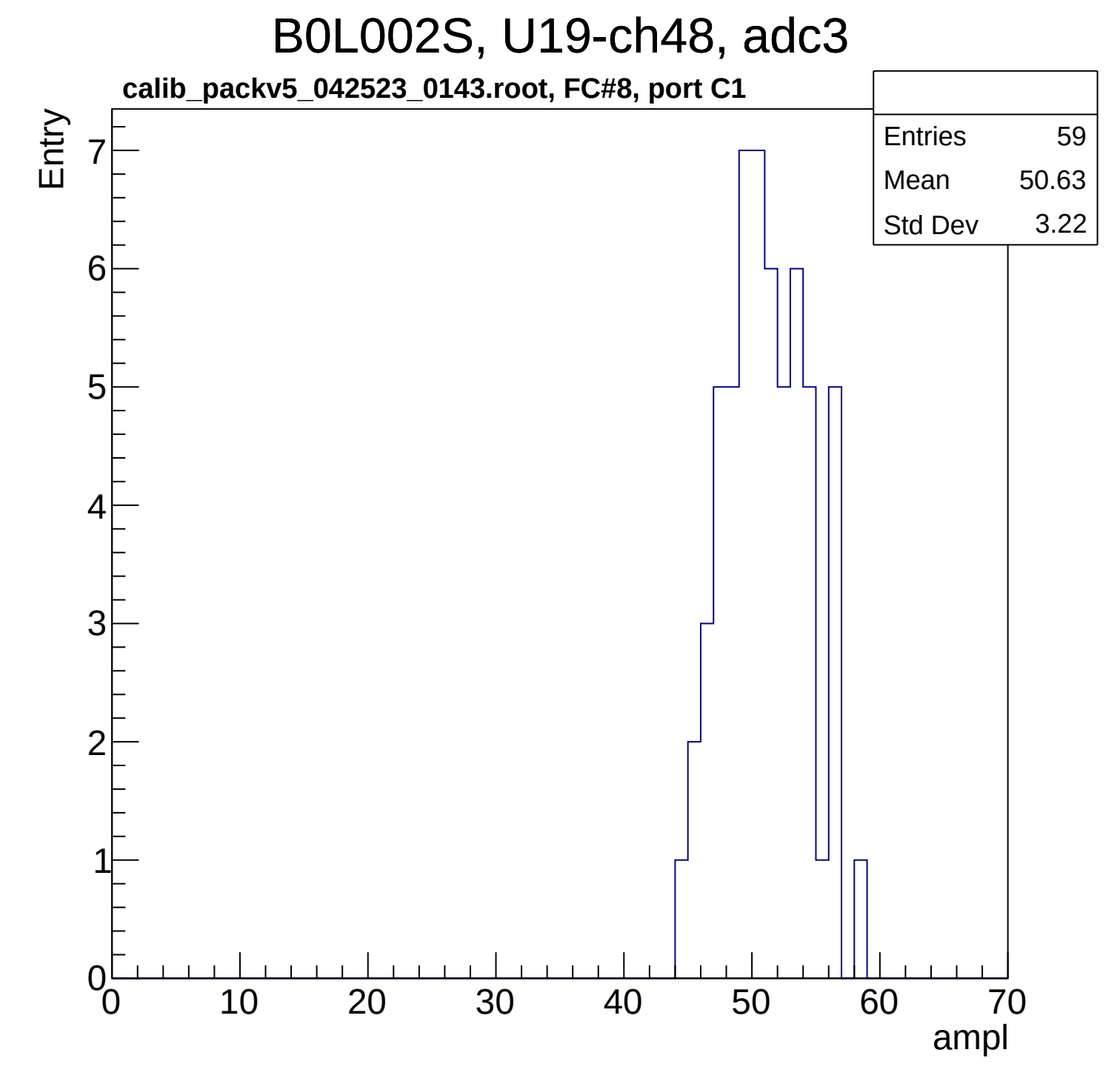

B0L002S, U19-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

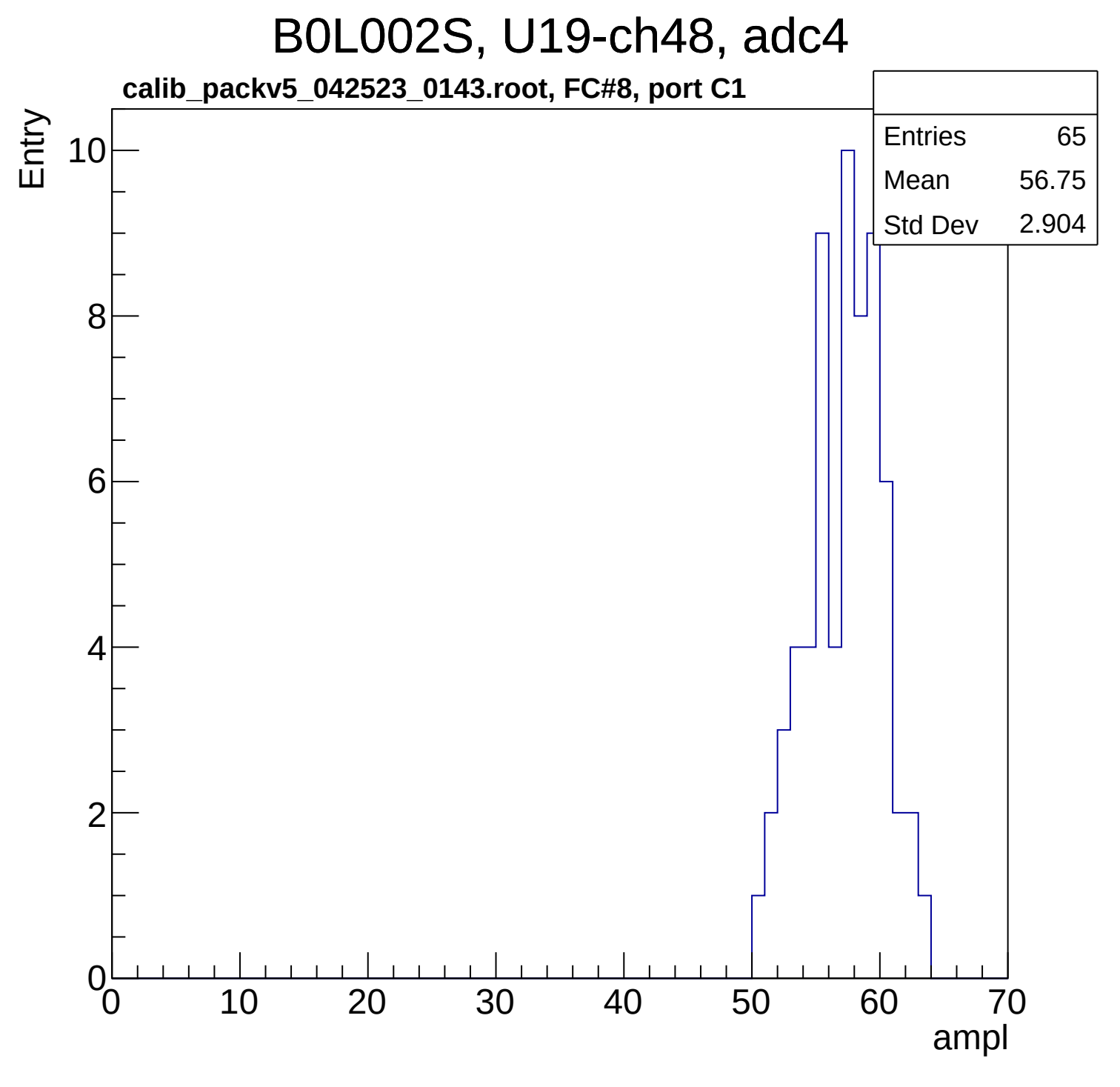

B0L002S, U19-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

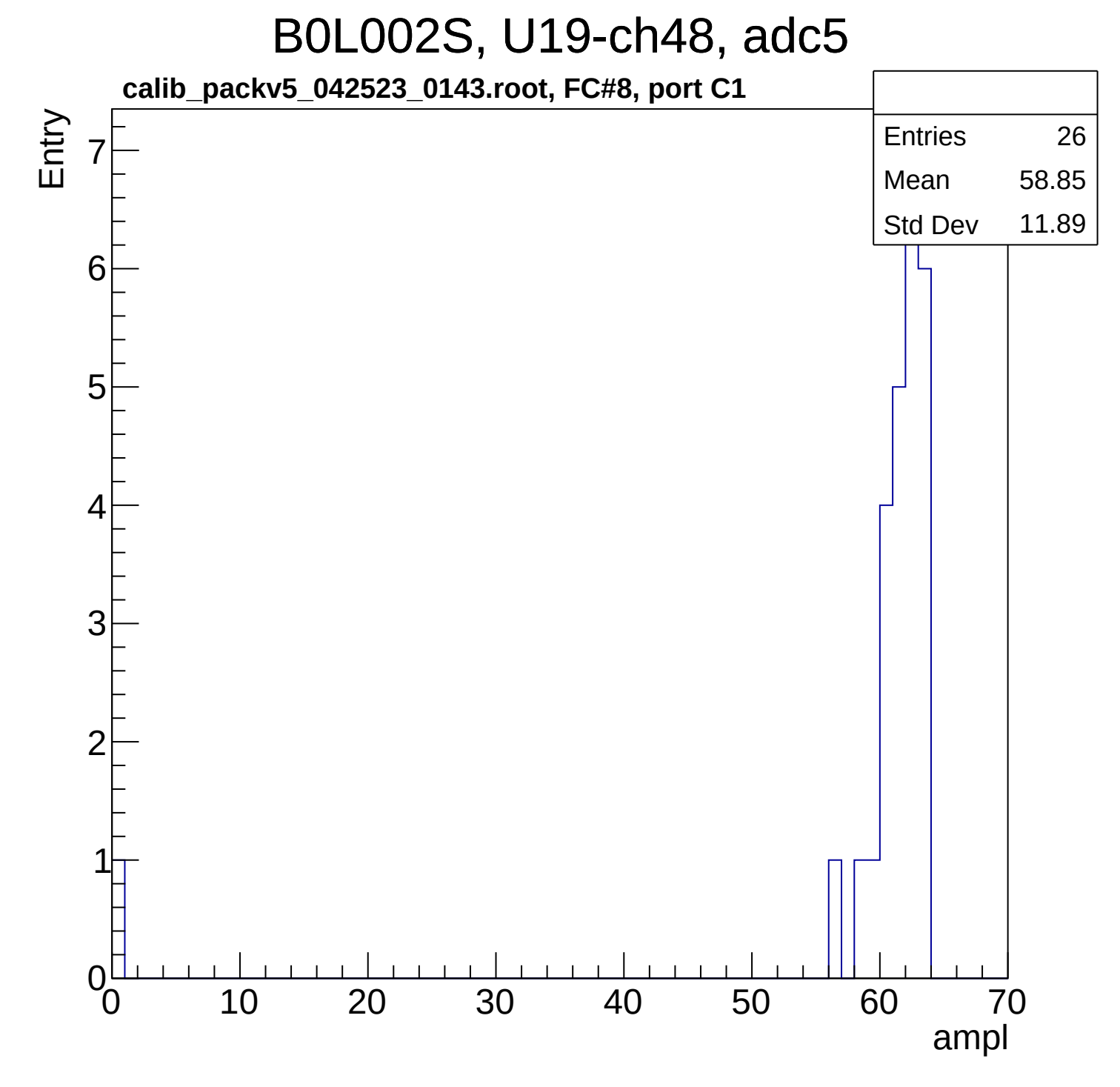

B0L002S, U19-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.79
Std Dev 5.002
Gaus mean : 31.0400
Gaus Width: 3.3051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

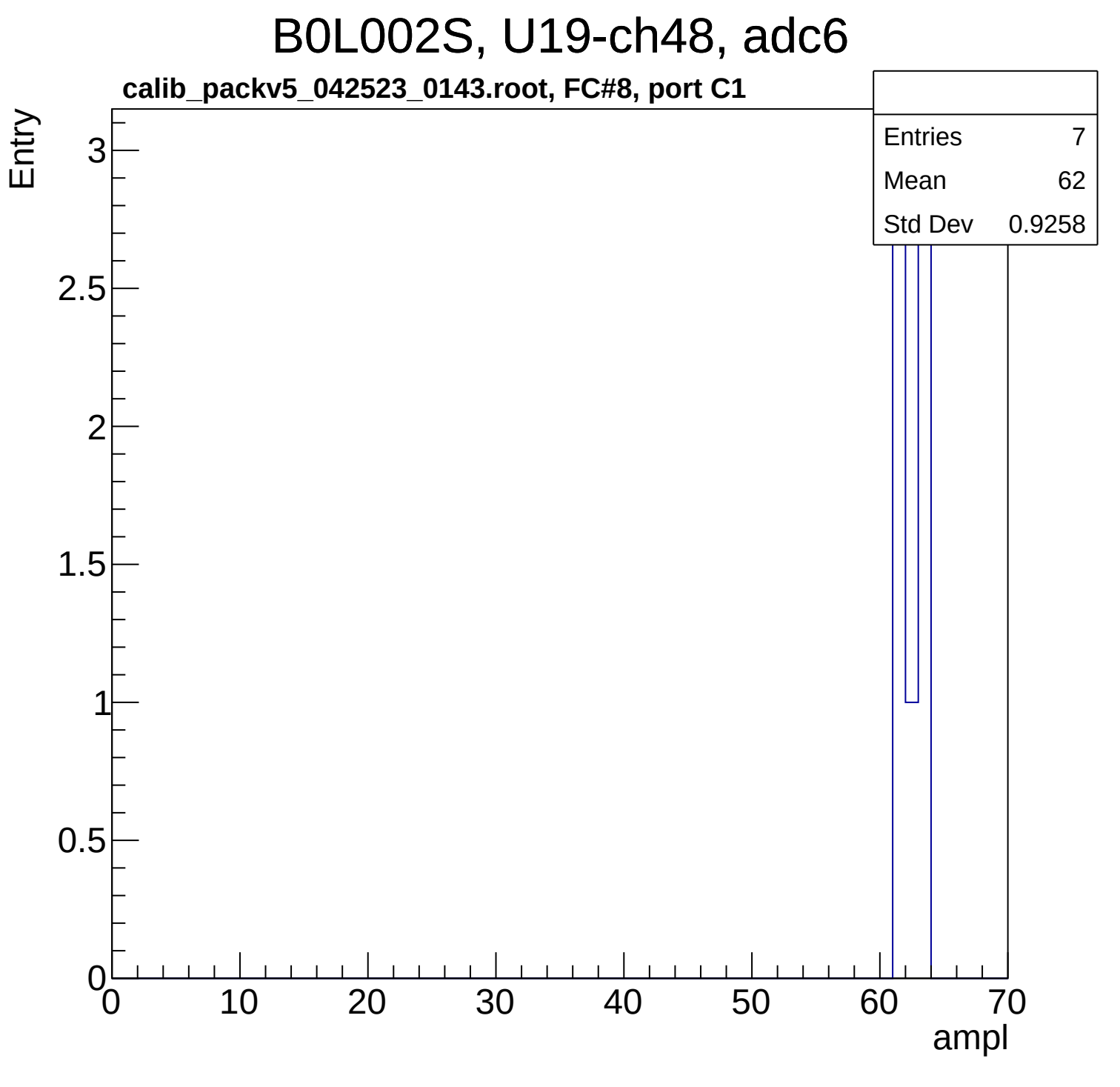

B0L002S, U19-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.45
Std Dev 3.34
Gaus mean : 38.0441
Gaus Width: 3.5518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

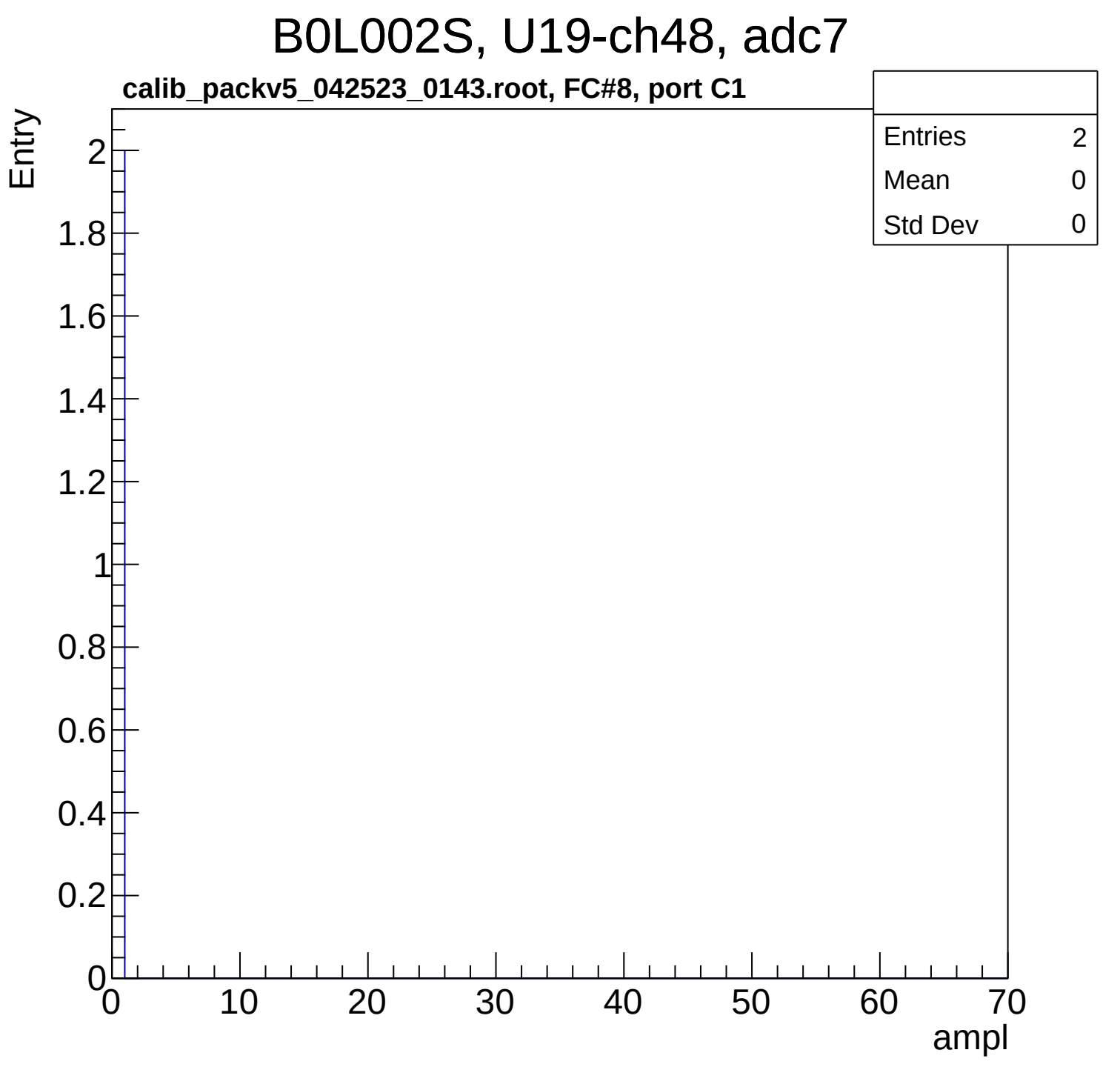

B0L002S, U19-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.86
Std Dev 3.503
Gaus mean : 45.1719
Gaus Width: 3.7531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

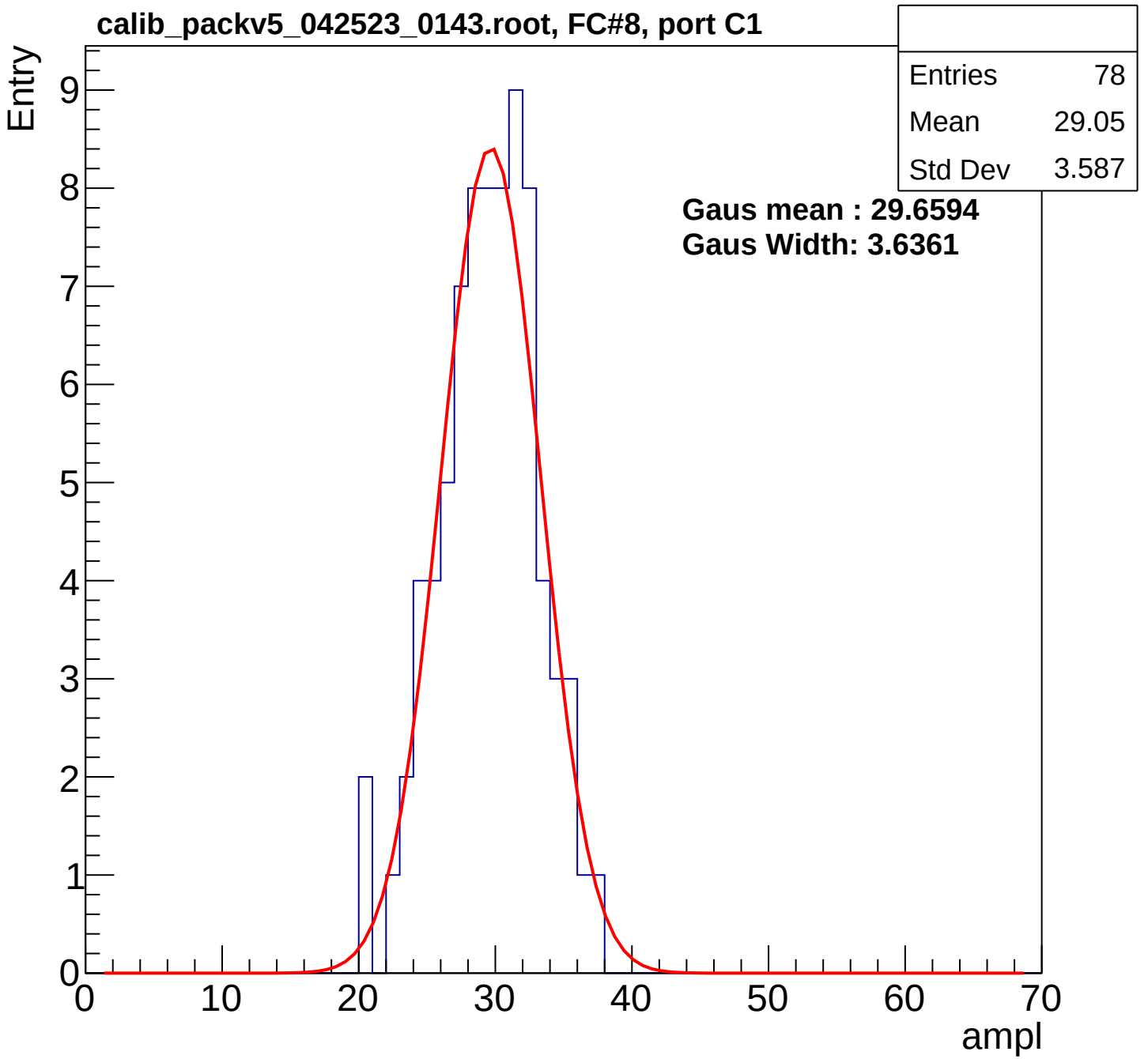

B0L002S, U19-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.63
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

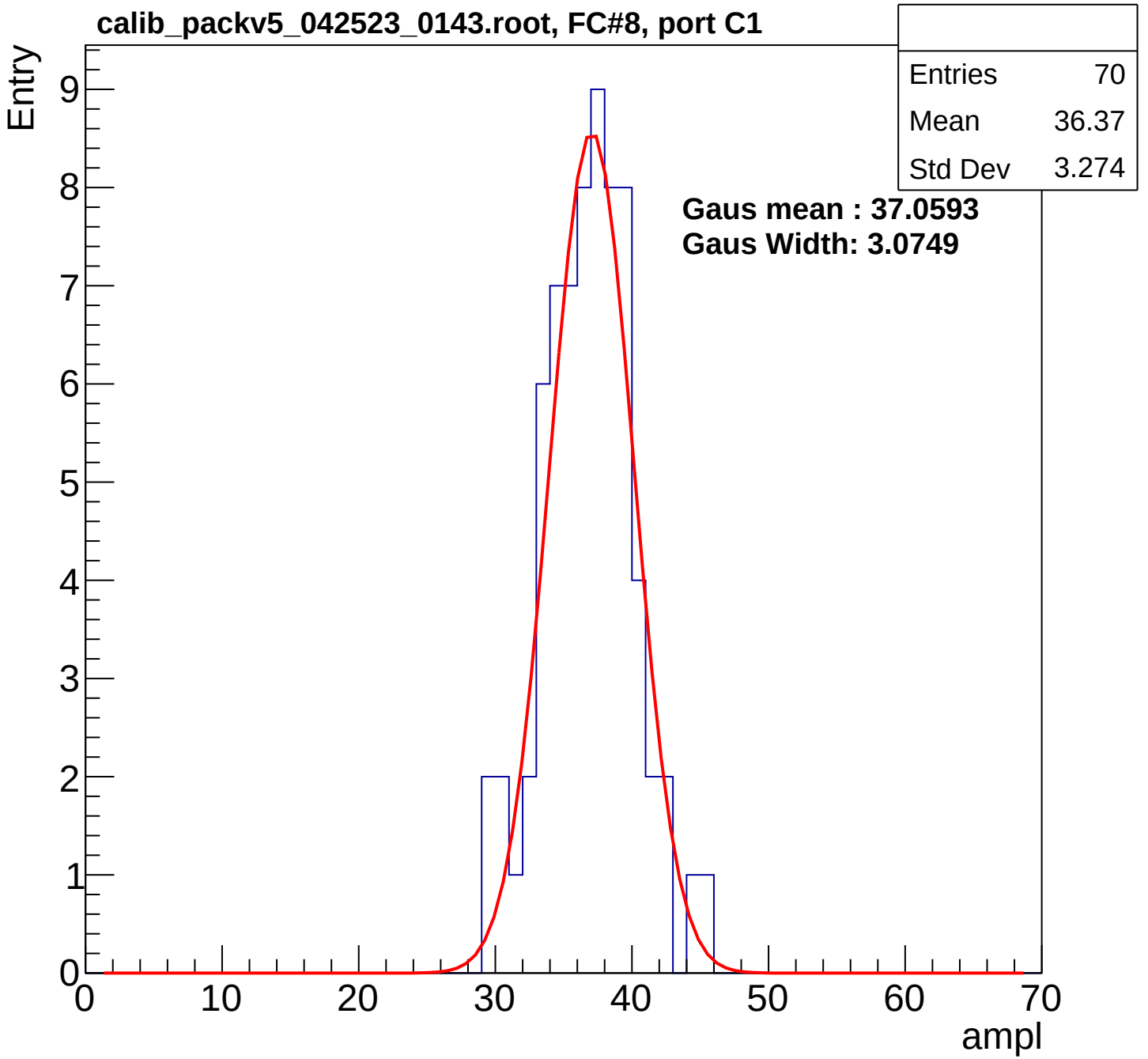

B0L002S, U19-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.75
Std Dev 2.904
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

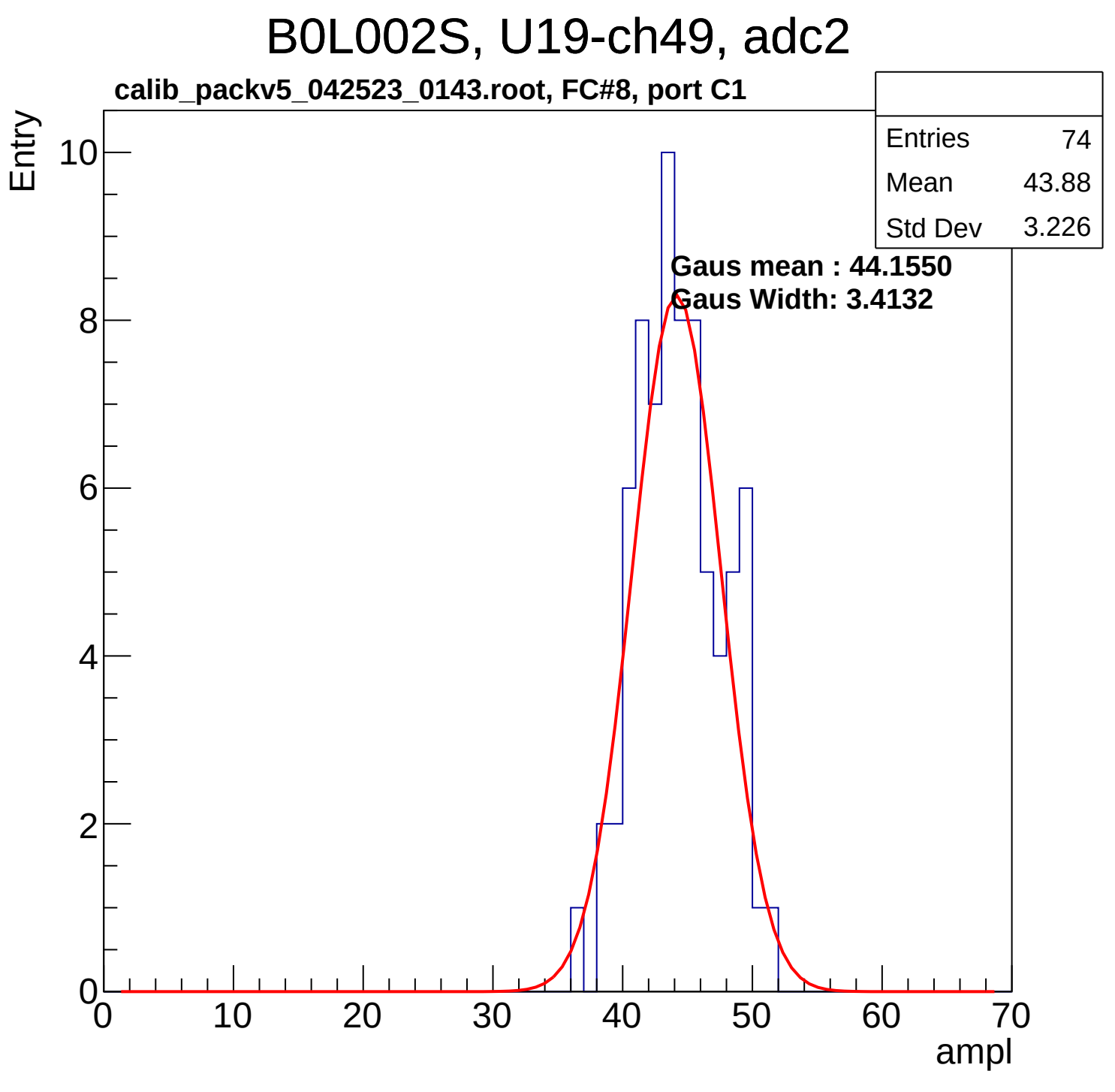

B0L002S, U19-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.85
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

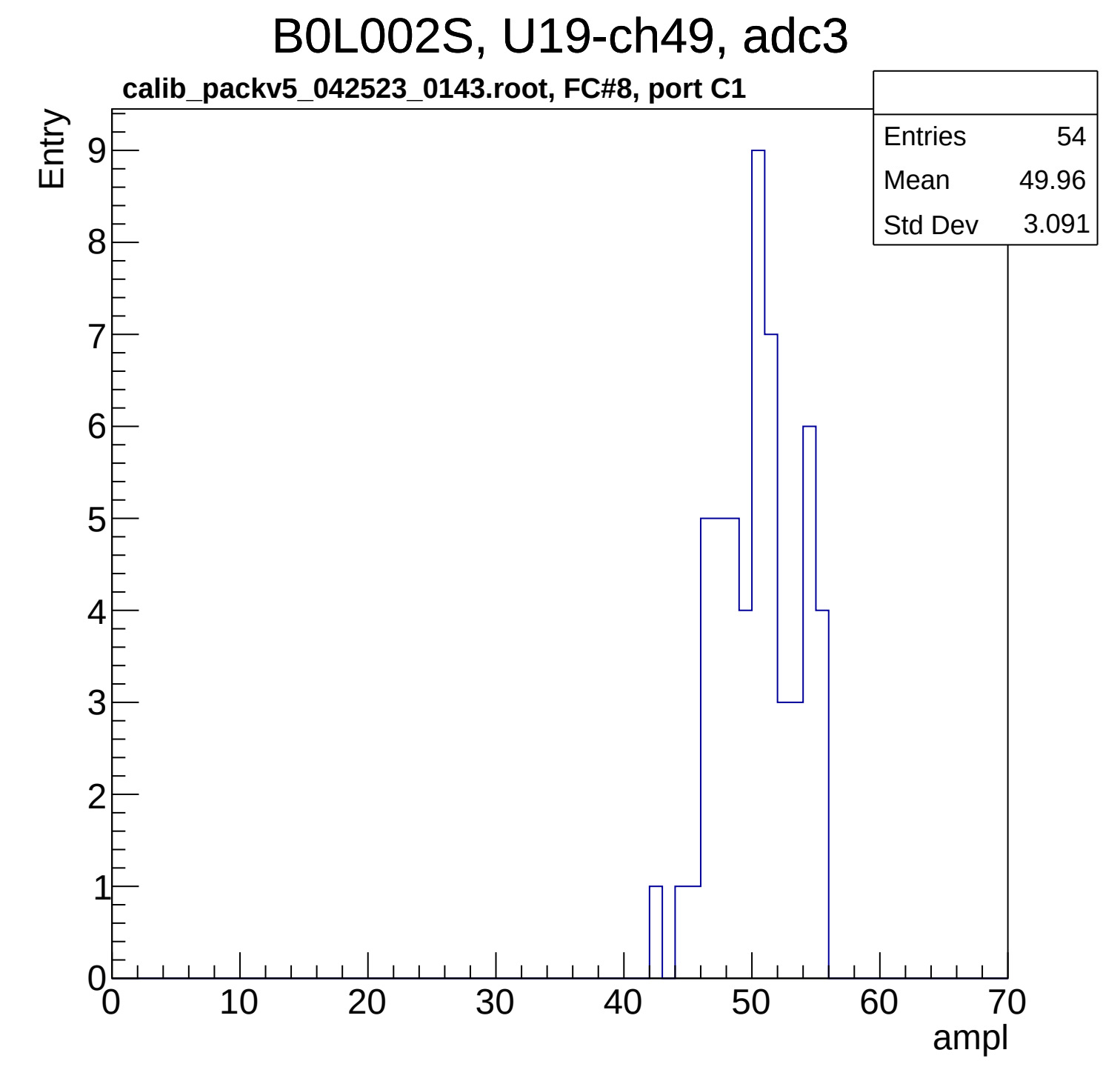

B0L002S, U19-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62
Std Dev 0.9258
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

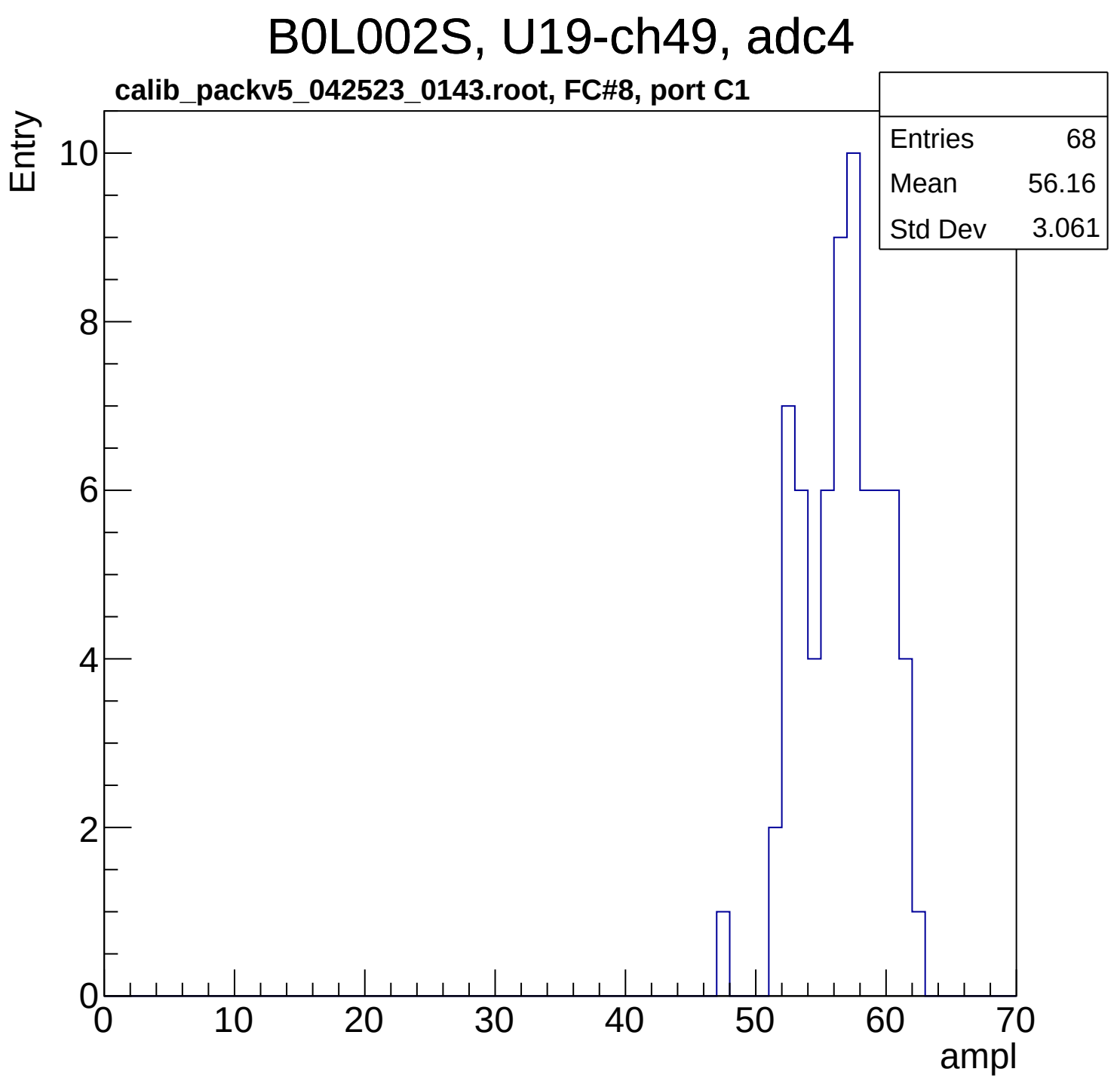

B0L002S, U19-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

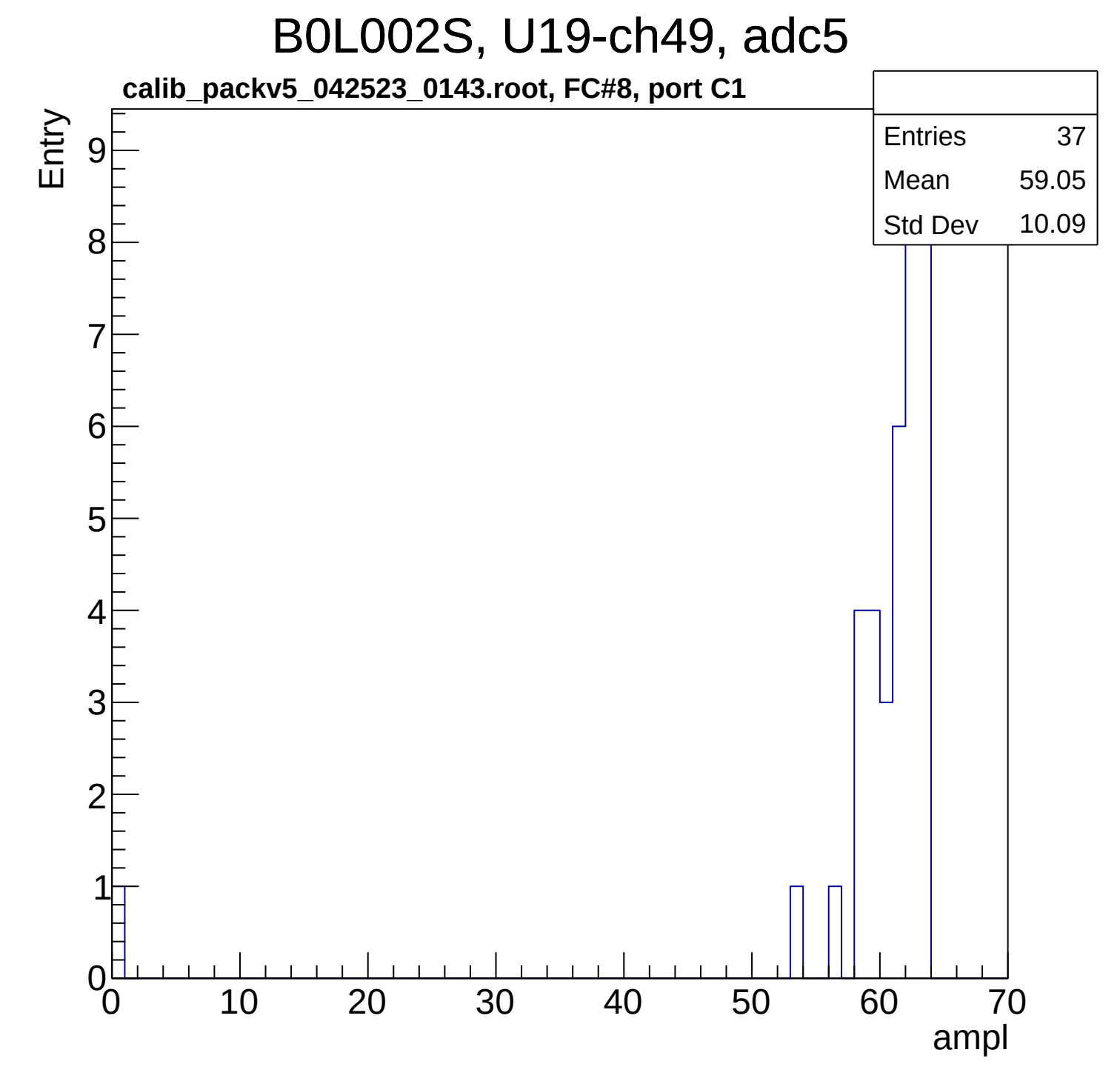

B0L002S, U19-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.05
Std Dev 3.587
Gaus mean : 29.6594
Gaus Width: 3.6361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

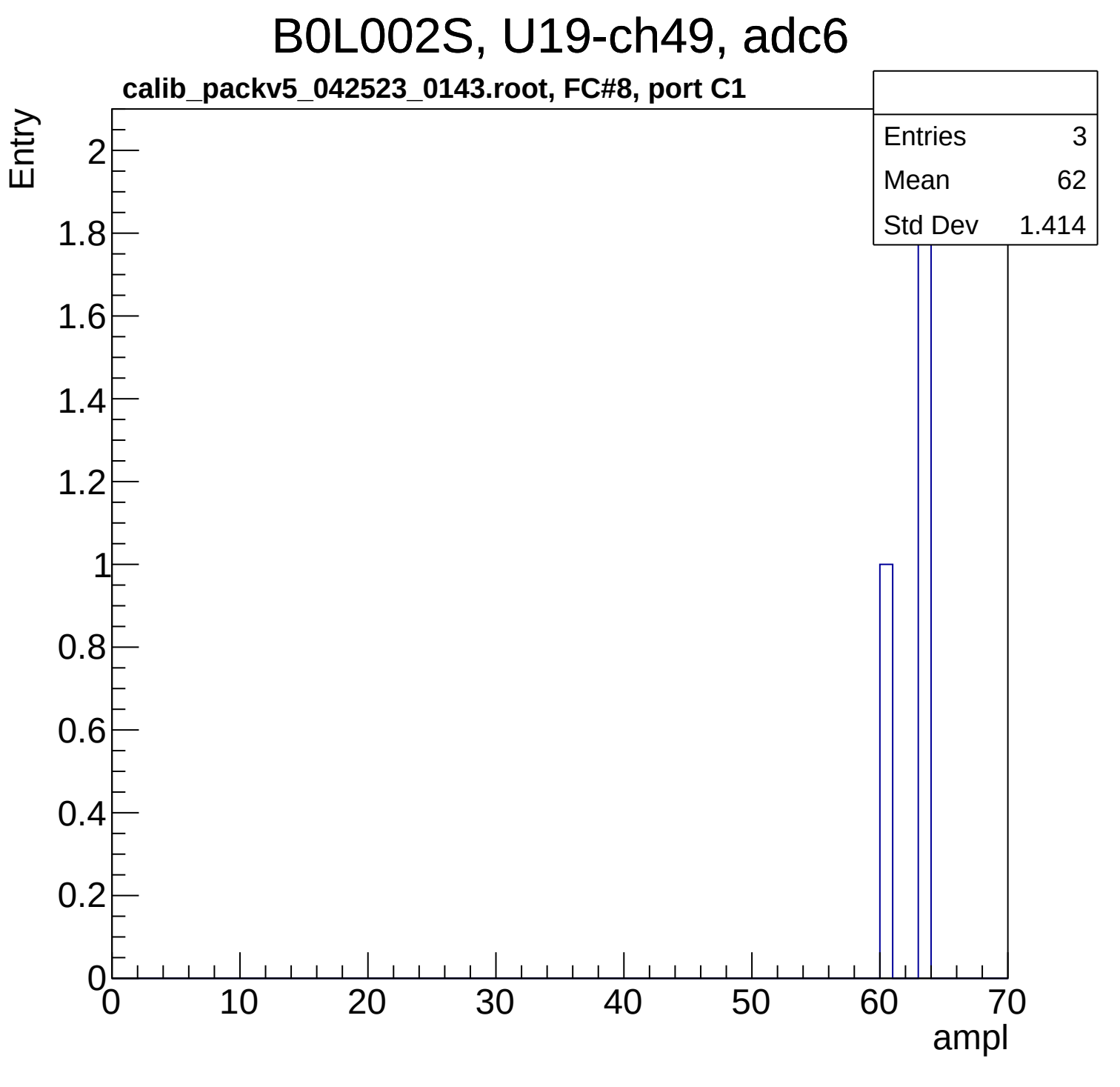

B0L002S, U19-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.37
Std Dev 3.274
Gaus mean : 37.0593
Gaus Width: 3.0749
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

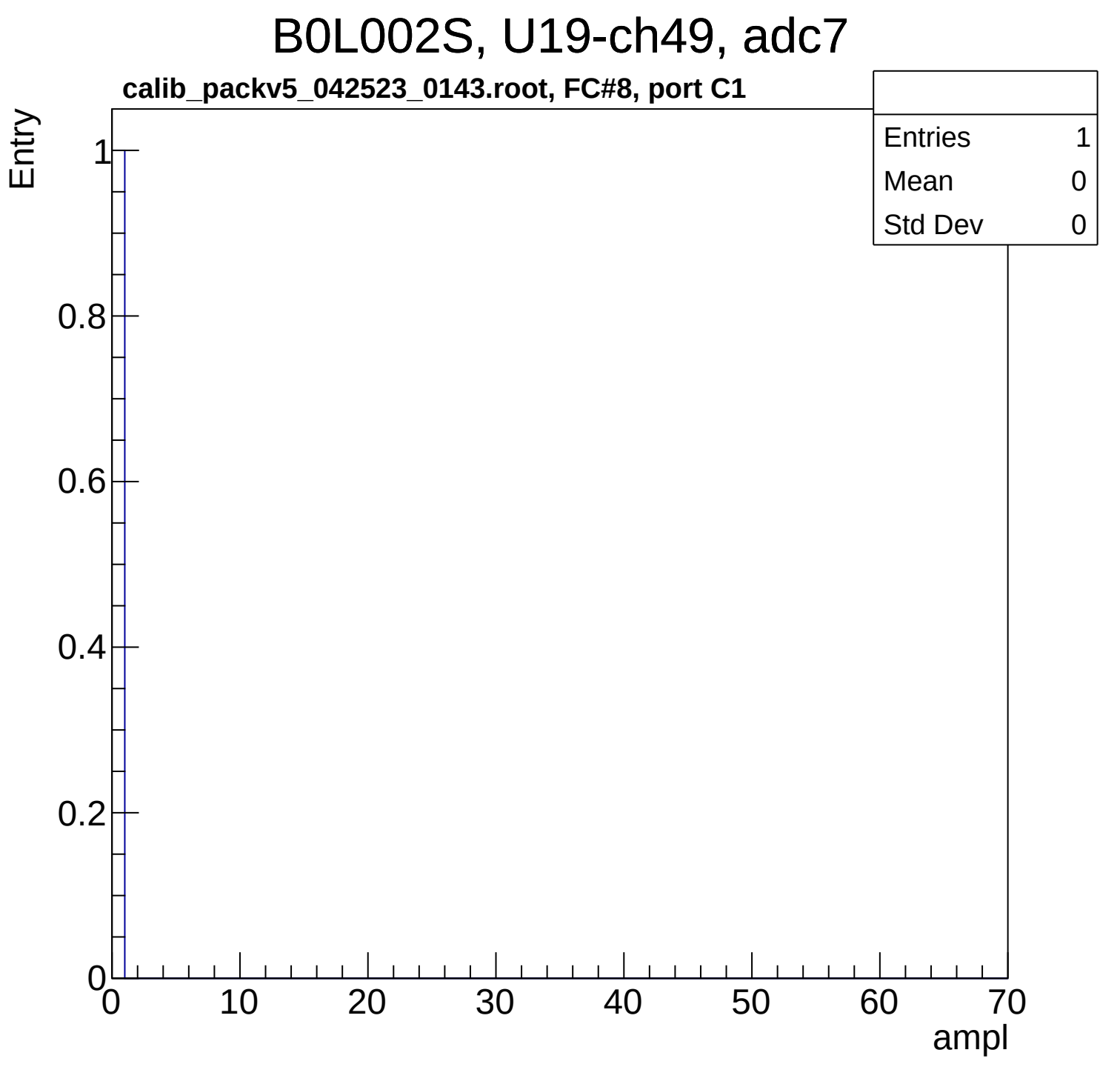

B0L002S, U19-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.88
Std Dev 3.226
Gaus mean : 44.1550
Gaus Width: 3.4132
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

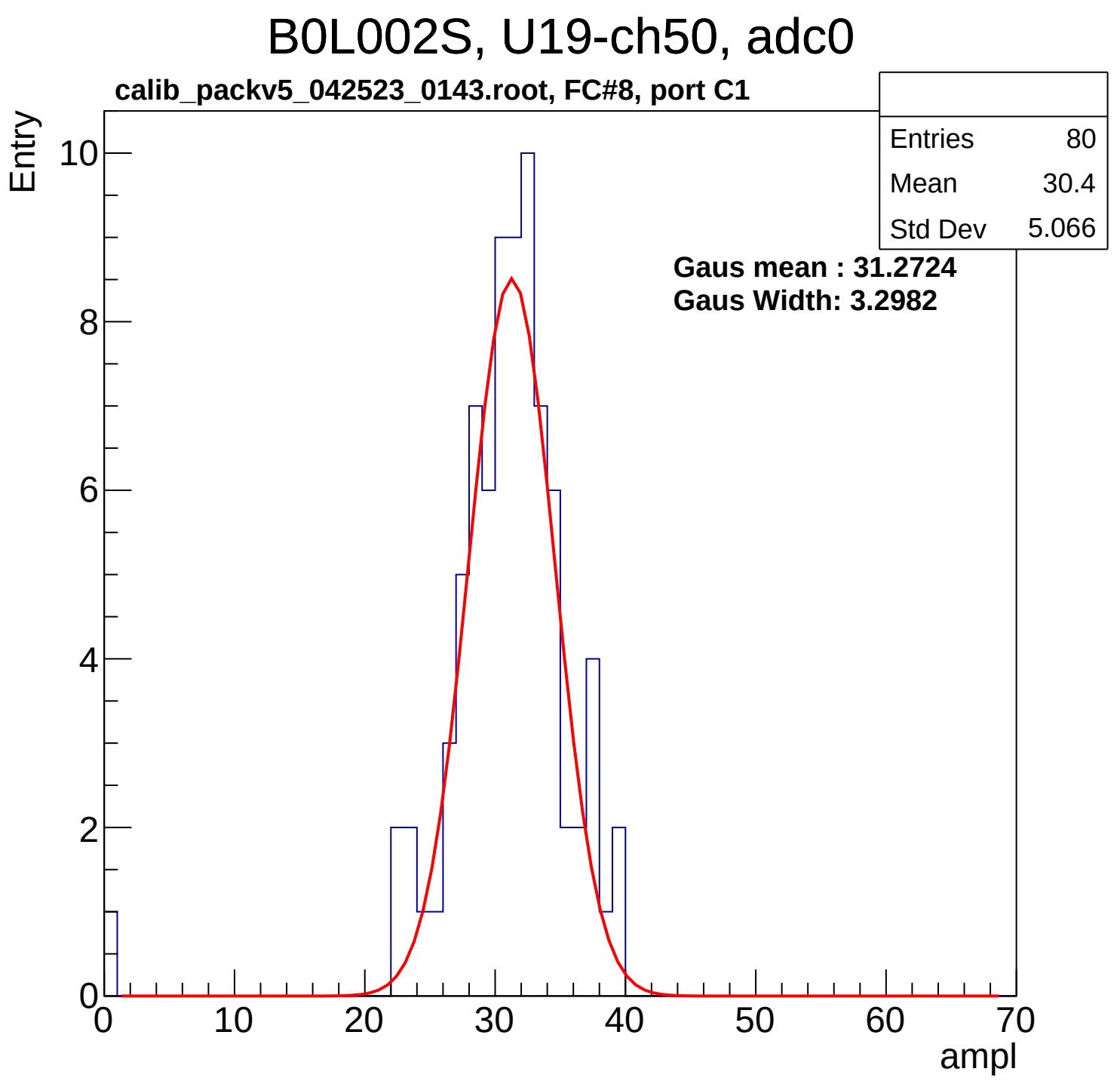

B0L002S, U19-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 49.96
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

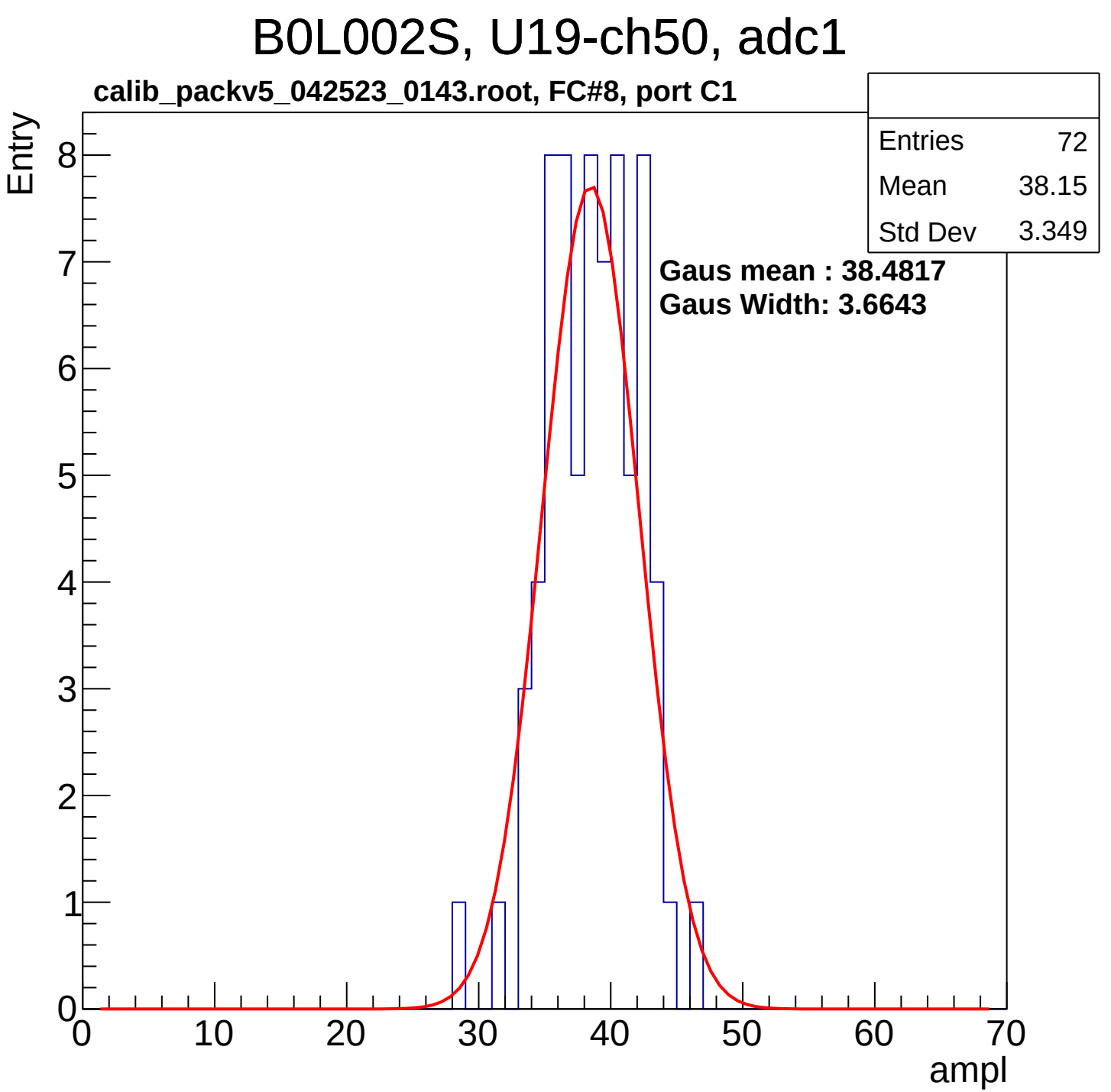

B0L002S, U19-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.16
Std Dev 3.061
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

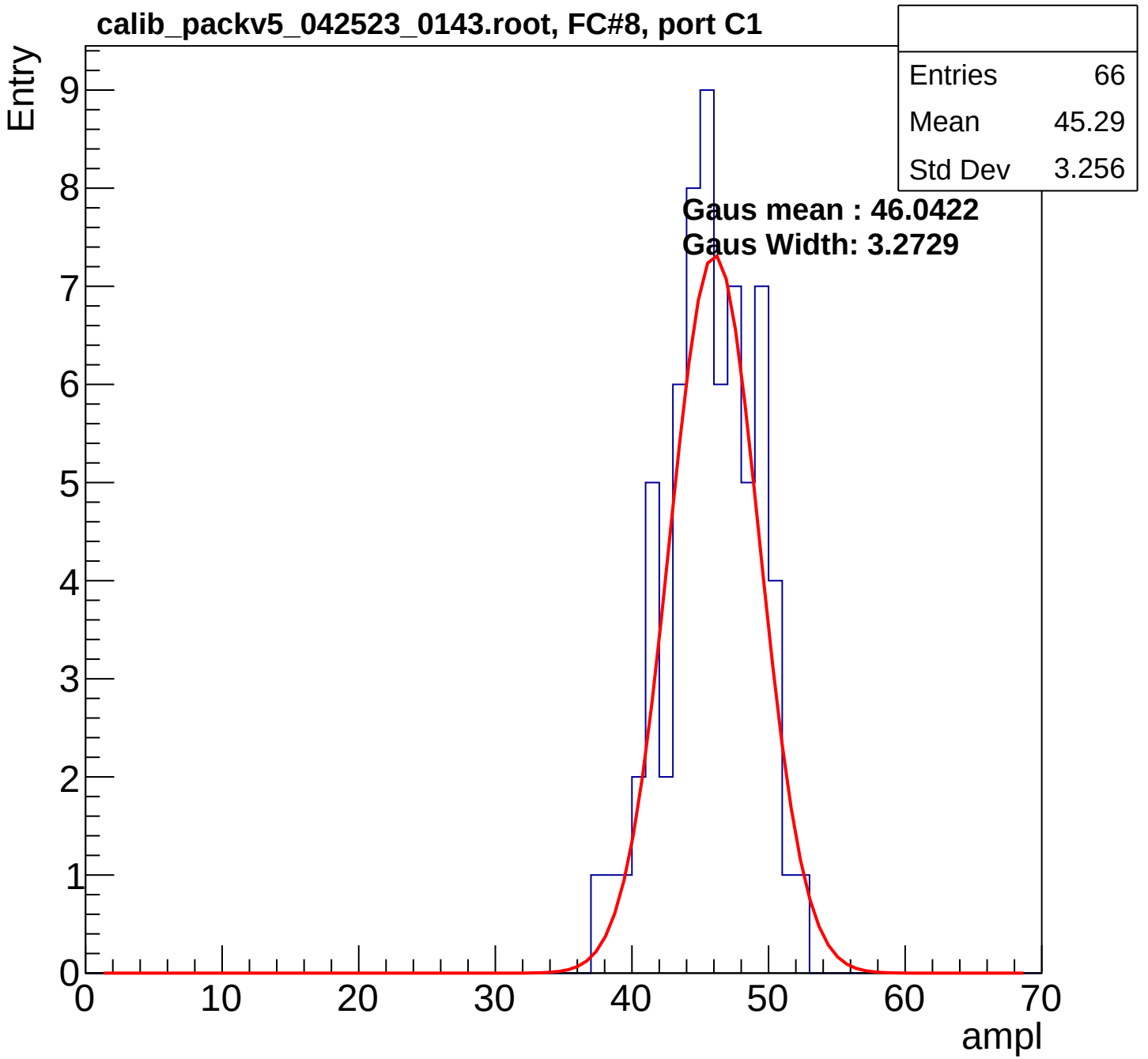

B0L002S, U19-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.05
Std Dev 10.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

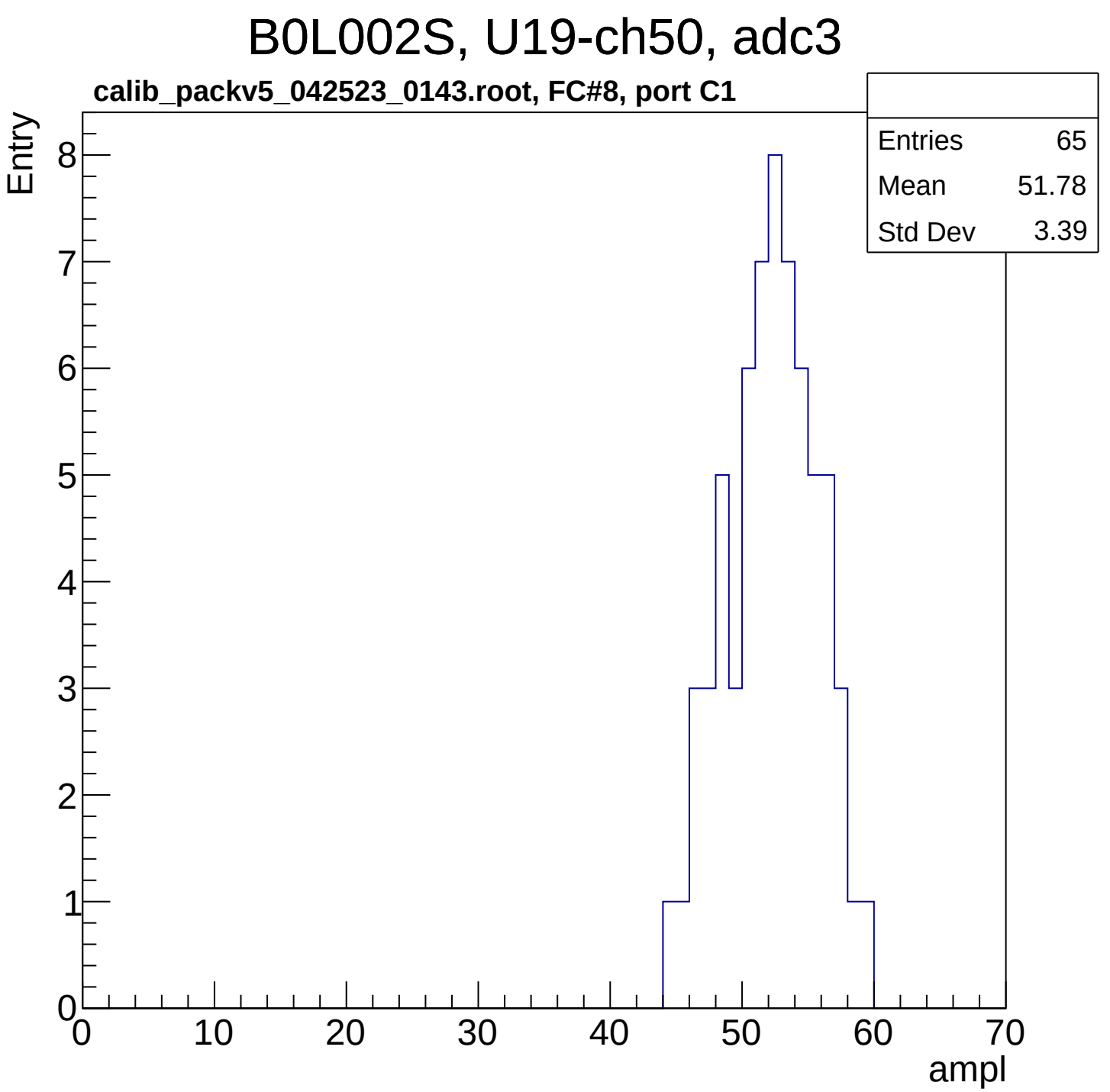

B0L002S, U19-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

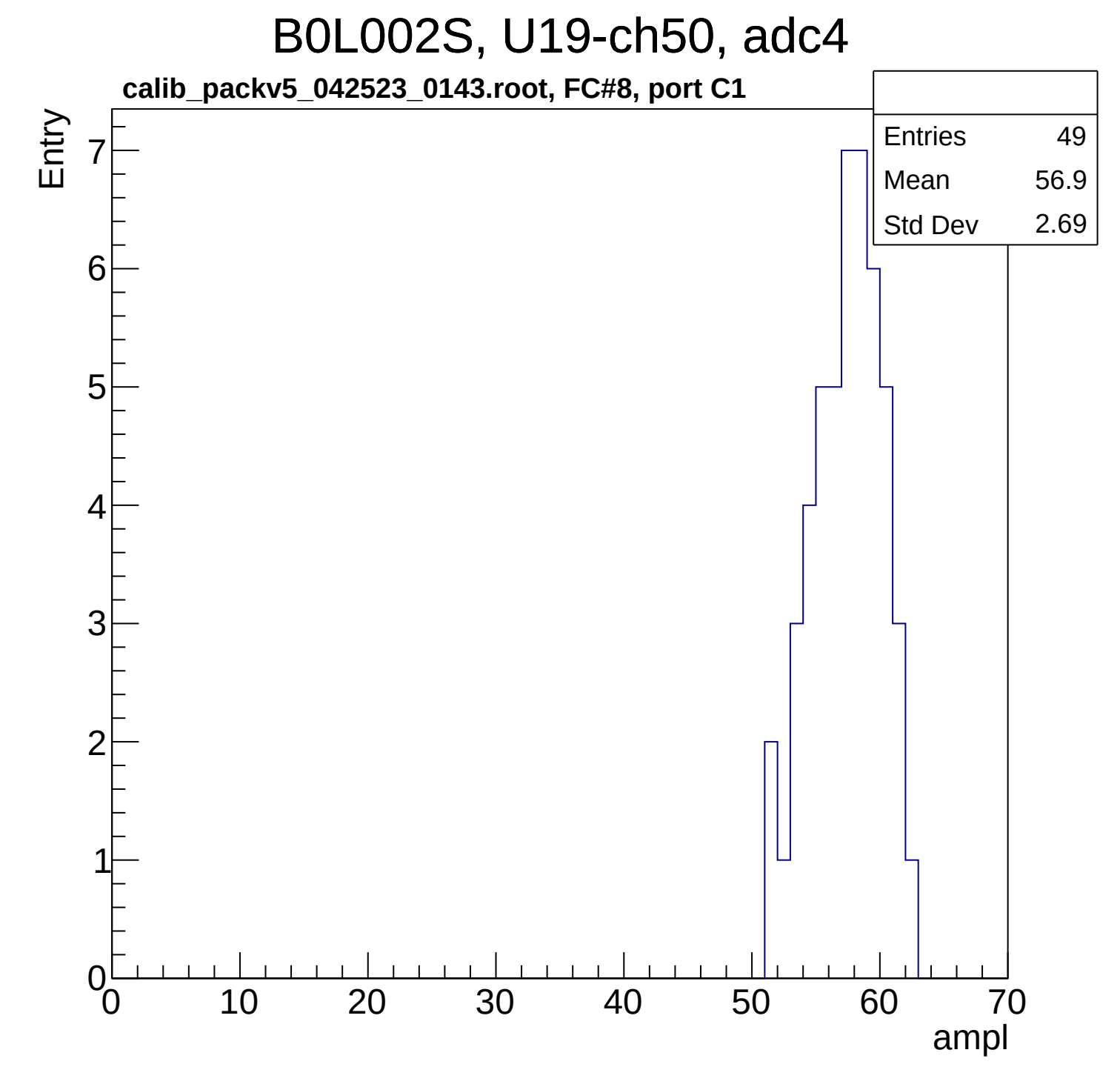

B0L002S, U19-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

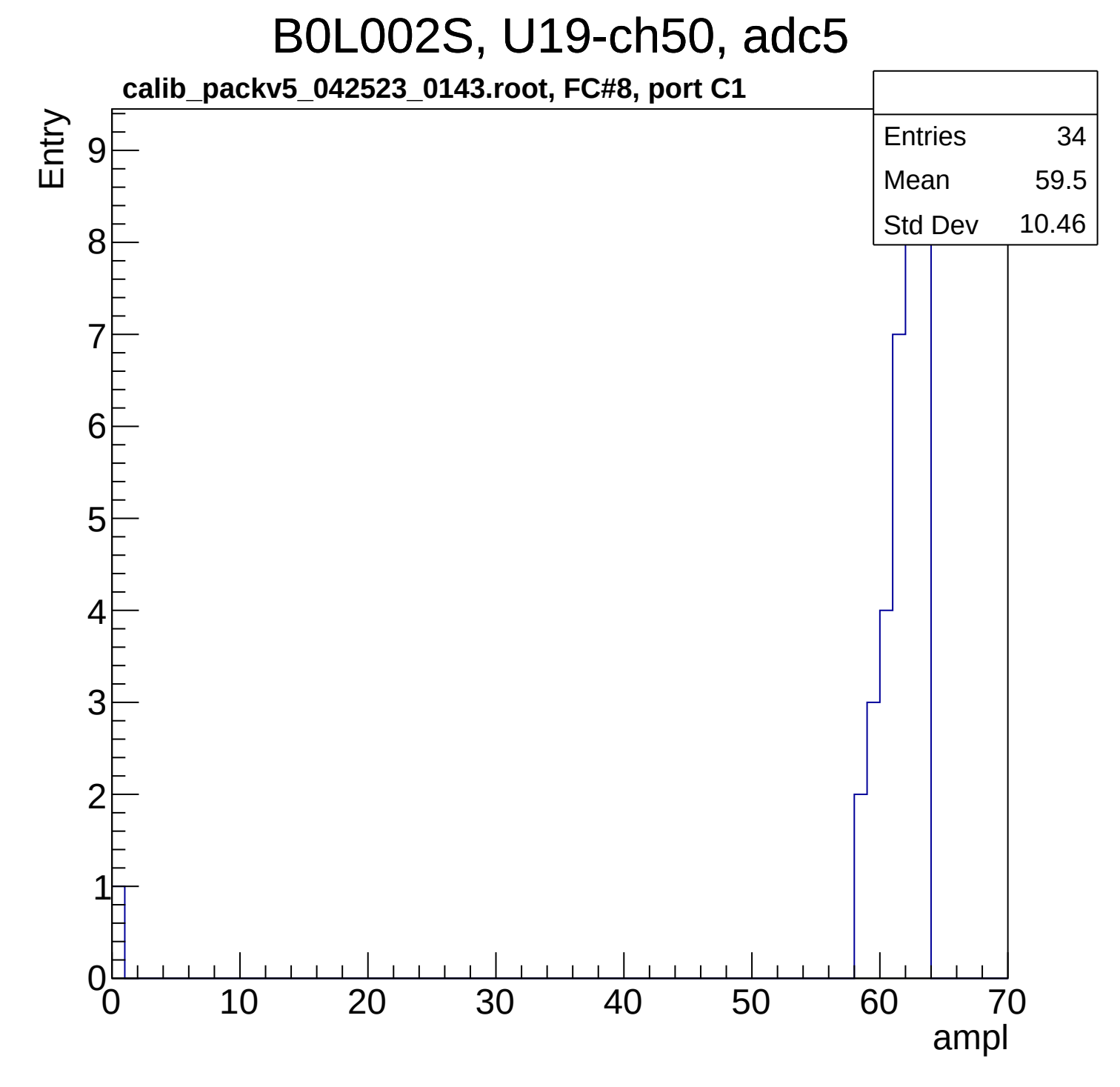

B0L002S, U19-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.4
Std Dev 5.066
Gaus mean : 31.2724
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

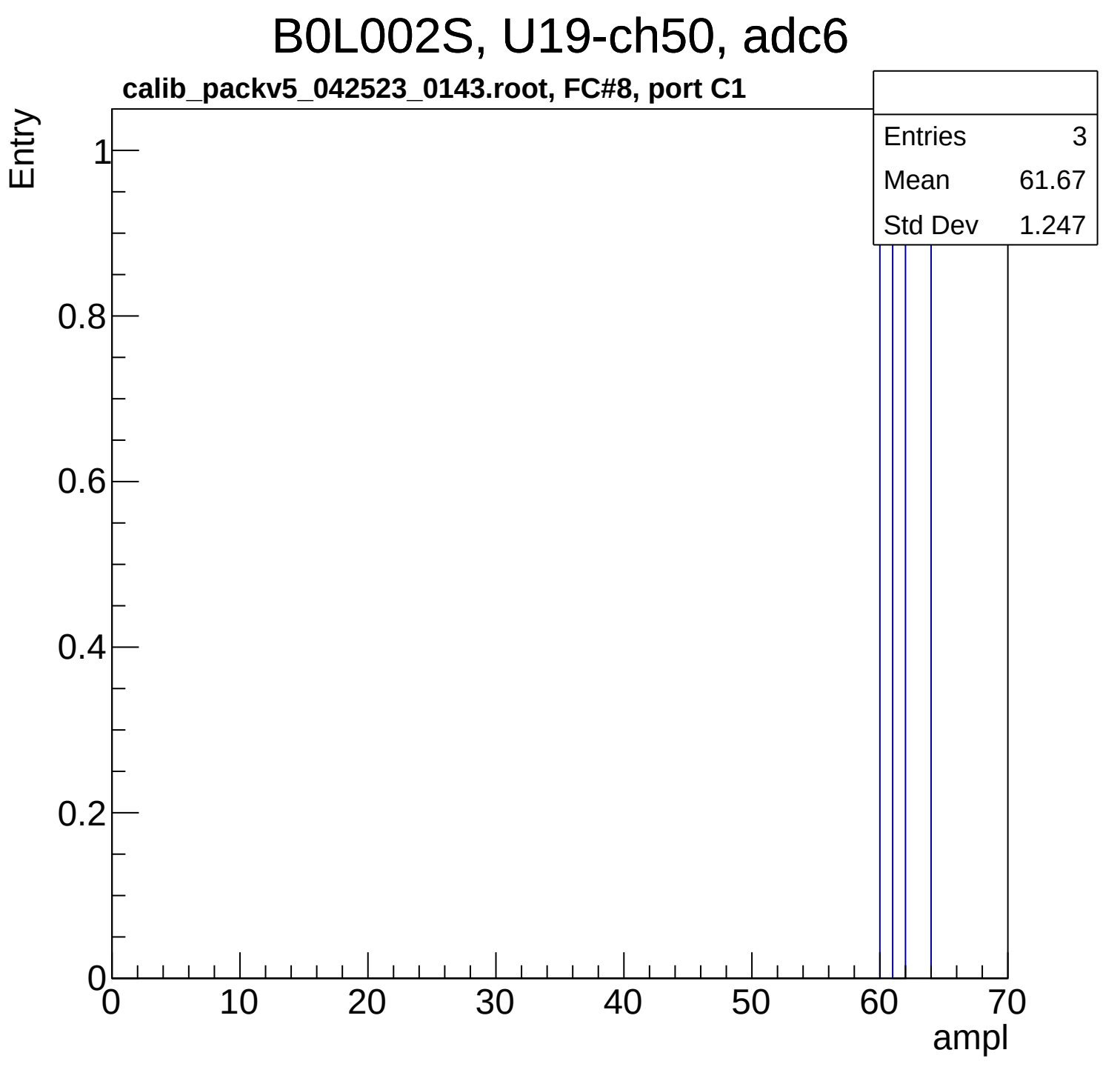

B0L002S, U19-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.15
Std Dev 3.349
Gaus mean : 38.4817
Gaus Width: 3.6643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

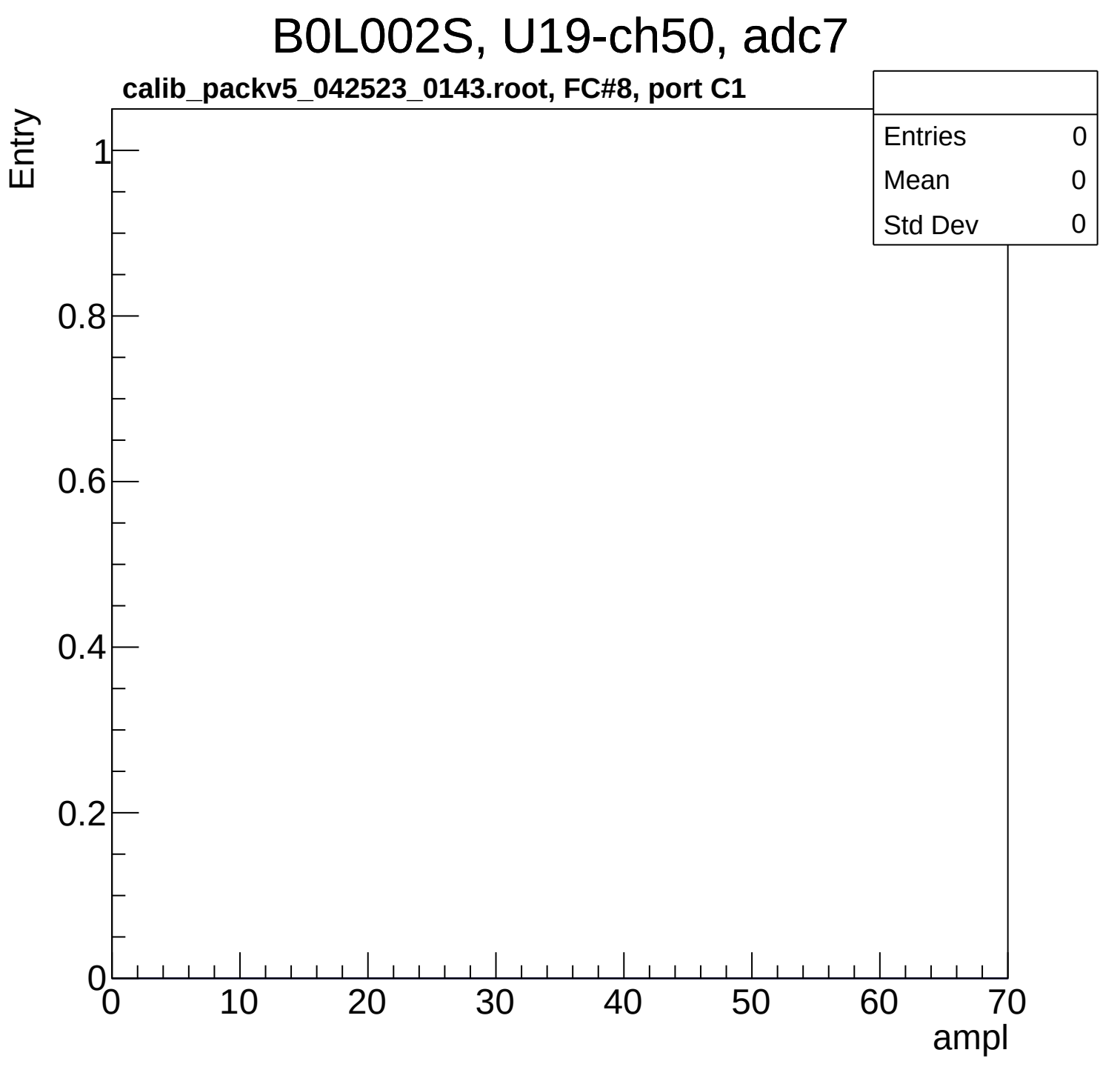

B0L002S, U19-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.29
Std Dev 3.256
Gaus mean : 46.0422
Gaus Width: 3.2729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

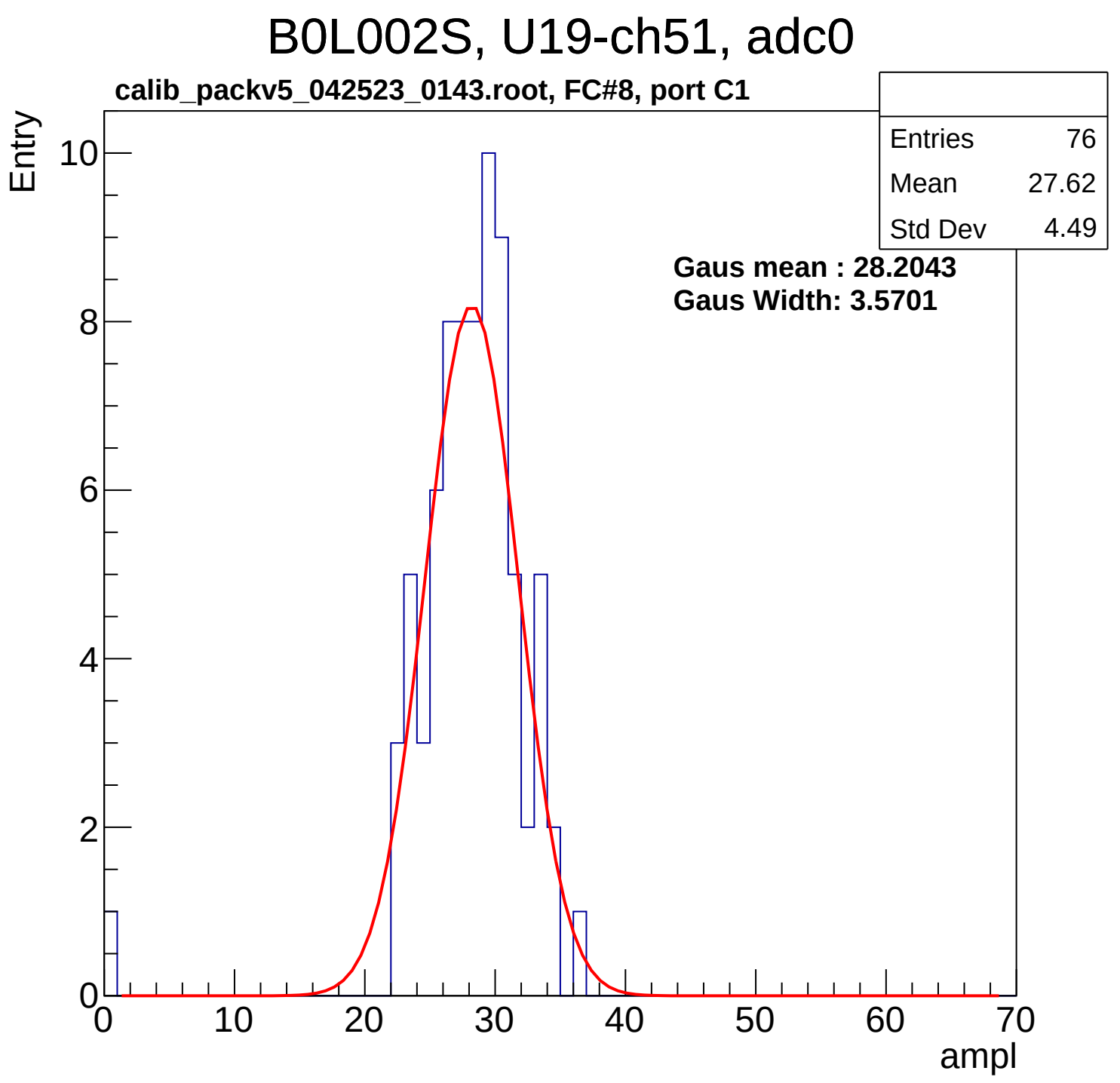

B0L002S, U19-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.78
Std Dev 3.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

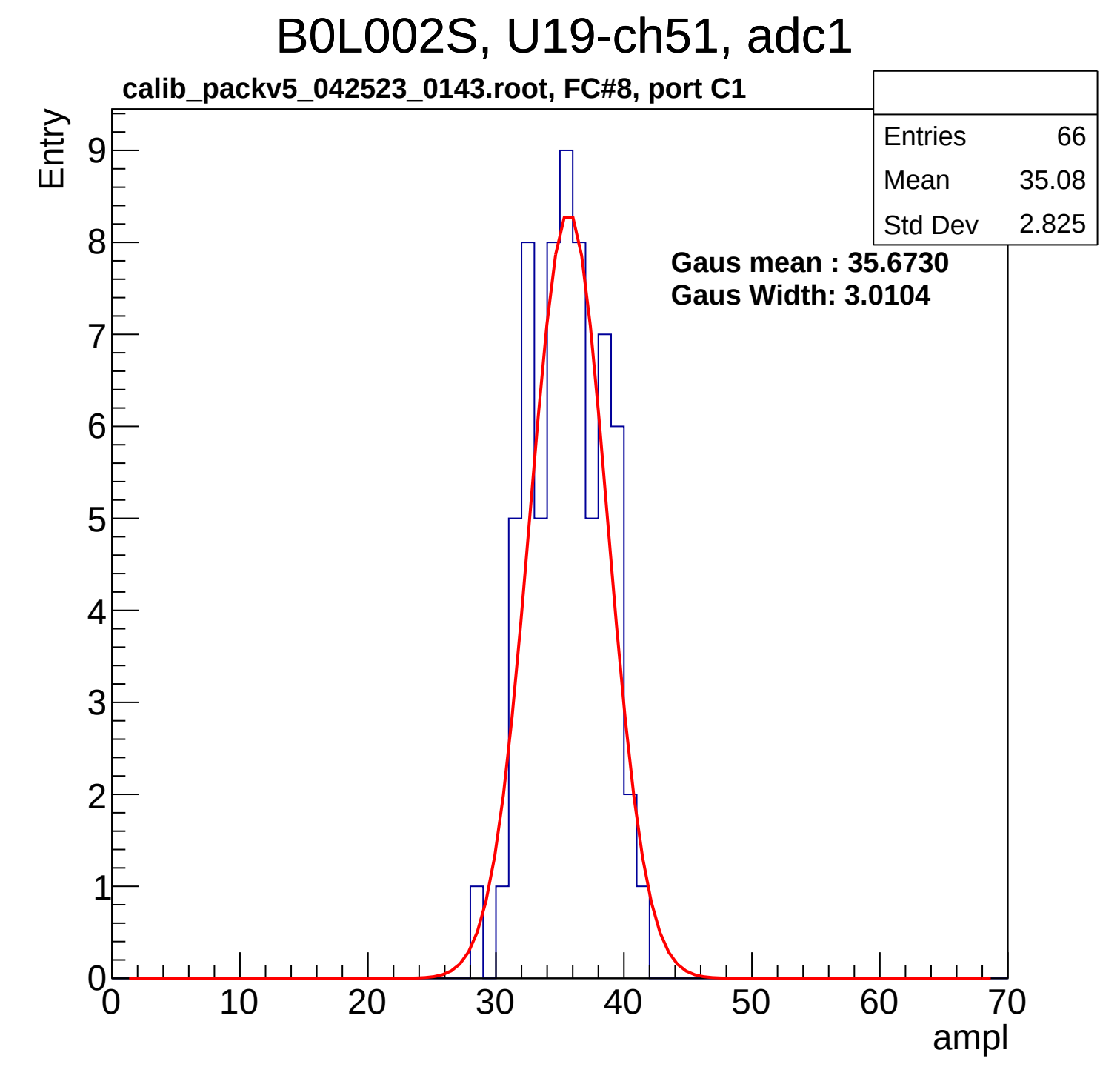

B0L002S, U19-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 56.9
Std Dev 2.69
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

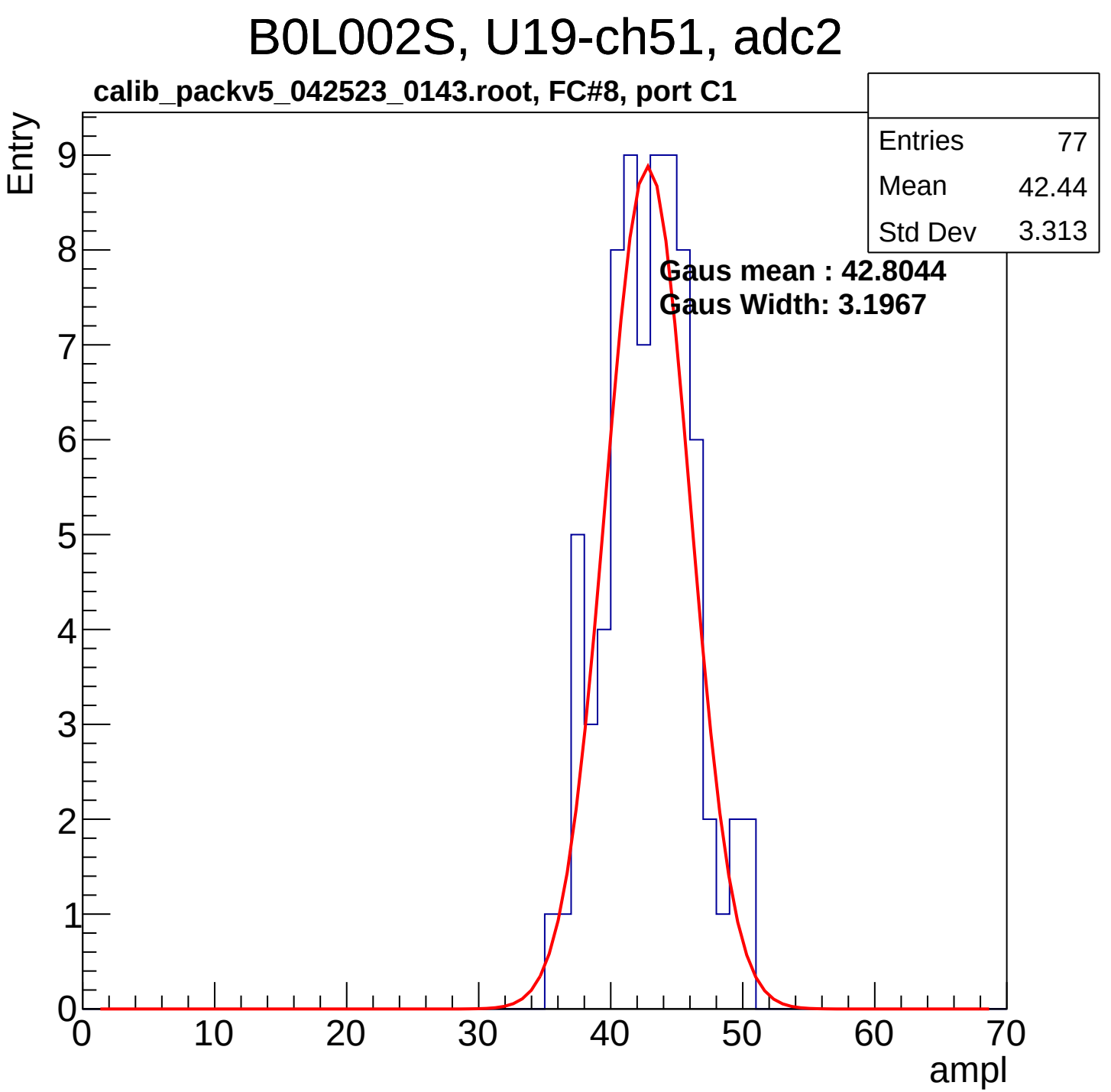

B0L002S, U19-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.5
Std Dev 10.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

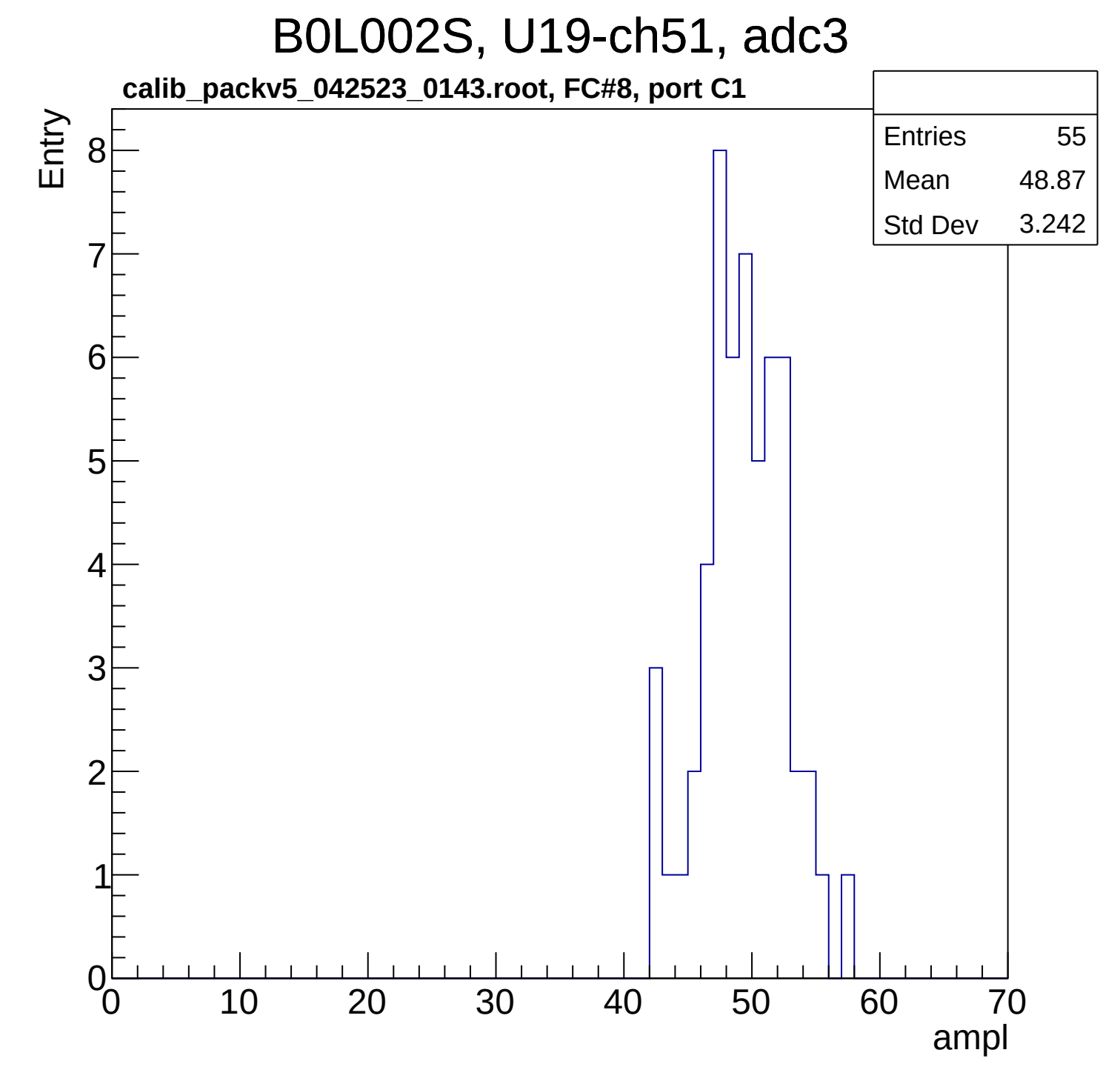

B0L002S, U19-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

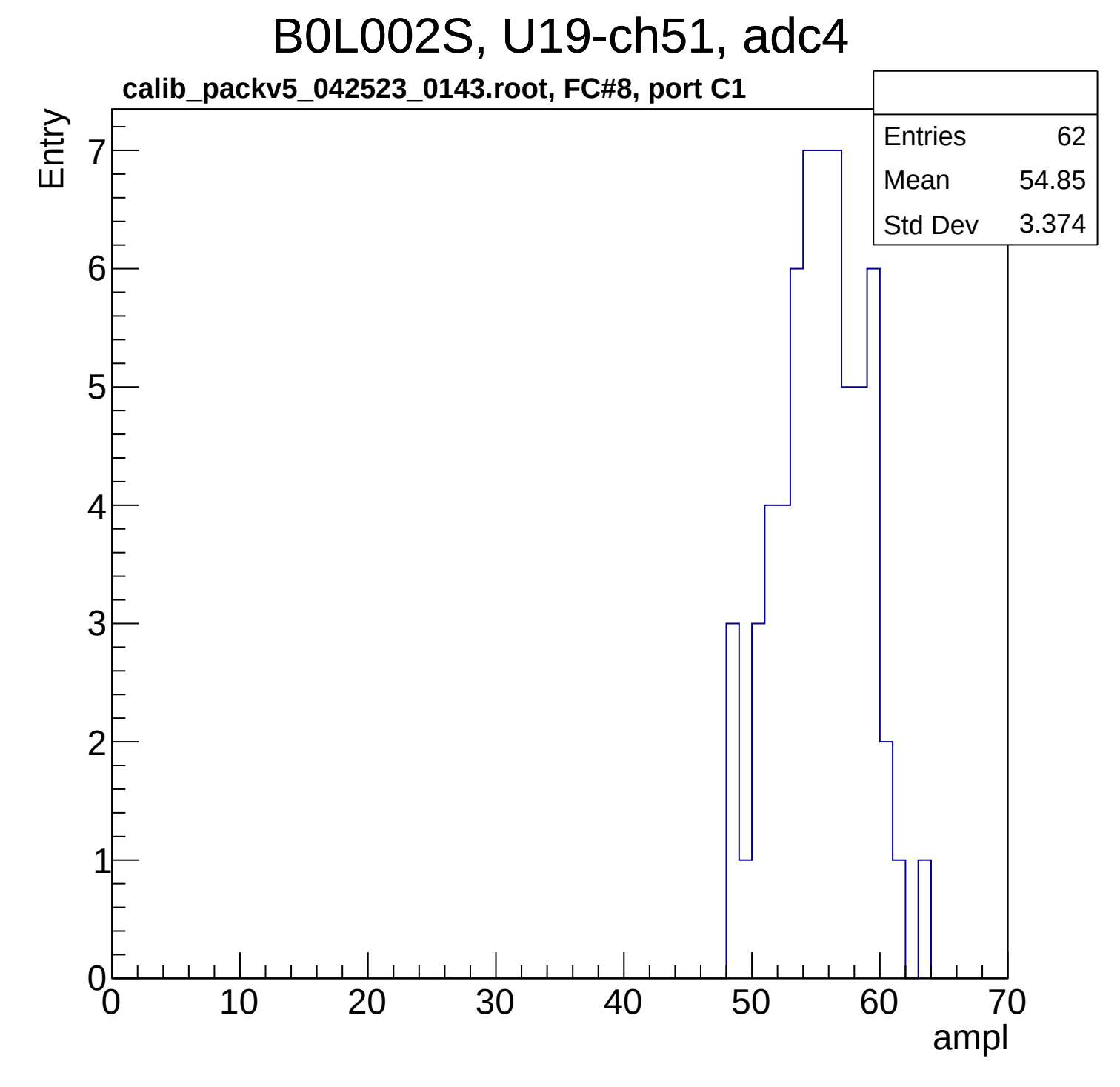

B0L002S, U19-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

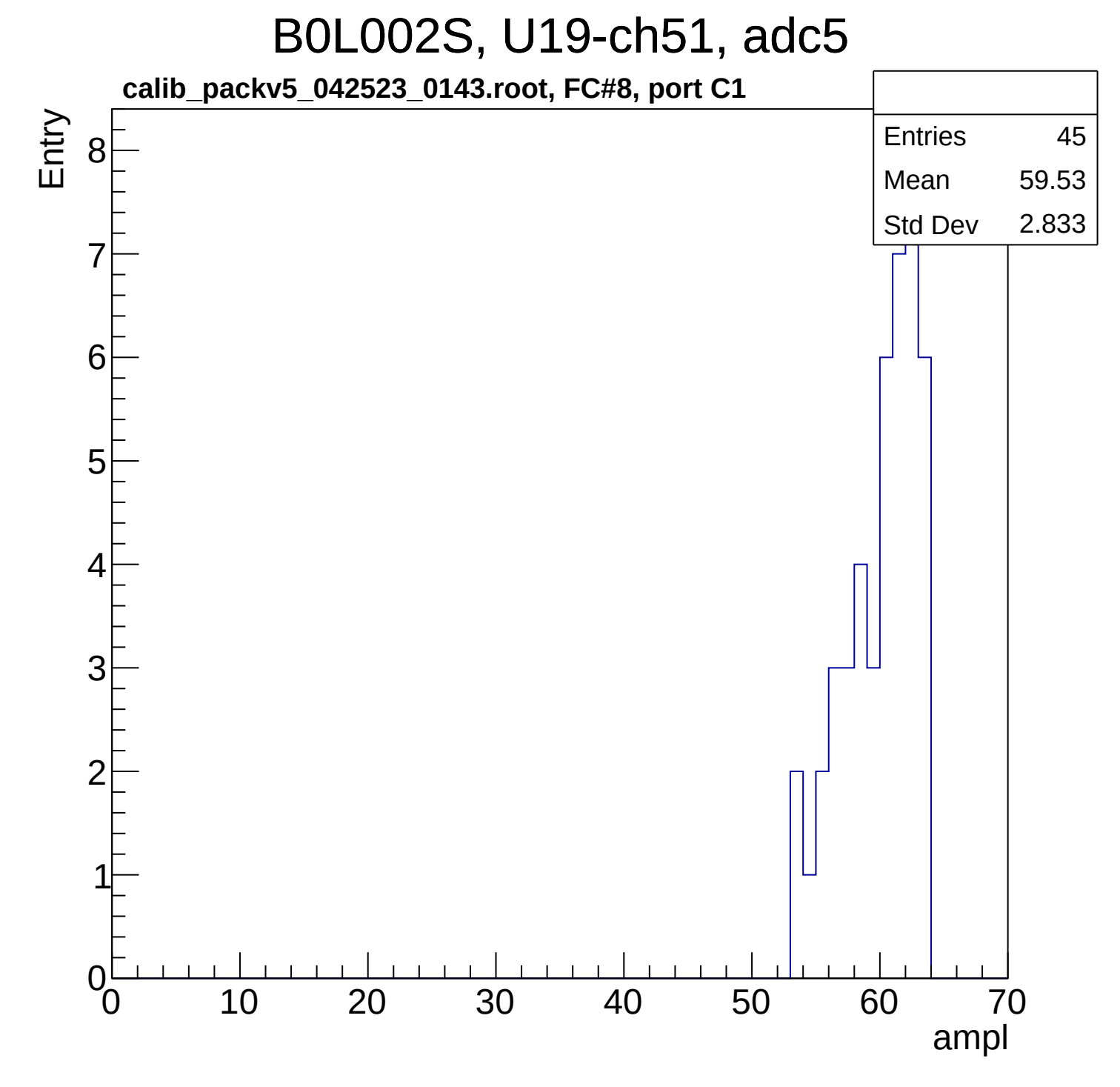

B0L002S, U19-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 27.62
Std Dev 4.49
Gaus mean : 28.2043
Gaus Width: 3.5701
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

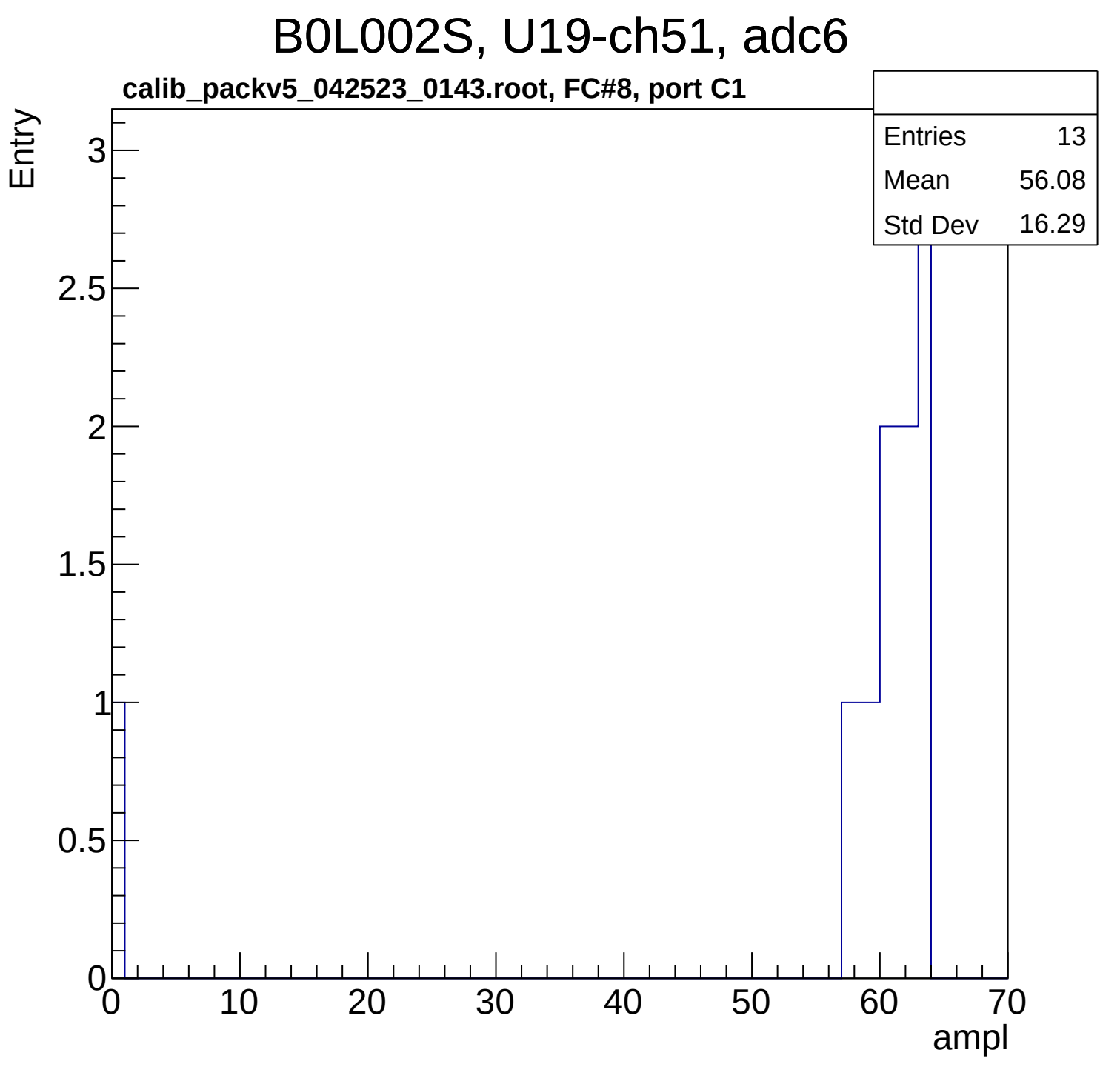

B0L002S, U19-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 35.08
Std Dev 2.825
Gaus mean : 35.6730
Gaus Width: 3.0104
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

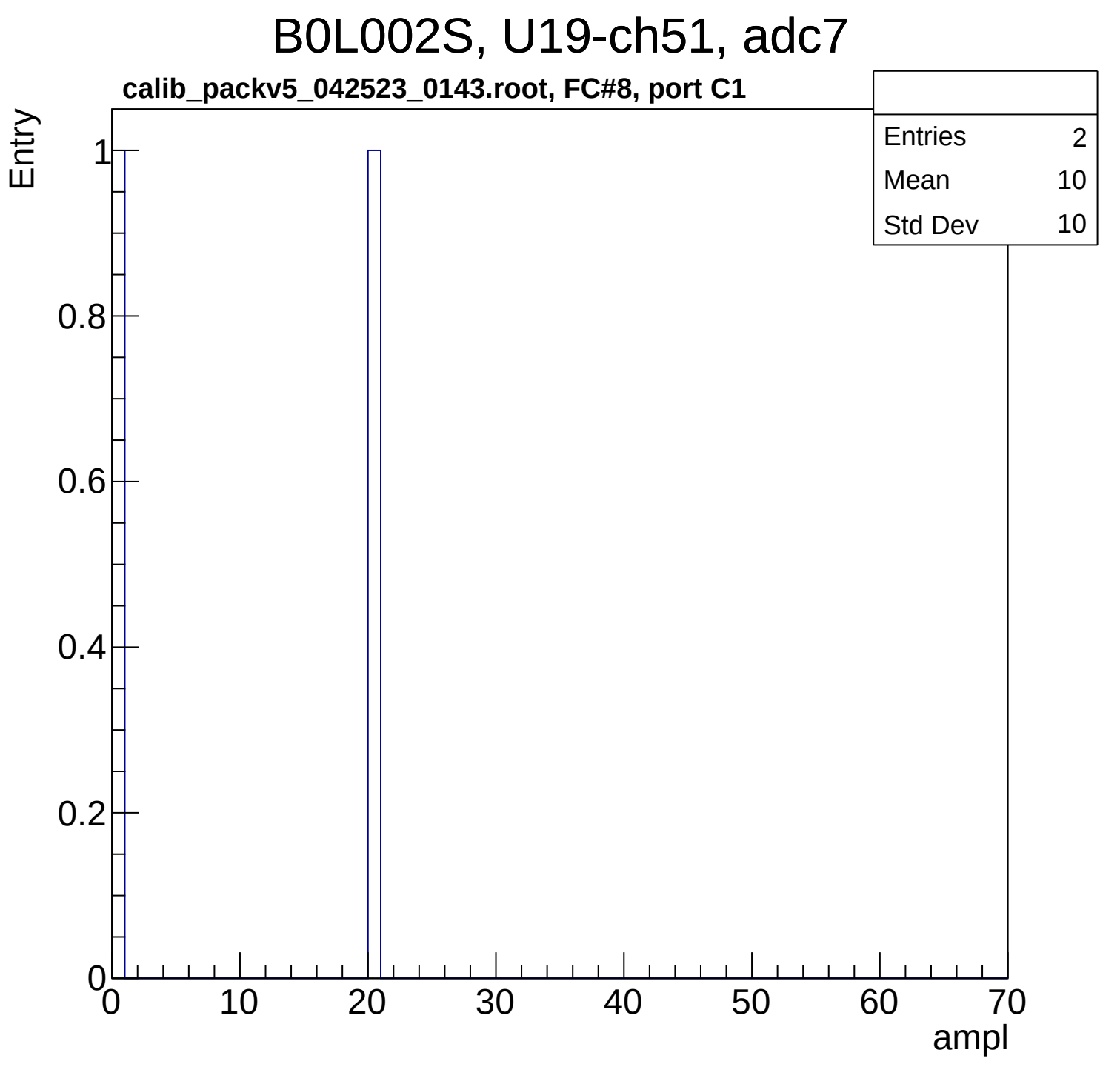

B0L002S, U19-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 42.44
Std Dev 3.313
Gaus mean : 42.8044
Gaus Width: 3.1967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

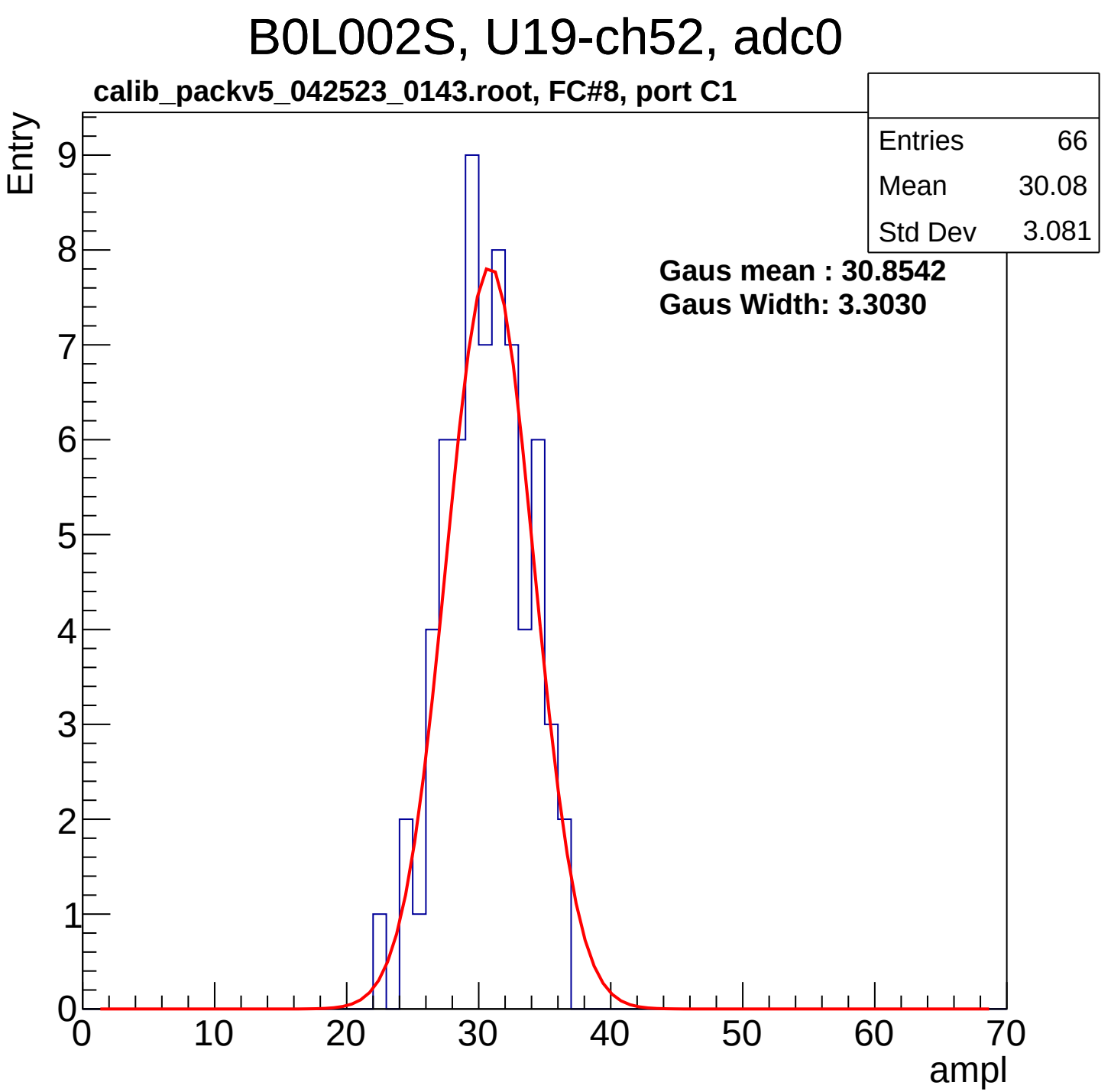

B0L002S, U19-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 48.87
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

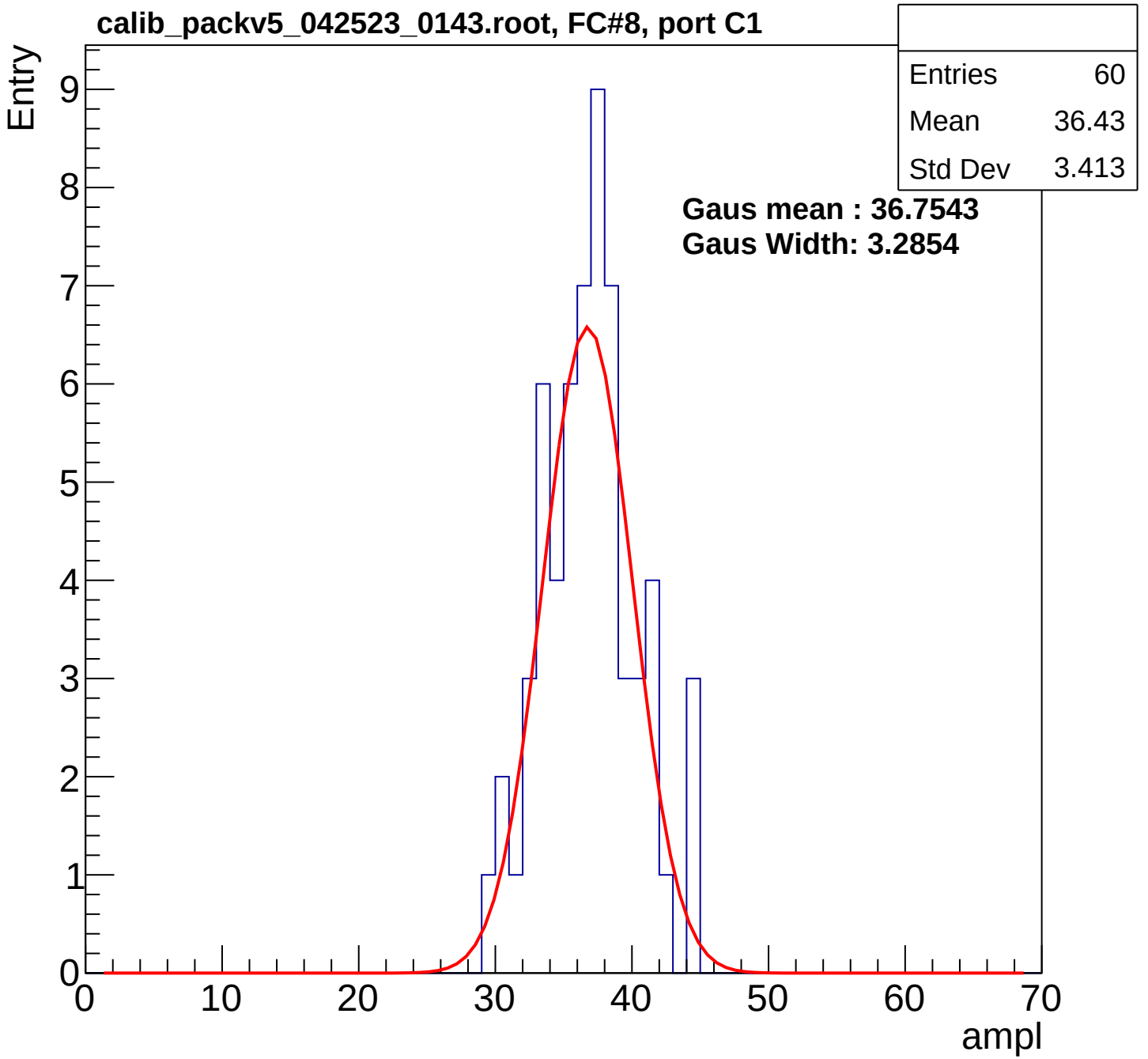

B0L002S, U19-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 54.85
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

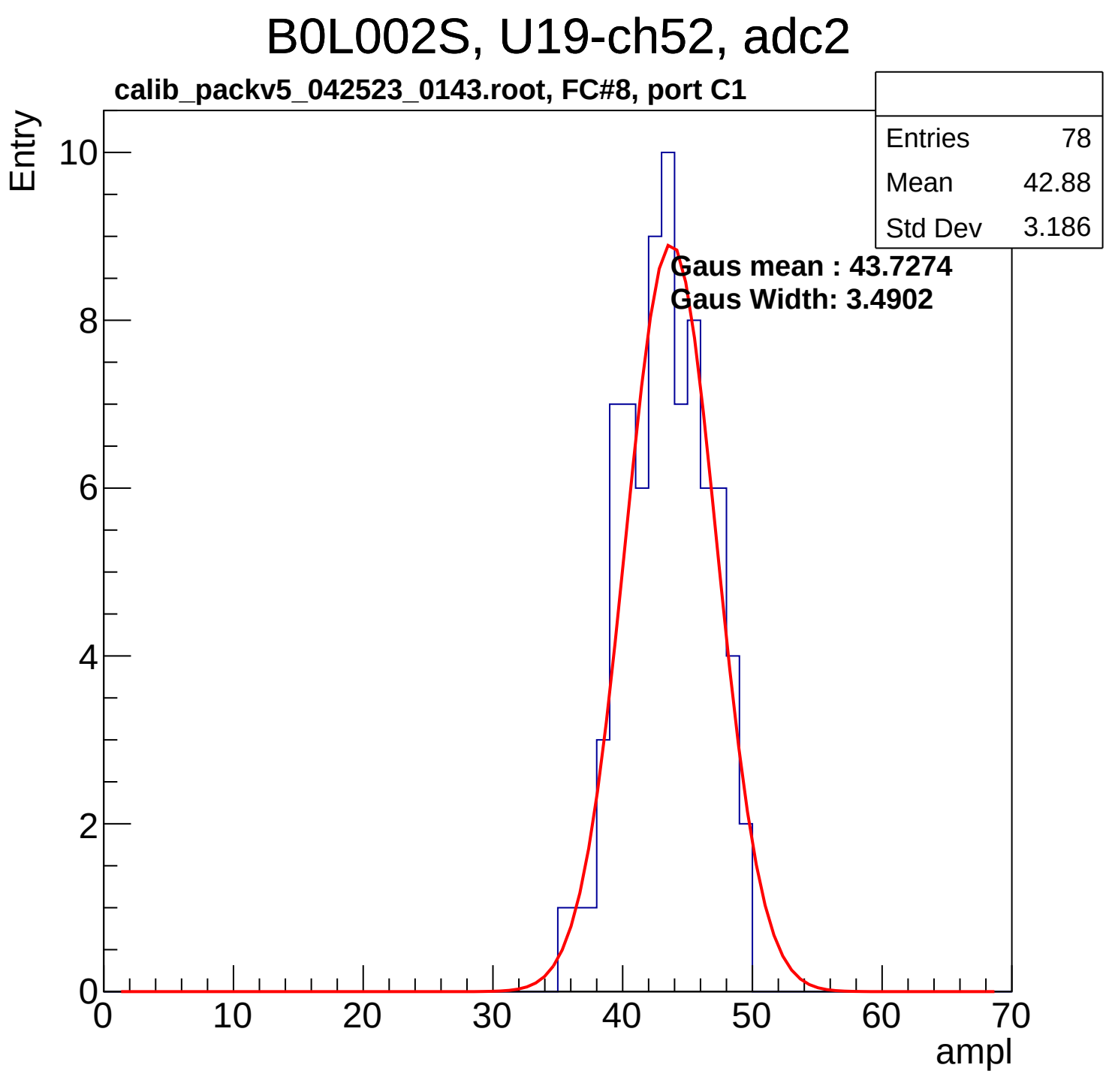

B0L002S, U19-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.53
Std Dev 2.833
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

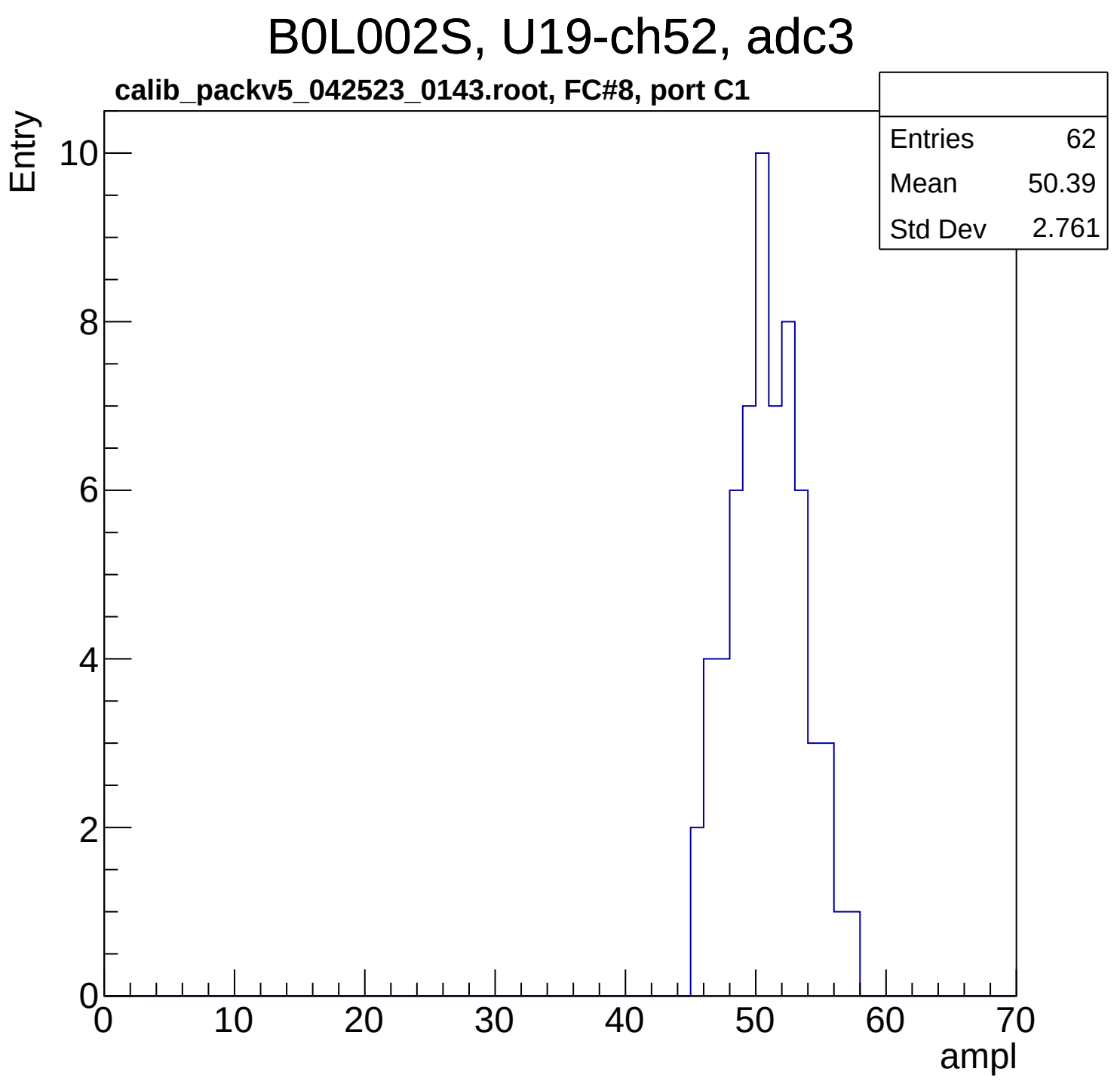

B0L002S, U19-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 56.08
Std Dev 16.29
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

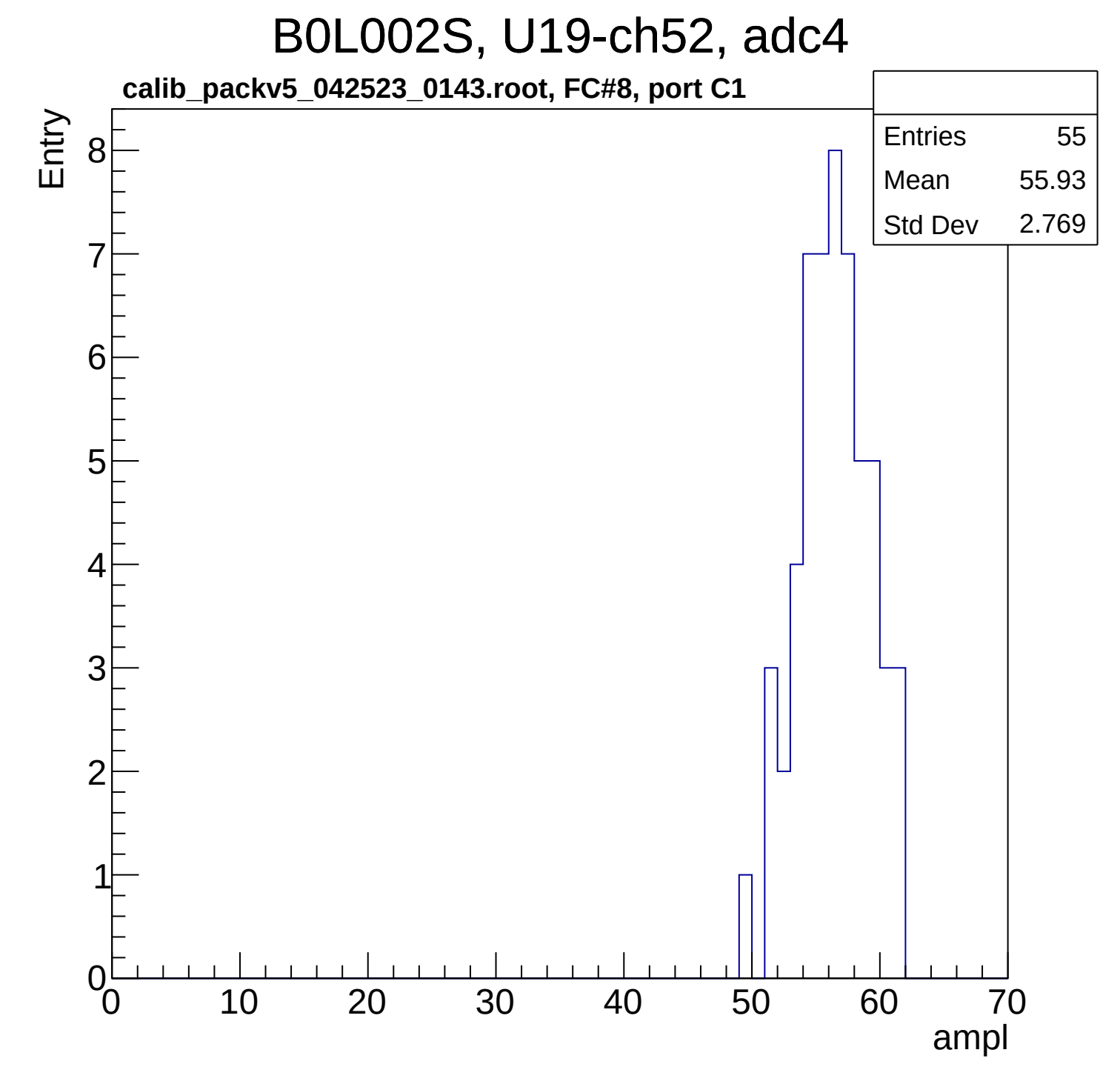

B0L002S, U19-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

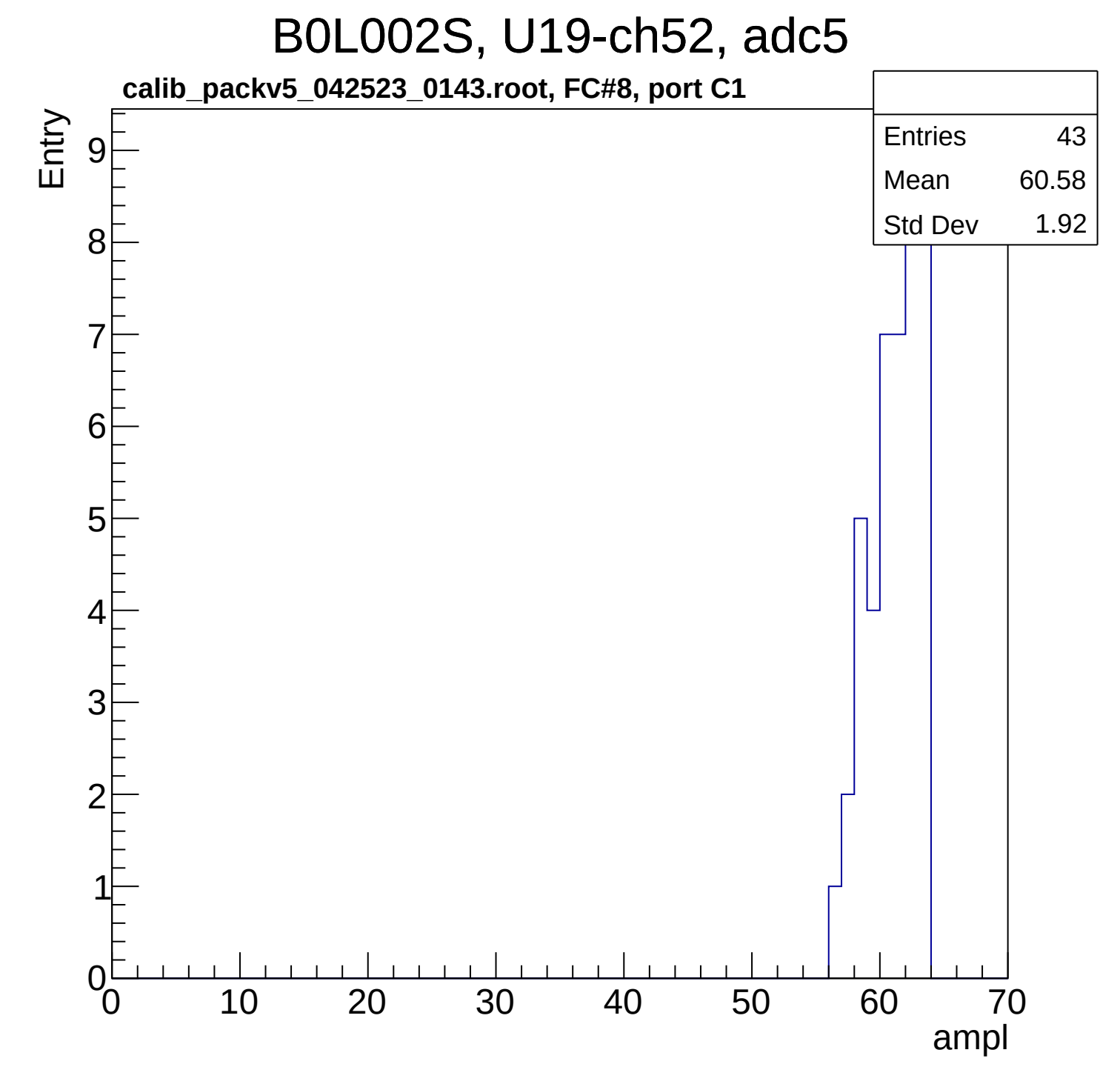

B0L002S, U19-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.08
Std Dev 3.081
Gaus mean : 30.8542
Gaus Width: 3.3030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

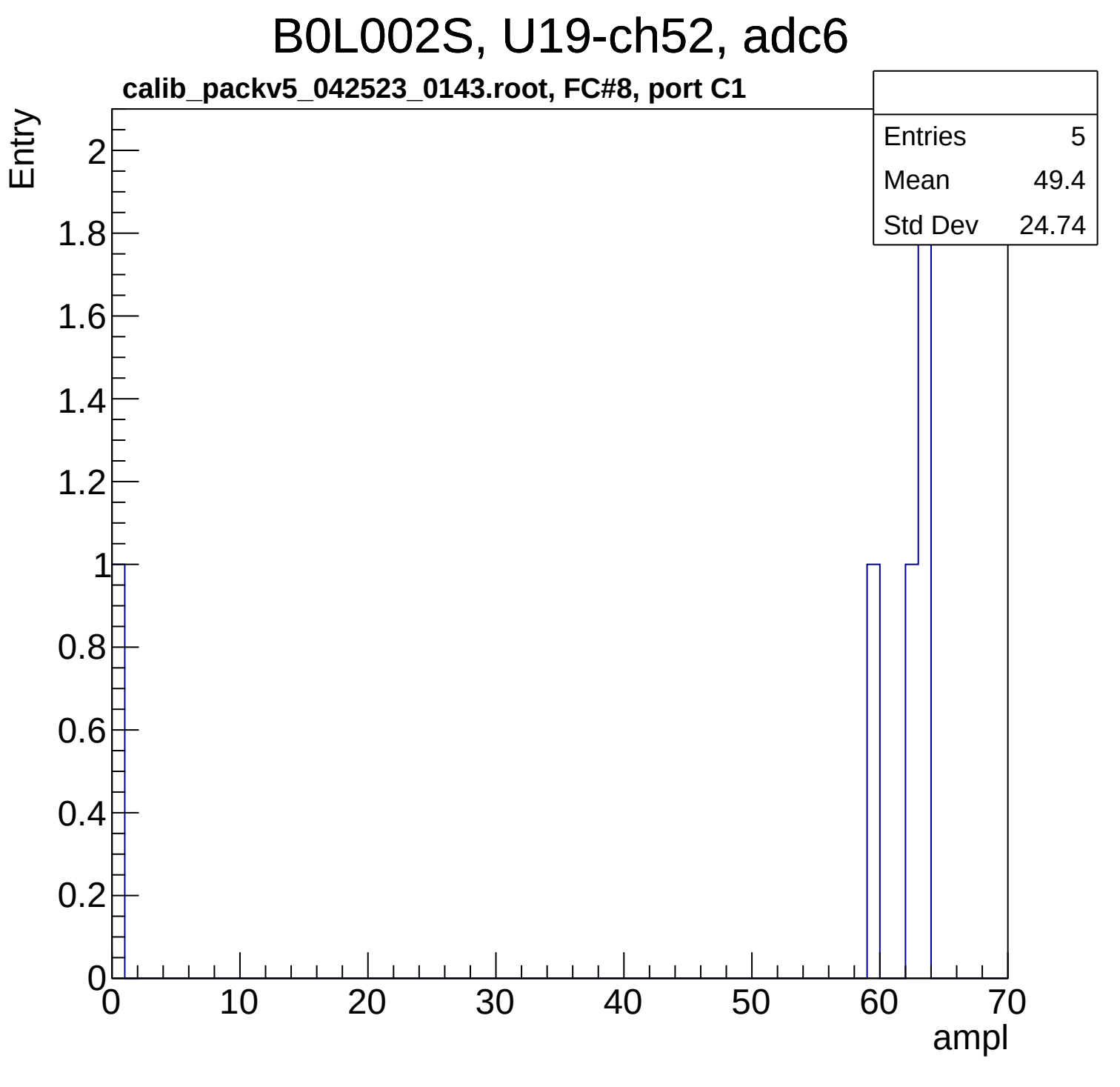

B0L002S, U19-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.43
Std Dev 3.413
Gaus mean : 36.7543
Gaus Width: 3.2854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

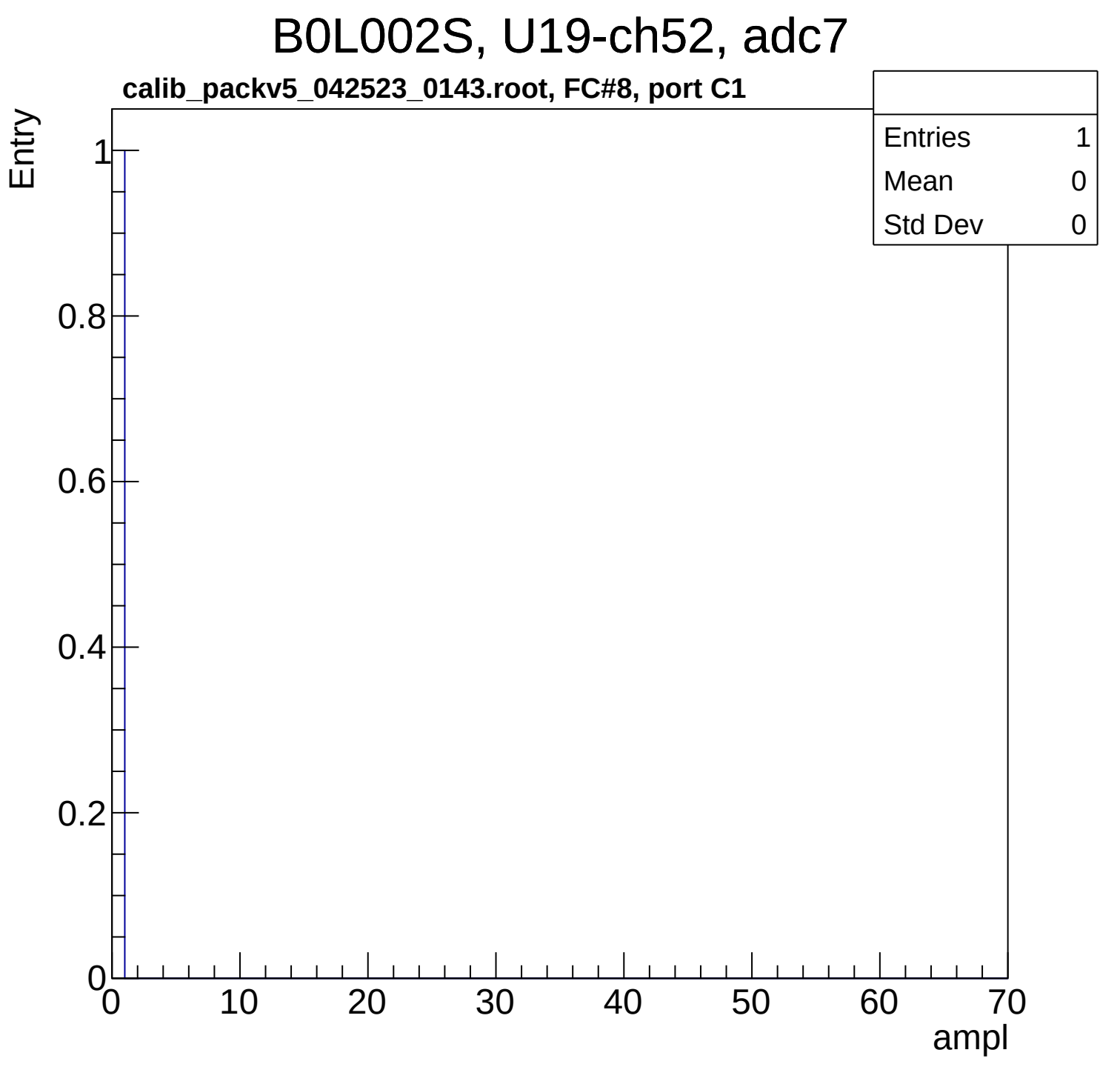

B0L002S, U19-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 42.88
Std Dev 3.186
Gaus mean : 43.7274
Gaus Width: 3.4902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

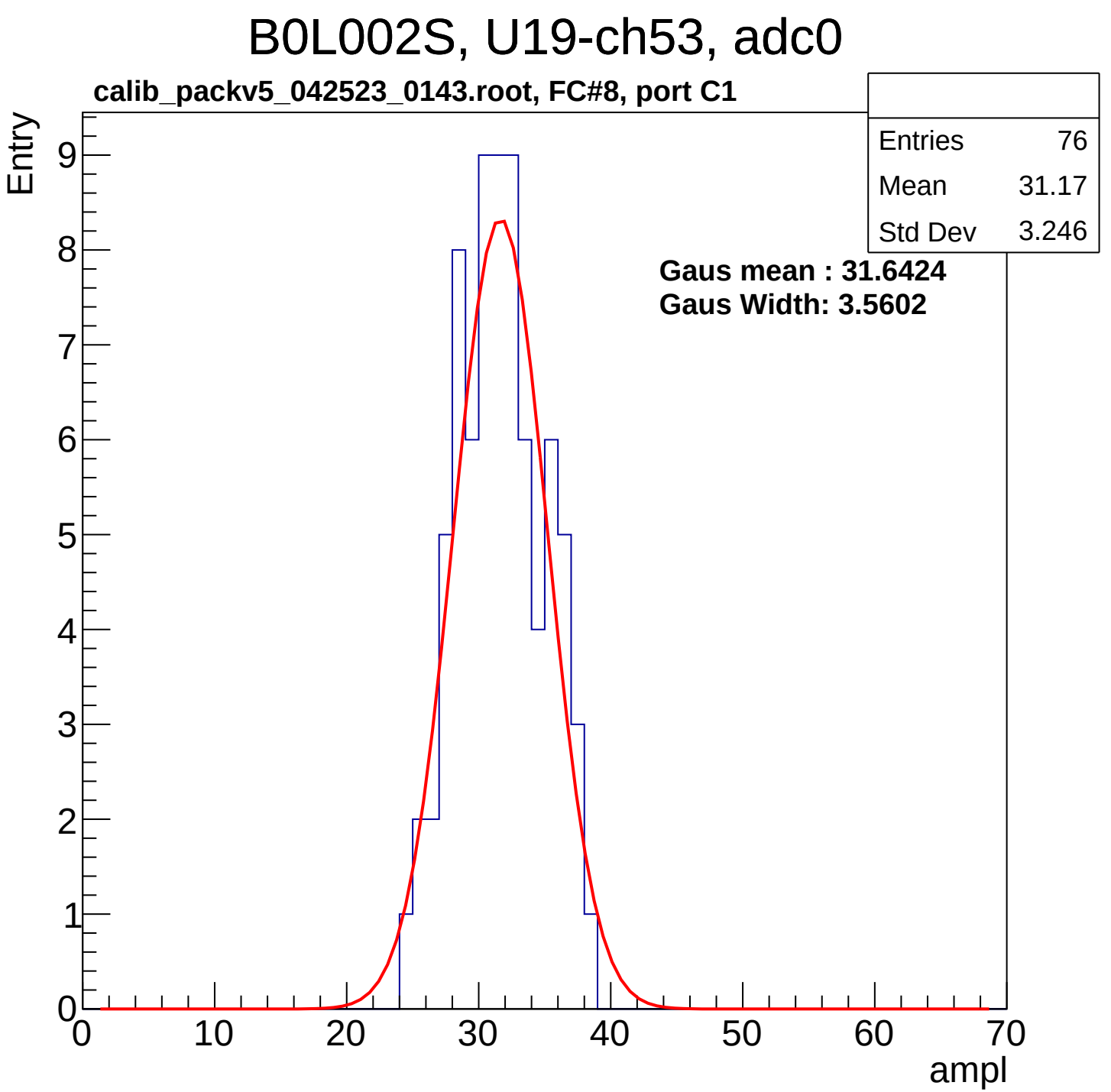

B0L002S, U19-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.39
Std Dev 2.761
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

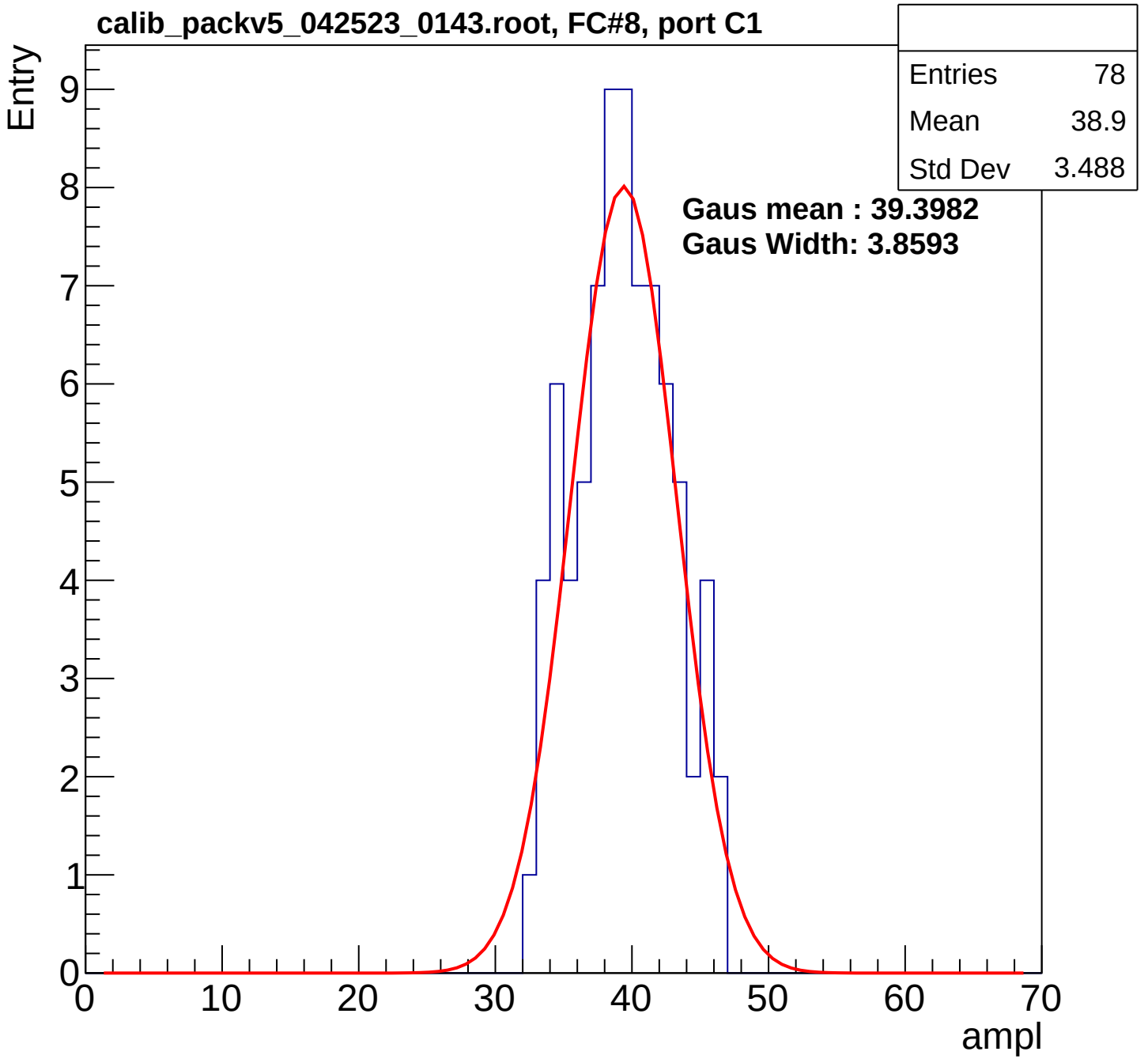

B0L002S, U19-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 55.93
Std Dev 2.769
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

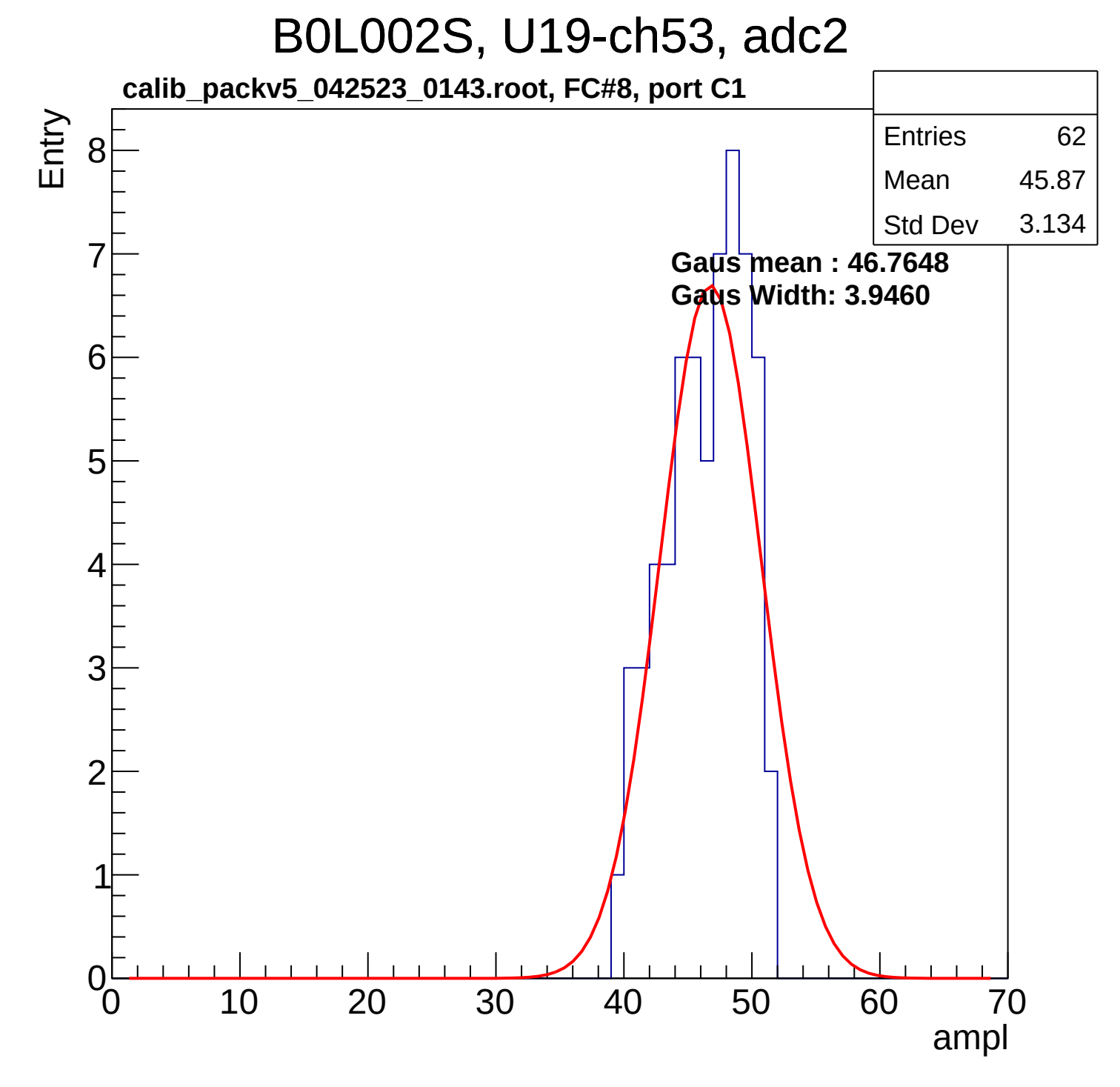

B0L002S, U19-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 60.58
Std Dev 1.92
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

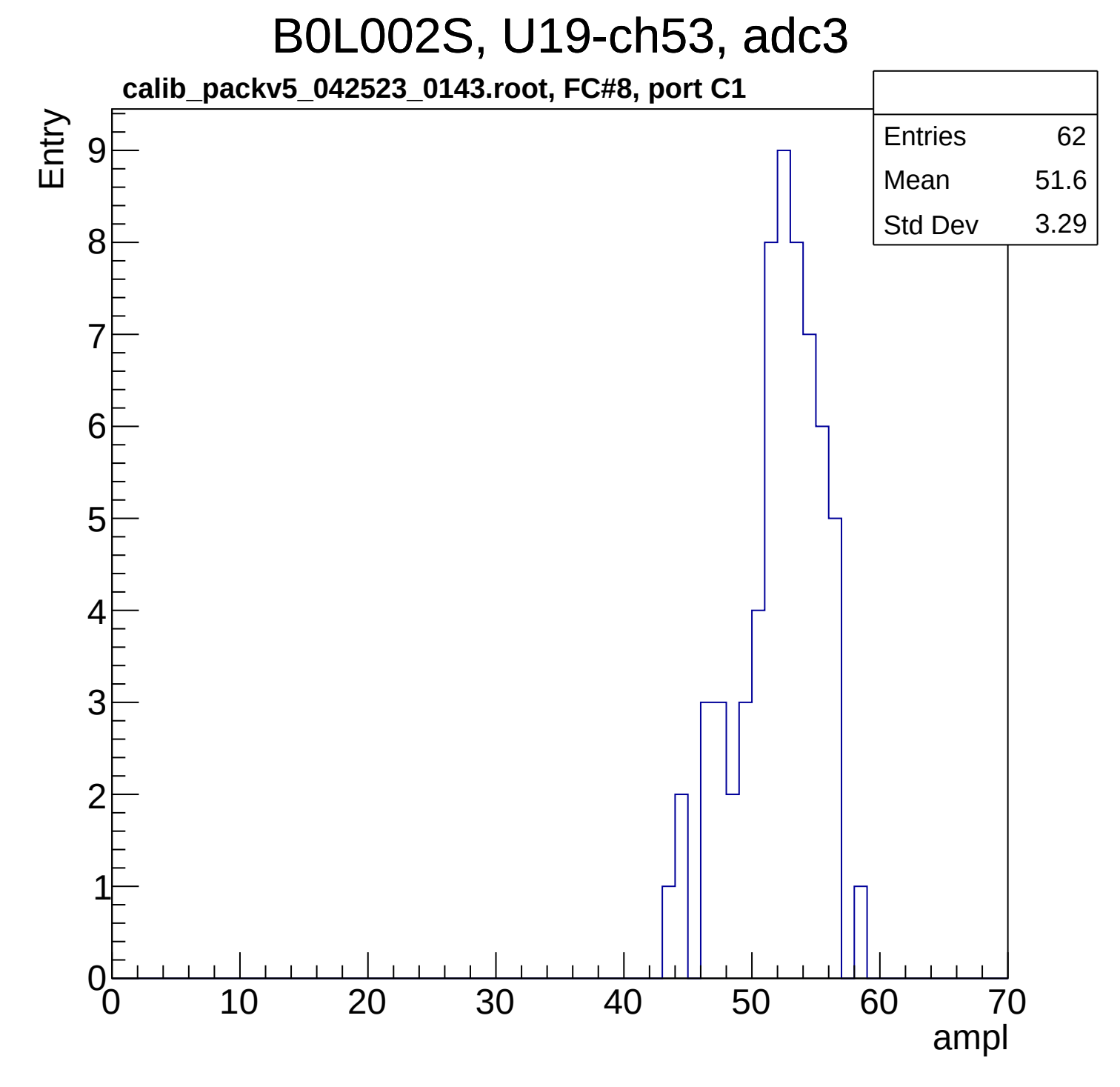

B0L002S, U19-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49.4
Std Dev 24.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

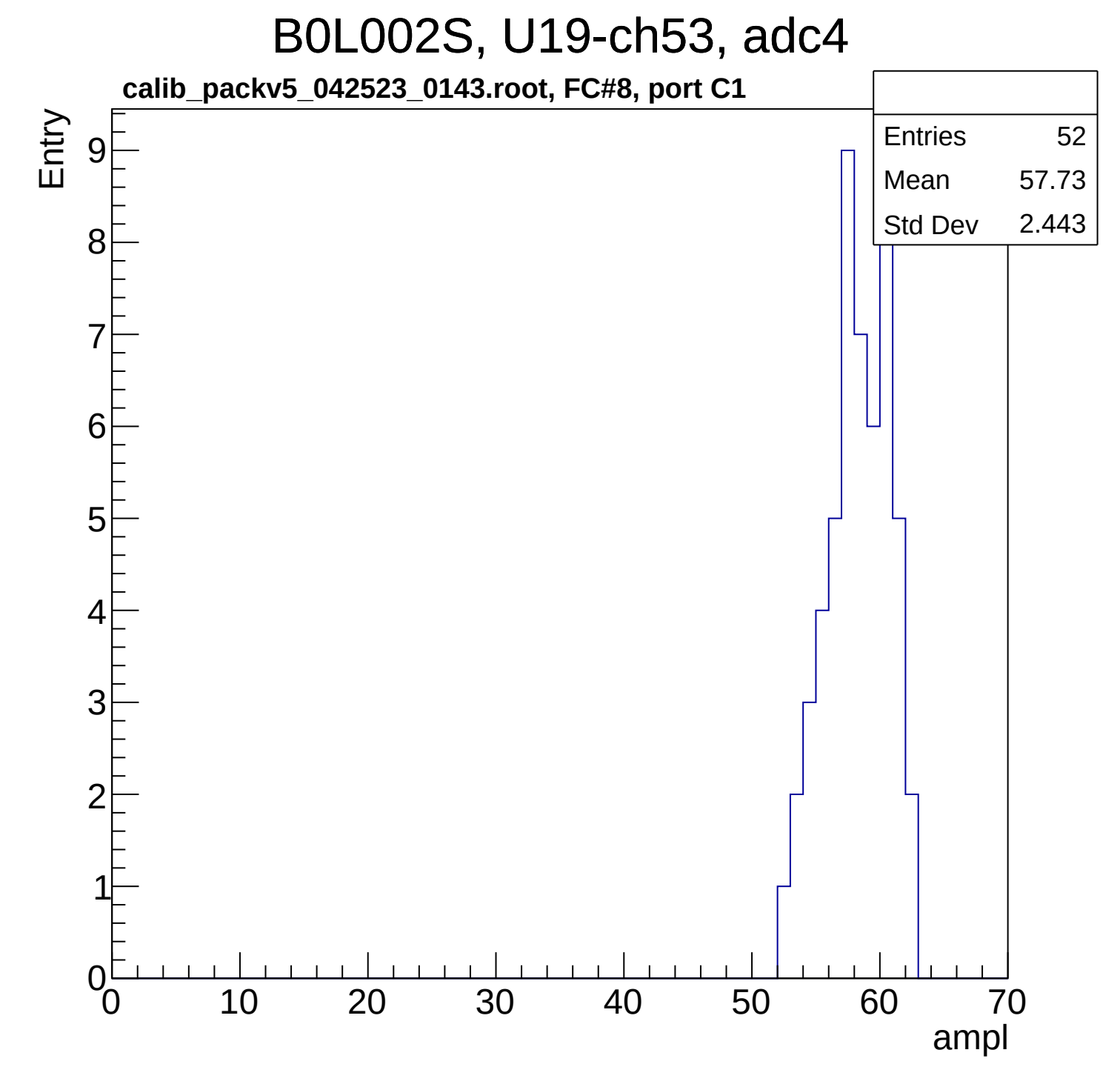

B0L002S, U19-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

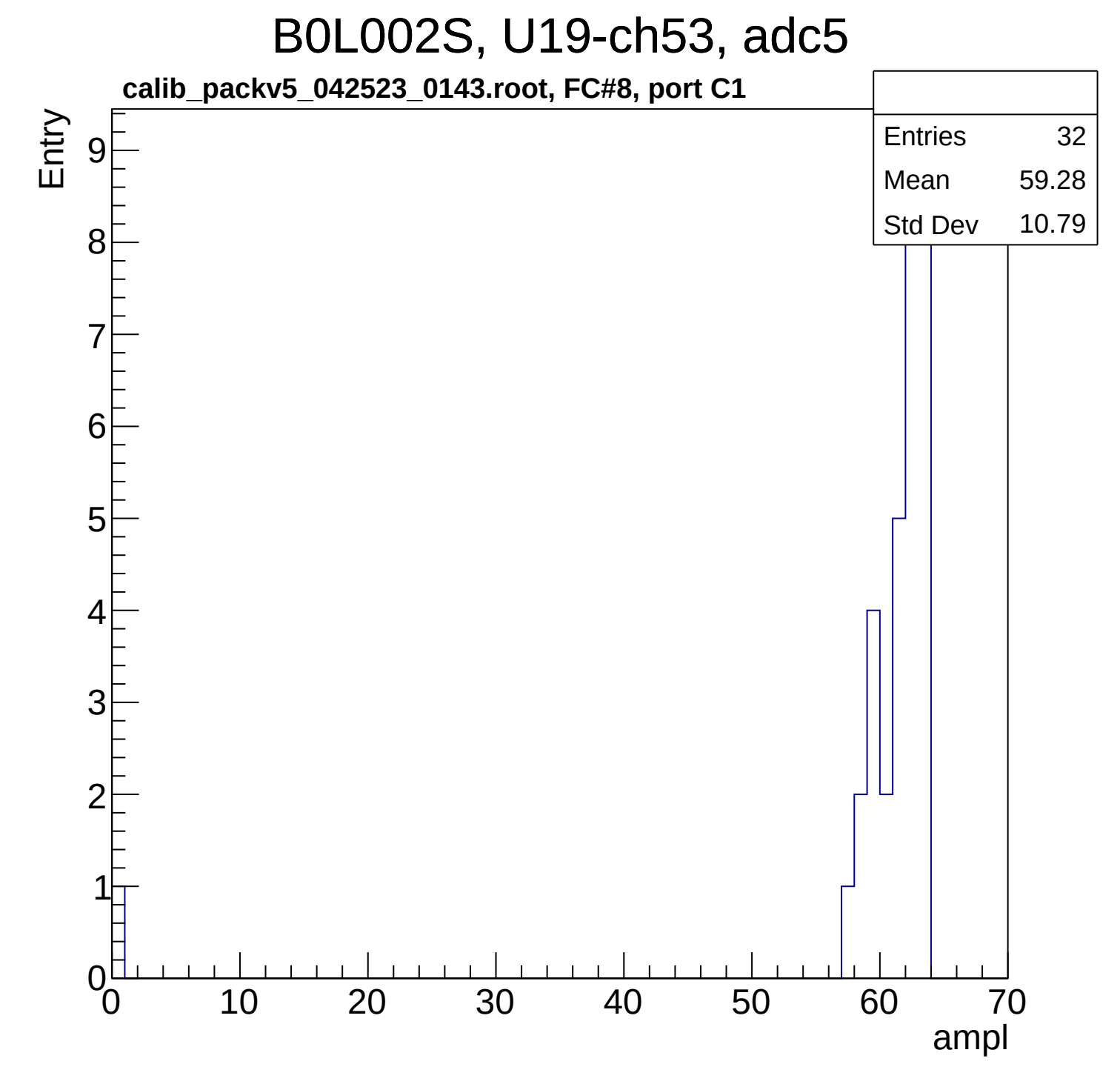

B0L002S, U19-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 31.17
Std Dev 3.246
Gaus mean : 31.6424
Gaus Width: 3.5602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

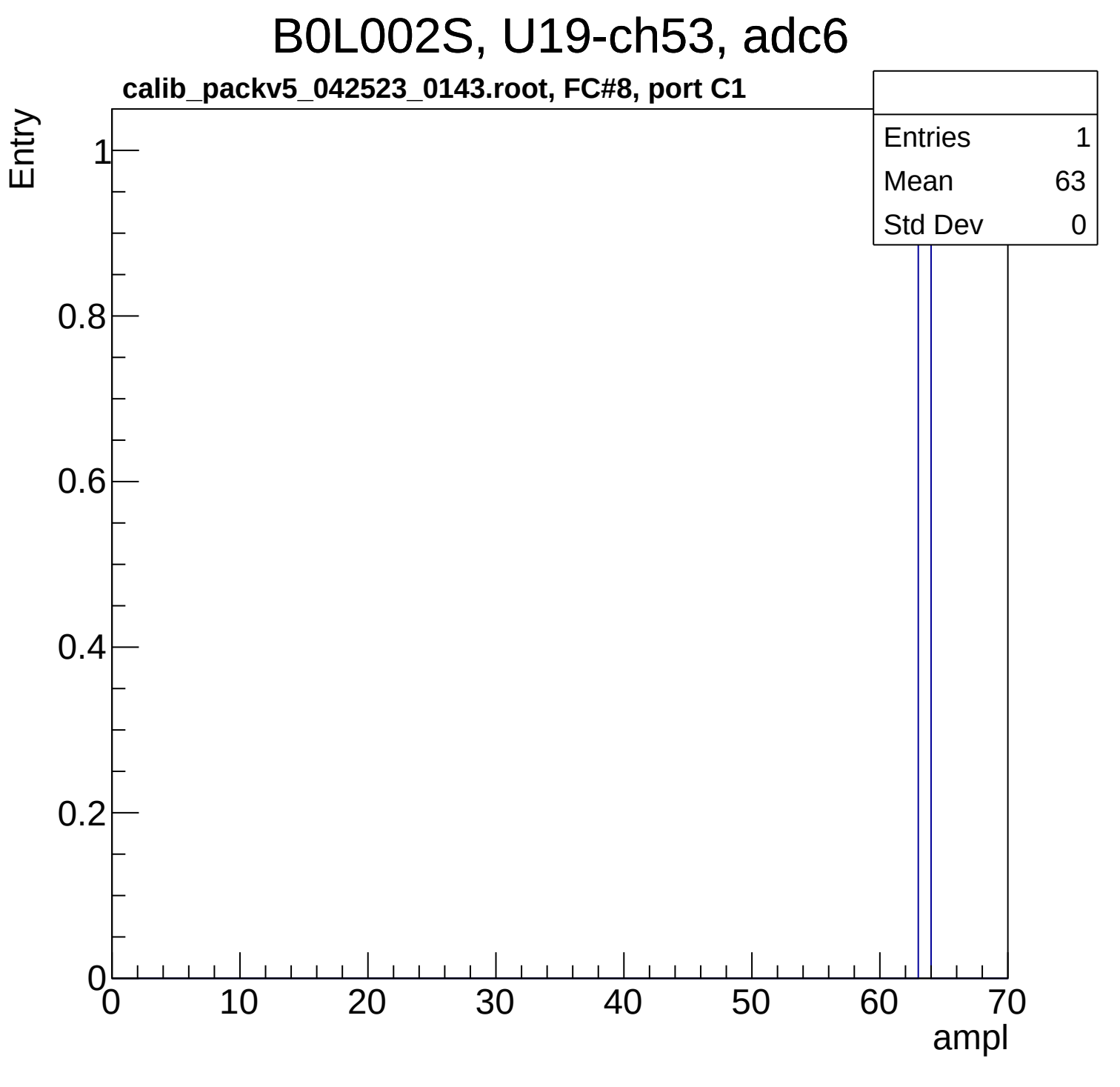

B0L002S, U19-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.9
Std Dev 3.488
Gaus mean : 39.3982
Gaus Width: 3.8593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

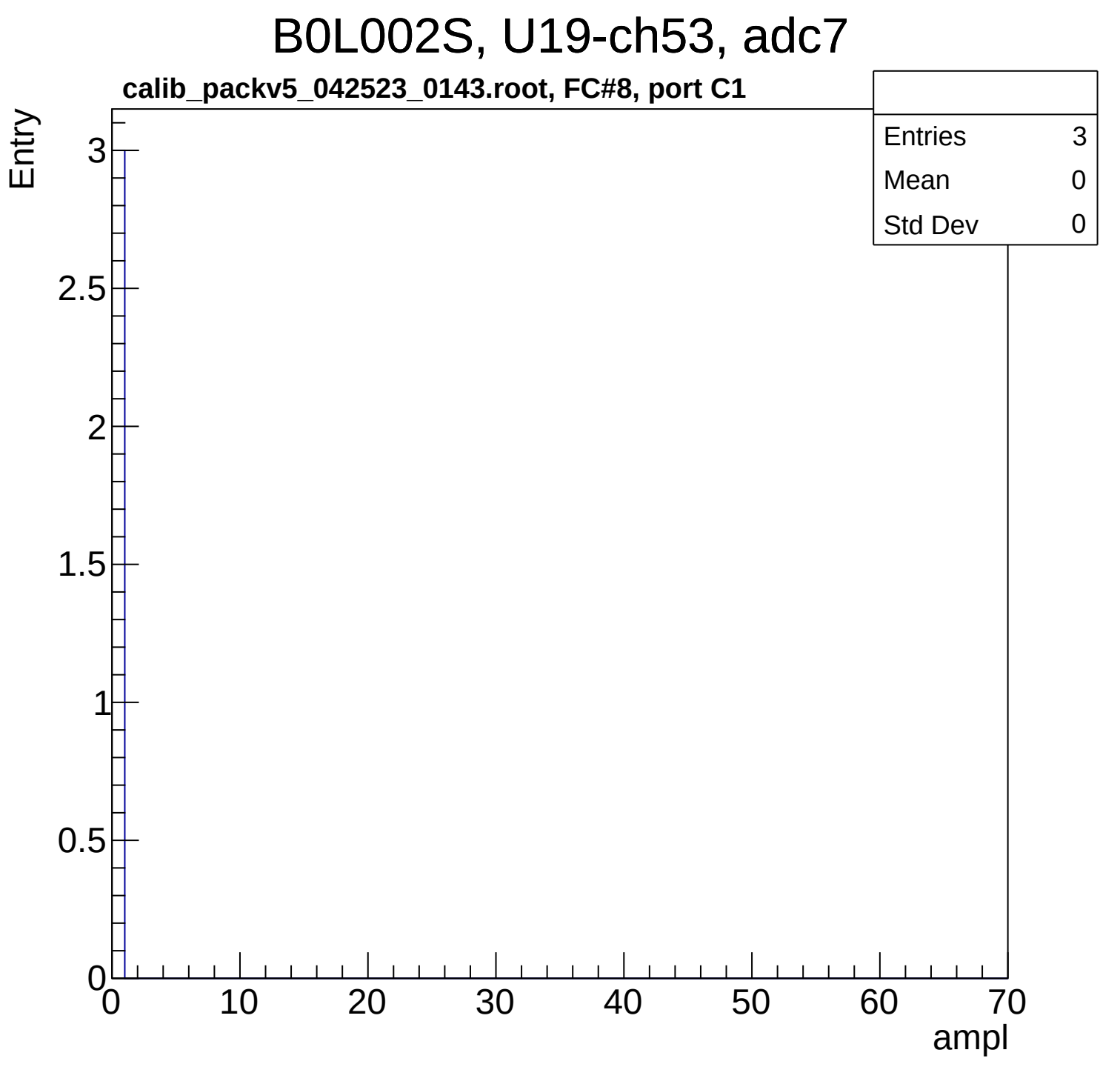

B0L002S, U19-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.87
Std Dev 3.134
Gaus mean : 46.7648
Gaus Width: 3.9460
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

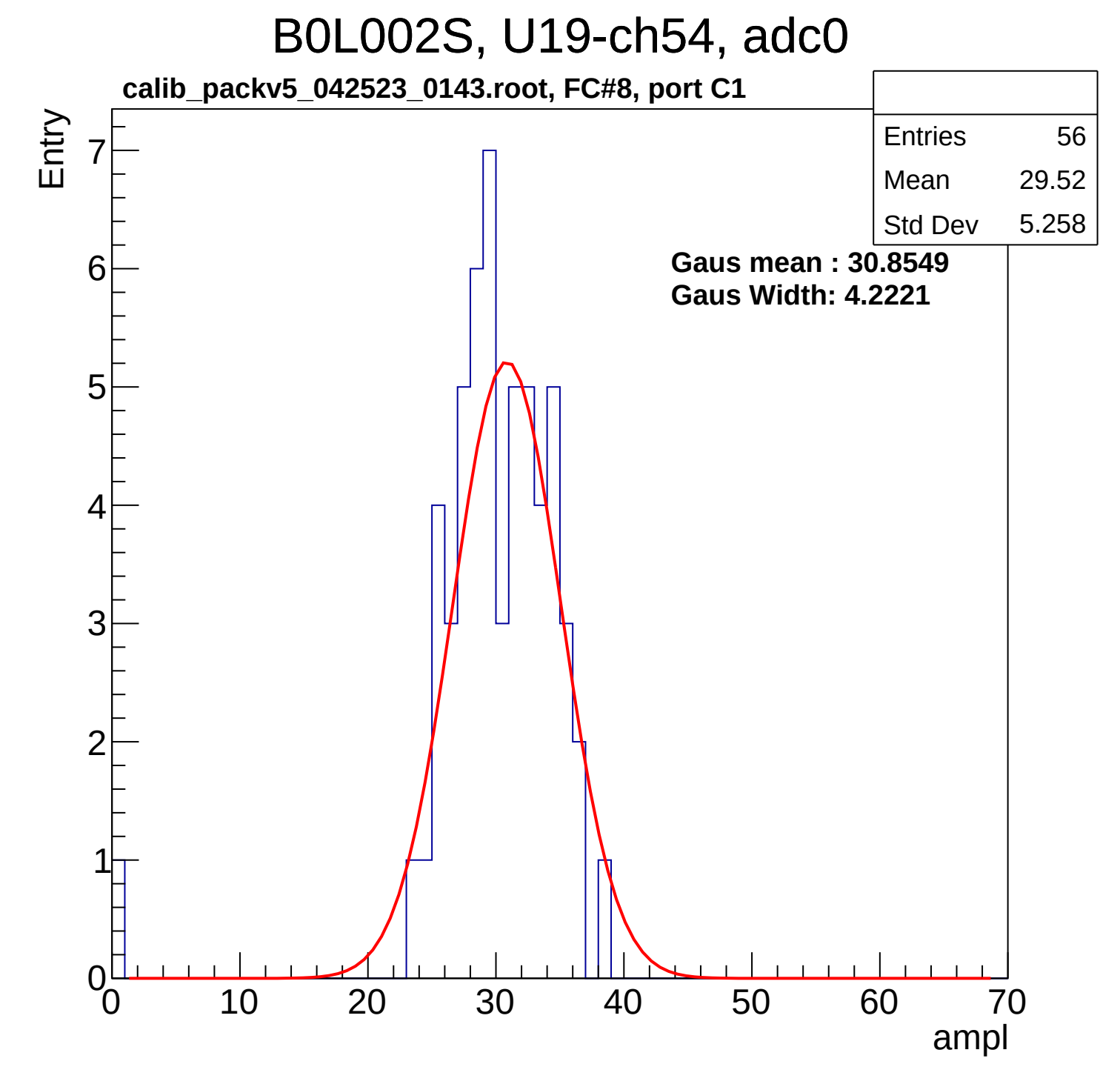

B0L002S, U19-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.6
Std Dev 3.29
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

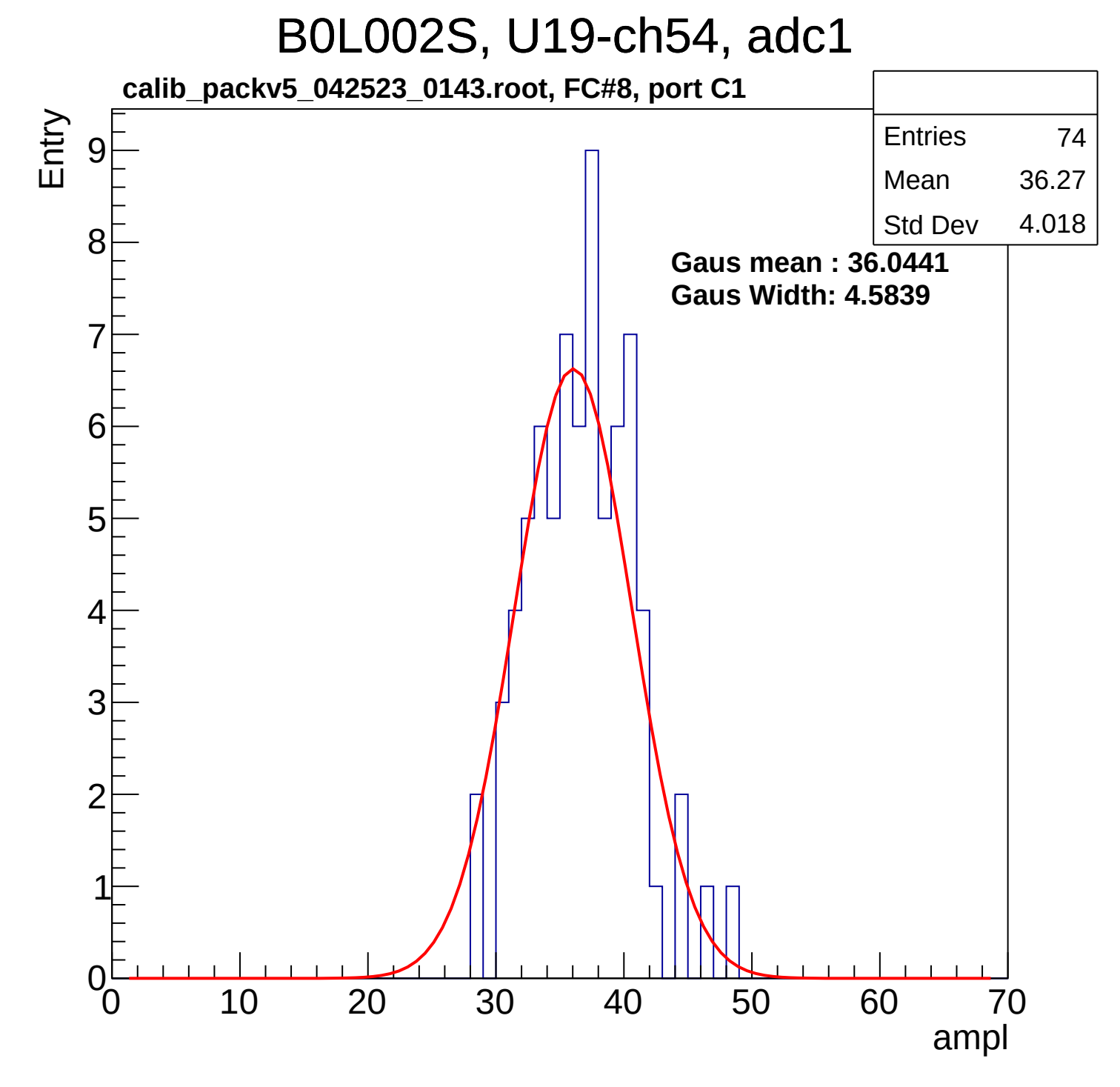

B0L002S, U19-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.73
Std Dev 2.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

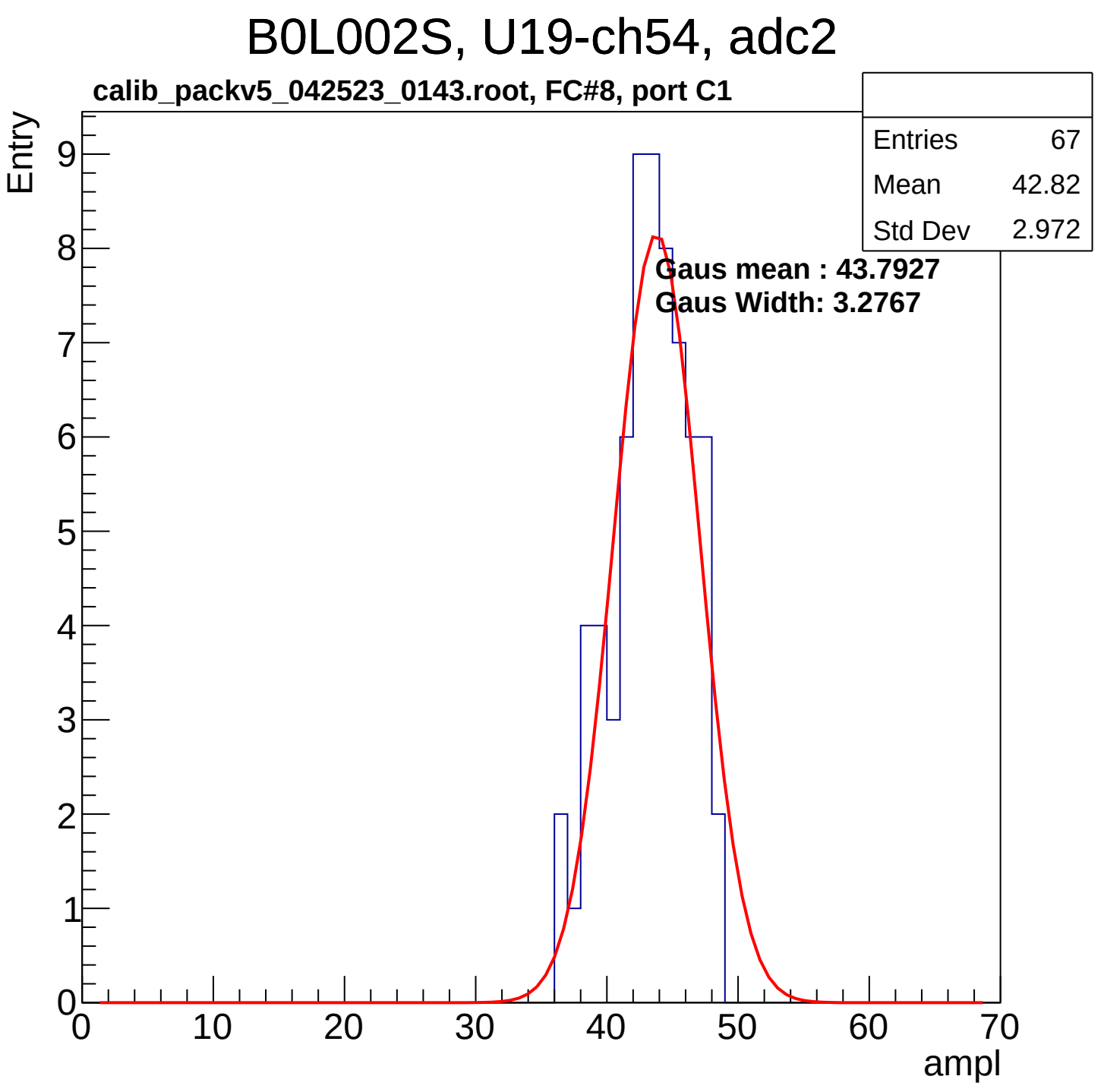

B0L002S, U19-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.28
Std Dev 10.79
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

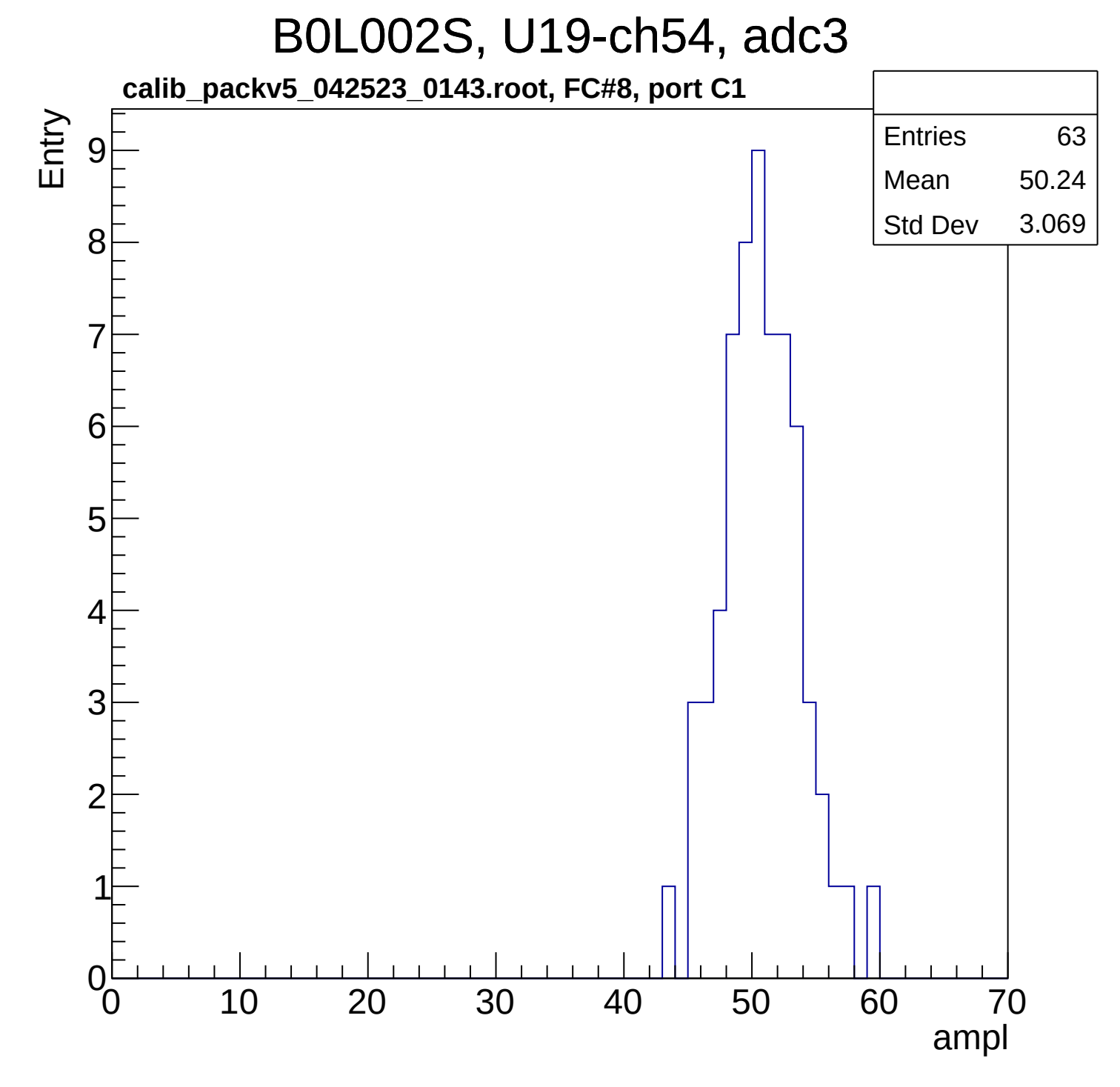

B0L002S, U19-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

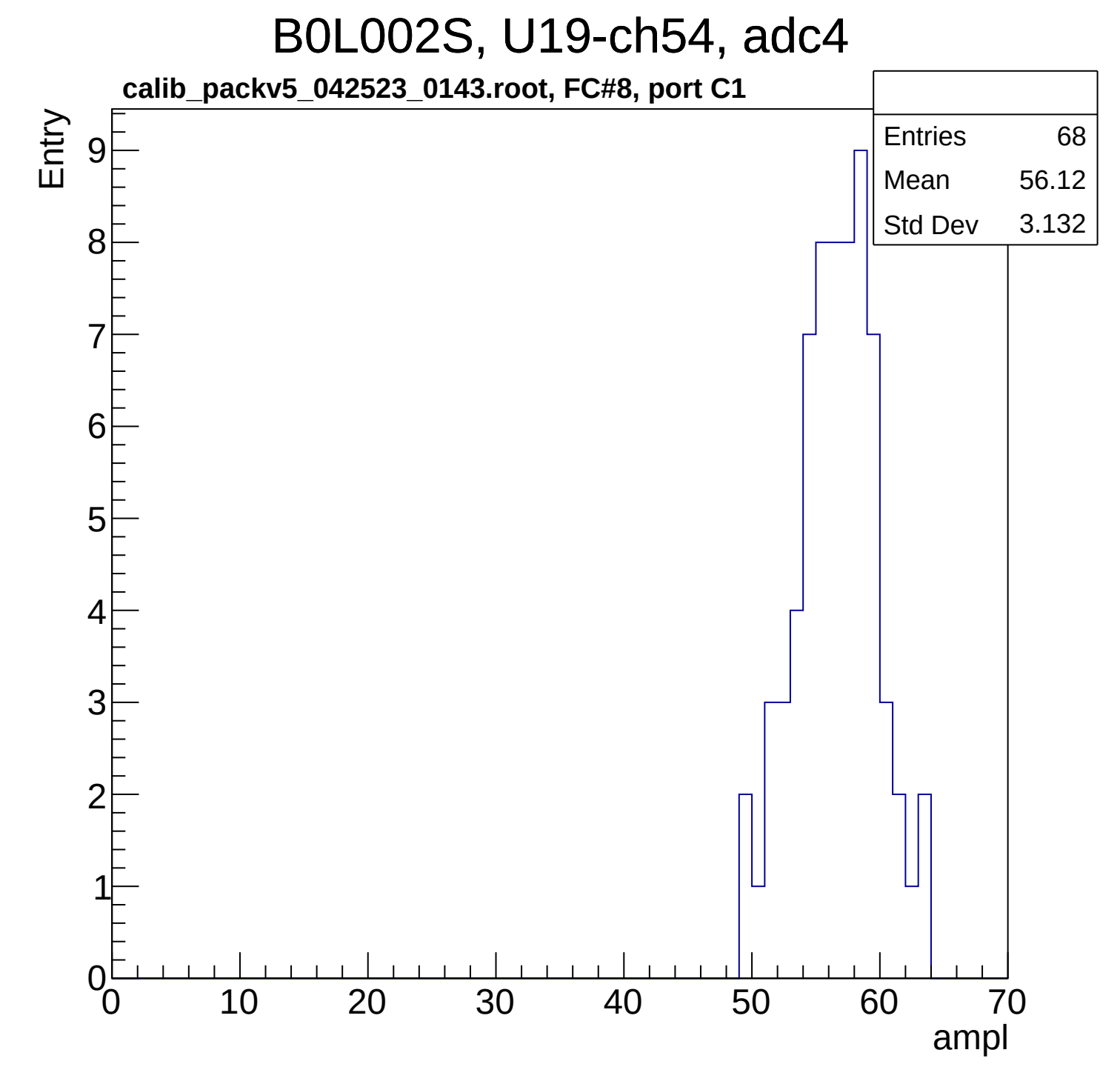

B0L002S, U19-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

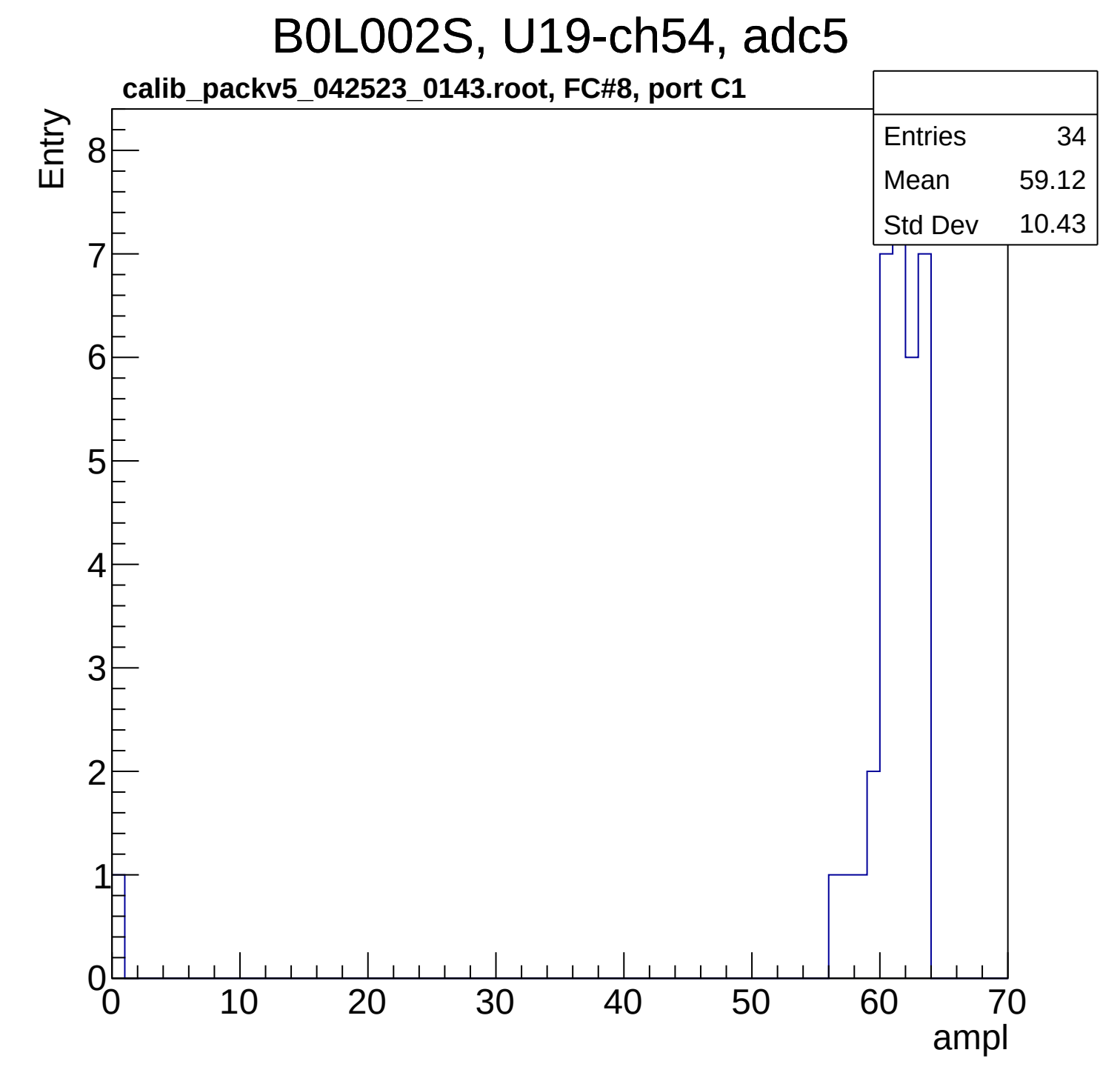

B0L002S, U19-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 29.52
Std Dev 5.258
Gaus mean : 30.8549
Gaus Width: 4.2221
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

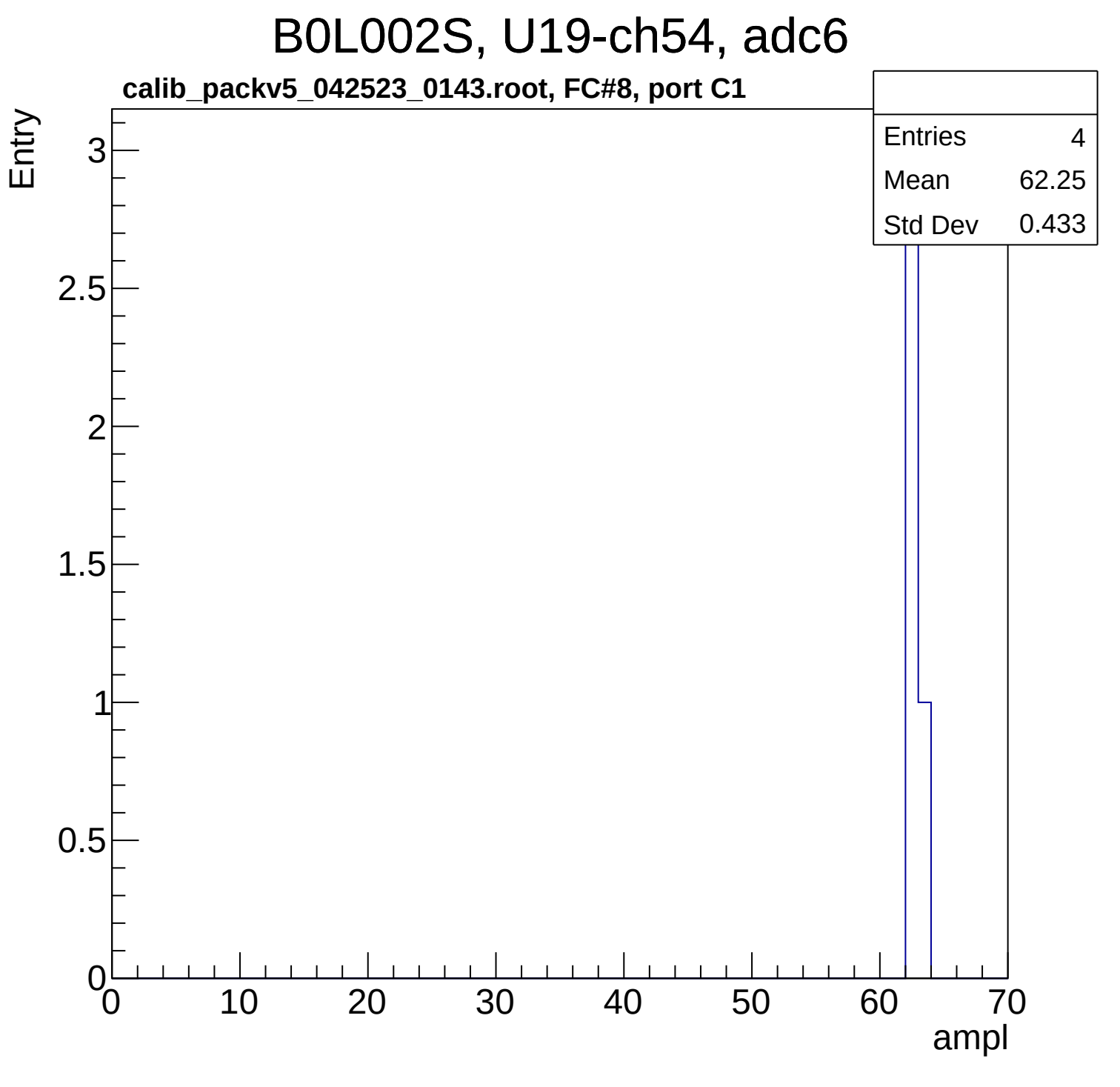

B0L002S, U19-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.27
Std Dev 4.018
Gaus mean : 36.0441
Gaus Width: 4.5839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

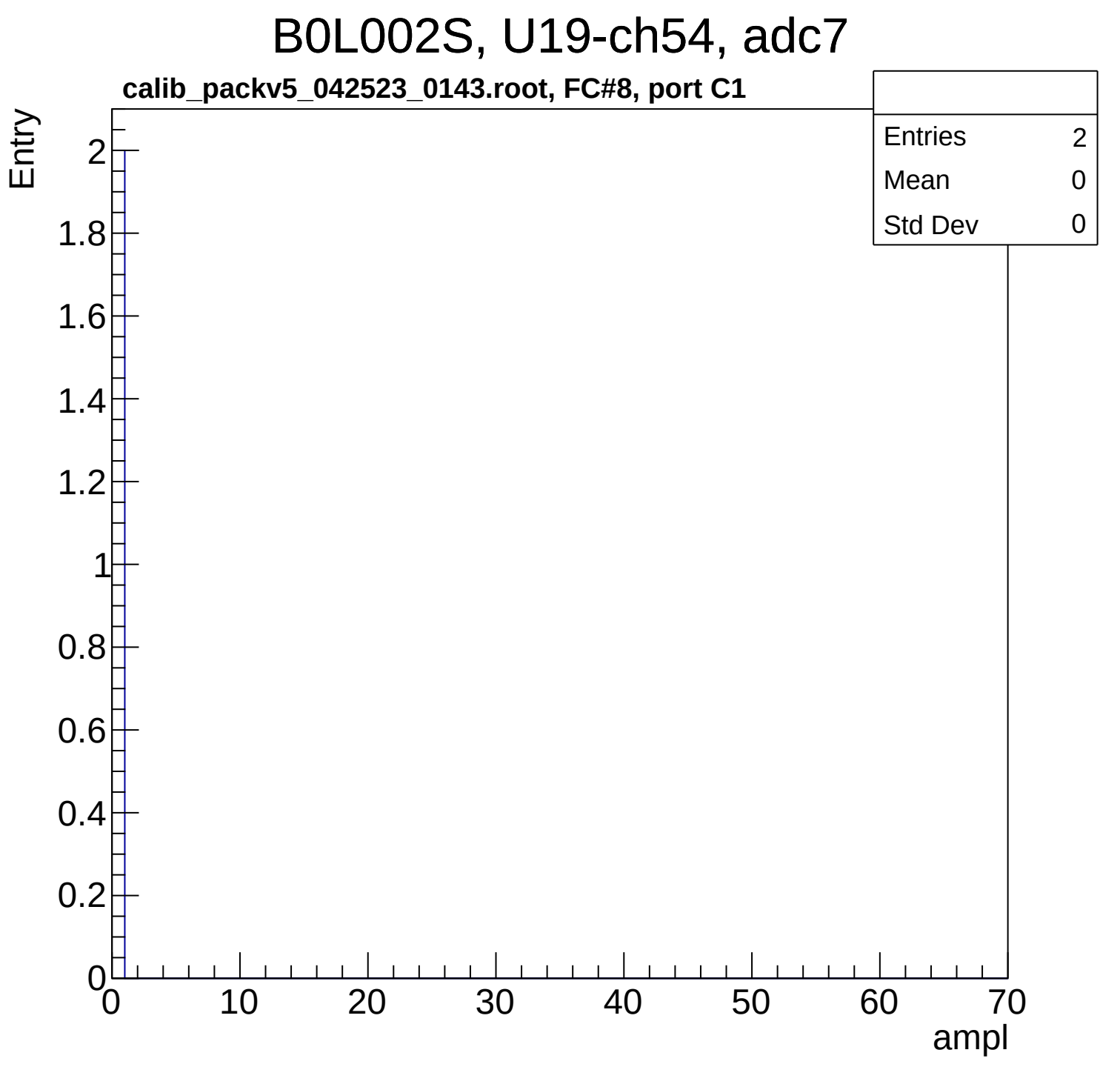

B0L002S, U19-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42.82
Std Dev 2.972
Gaus mean : 43.7927
Gaus Width: 3.2767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

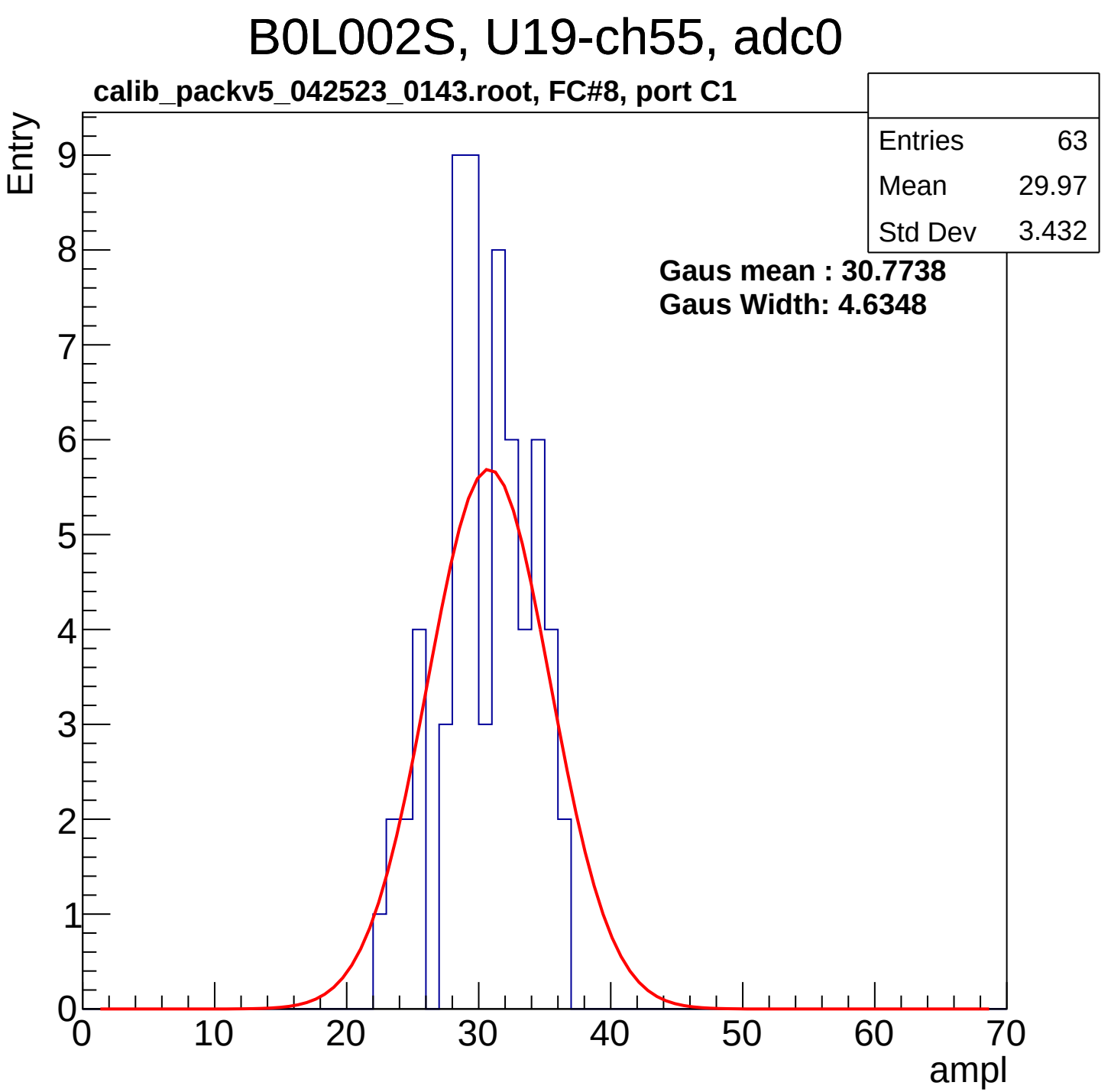

B0L002S, U19-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.24
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

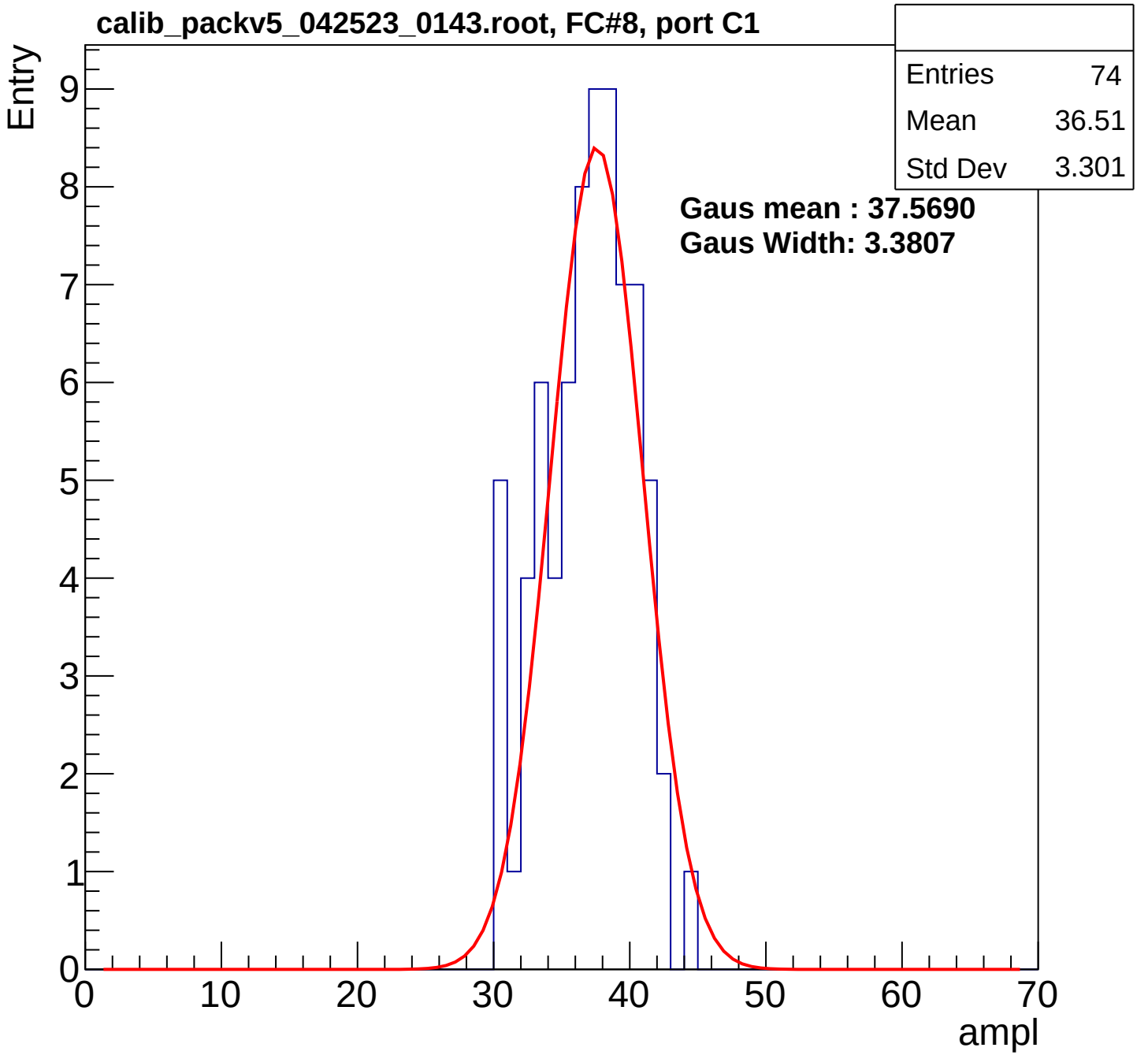

B0L002S, U19-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.12
Std Dev 3.132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

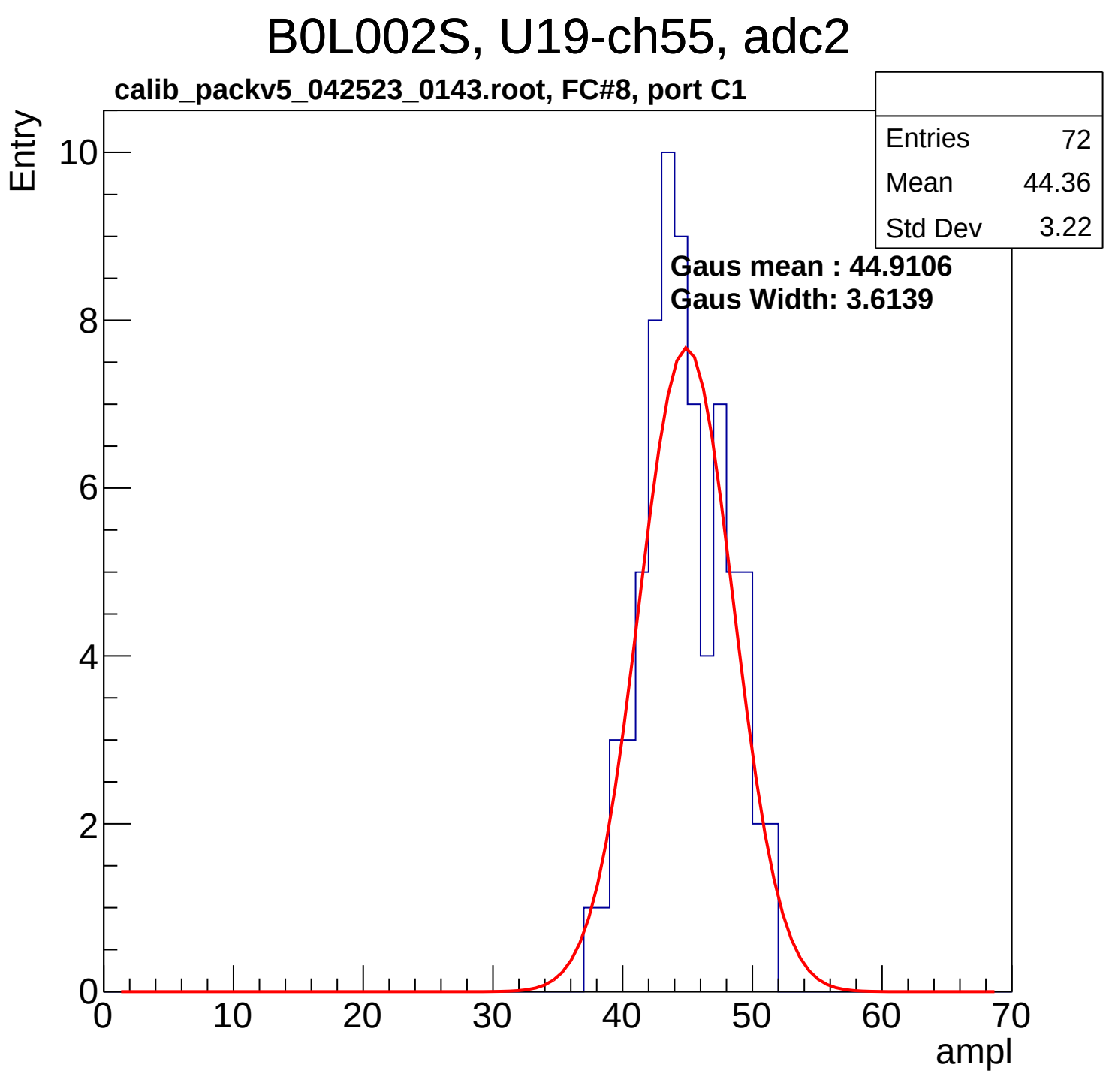

B0L002S, U19-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.12
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

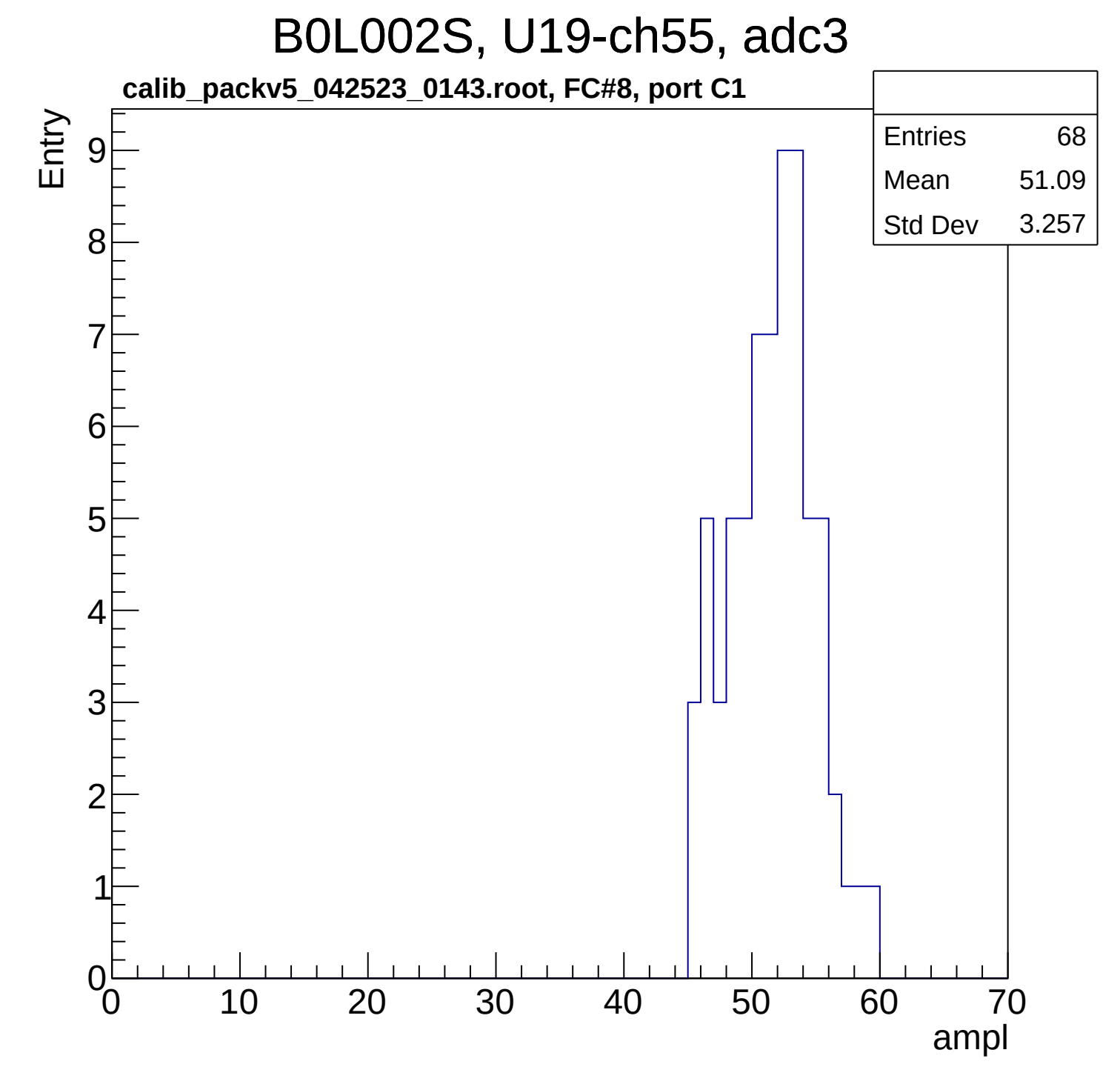

B0L002S, U19-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

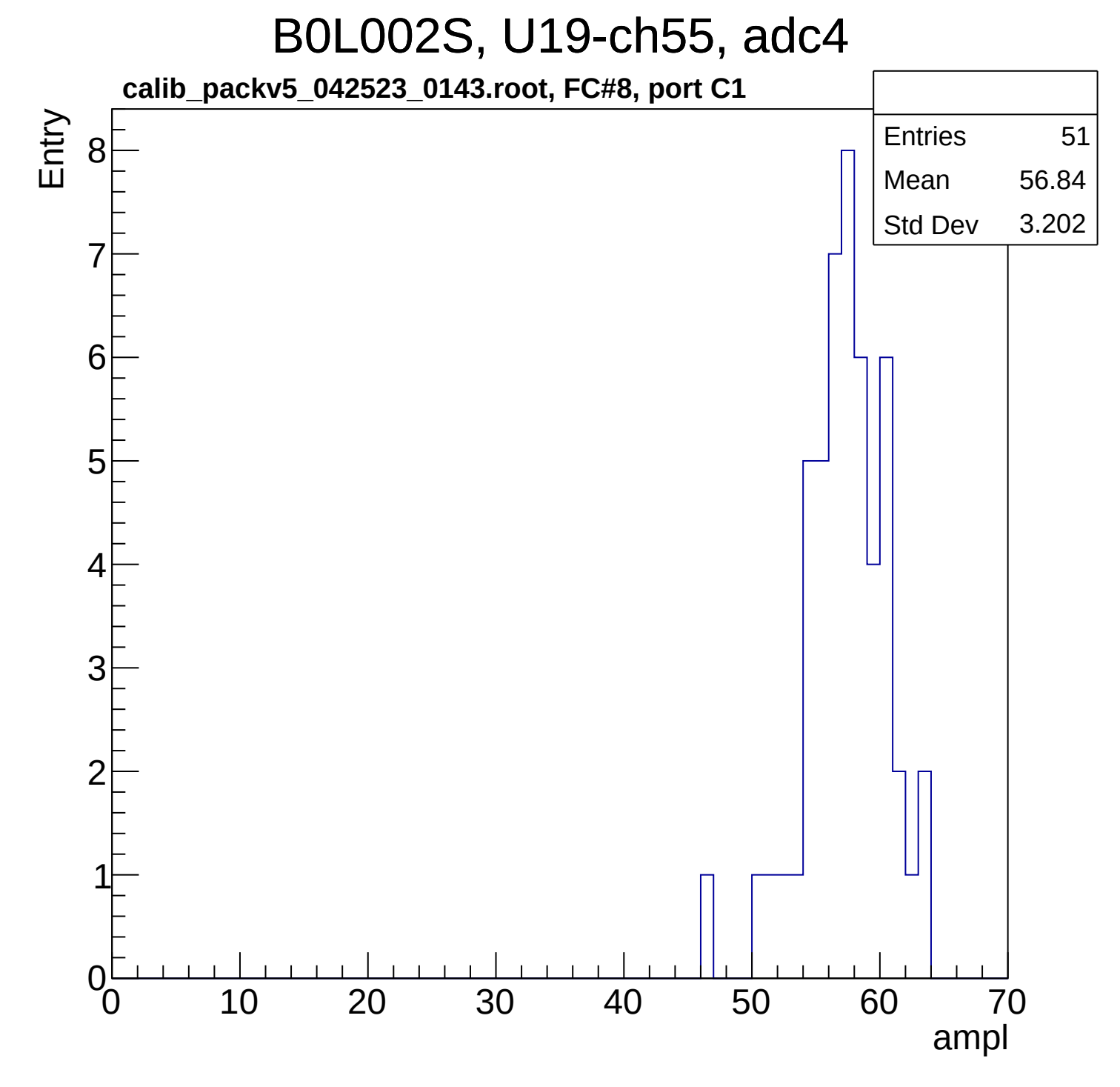

B0L002S, U19-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

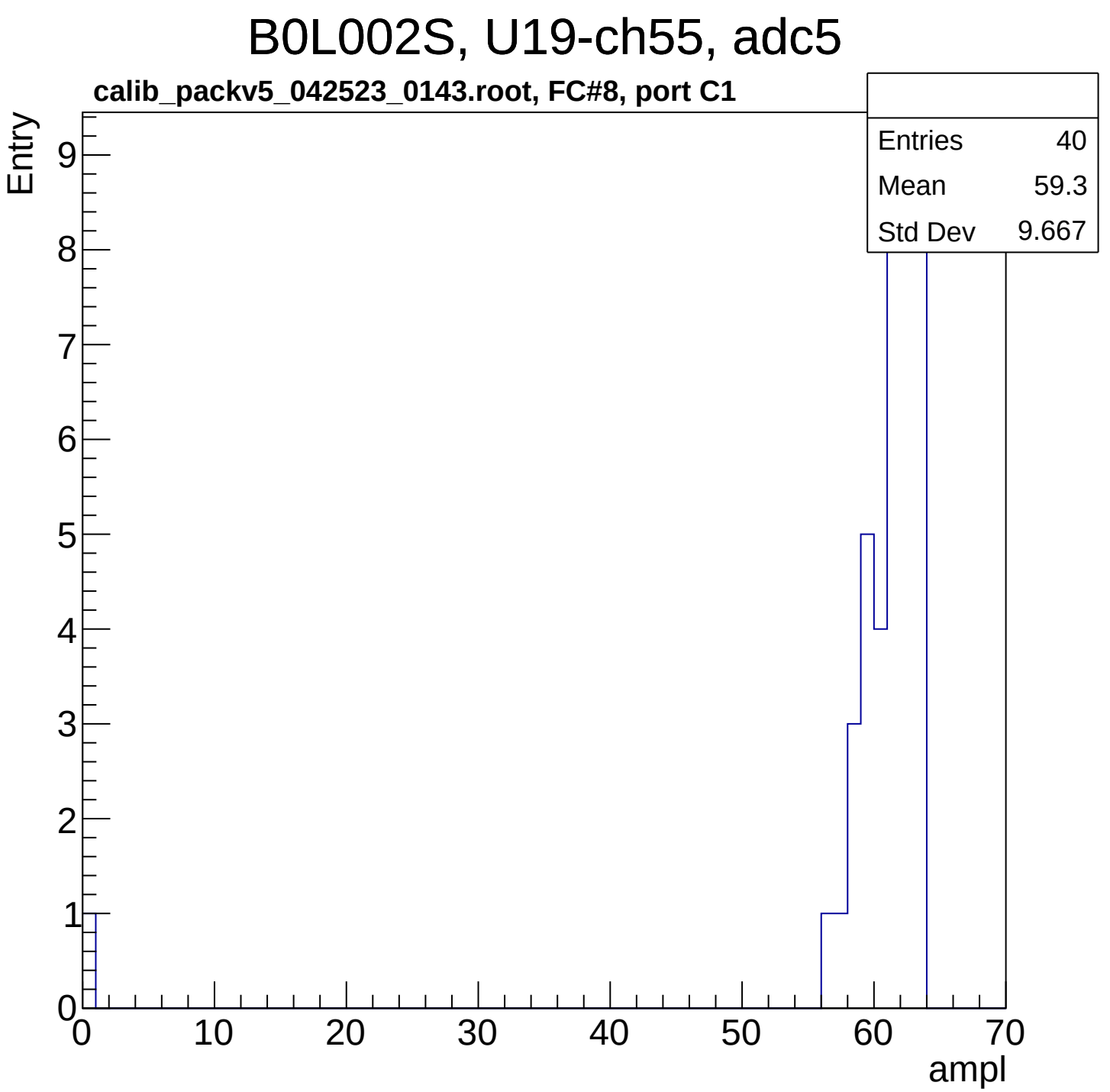

B0L002S, U19-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.97
Std Dev 3.432
Gaus mean : 30.7738
Gaus Width: 4.6348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

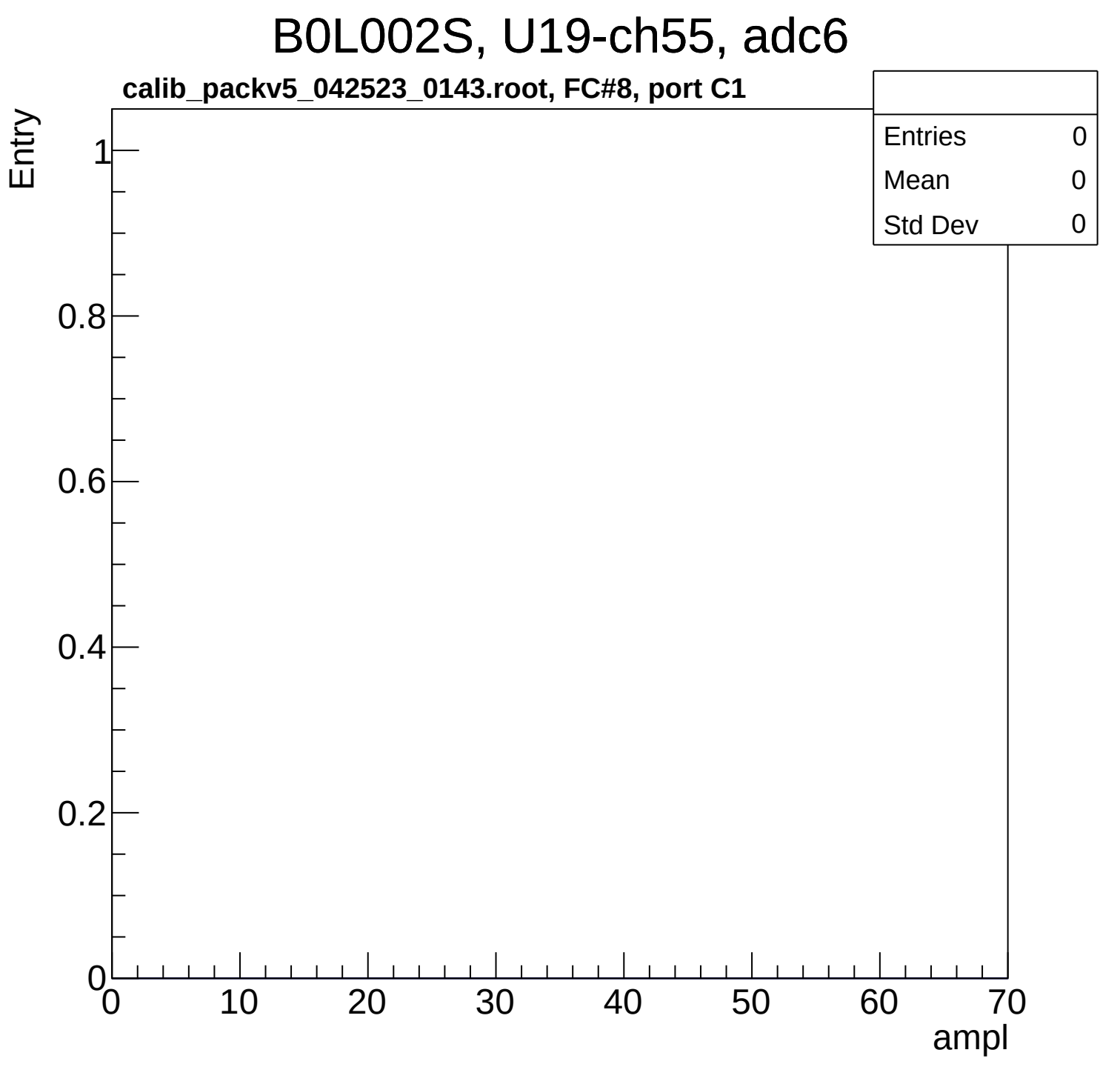

B0L002S, U19-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.51
Std Dev 3.301
Gaus mean : 37.5690
Gaus Width: 3.3807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

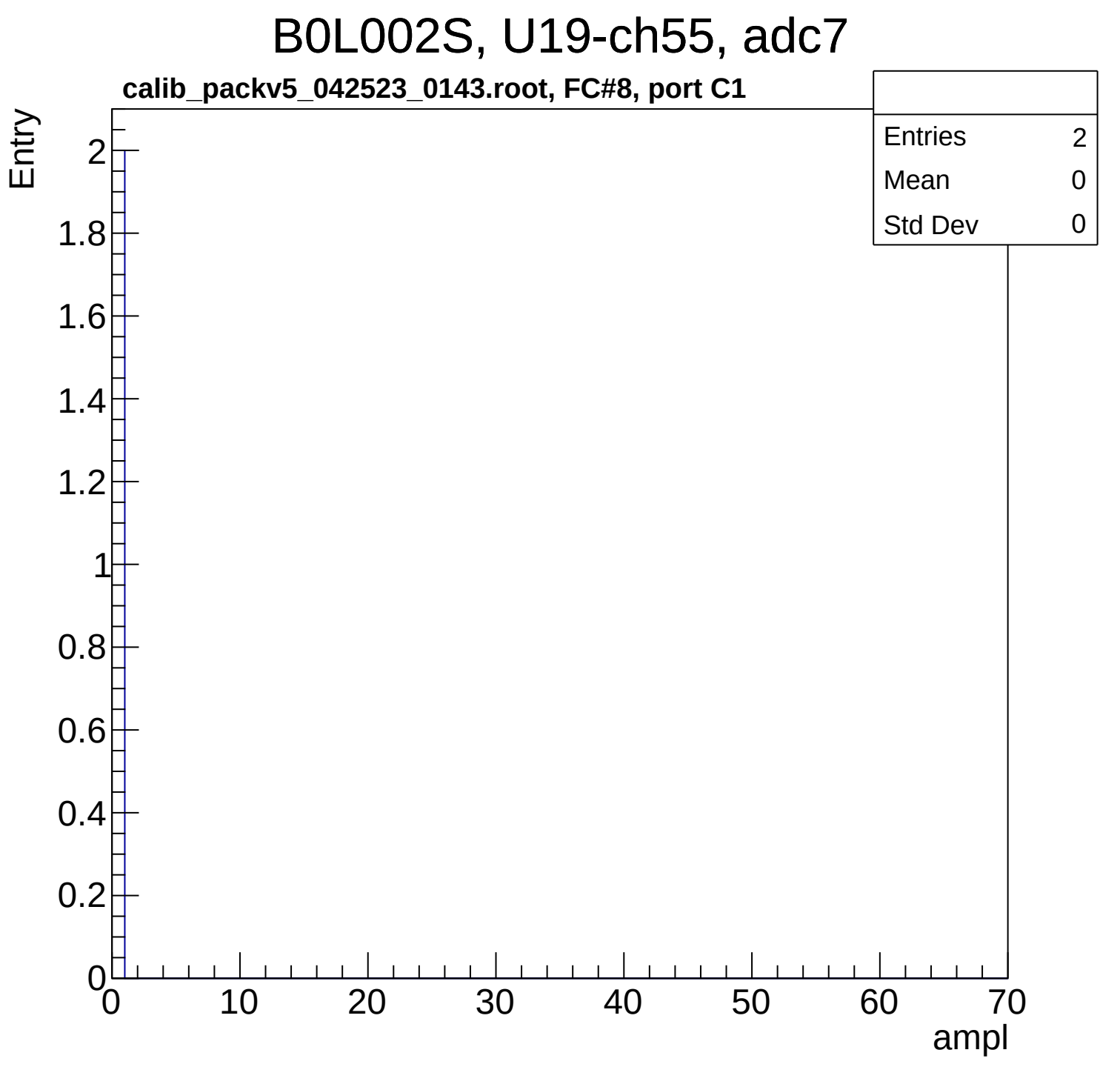

B0L002S, U19-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.36
Std Dev 3.22
Gaus mean : 44.9106
Gaus Width: 3.6139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

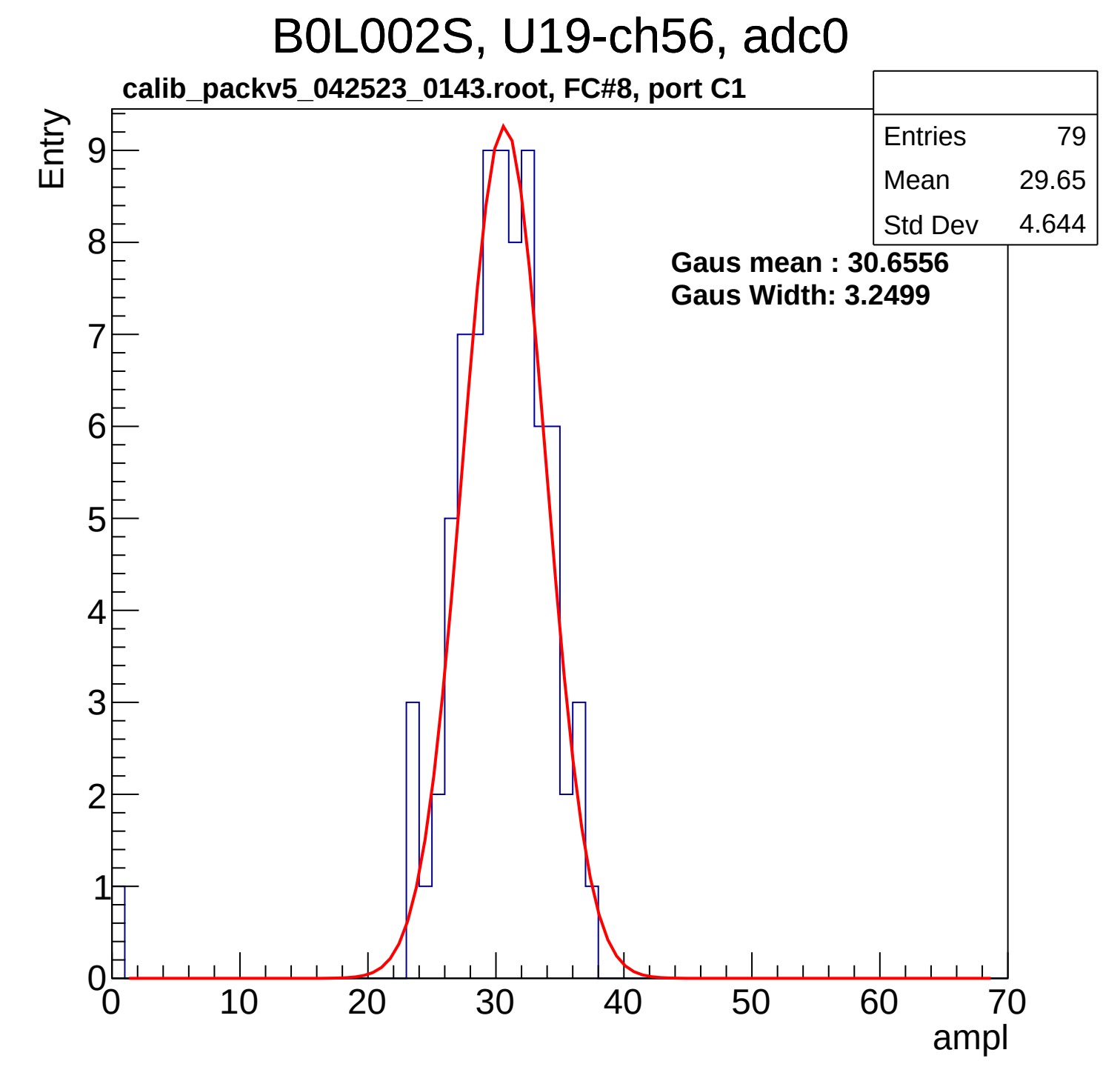

B0L002S, U19-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.09
Std Dev 3.257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

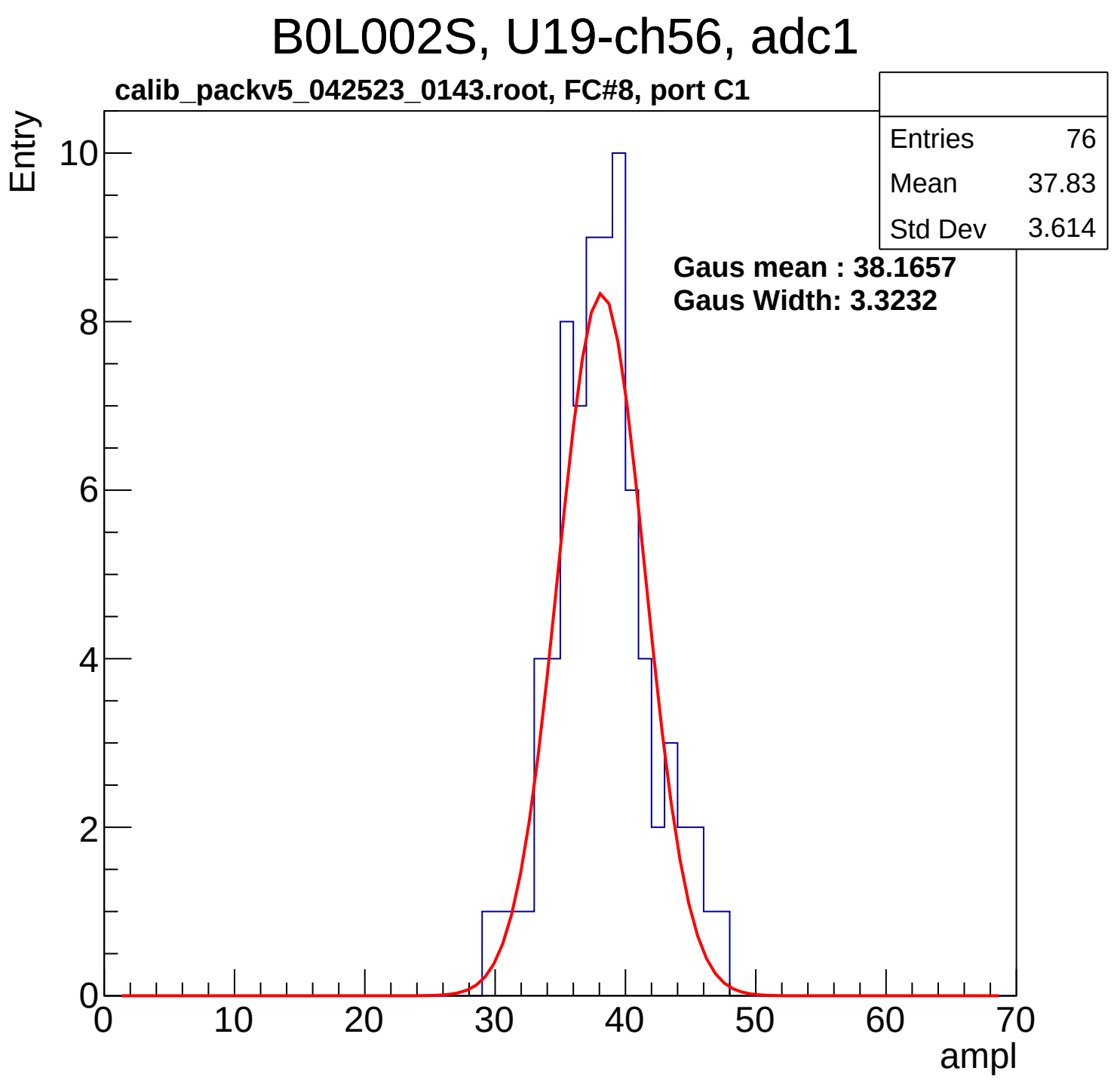

B0L002S, U19-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.84
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

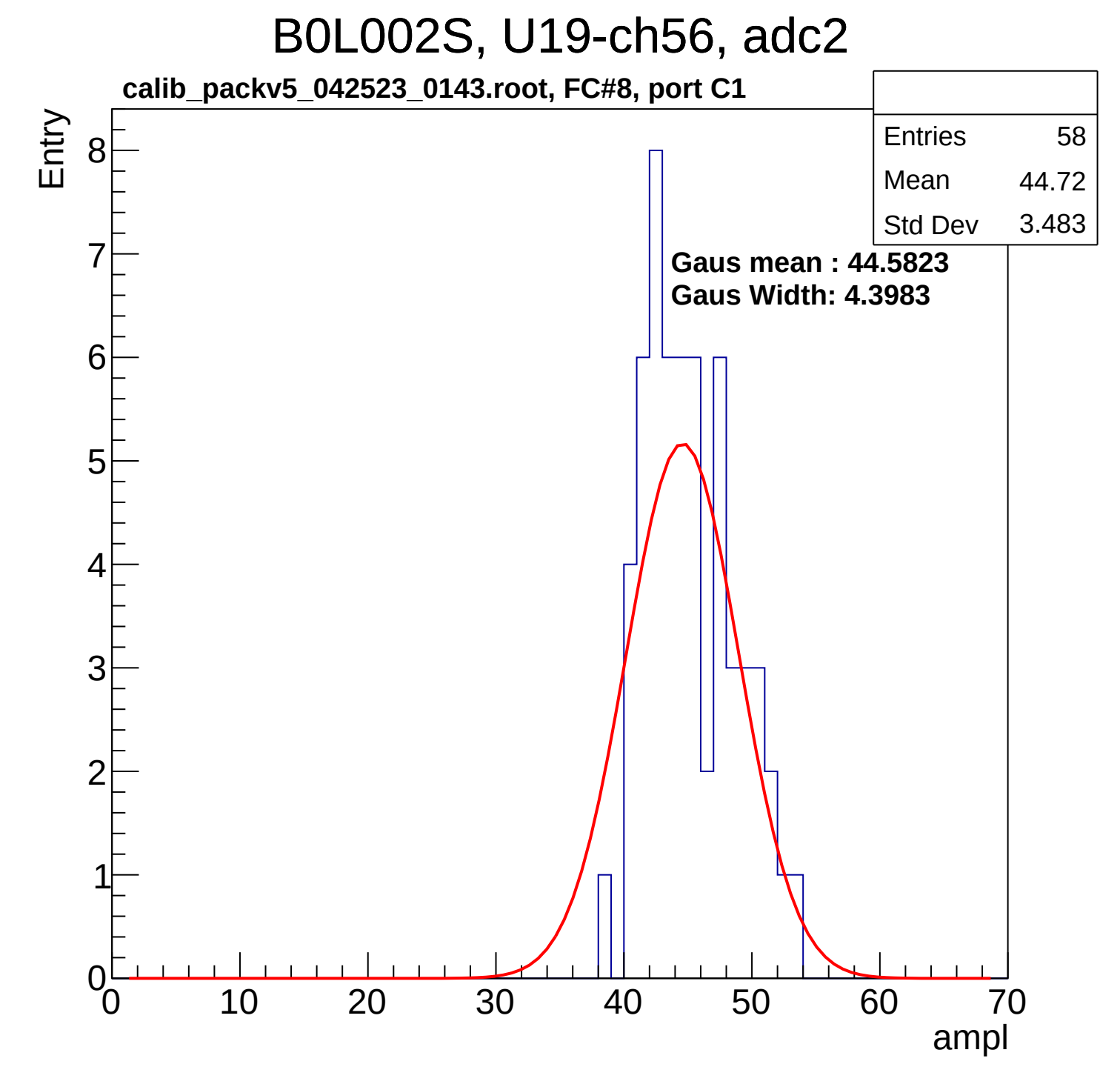

B0L002S, U19-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.3
Std Dev 9.667
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

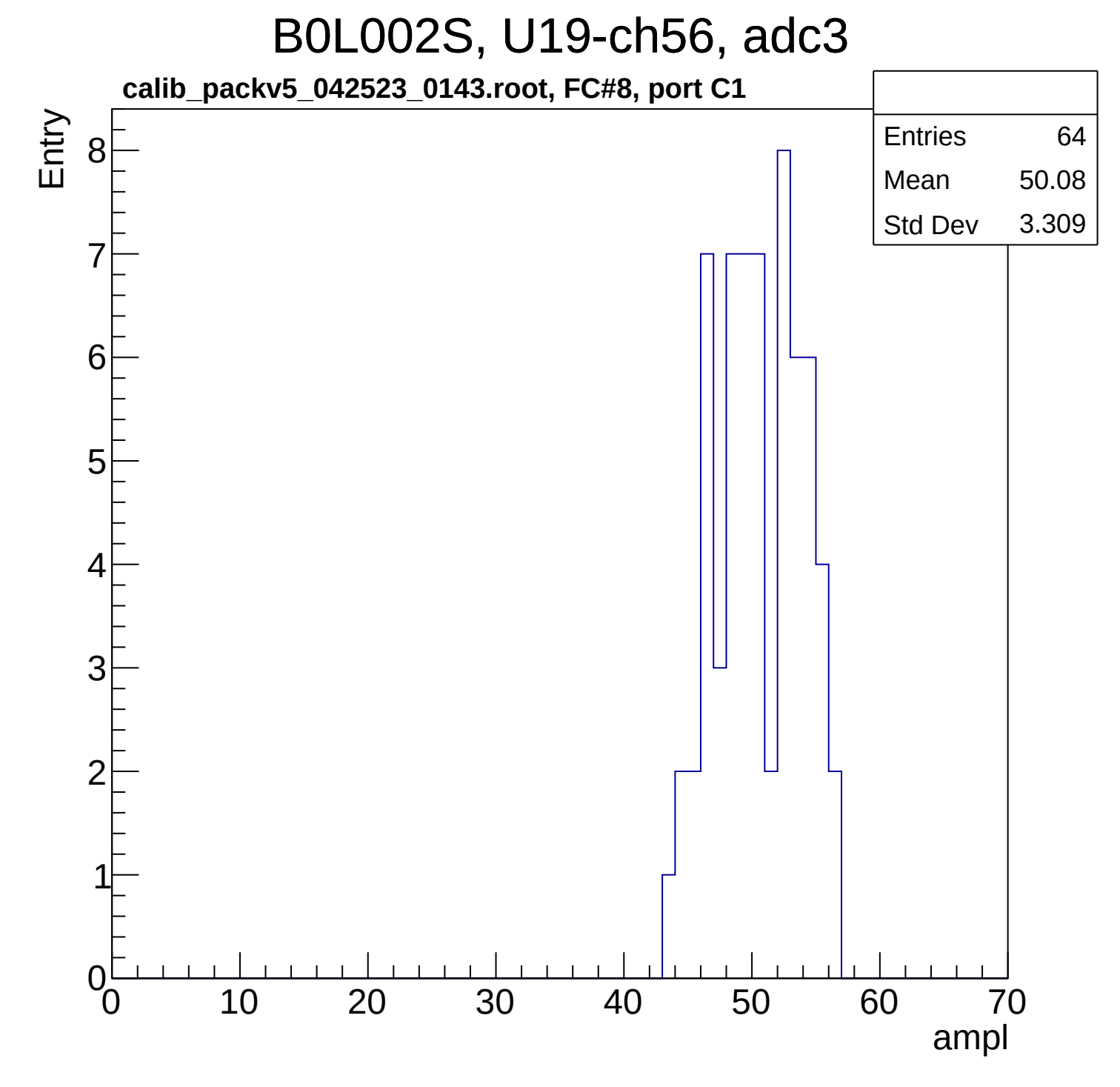

B0L002S, U19-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

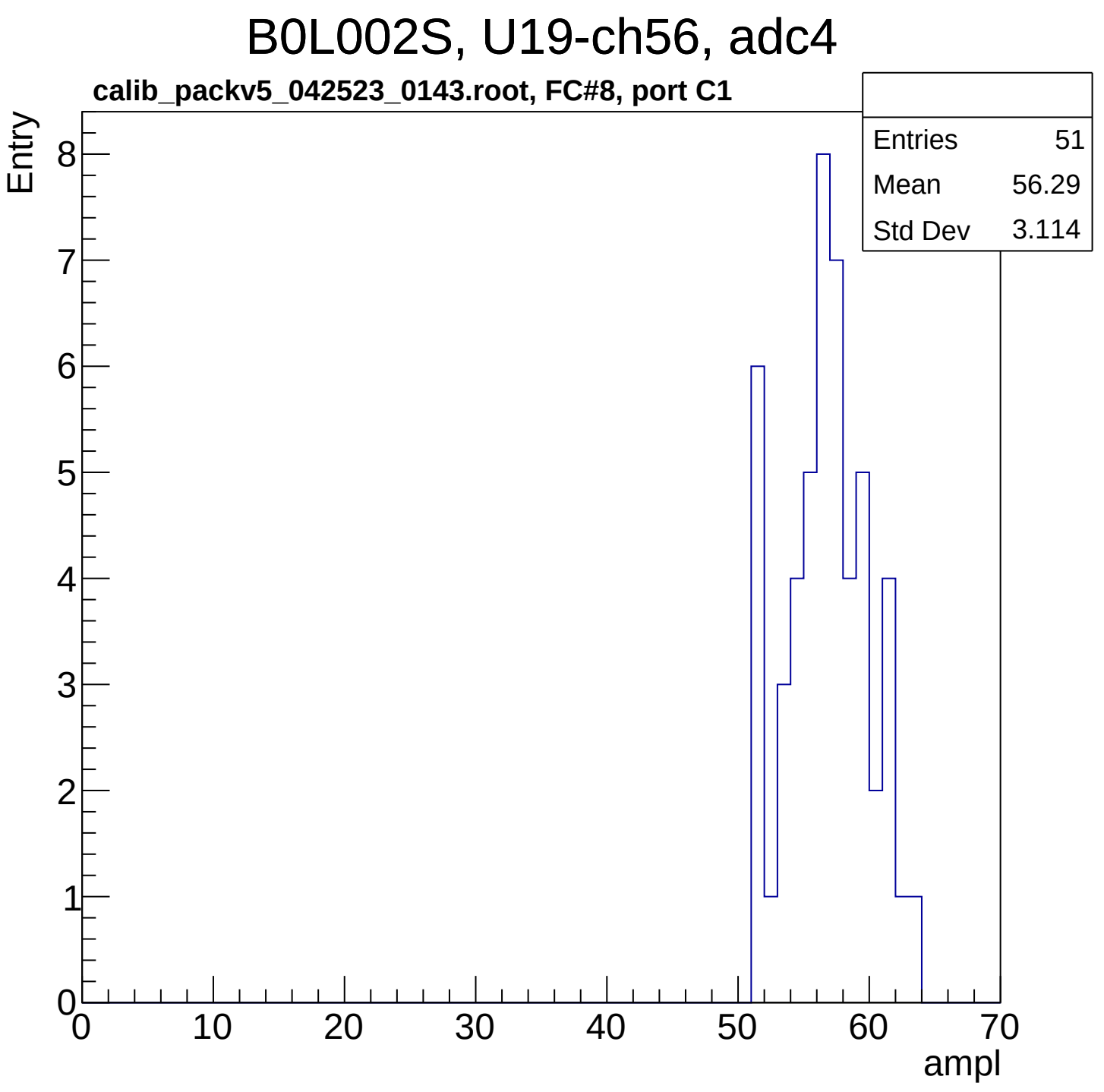

B0L002S, U19-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

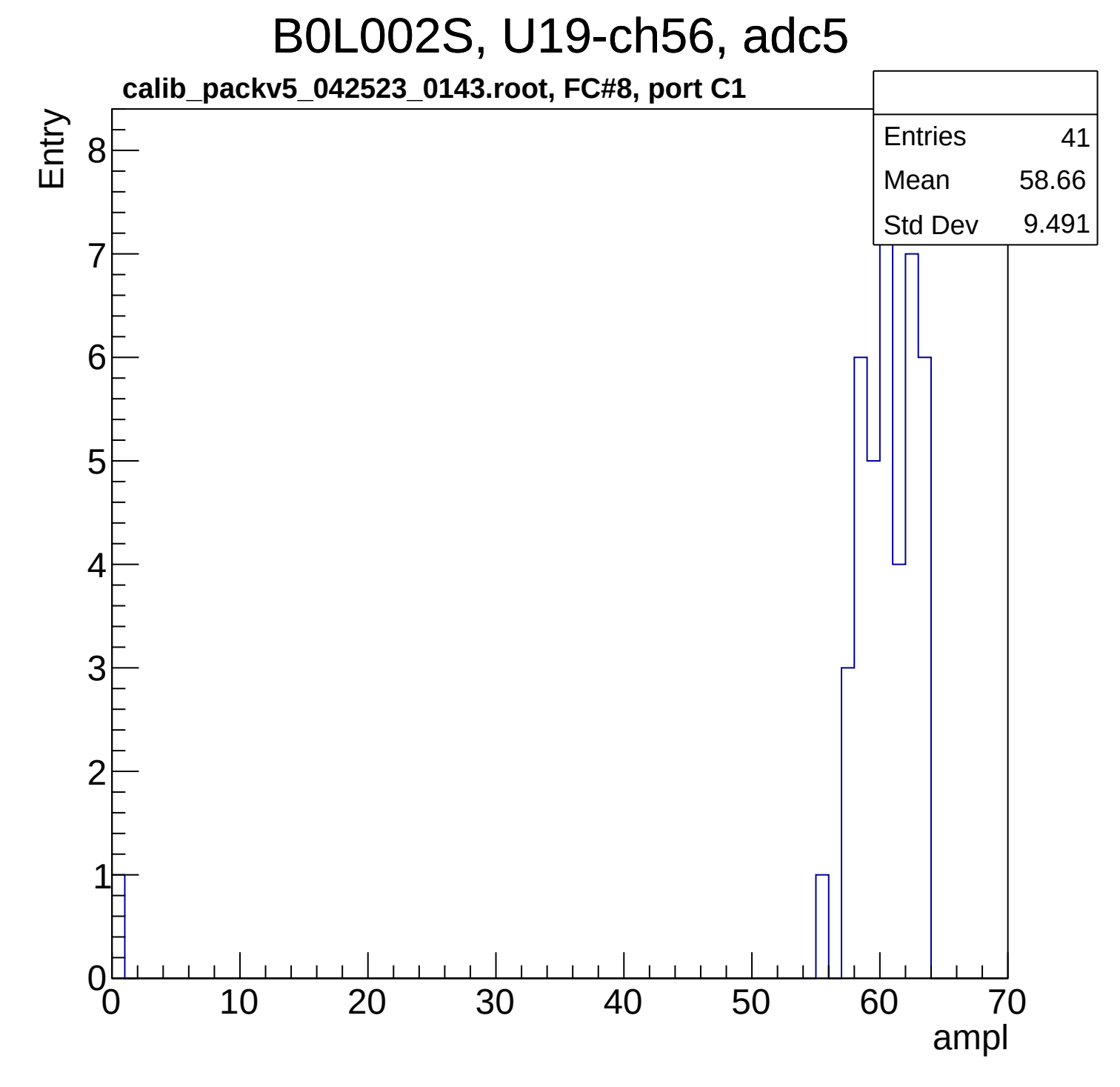

B0L002S, U19-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.65
Std Dev 4.644
Gaus mean : 30.6556
Gaus Width: 3.2499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

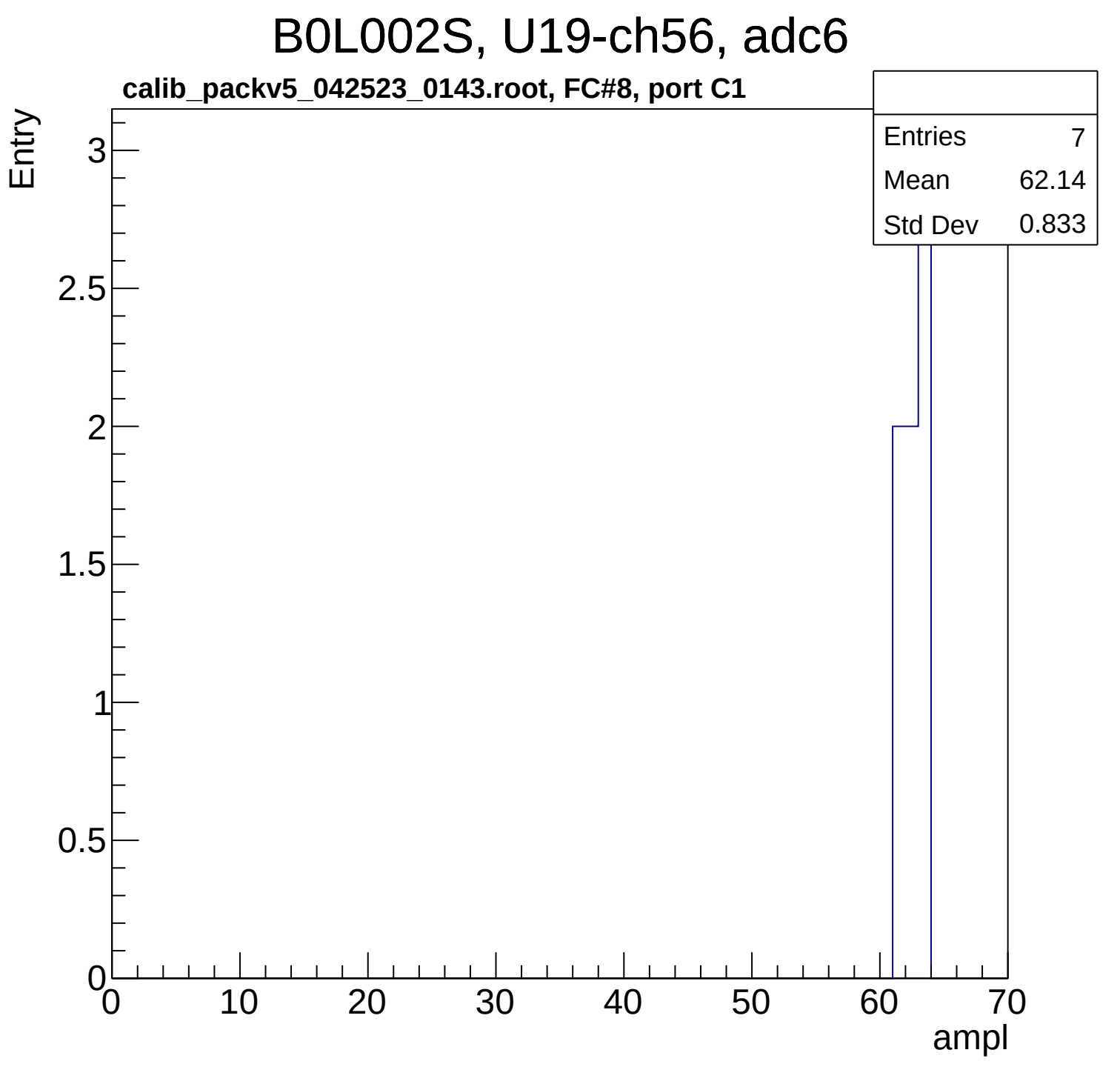

B0L002S, U19-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.83
Std Dev 3.614
Gaus mean : 38.1657
Gaus Width: 3.3232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

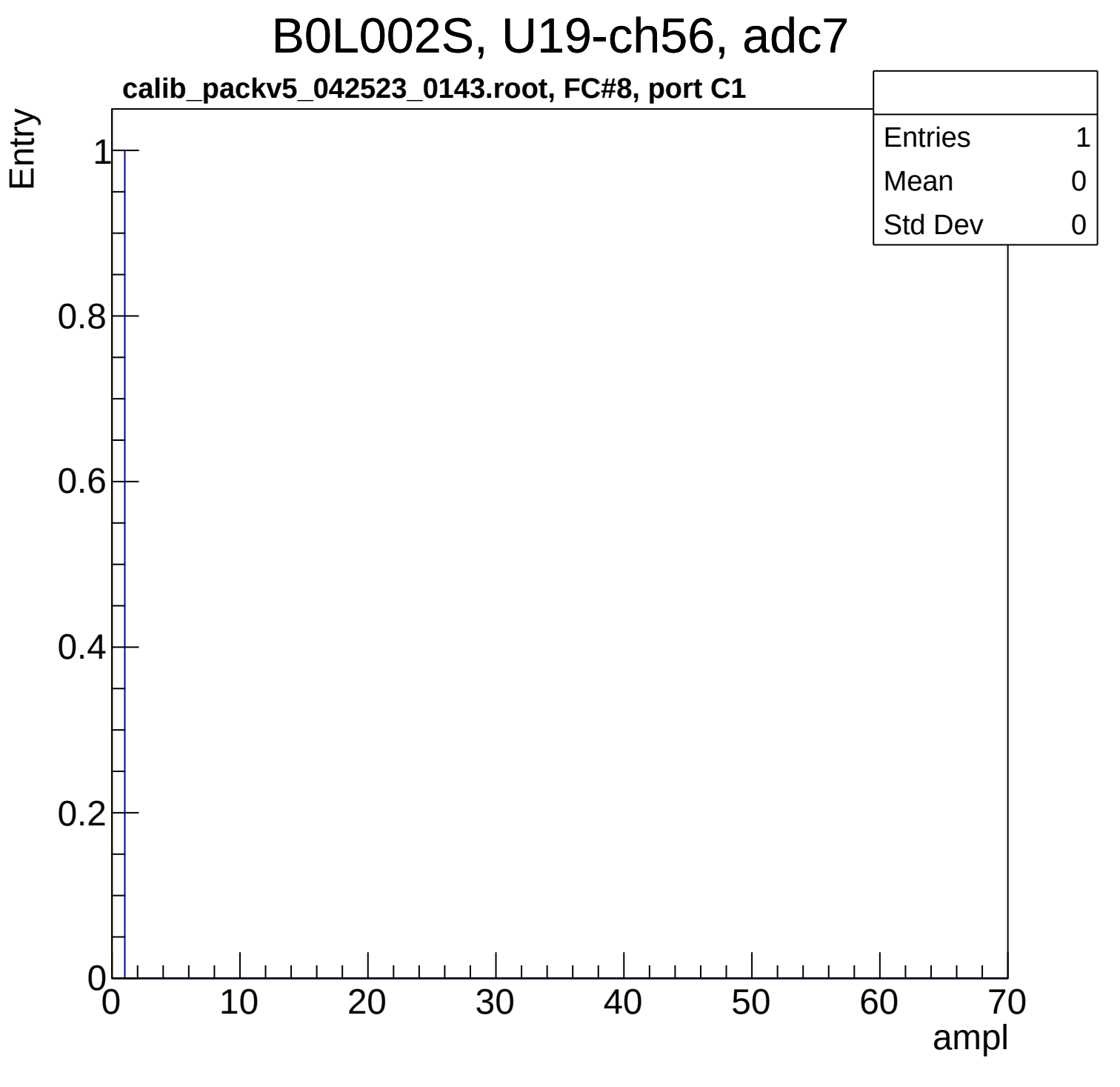

B0L002S, U19-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 44.72
Std Dev 3.483
Gaus mean : 44.5823
Gaus Width: 4.3983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

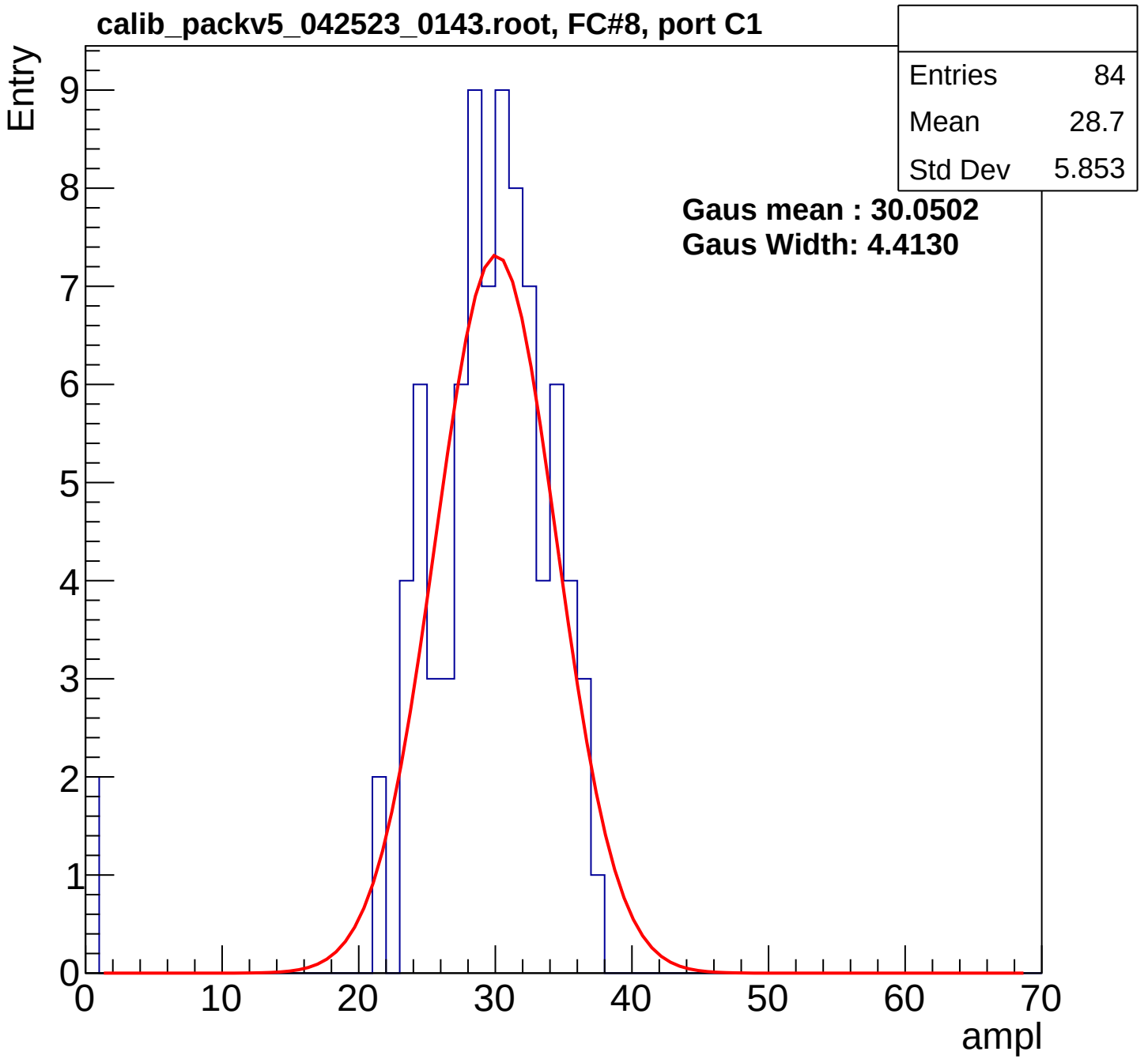

B0L002S, U19-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.08
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

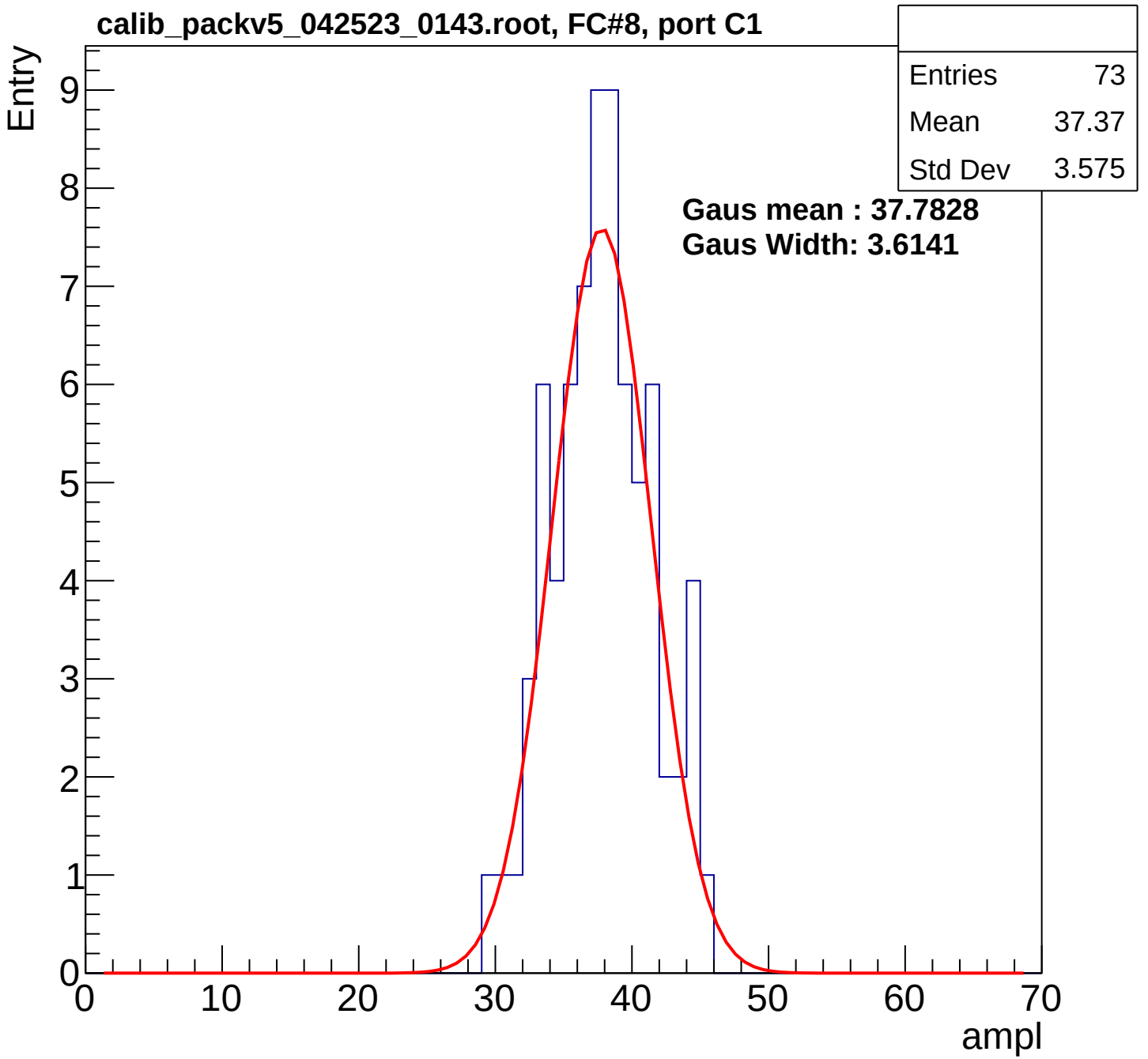

B0L002S, U19-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.29
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

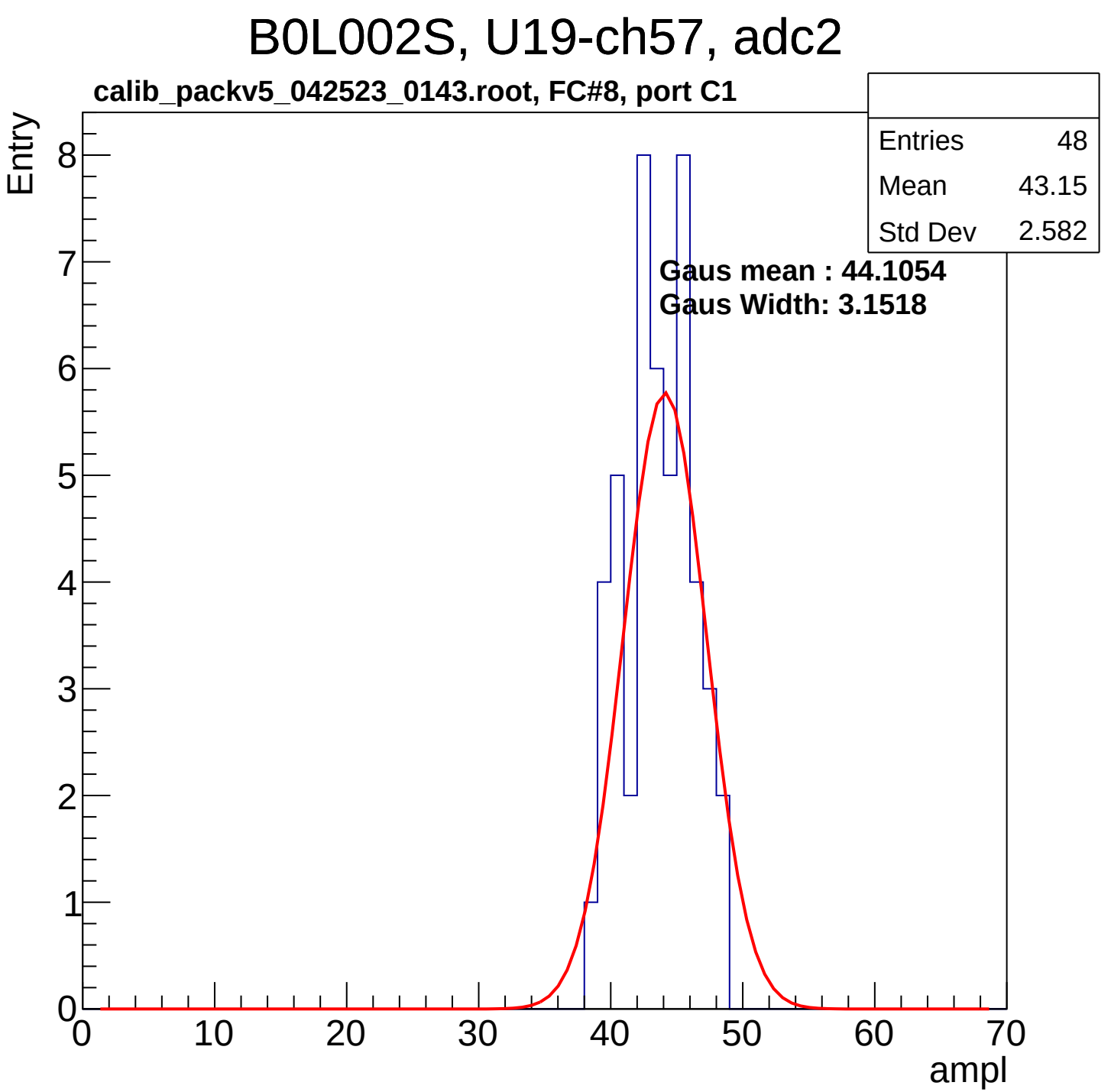

B0L002S, U19-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.66
Std Dev 9.491
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

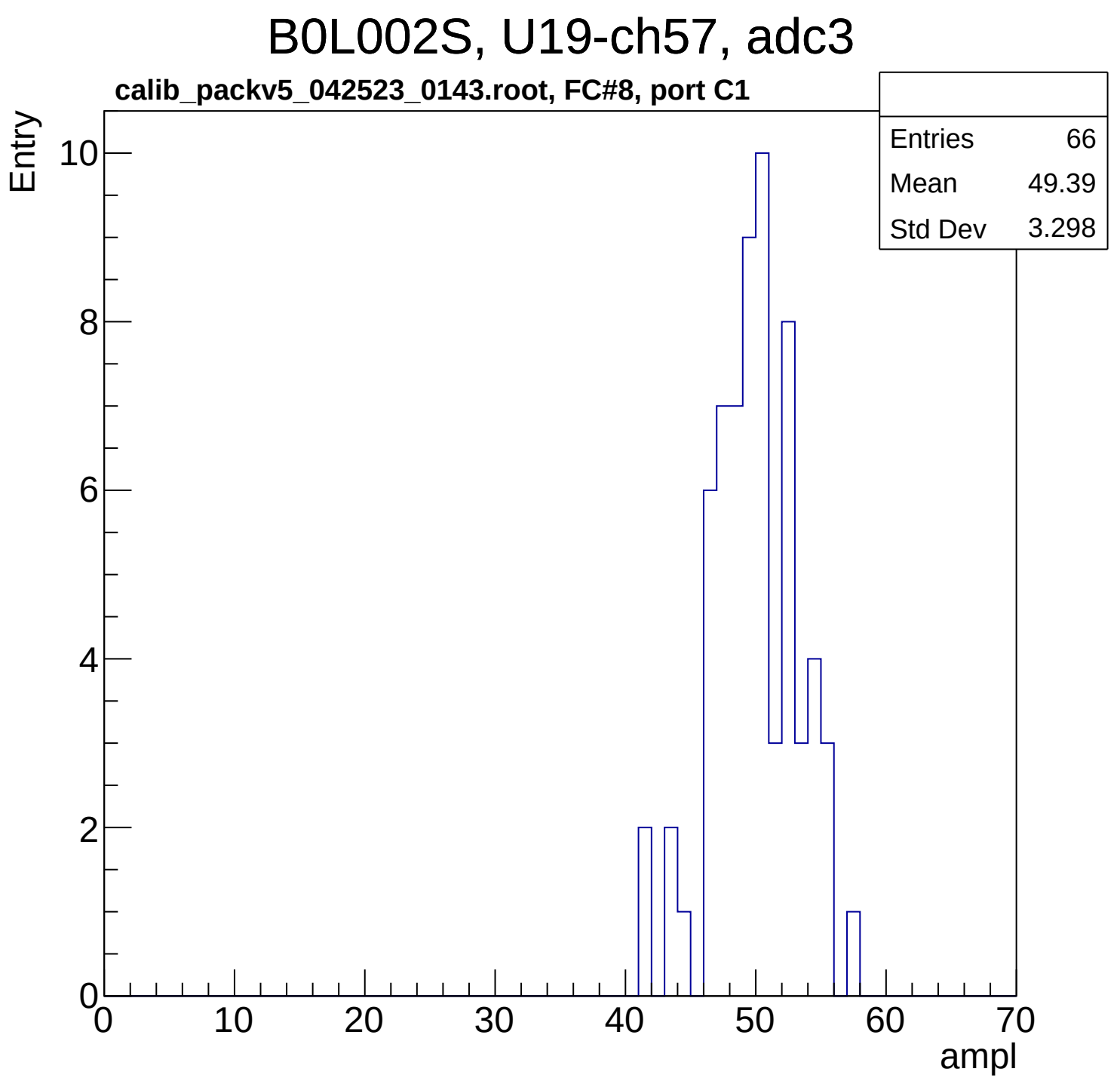

B0L002S, U19-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

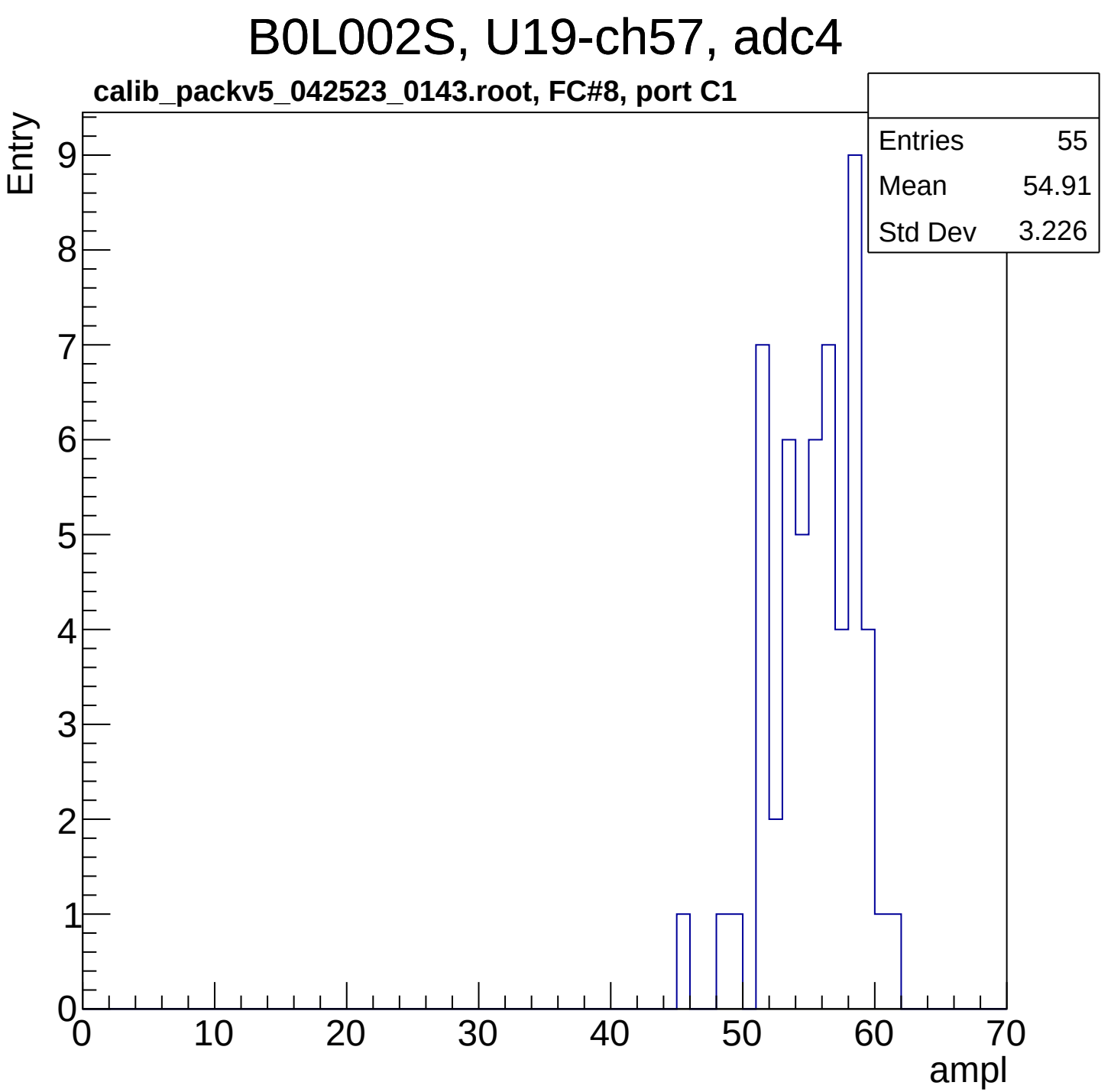

B0L002S, U19-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

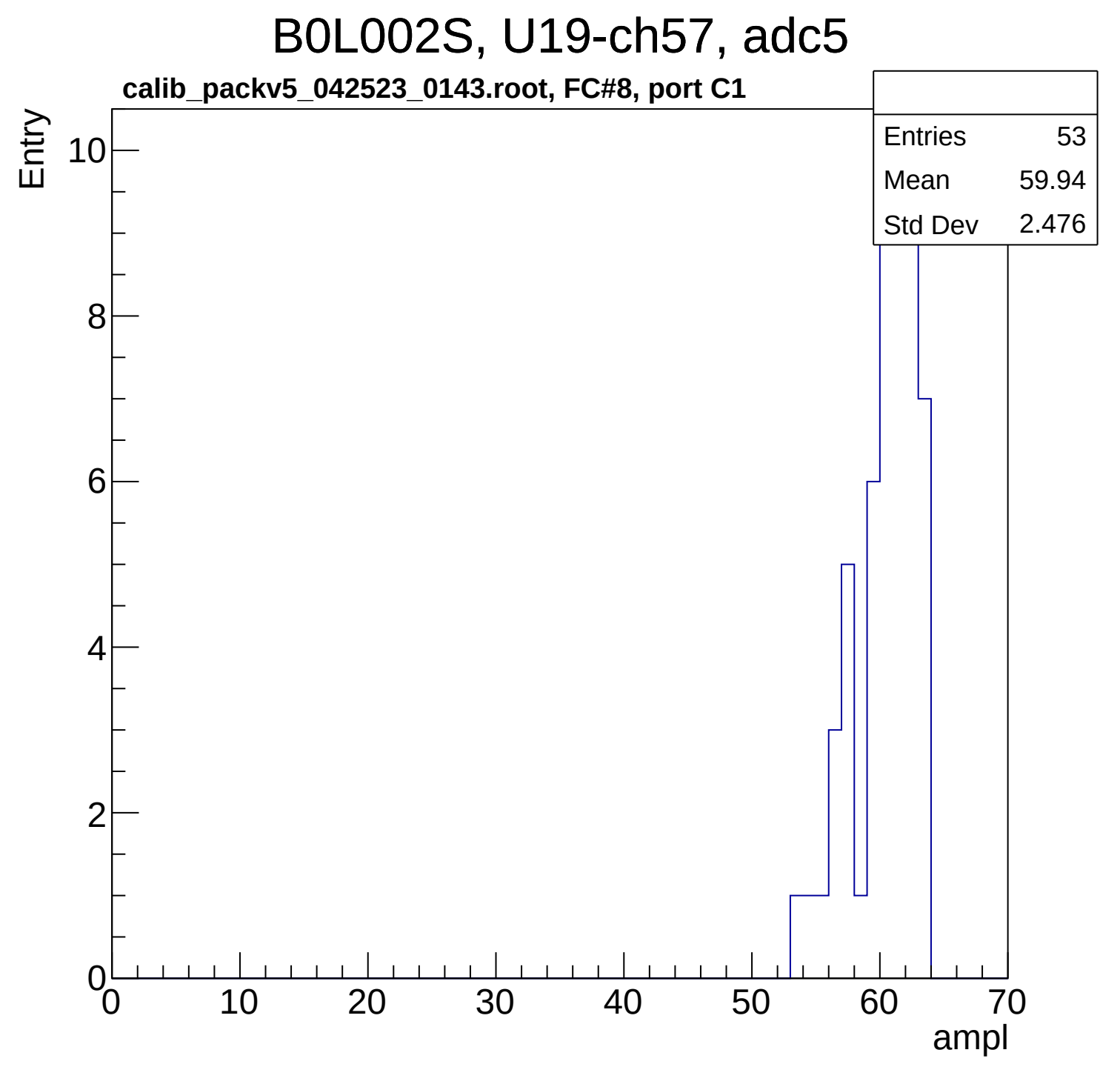

B0L002S, U19-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 28.7
Std Dev 5.853
Gaus mean : 30.0502
Gaus Width: 4.4130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

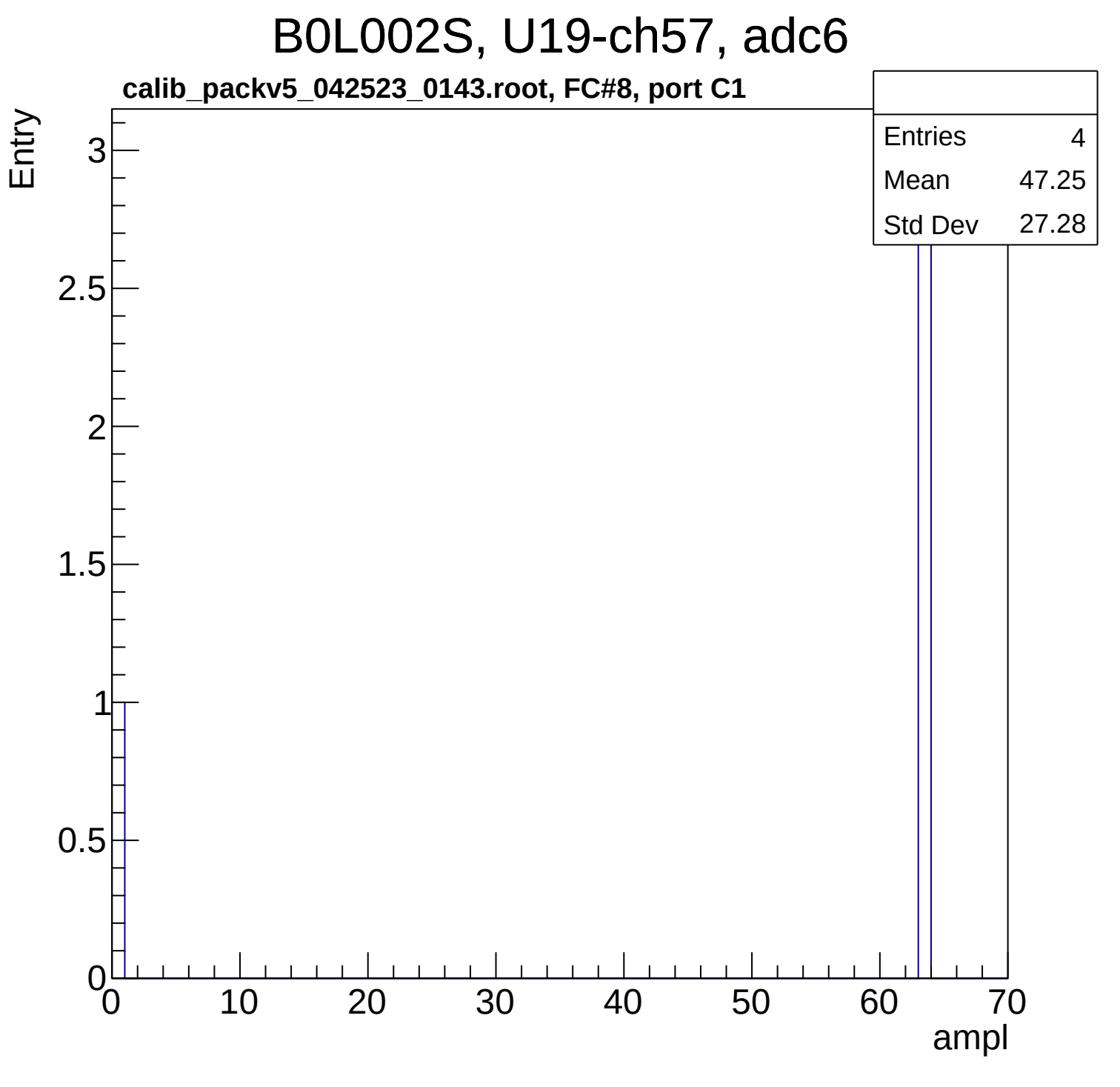

B0L002S, U19-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.37
Std Dev 3.575
Gaus mean : 37.7828
Gaus Width: 3.6141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

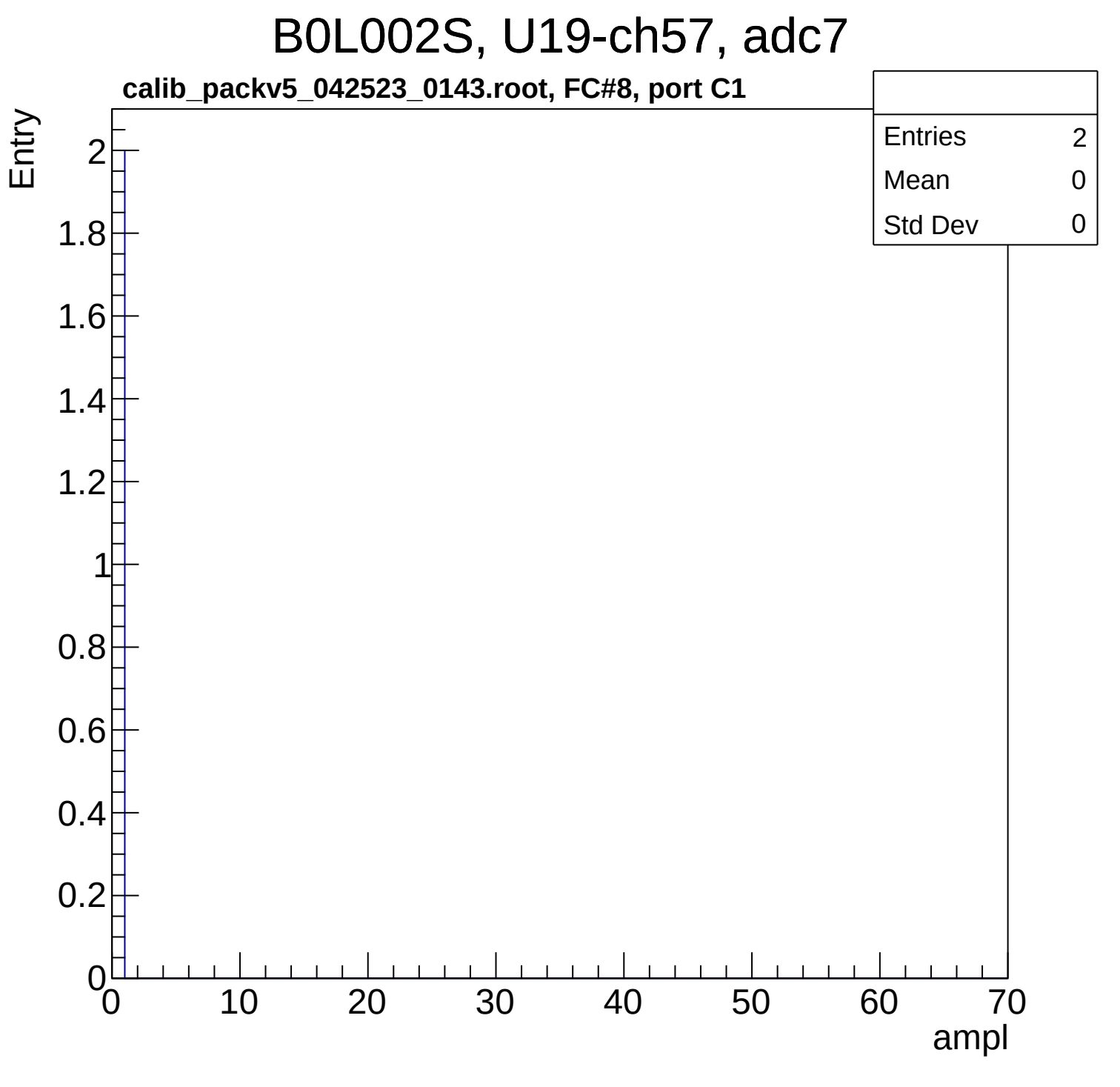

B0L002S, U19-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 43.15
Std Dev 2.582
Gaus mean : 44.1054
Gaus Width: 3.1518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

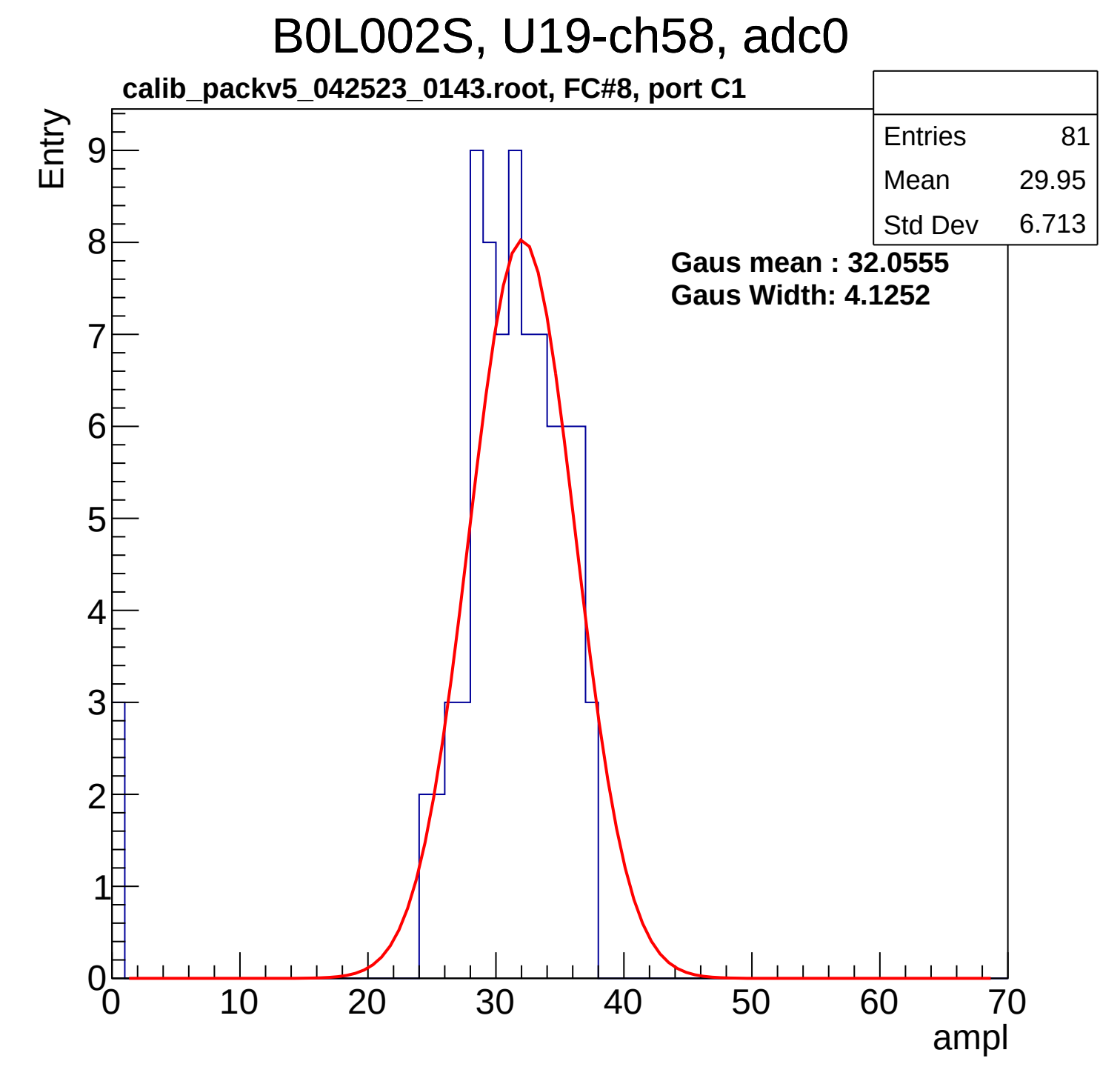

B0L002S, U19-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.39
Std Dev 3.298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

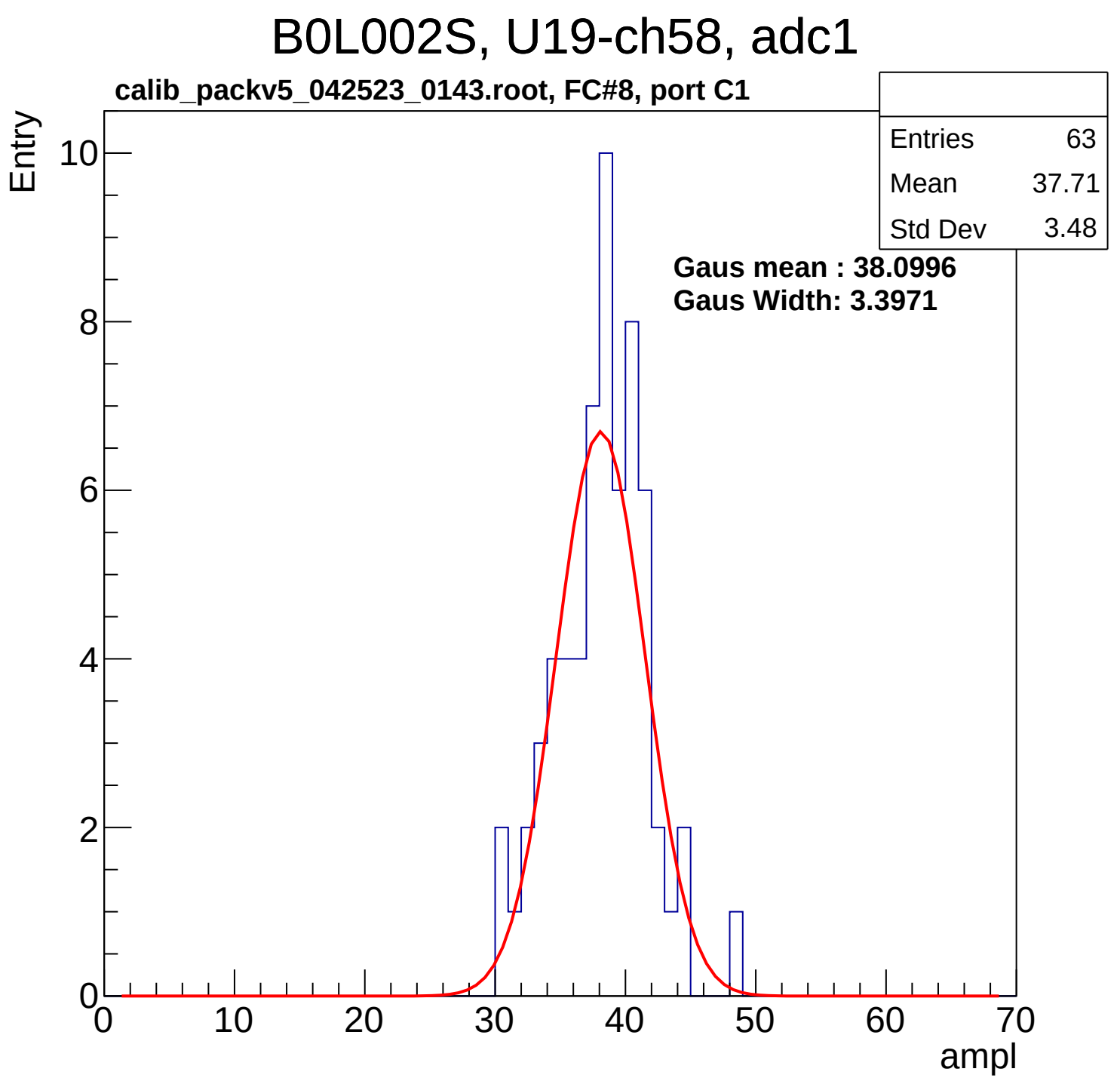

B0L002S, U19-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 54.91
Std Dev 3.226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

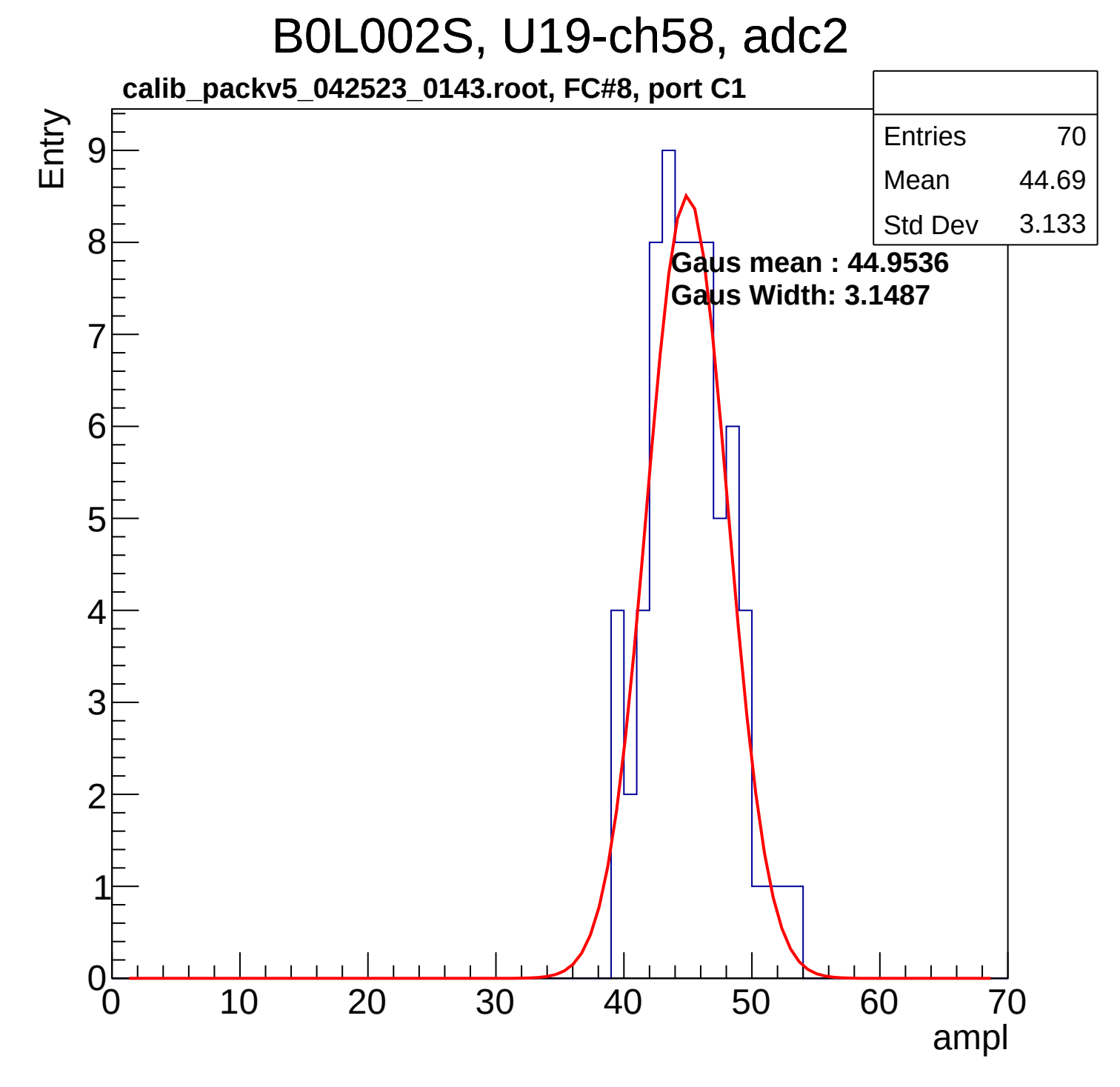

B0L002S, U19-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 59.94
Std Dev 2.476
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

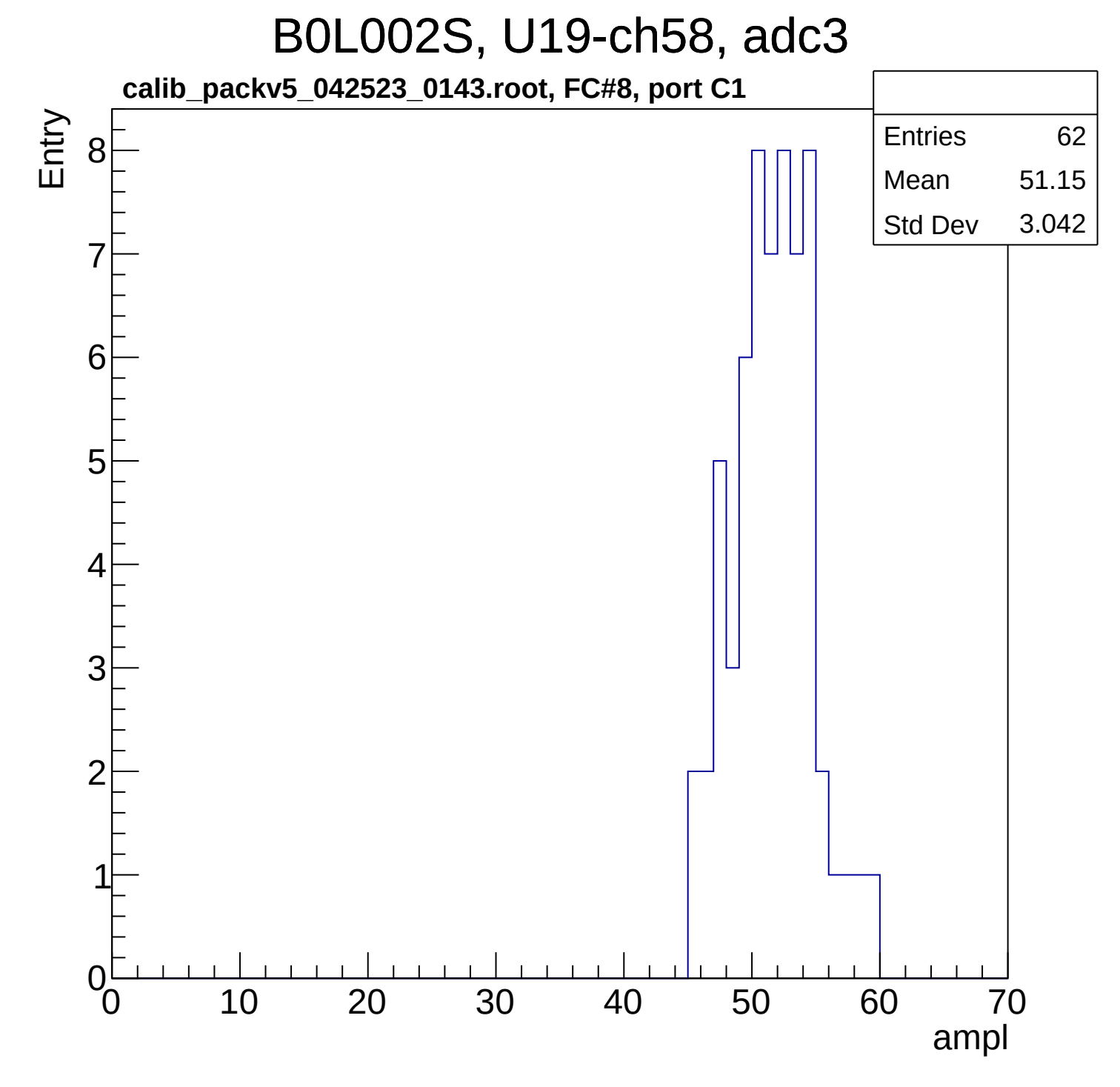

B0L002S, U19-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

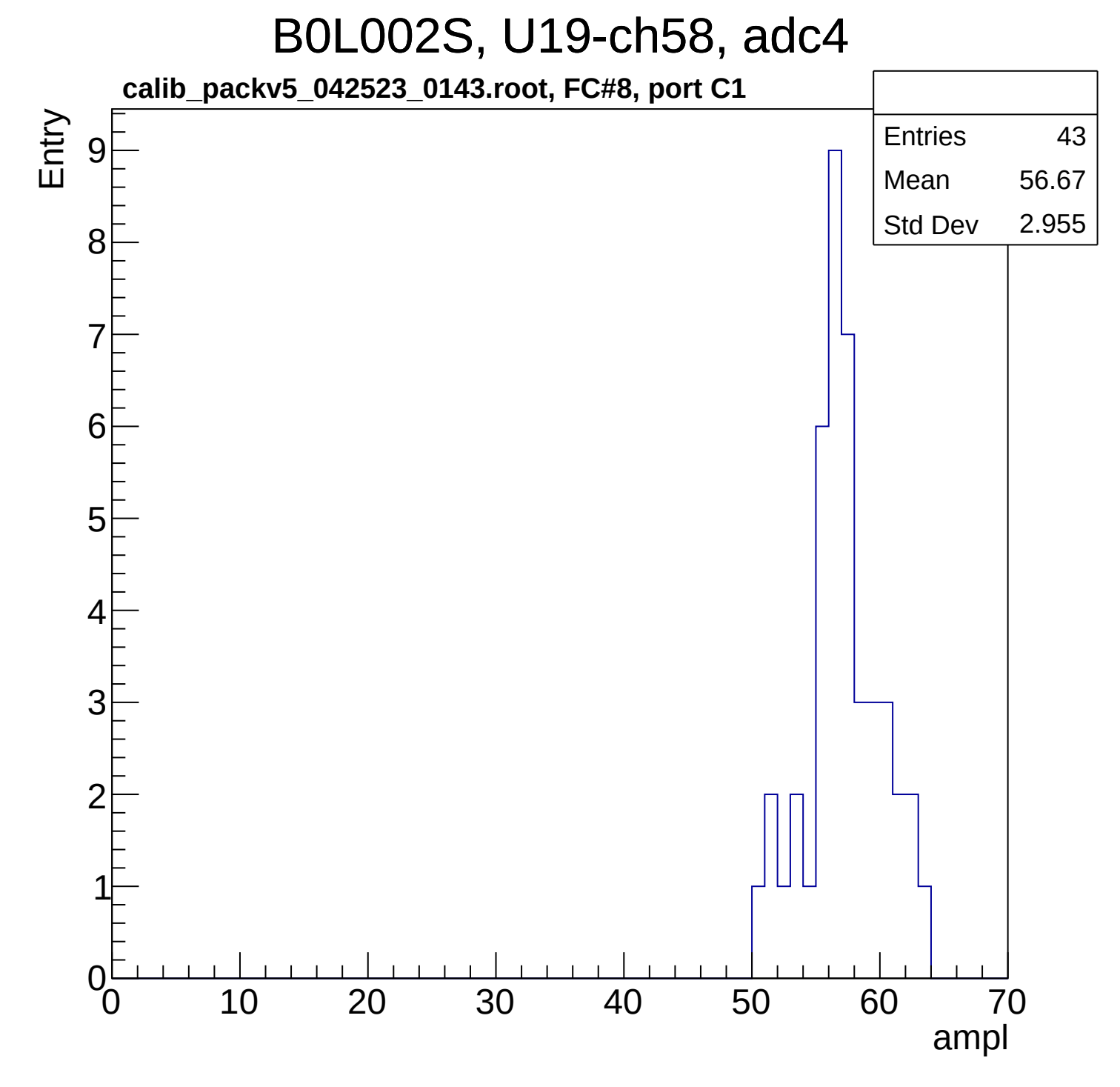

B0L002S, U19-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

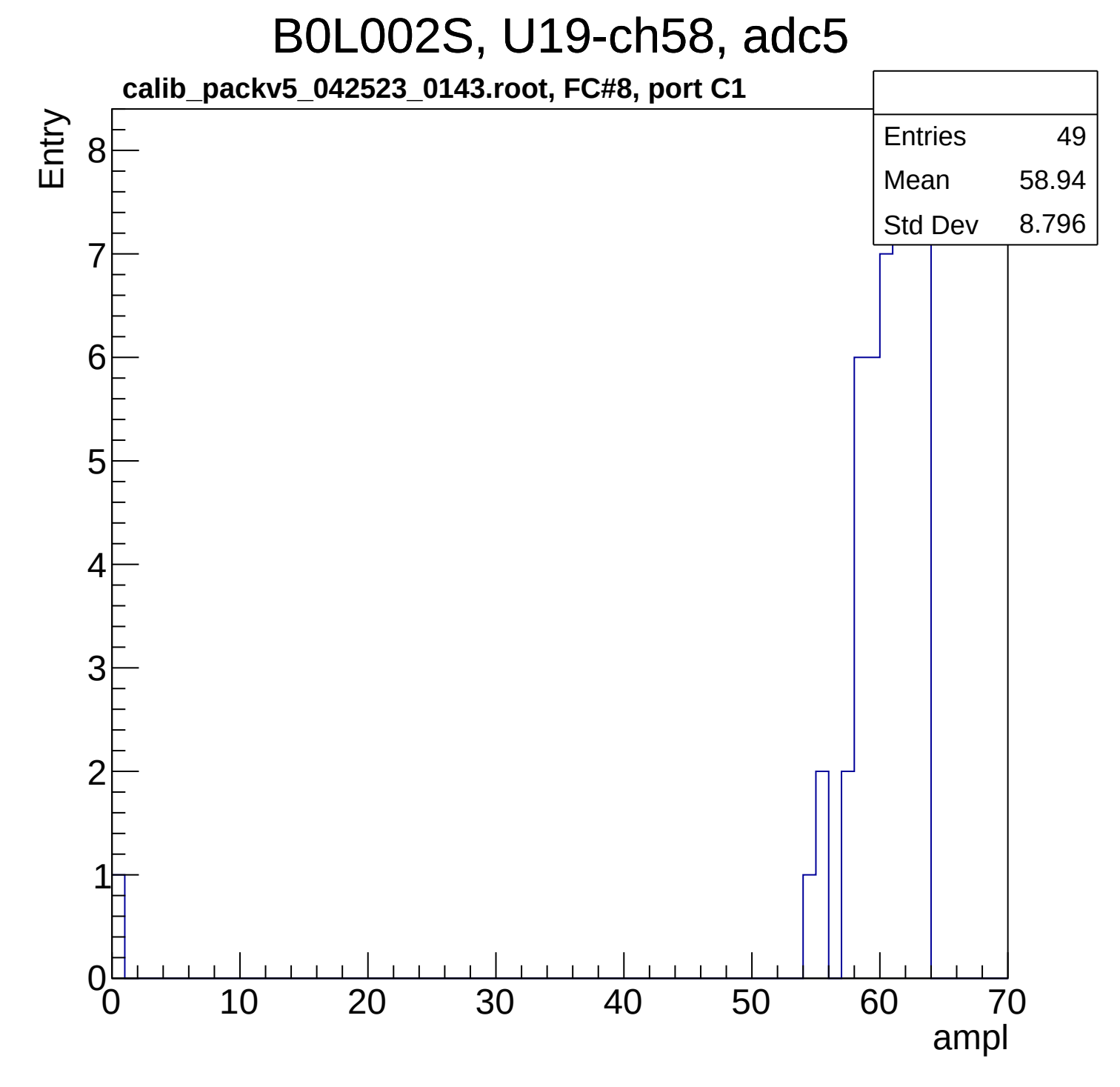

B0L002S, U19-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 29.95
Std Dev 6.713
Gaus mean : 32.0555
Gaus Width: 4.1252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

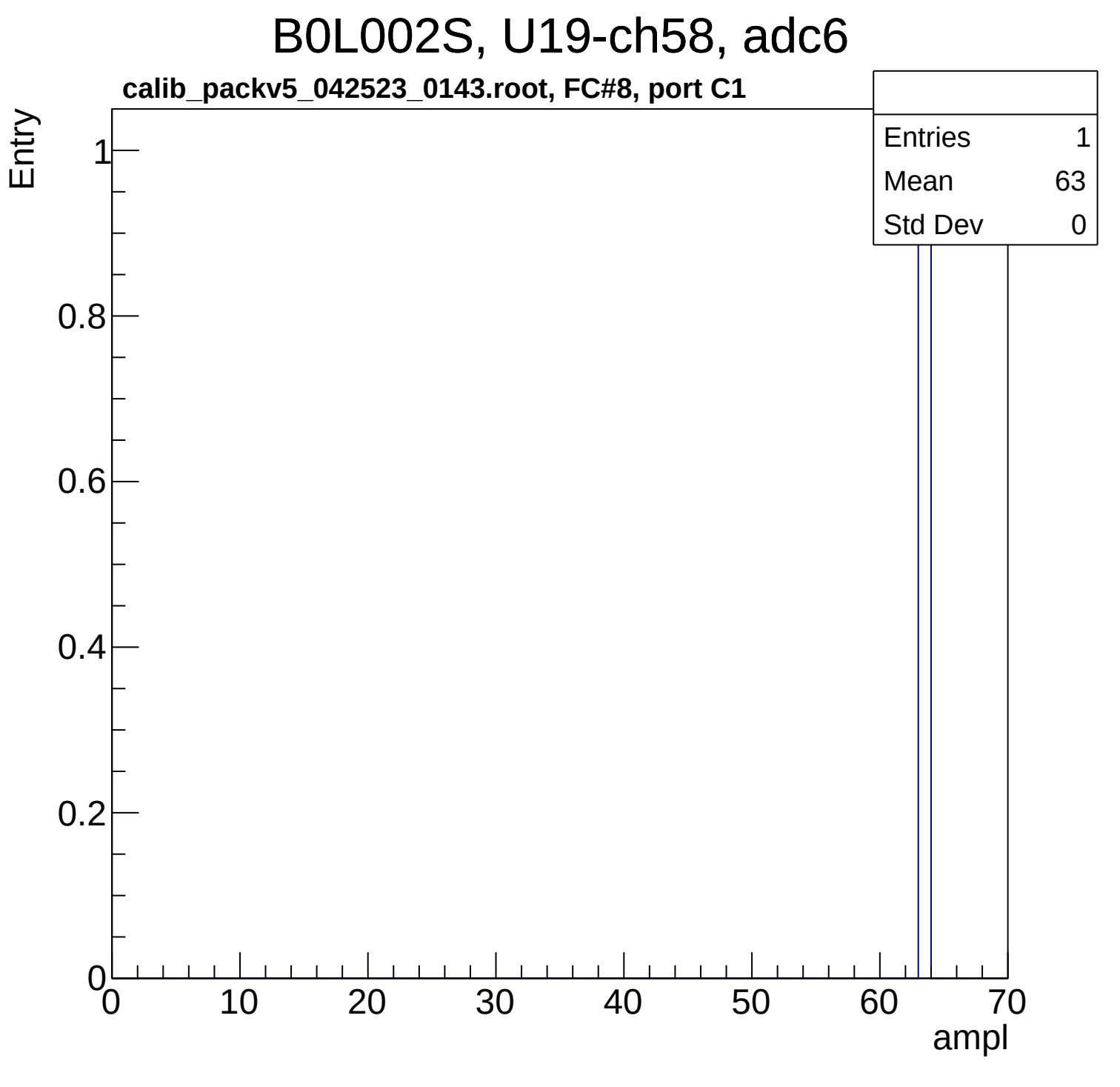

B0L002S, U19-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 37.71
Std Dev 3.48
Gaus mean : 38.0996
Gaus Width: 3.3971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

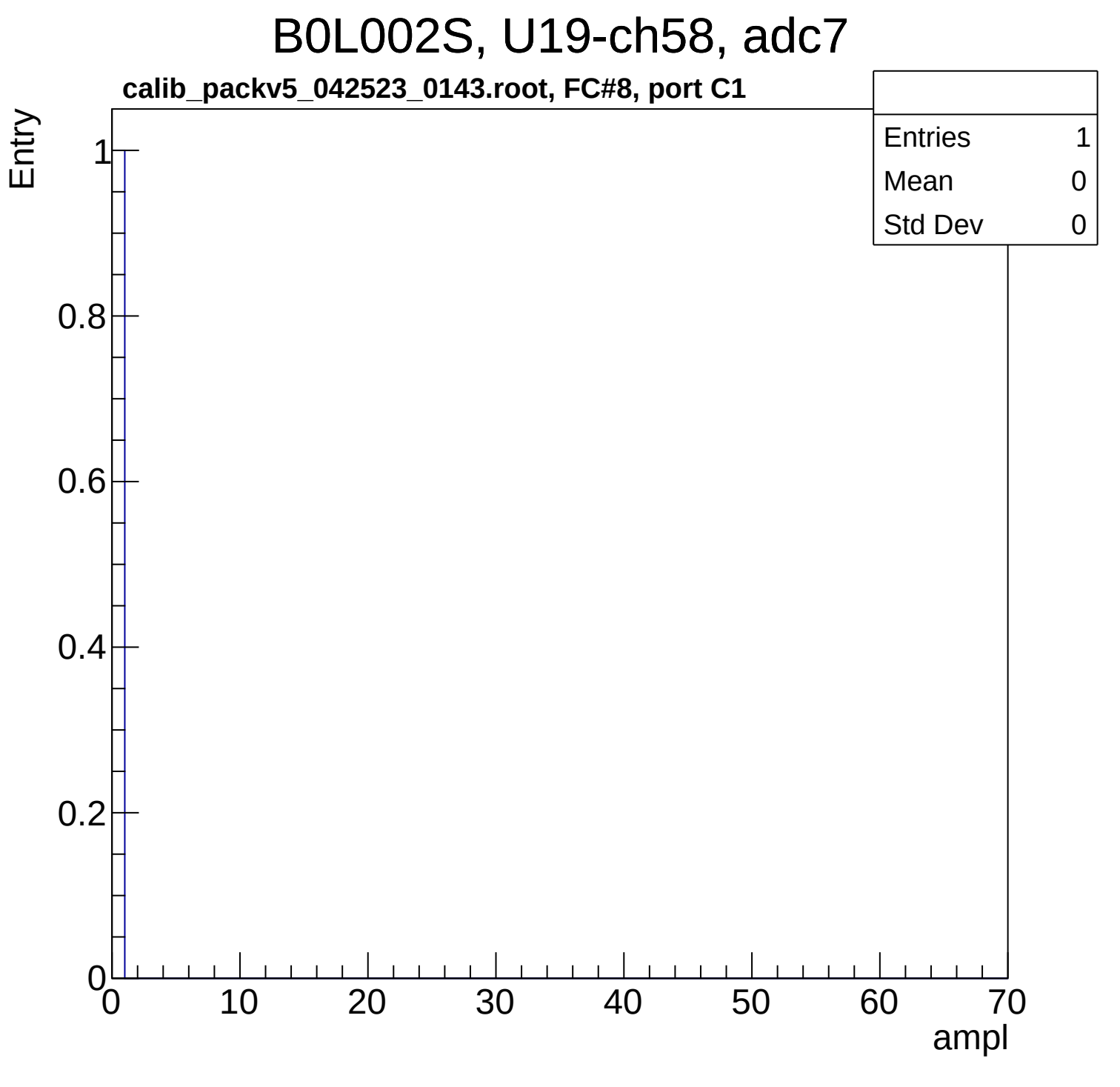

B0L002S, U19-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.69
Std Dev 3.133
Gaus mean : 44.9536
Gaus Width: 3.1487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

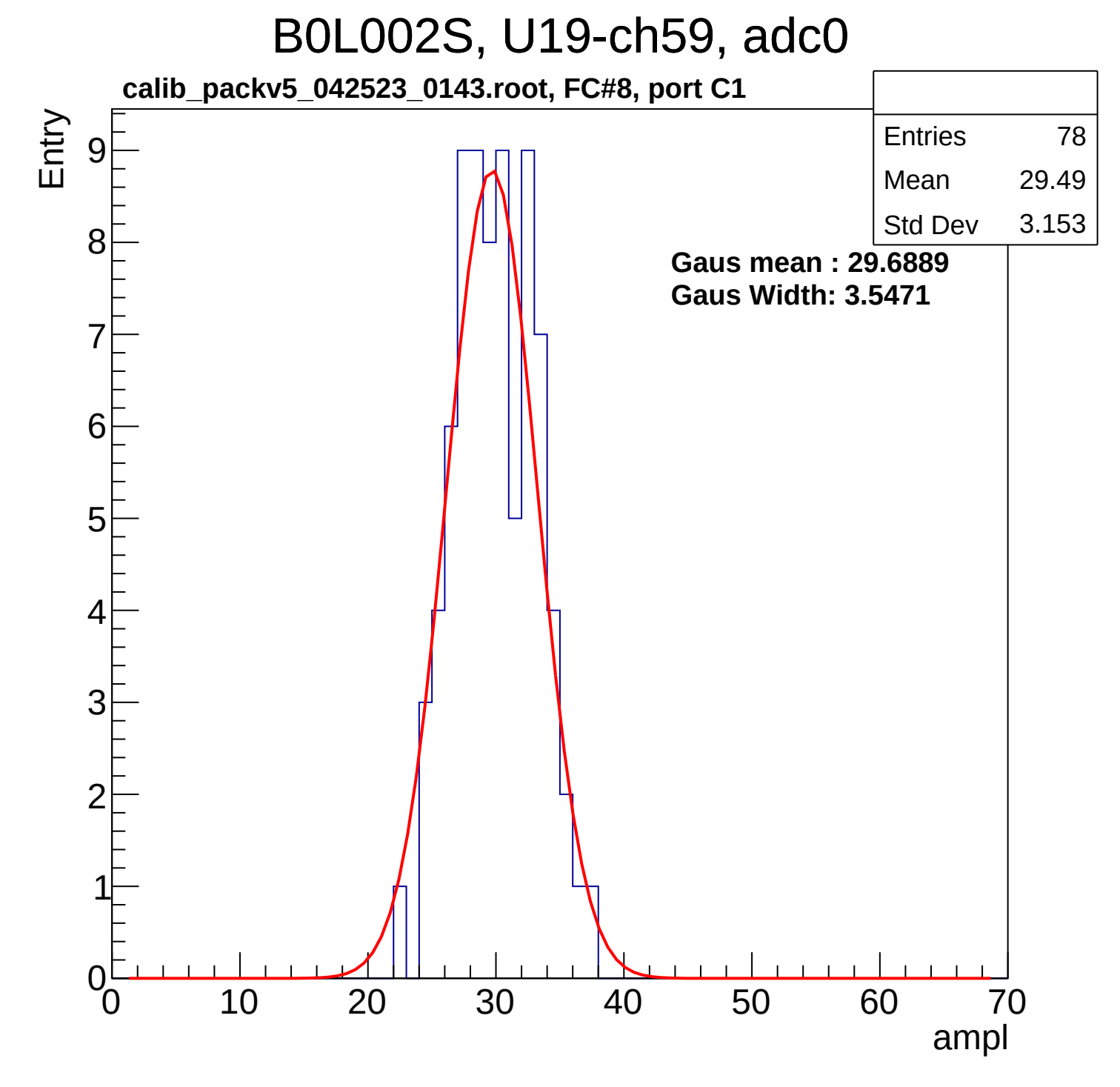

B0L002S, U19-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.15
Std Dev 3.042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

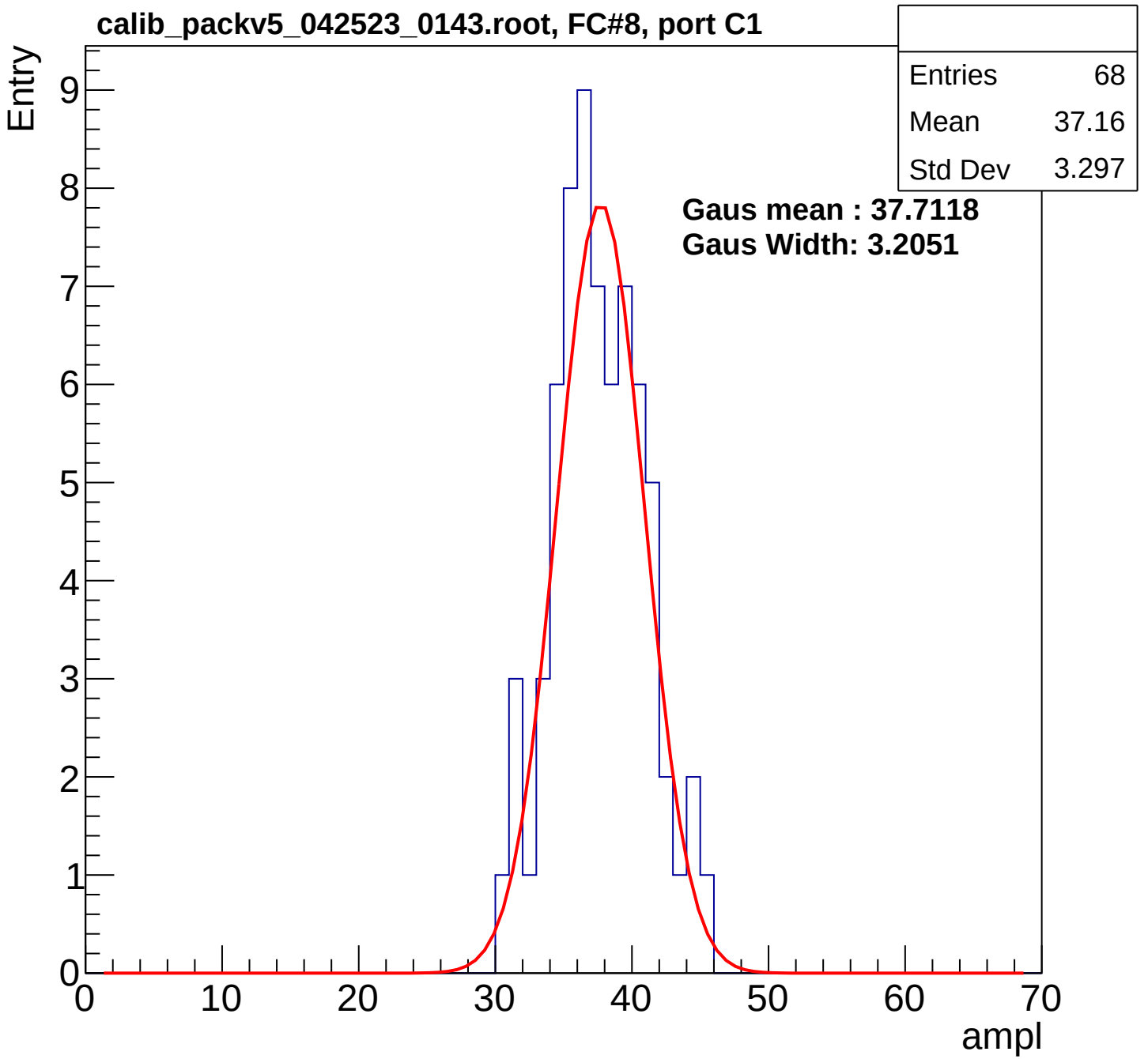

B0L002S, U19-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 56.67
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

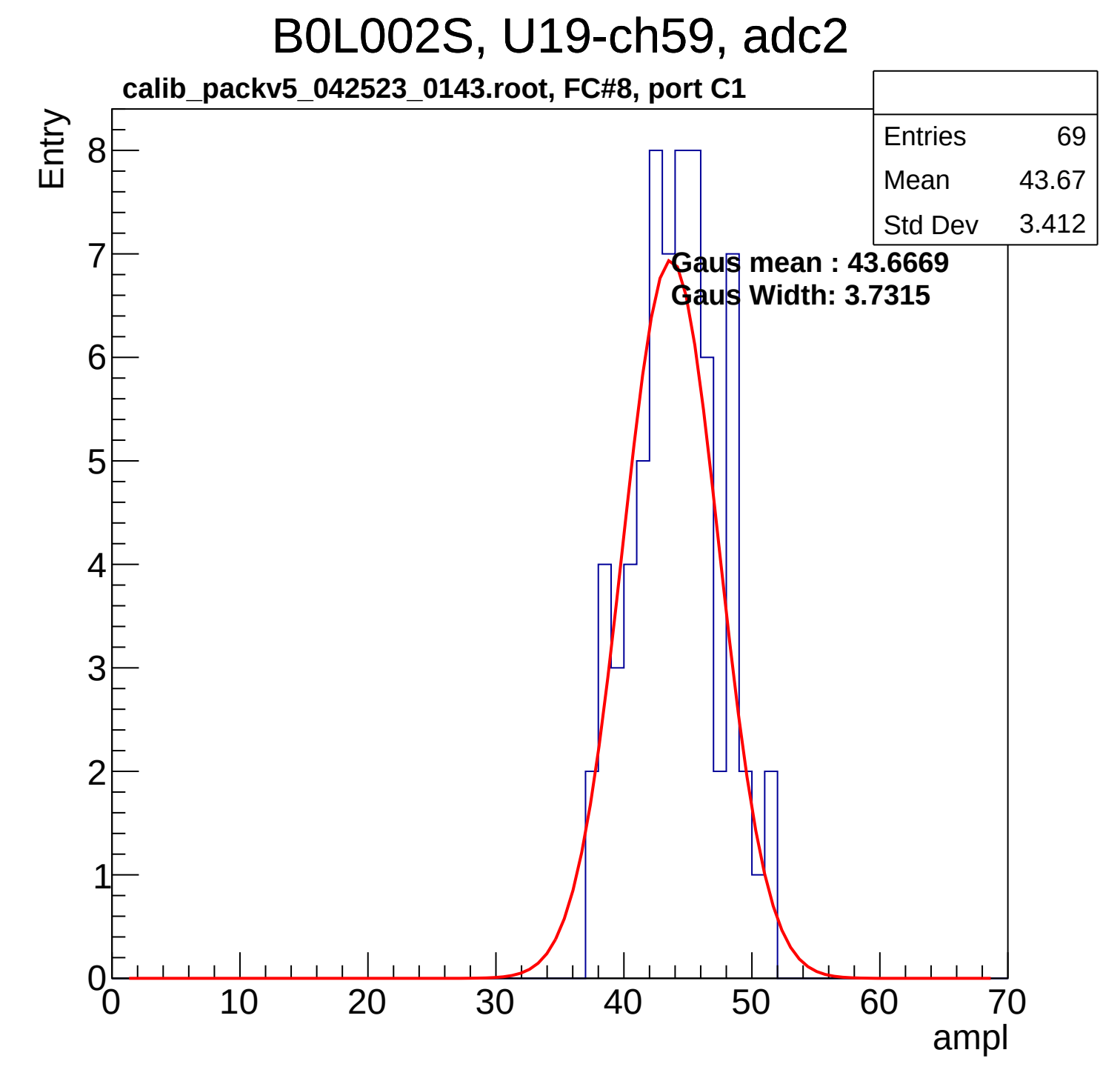

B0L002S, U19-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.94
Std Dev 8.796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

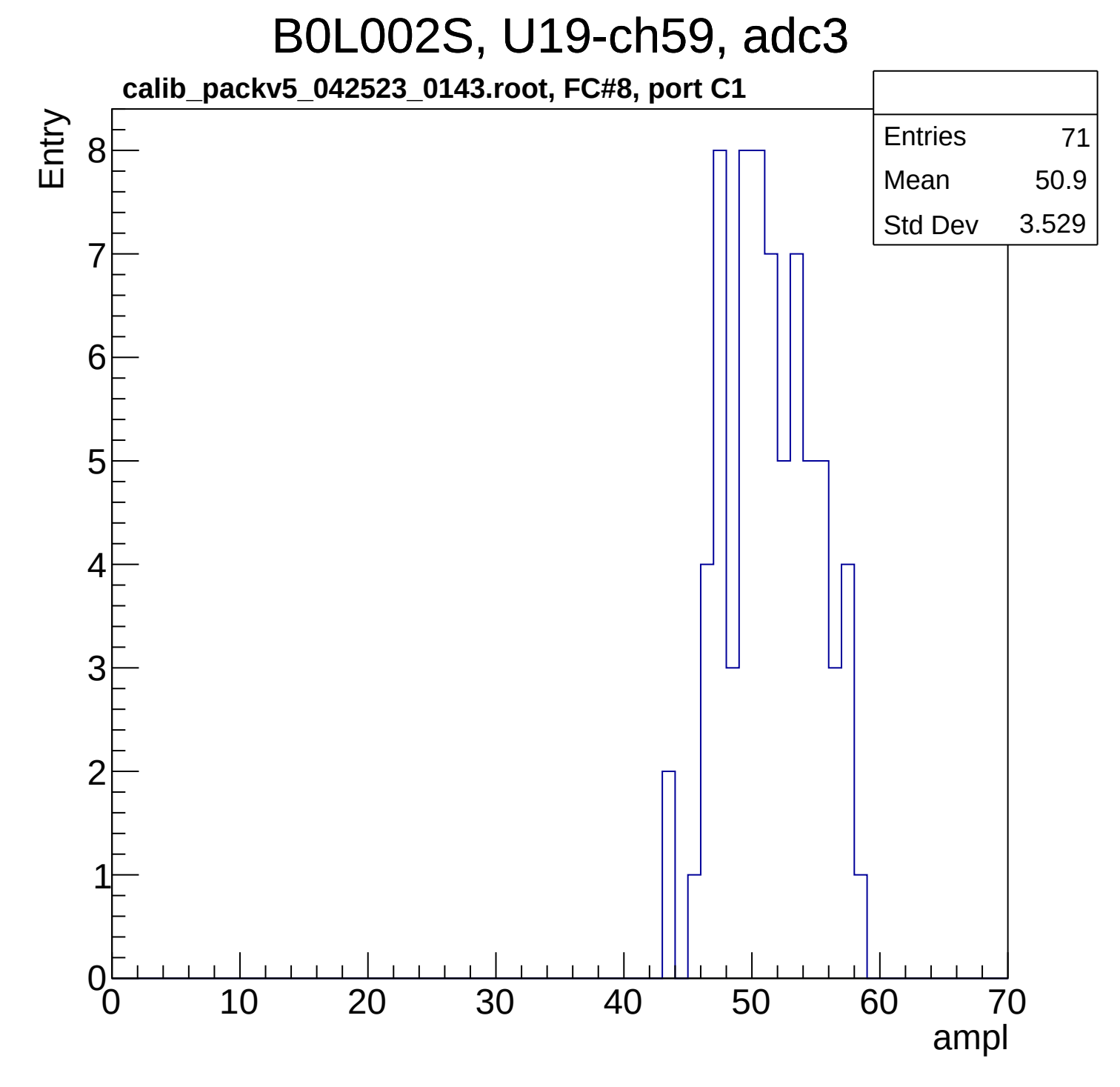

B0L002S, U19-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

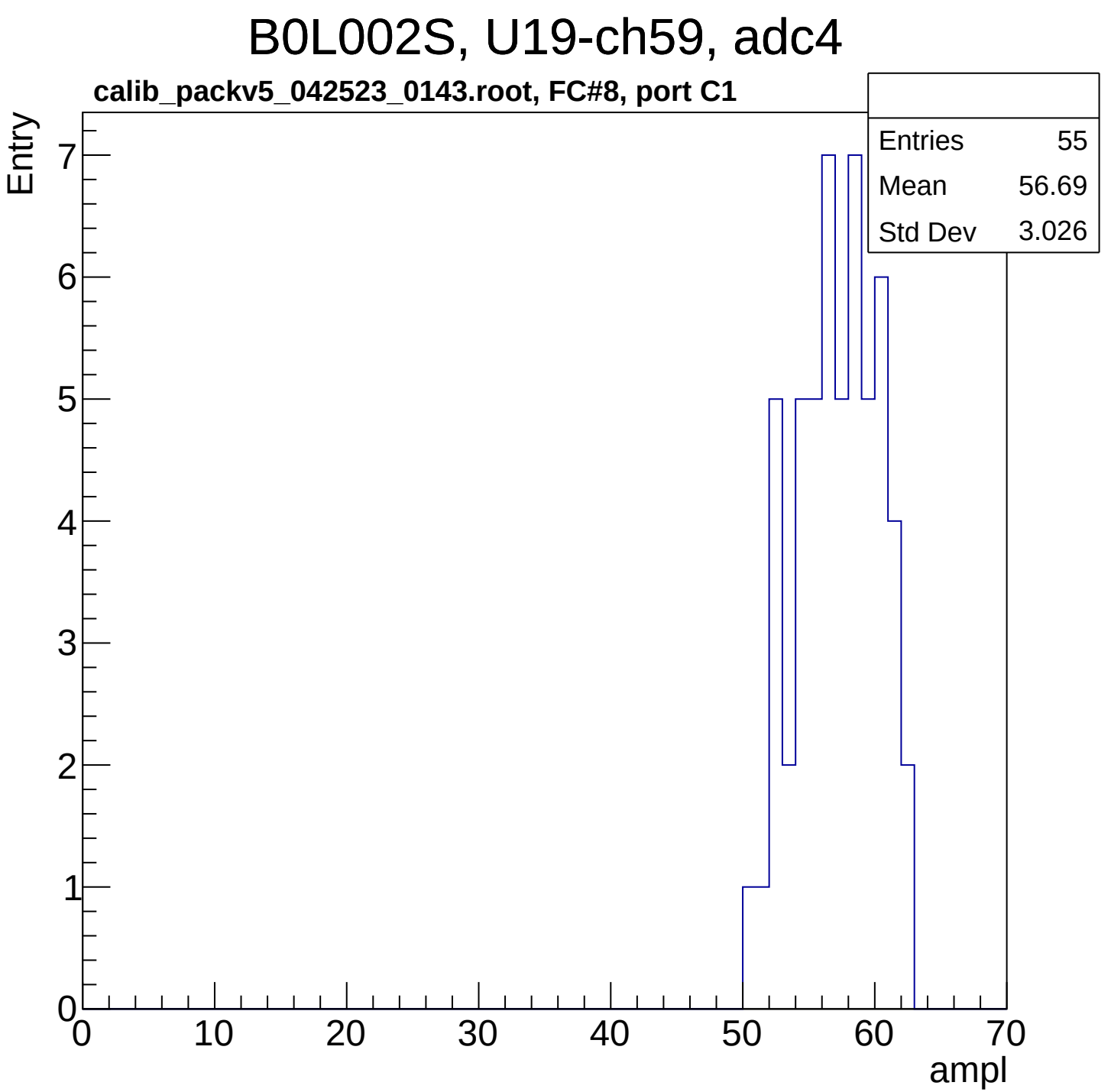

B0L002S, U19-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

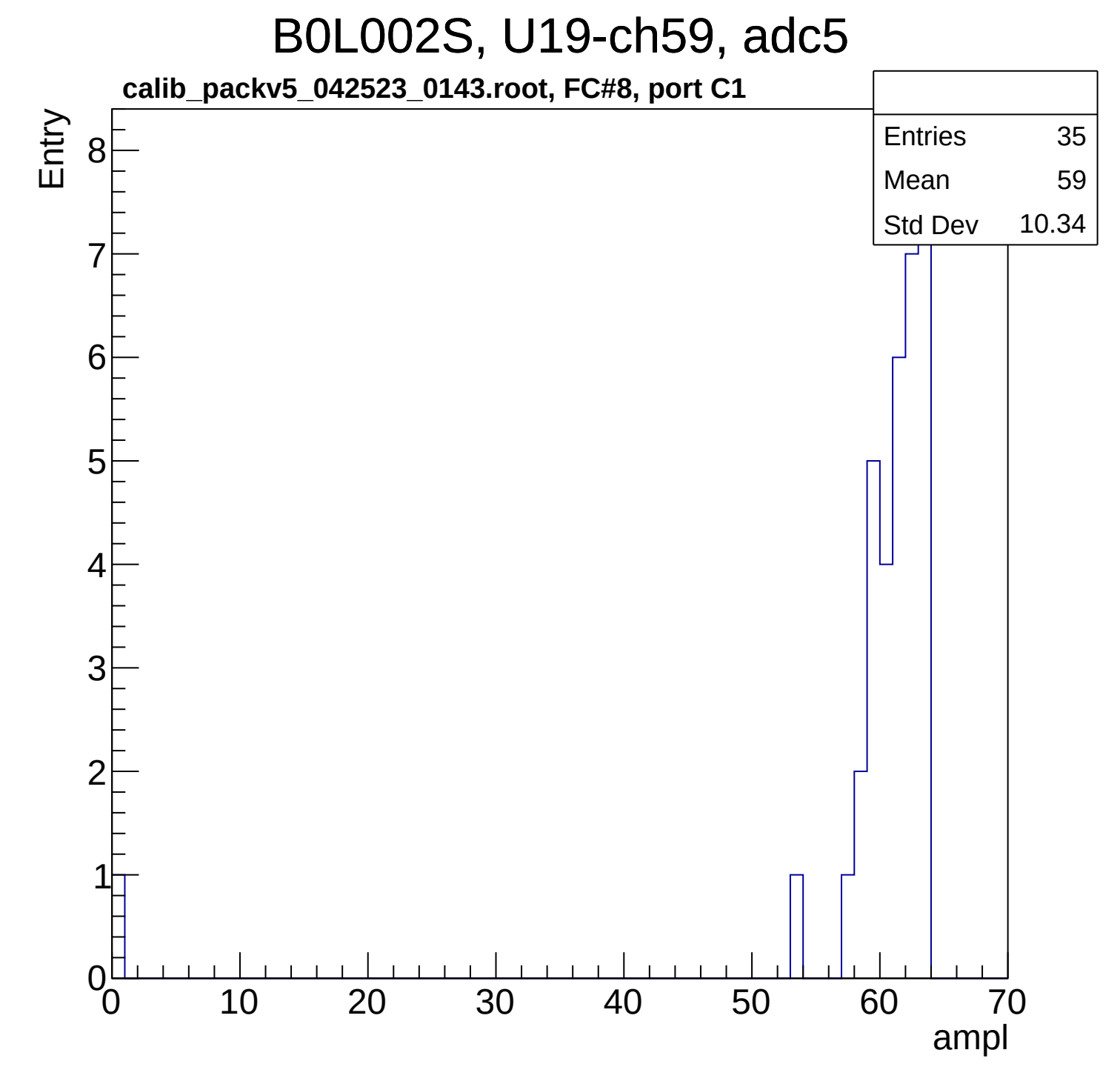

B0L002S, U19-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.49
Std Dev 3.153
Gaus mean : 29.6889
Gaus Width: 3.5471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

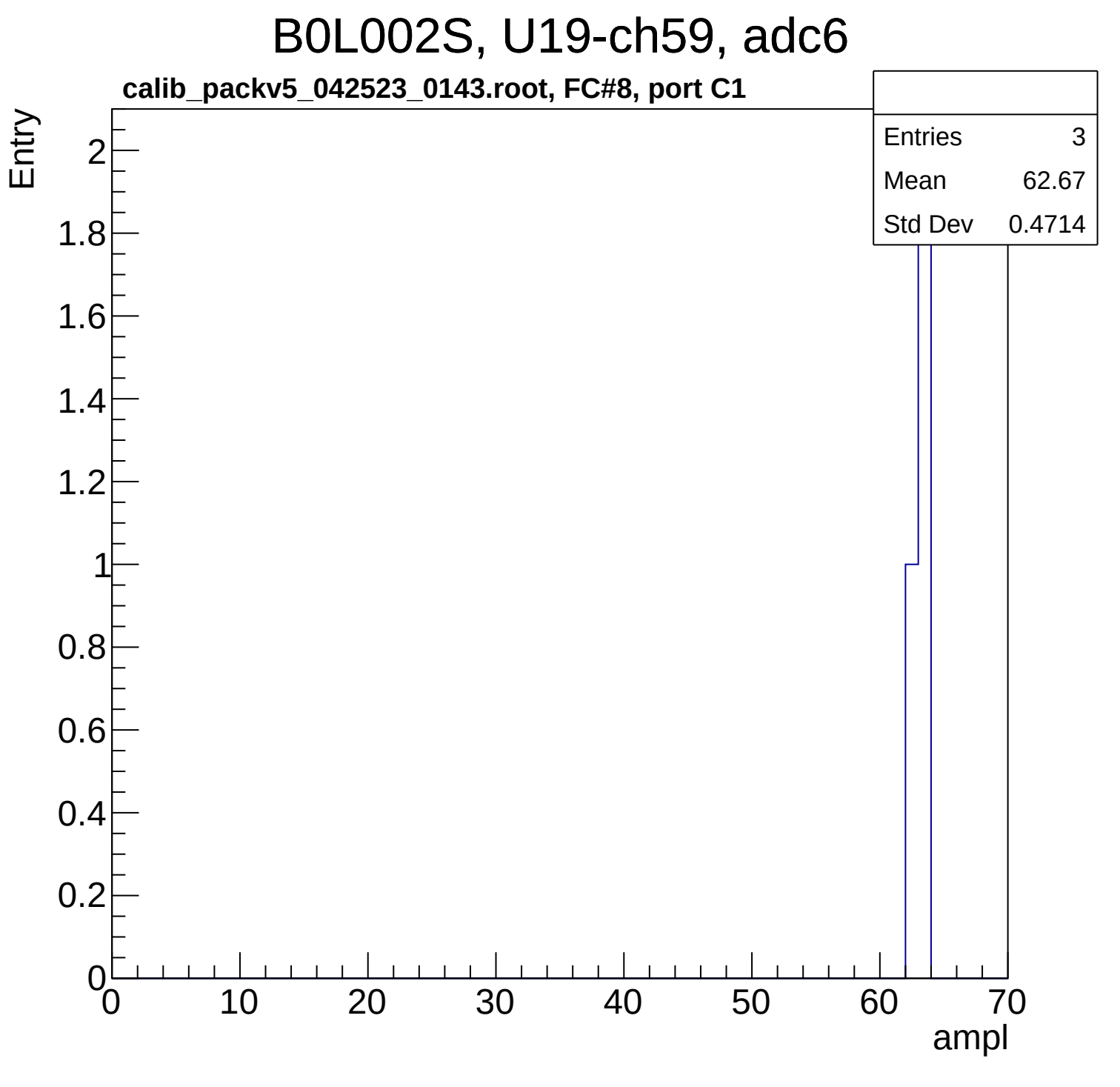

B0L002S, U19-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.16
Std Dev 3.297
Gaus mean : 37.7118
Gaus Width: 3.2051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

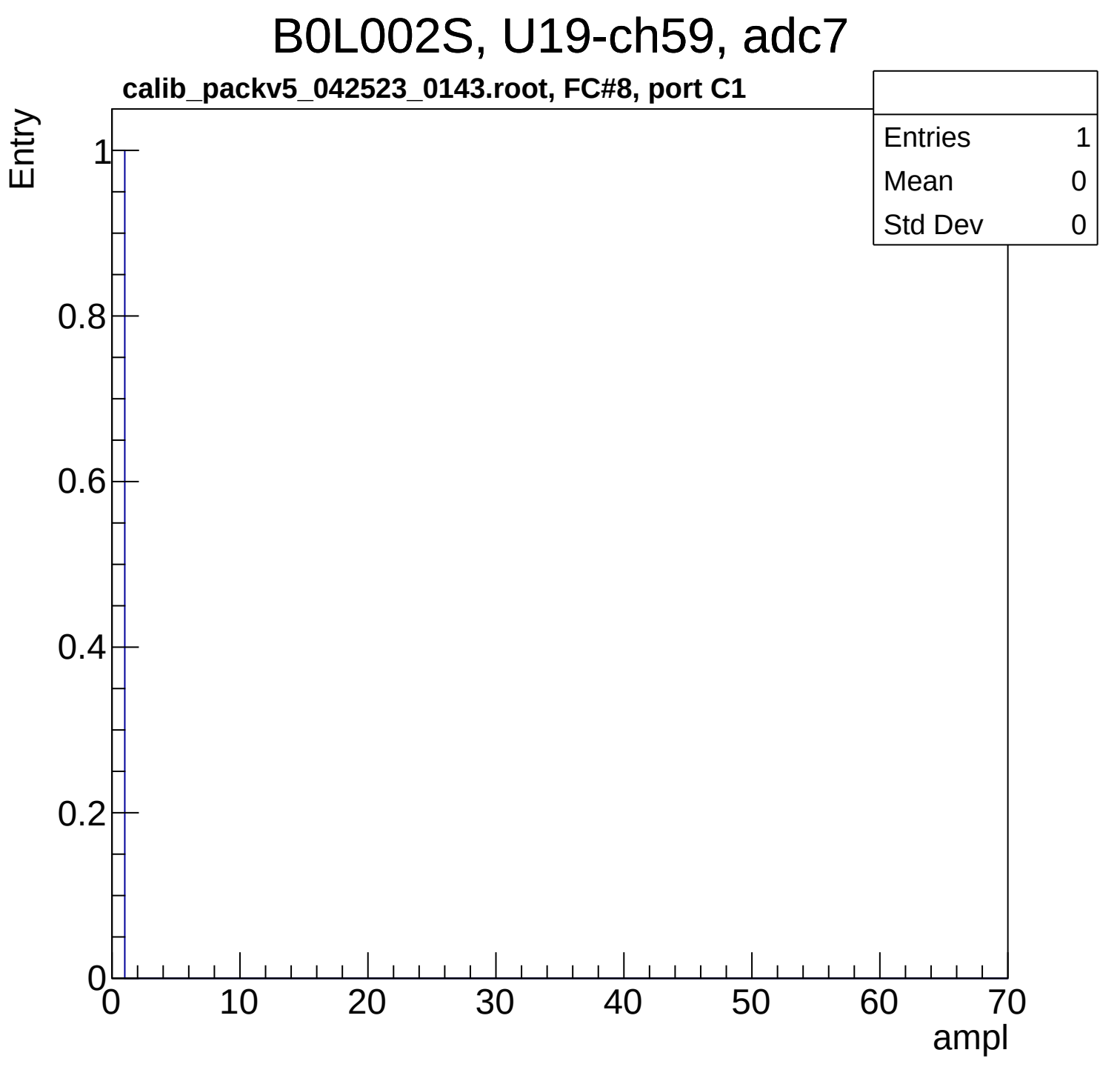

B0L002S, U19-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.67
Std Dev 3.412
Gaus mean : 43.6669
Gaus Width: 3.7315
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

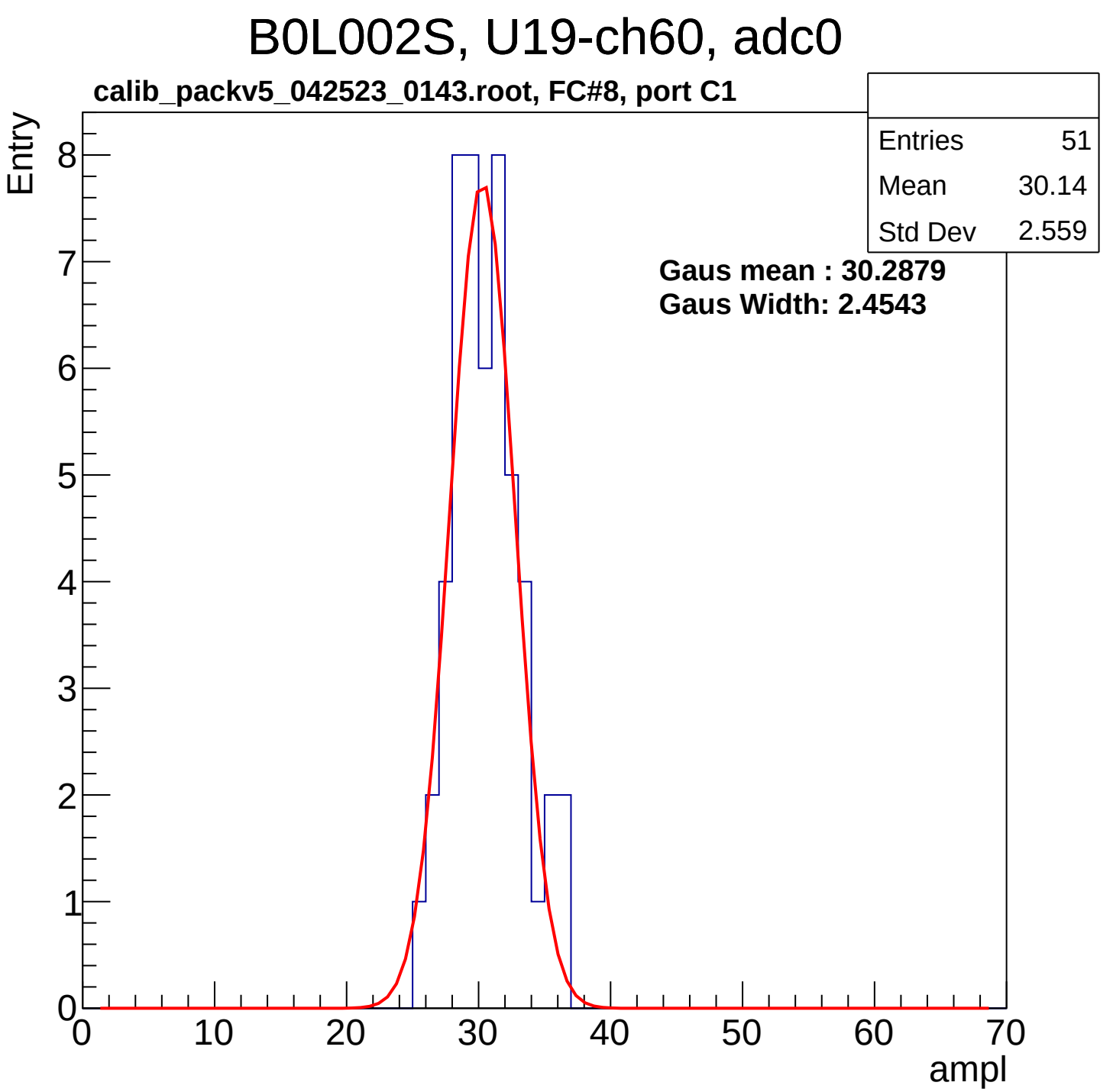

B0L002S, U19-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.9
Std Dev 3.529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

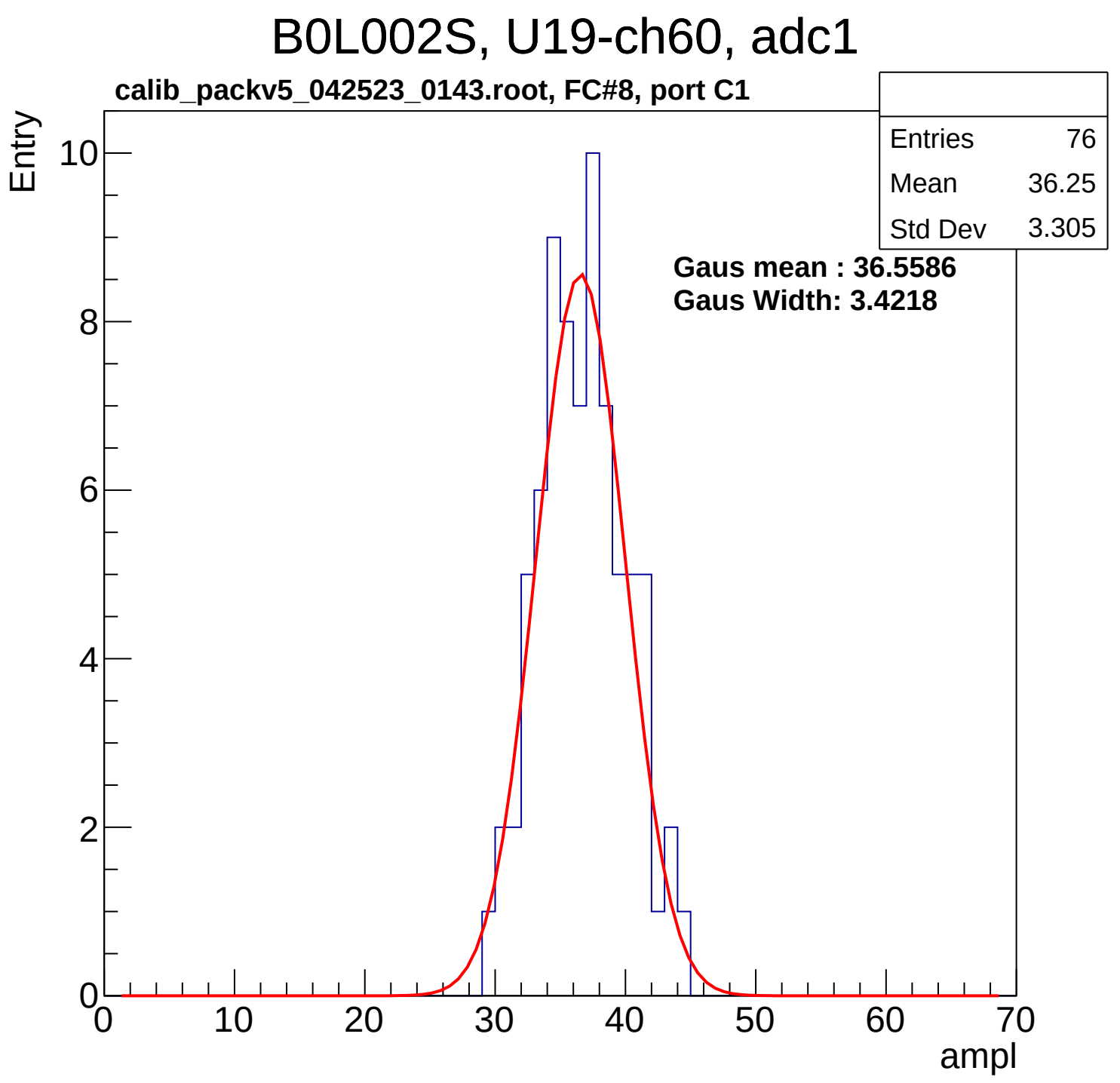

B0L002S, U19-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.69
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

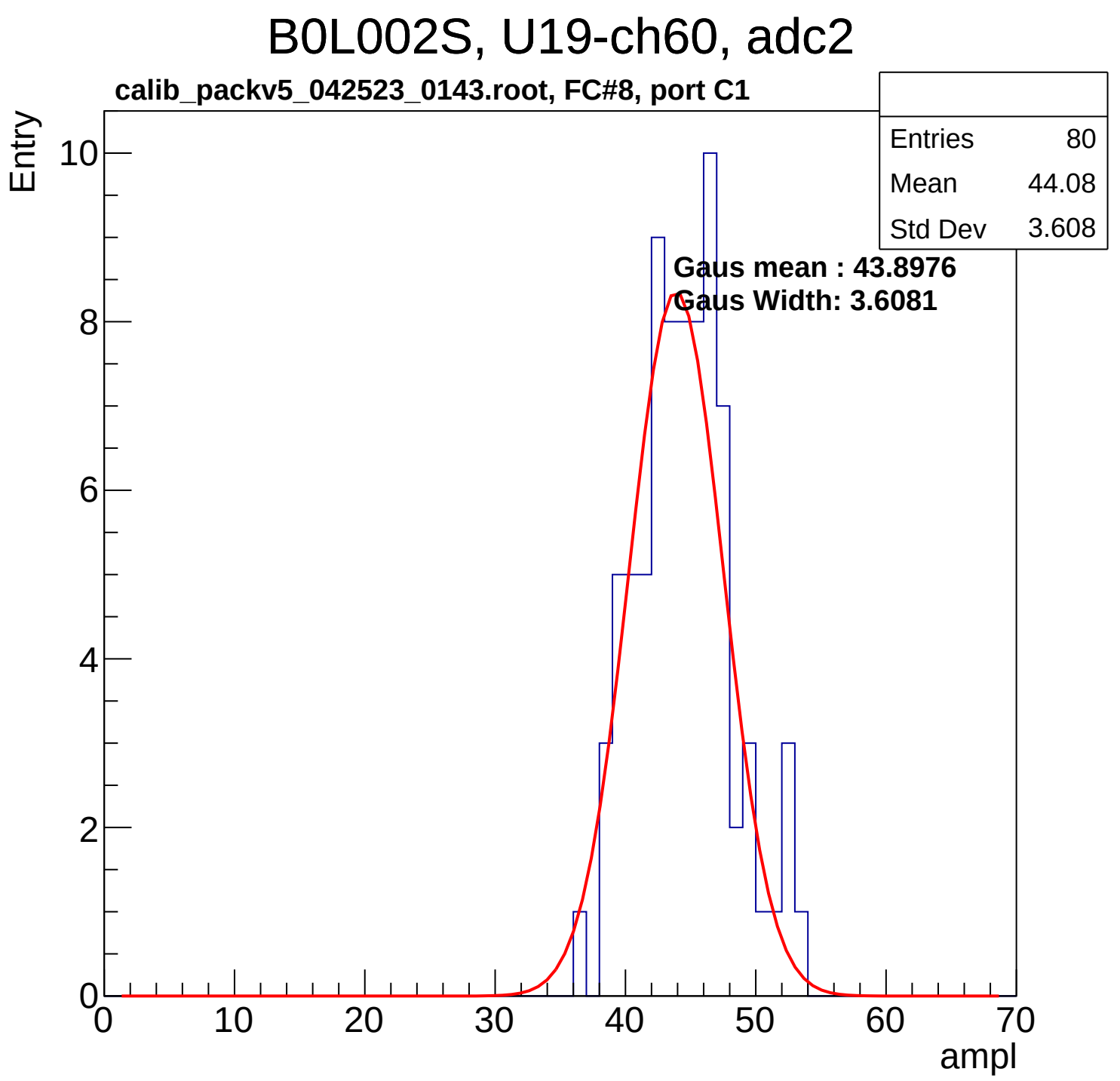

B0L002S, U19-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59
Std Dev 10.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

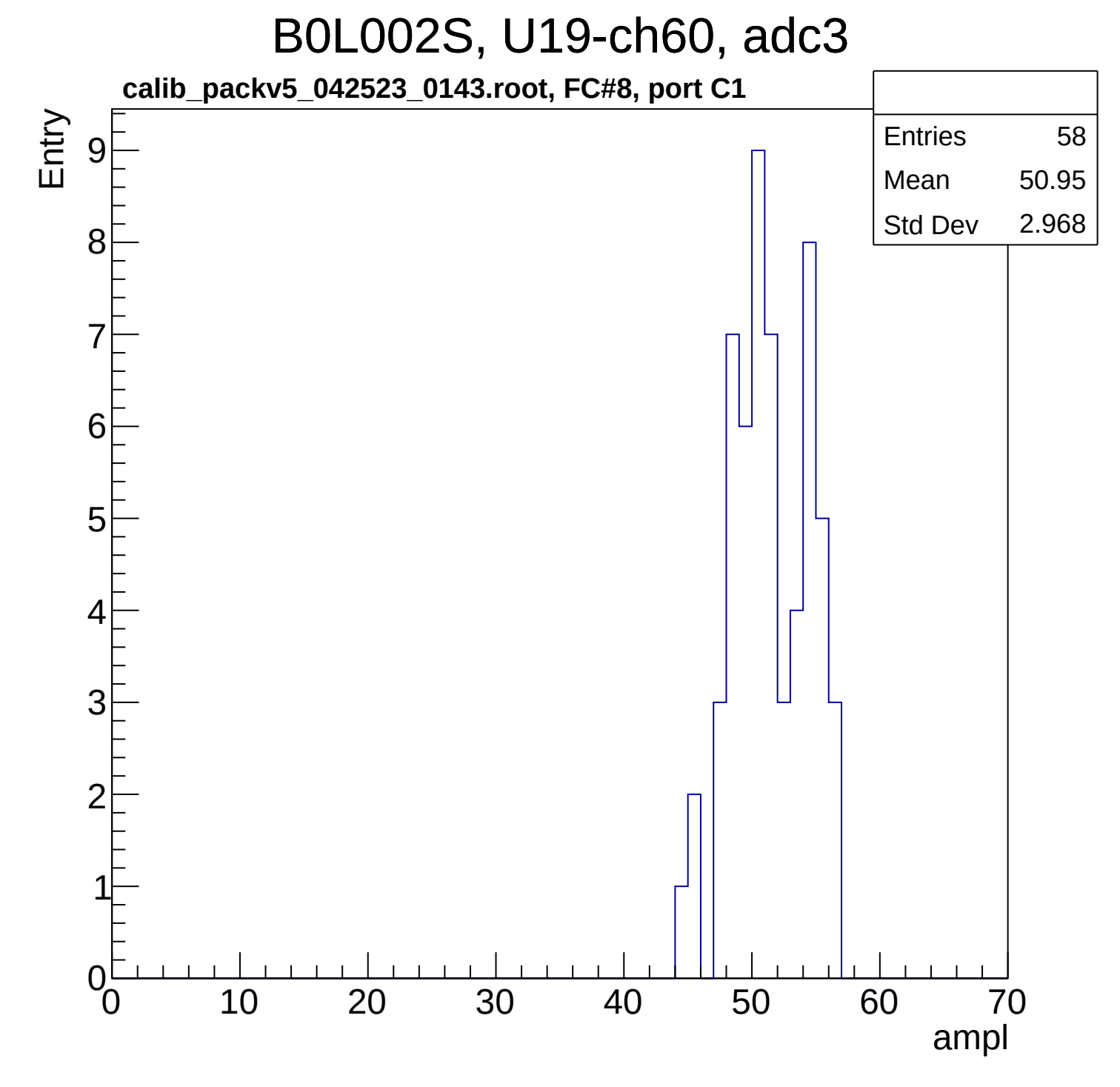

B0L002S, U19-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

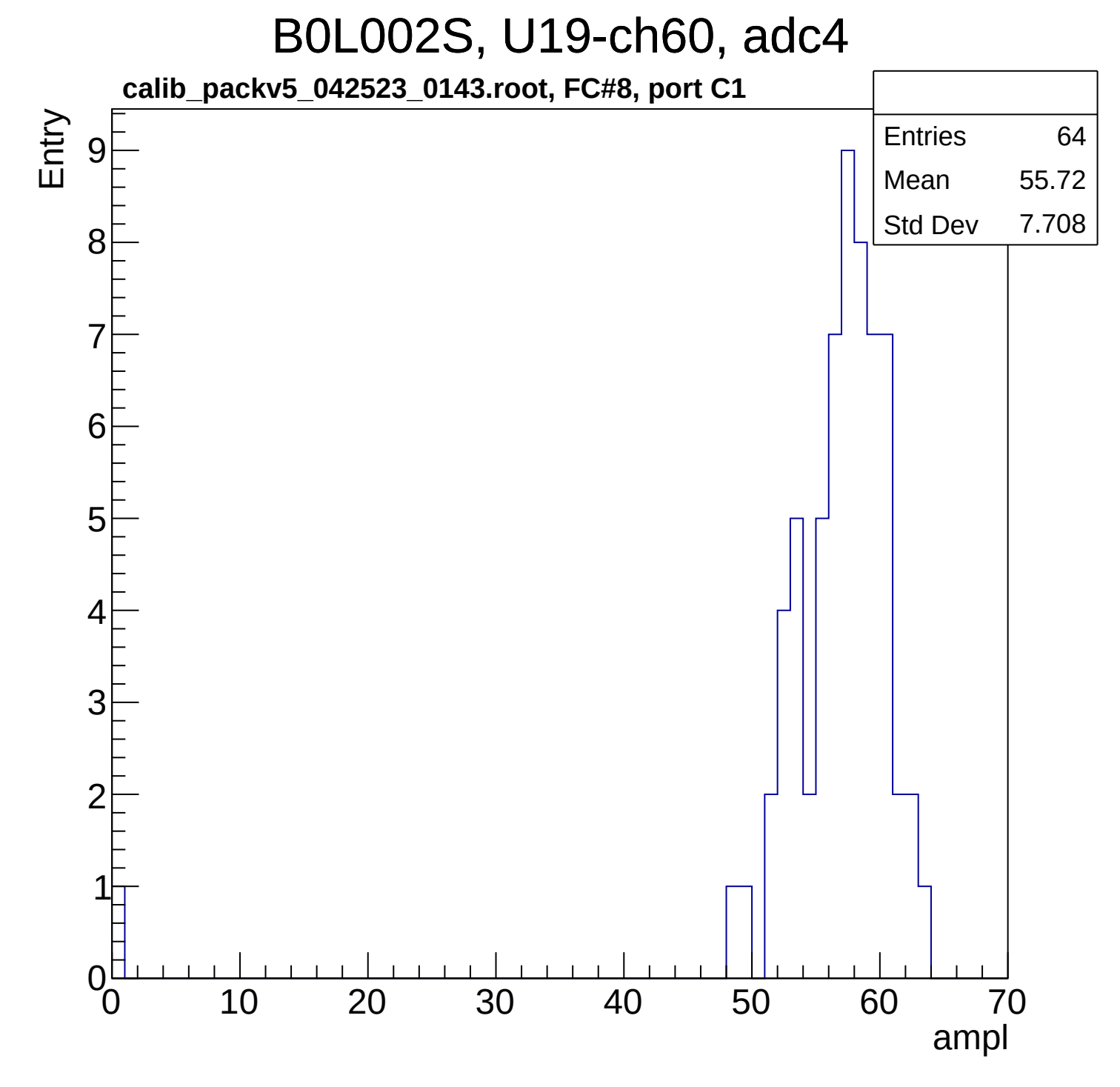

B0L002S, U19-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

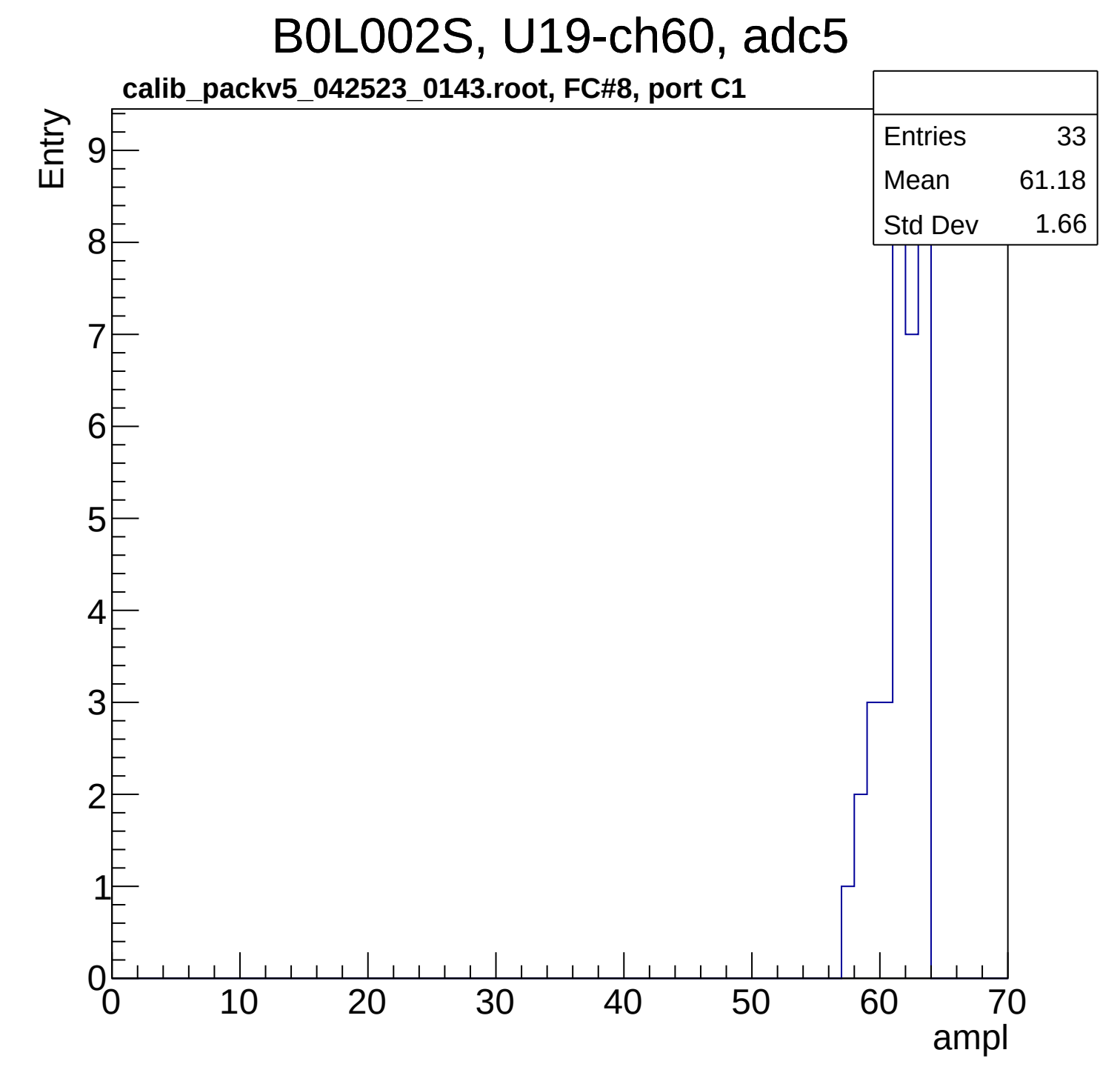

B0L002S, U19-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 30.14
Std Dev 2.559
Gaus mean : 30.2879
Gaus Width: 2.4543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

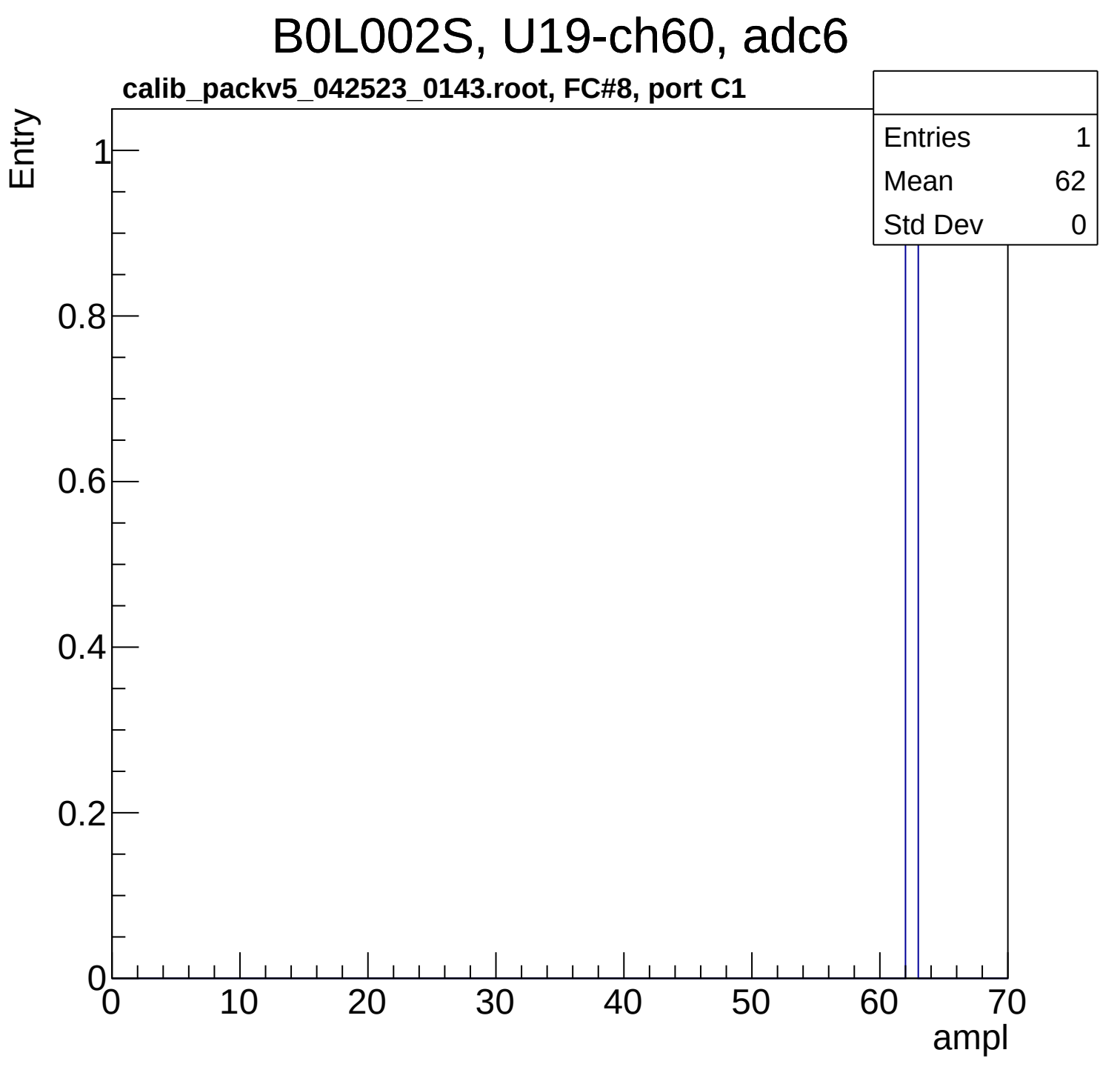

B0L002S, U19-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.25
Std Dev 3.305
Gaus mean : 36.5586
Gaus Width: 3.4218
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

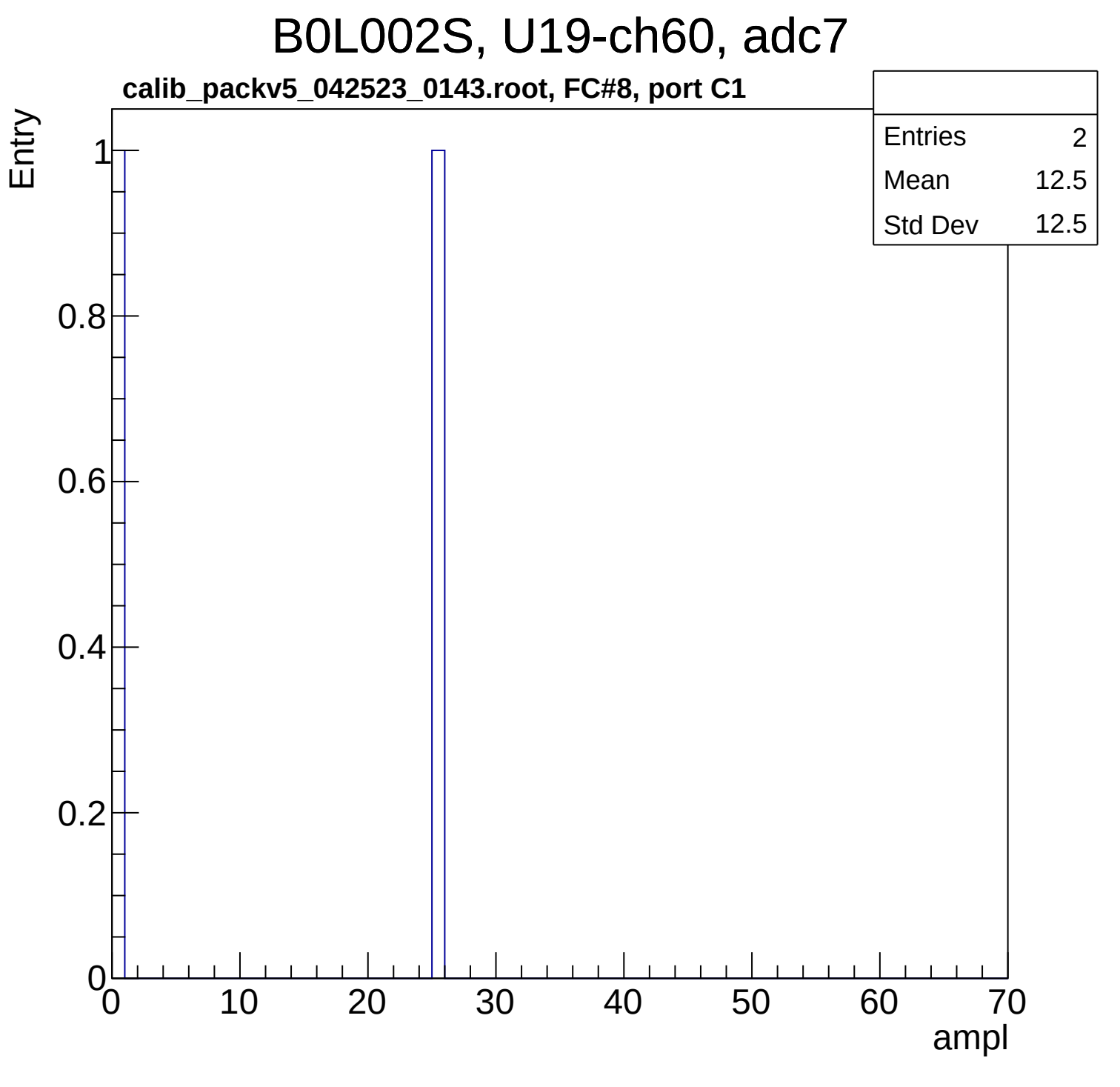

B0L002S, U19-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 44.08
Std Dev 3.608
Gaus mean : 43.8976
Gaus Width: 3.6081
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

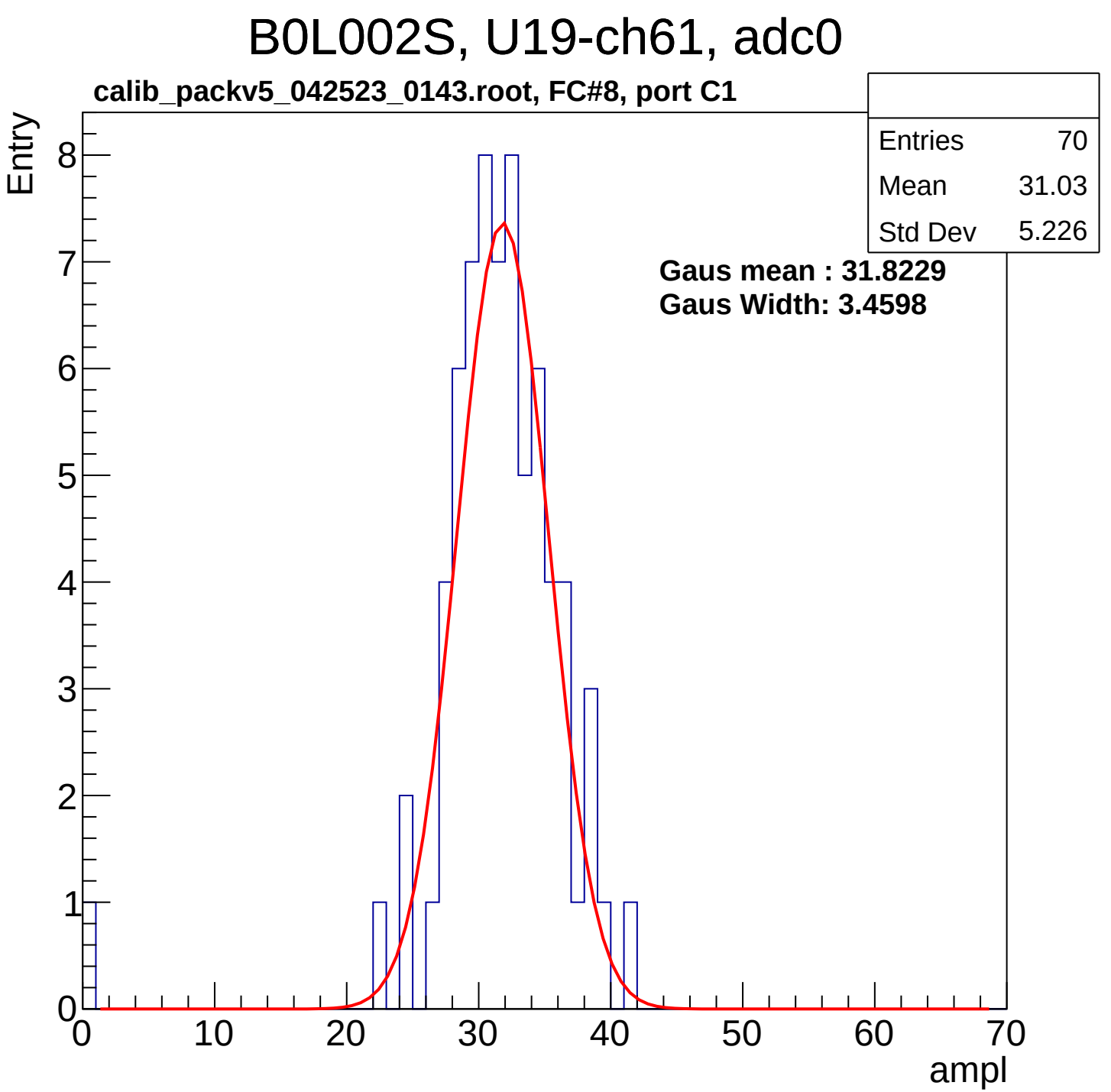

B0L002S, U19-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 50.95
Std Dev 2.968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

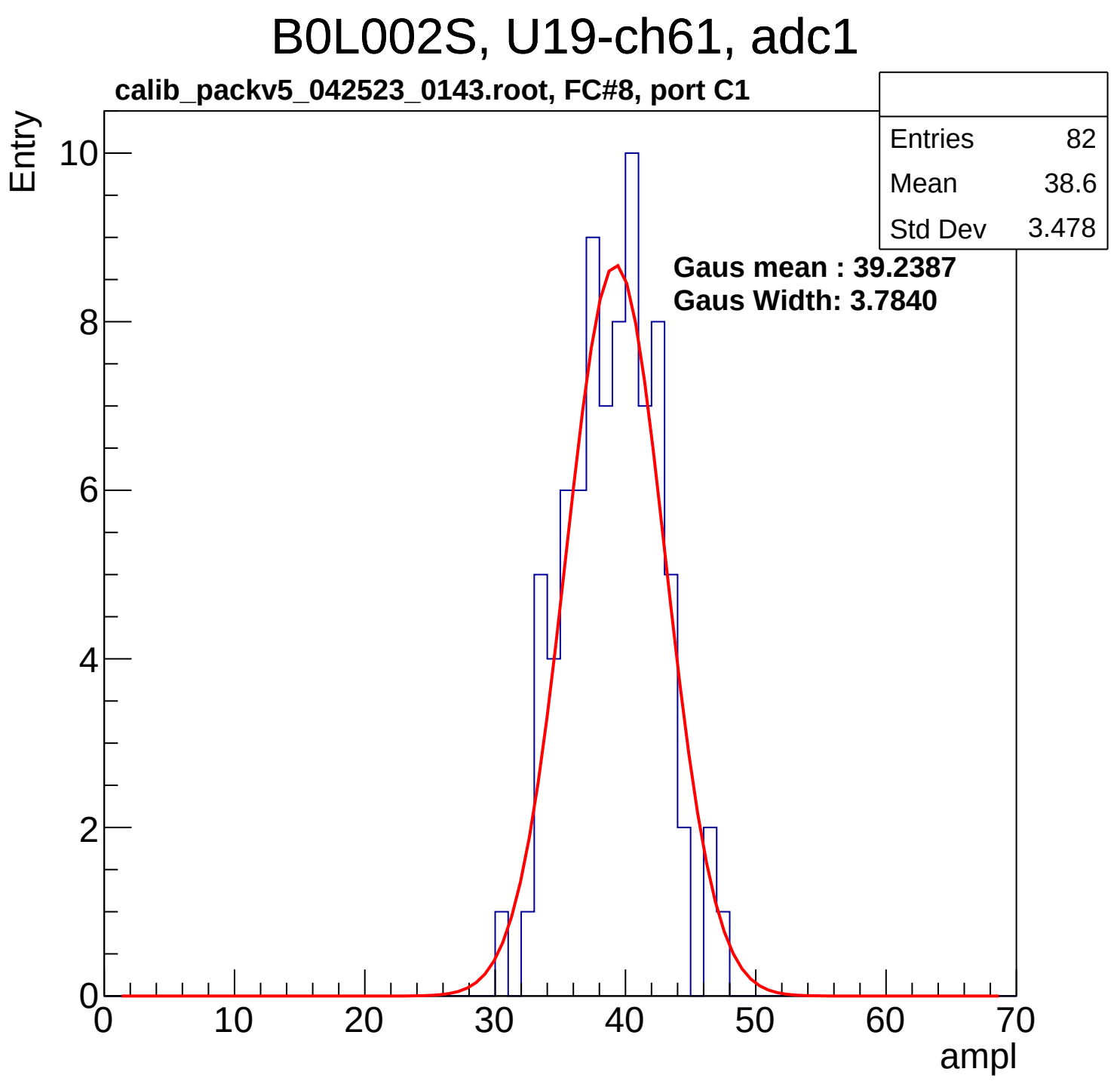

B0L002S, U19-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.72
Std Dev 7.708
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

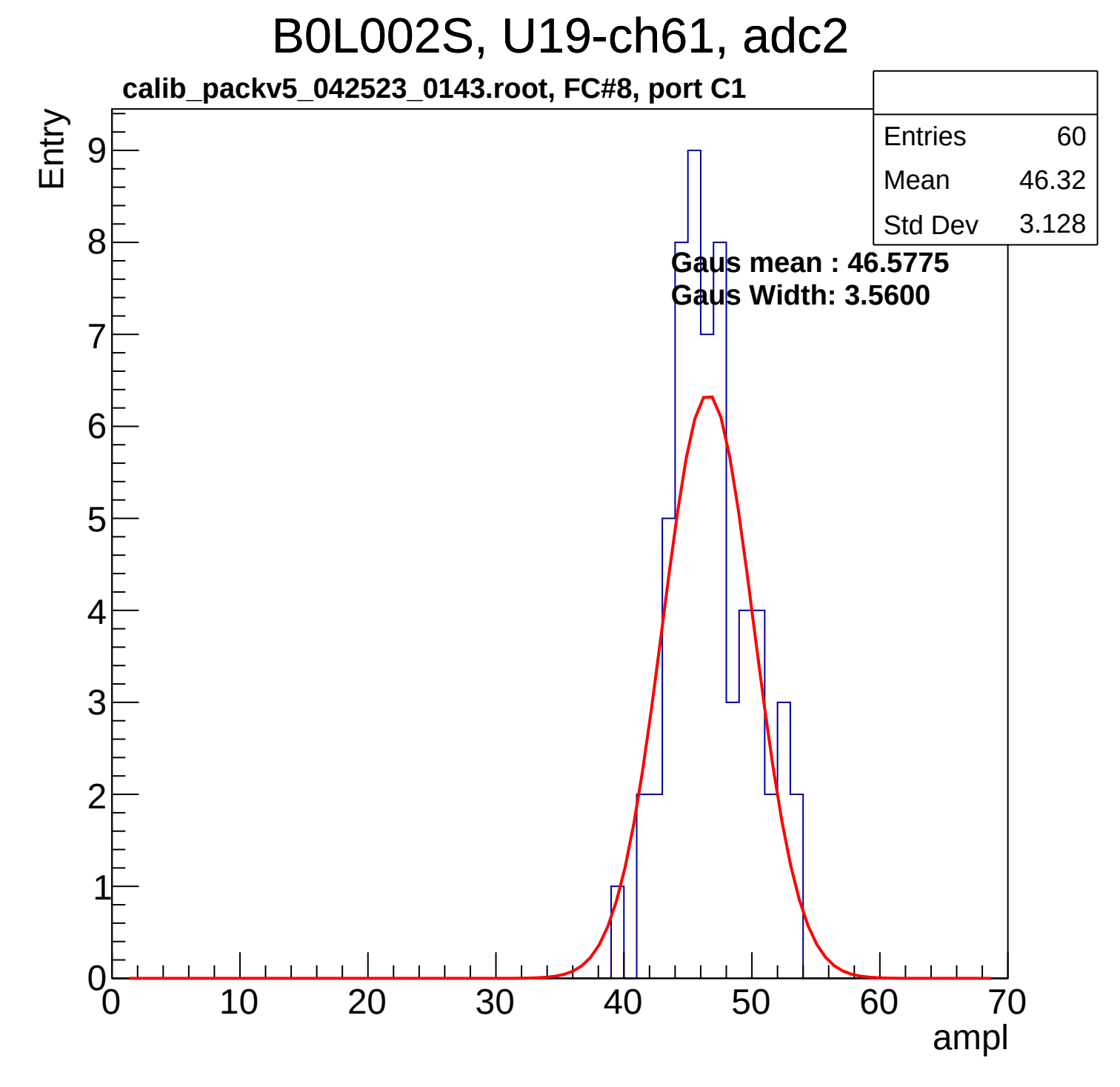

B0L002S, U19-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 61.18
Std Dev 1.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

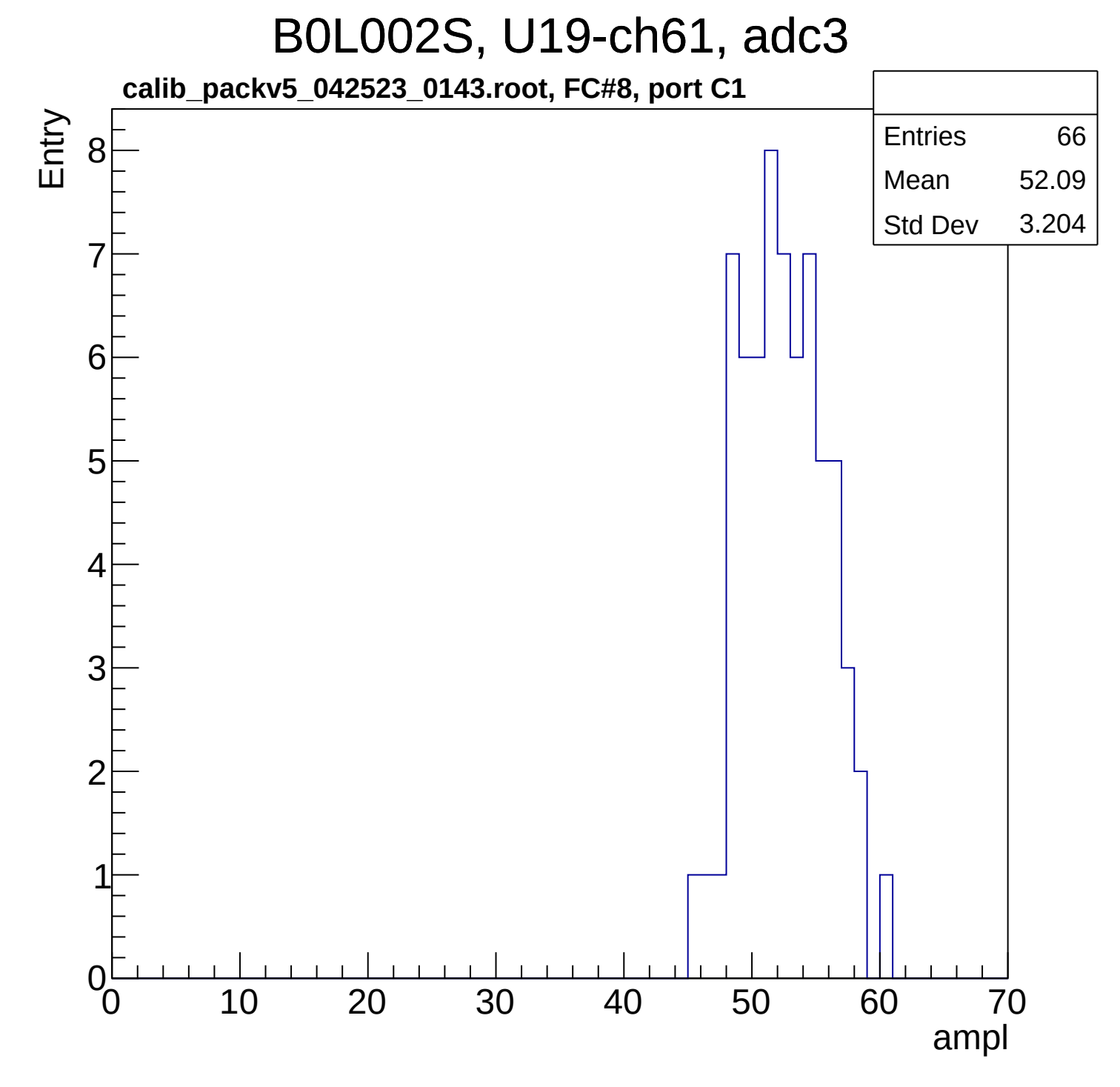

B0L002S, U19-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

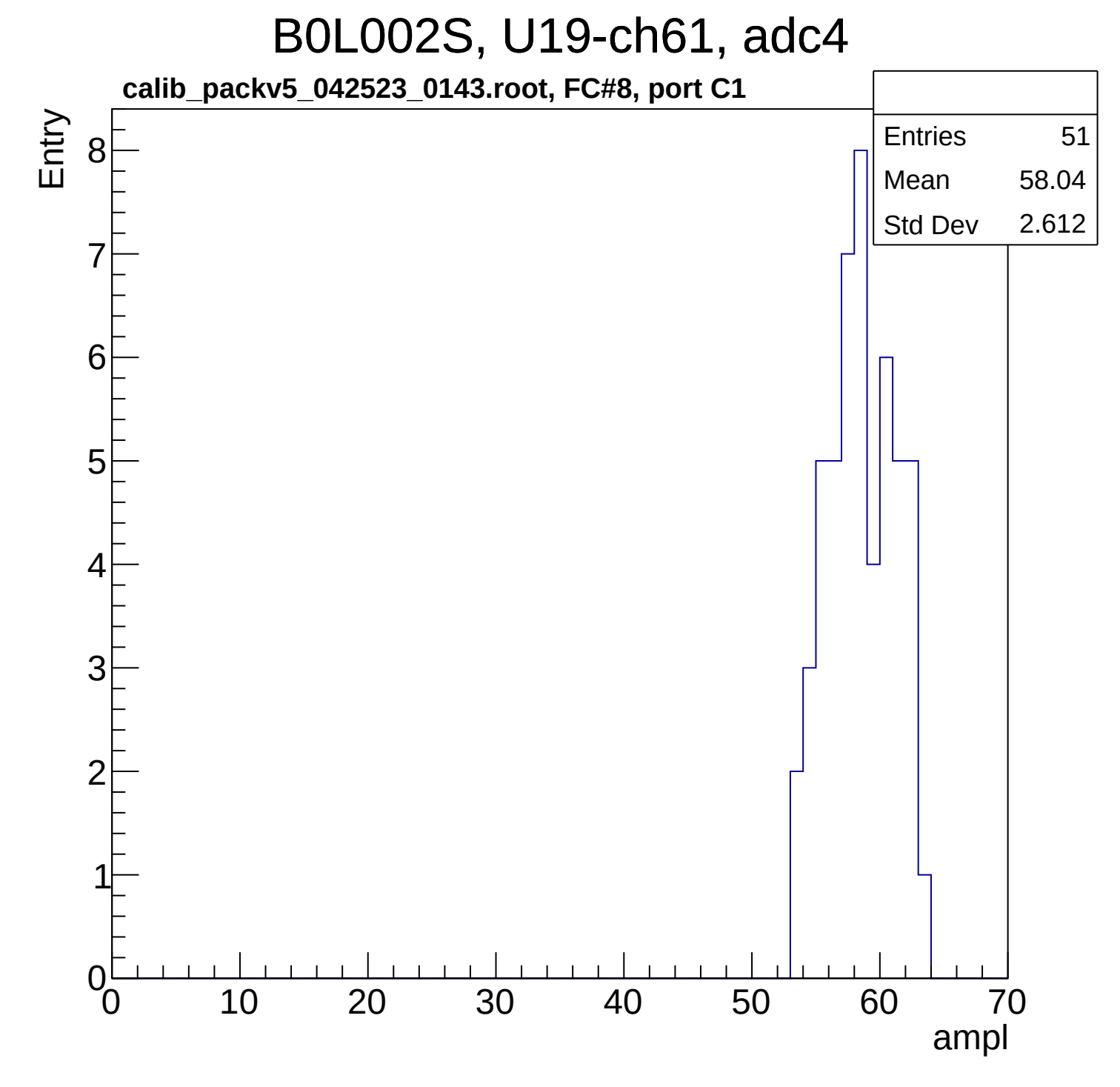

B0L002S, U19-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

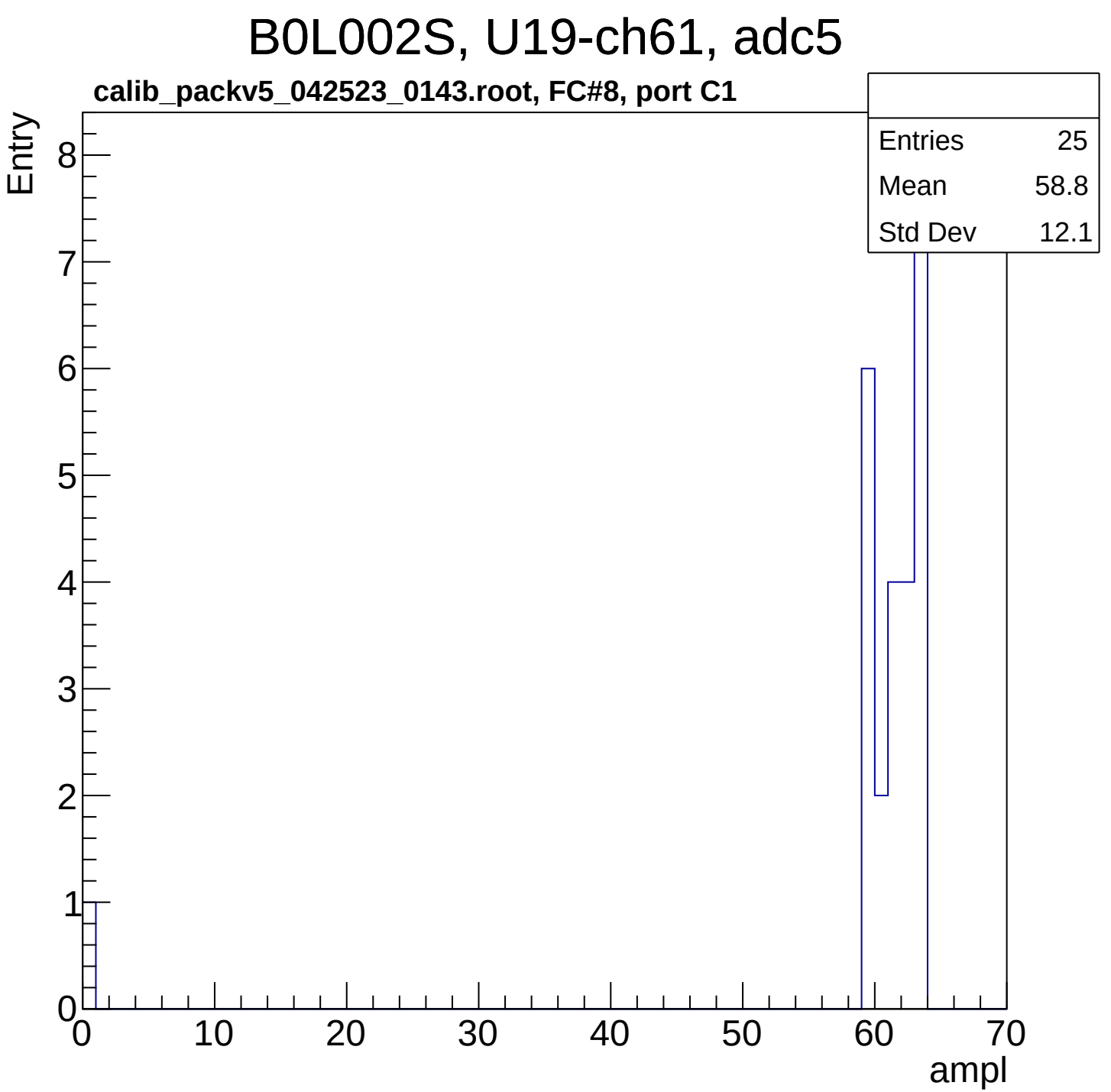

B0L002S, U19-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.03
Std Dev 5.226
Gaus mean : 31.8229
Gaus Width: 3.4598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

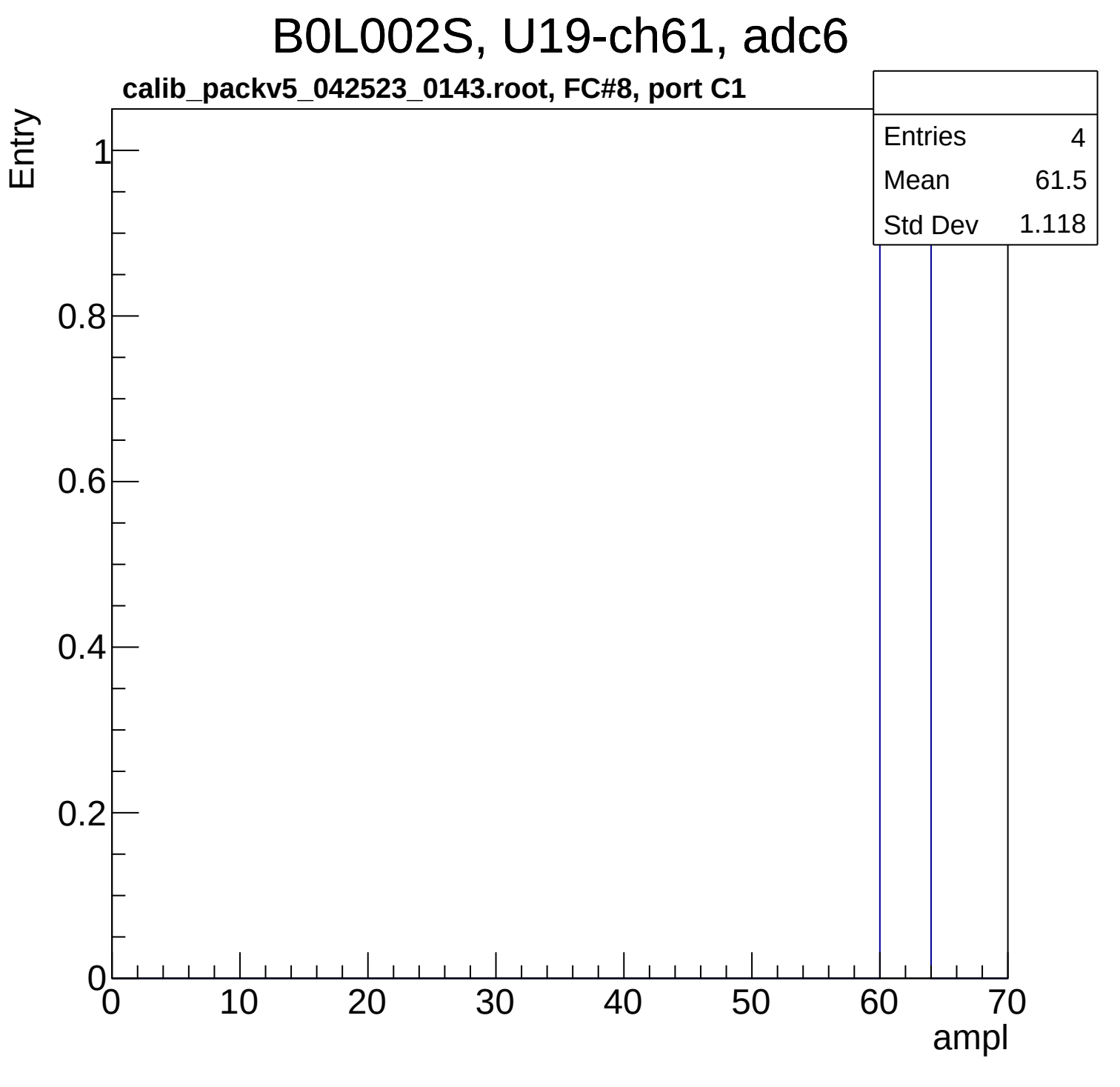

B0L002S, U19-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 38.6
Std Dev 3.478
Gaus mean : 39.2387
Gaus Width: 3.7840
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

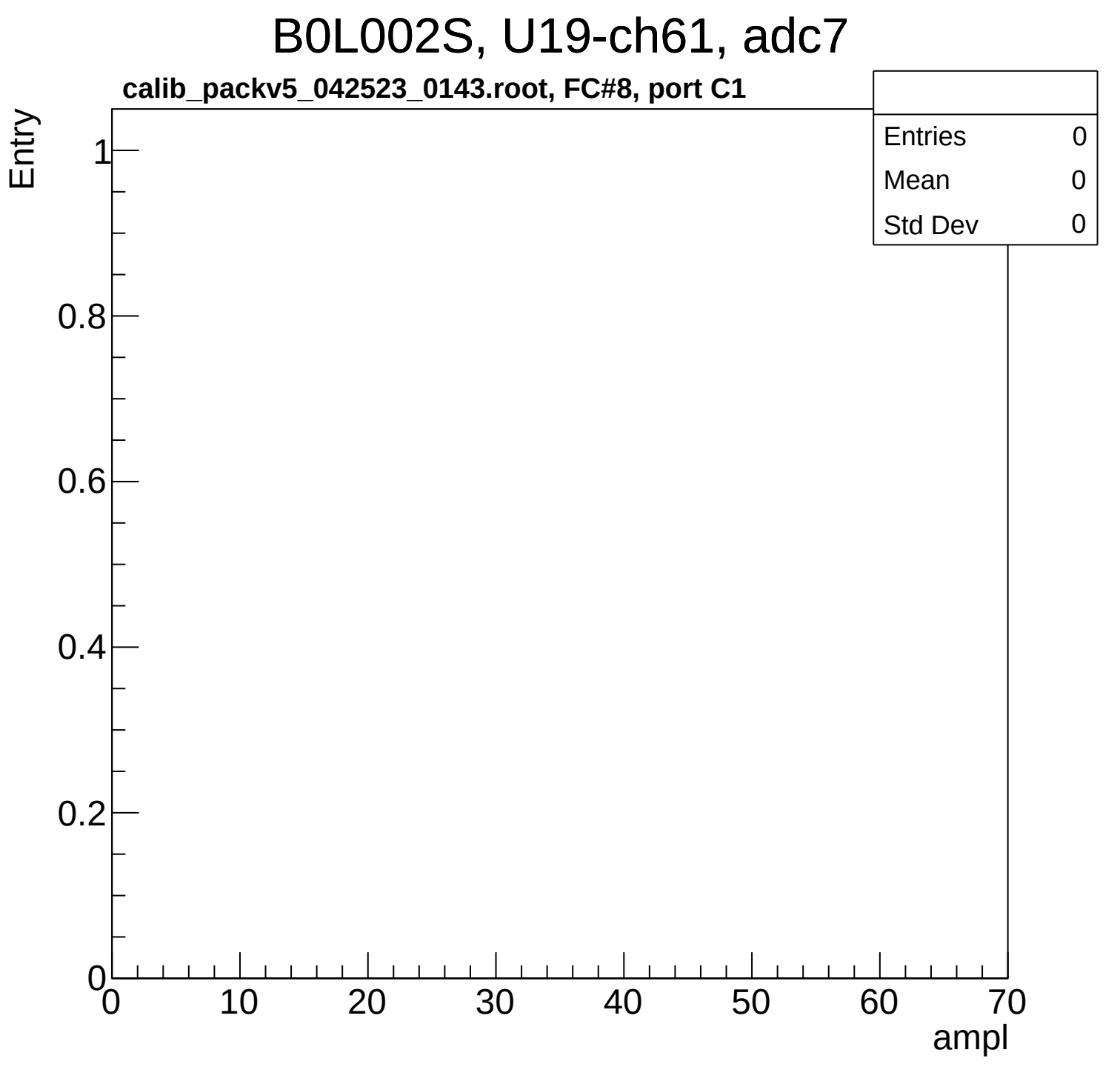

B0L002S, U19-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 46.32
Std Dev 3.128
Gaus mean : 46.5775
Gaus Width: 3.5600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

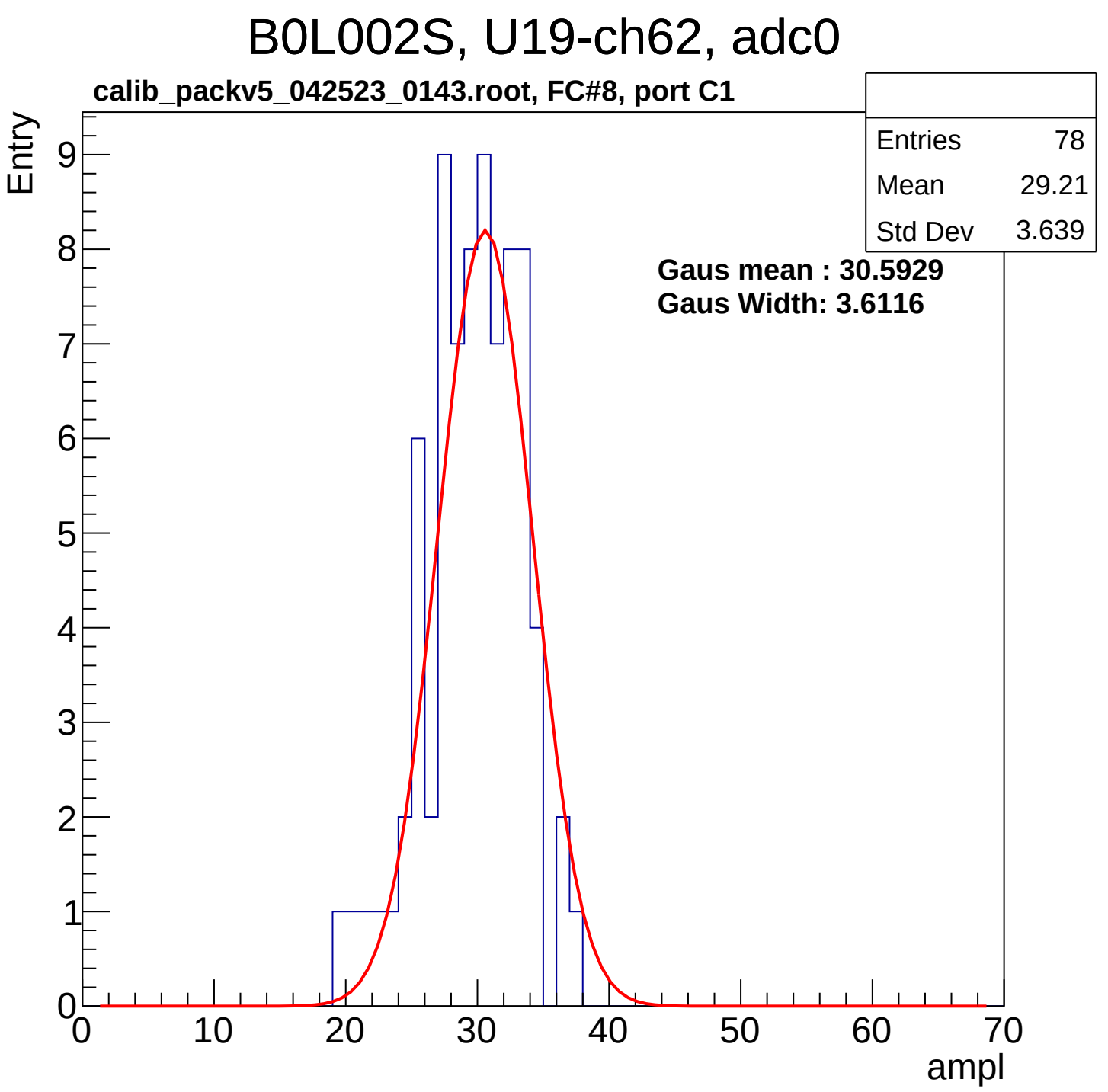

B0L002S, U19-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 52.09
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

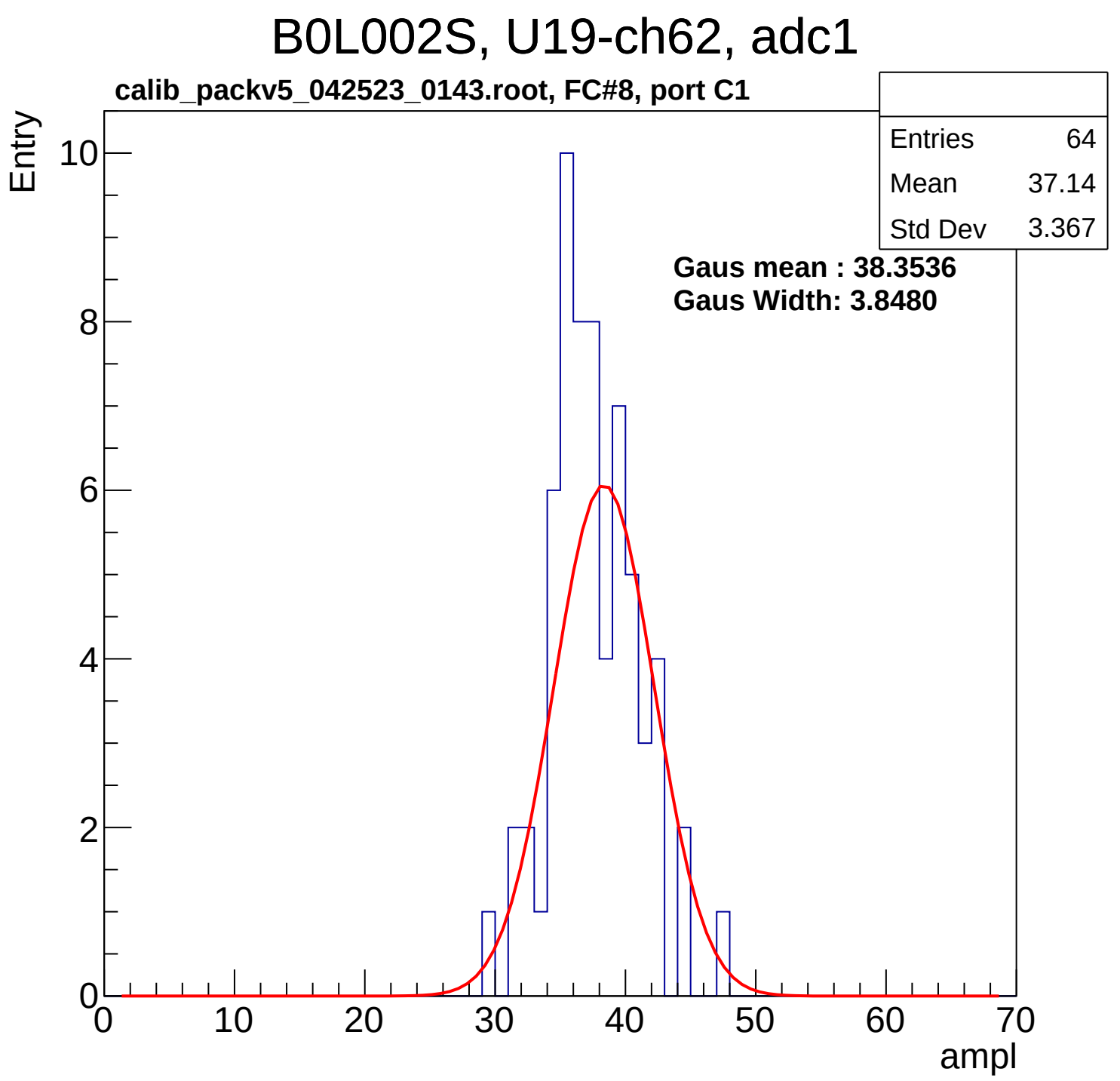

B0L002S, U19-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.04
Std Dev 2.612
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

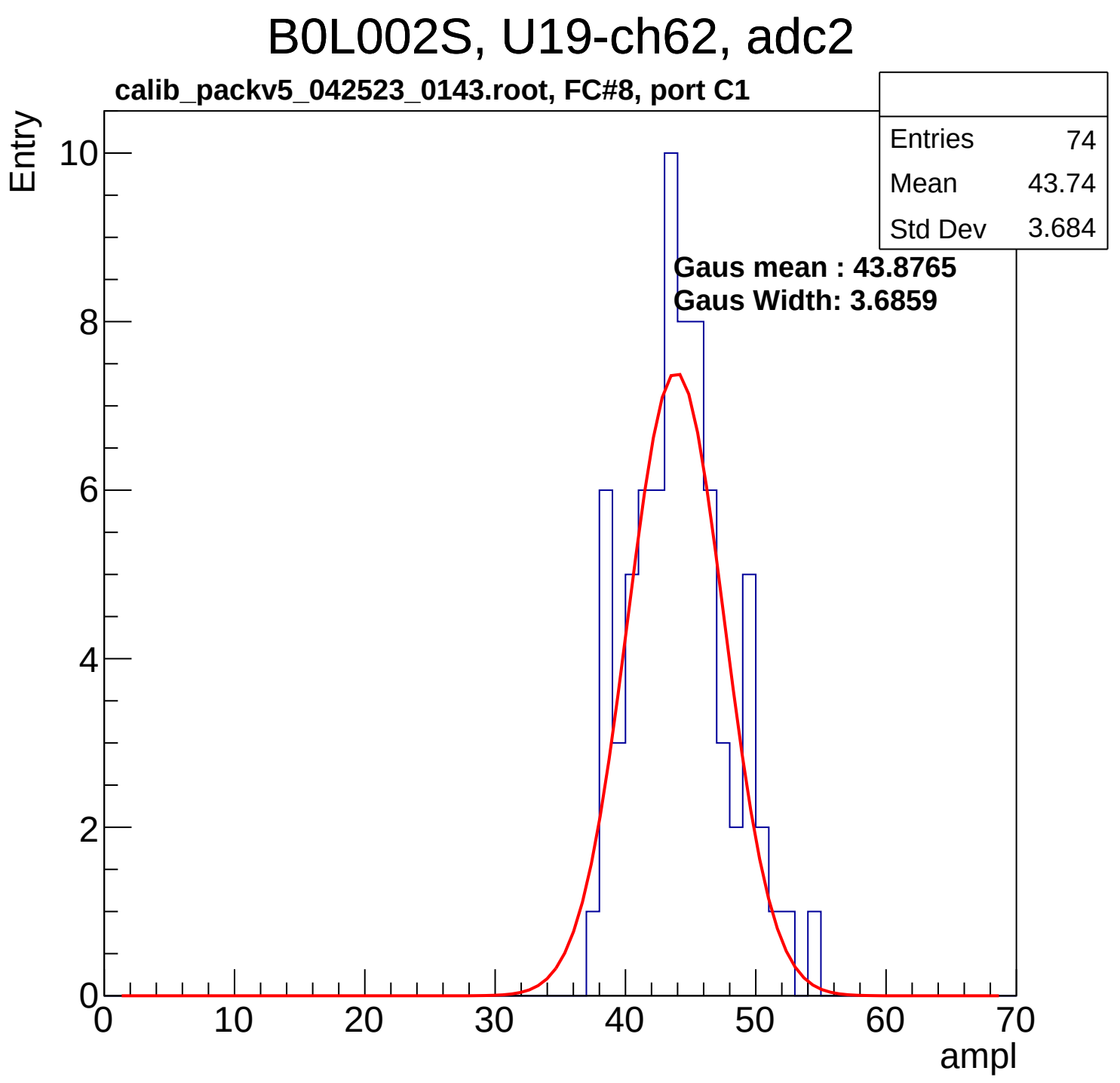

B0L002S, U19-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.8
Std Dev 12.1
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

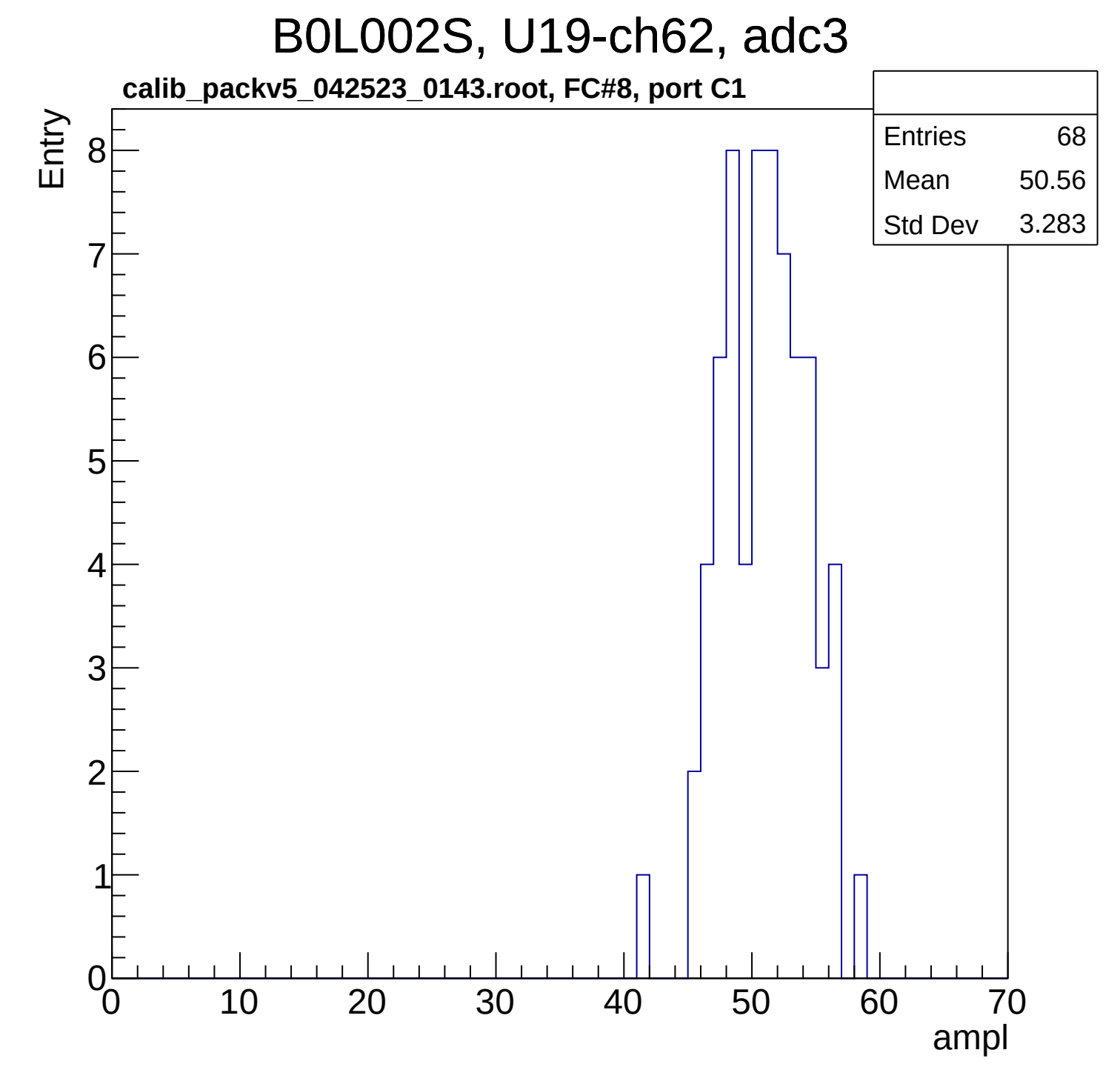

B0L002S, U19-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

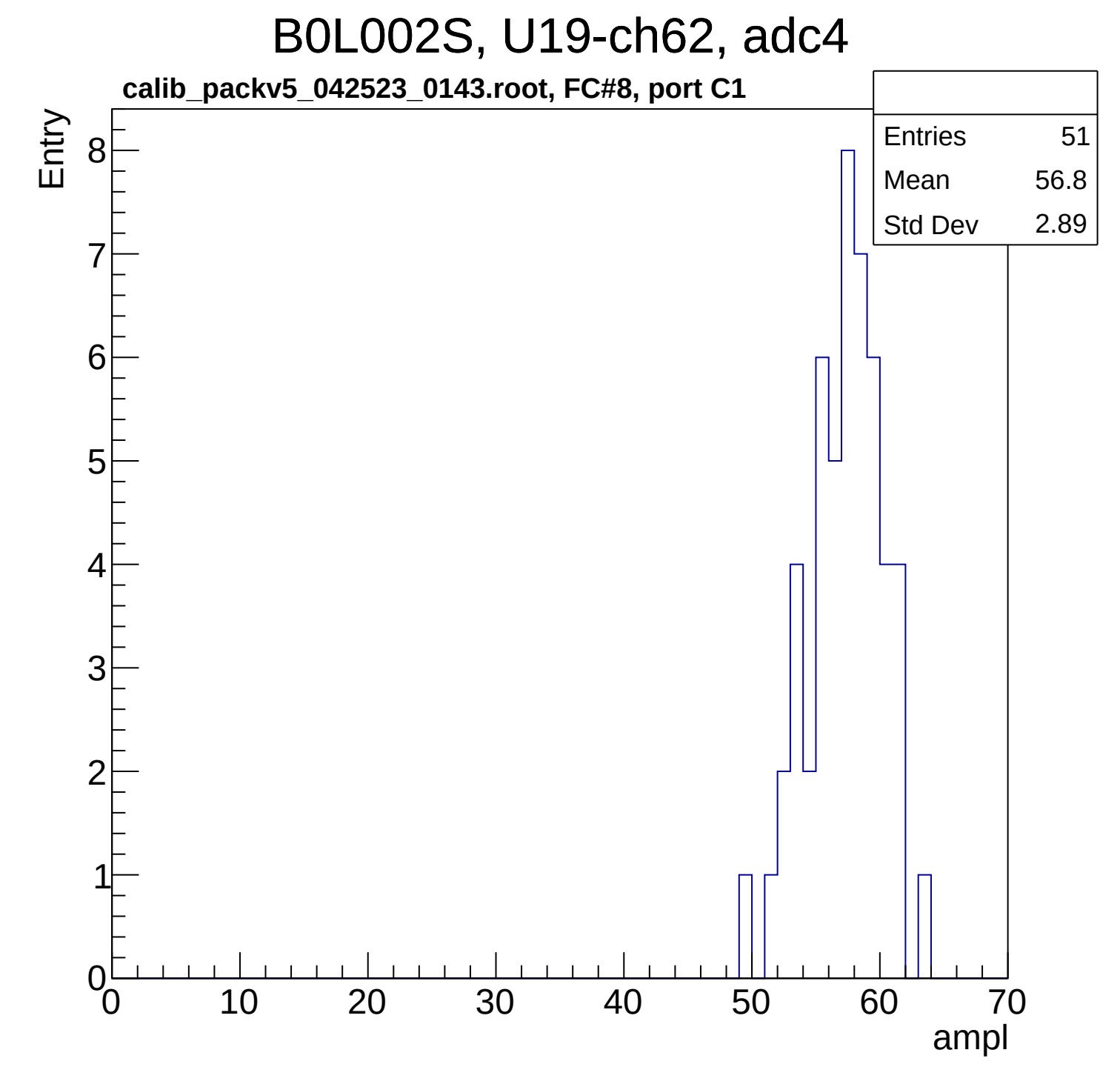

B0L002S, U19-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

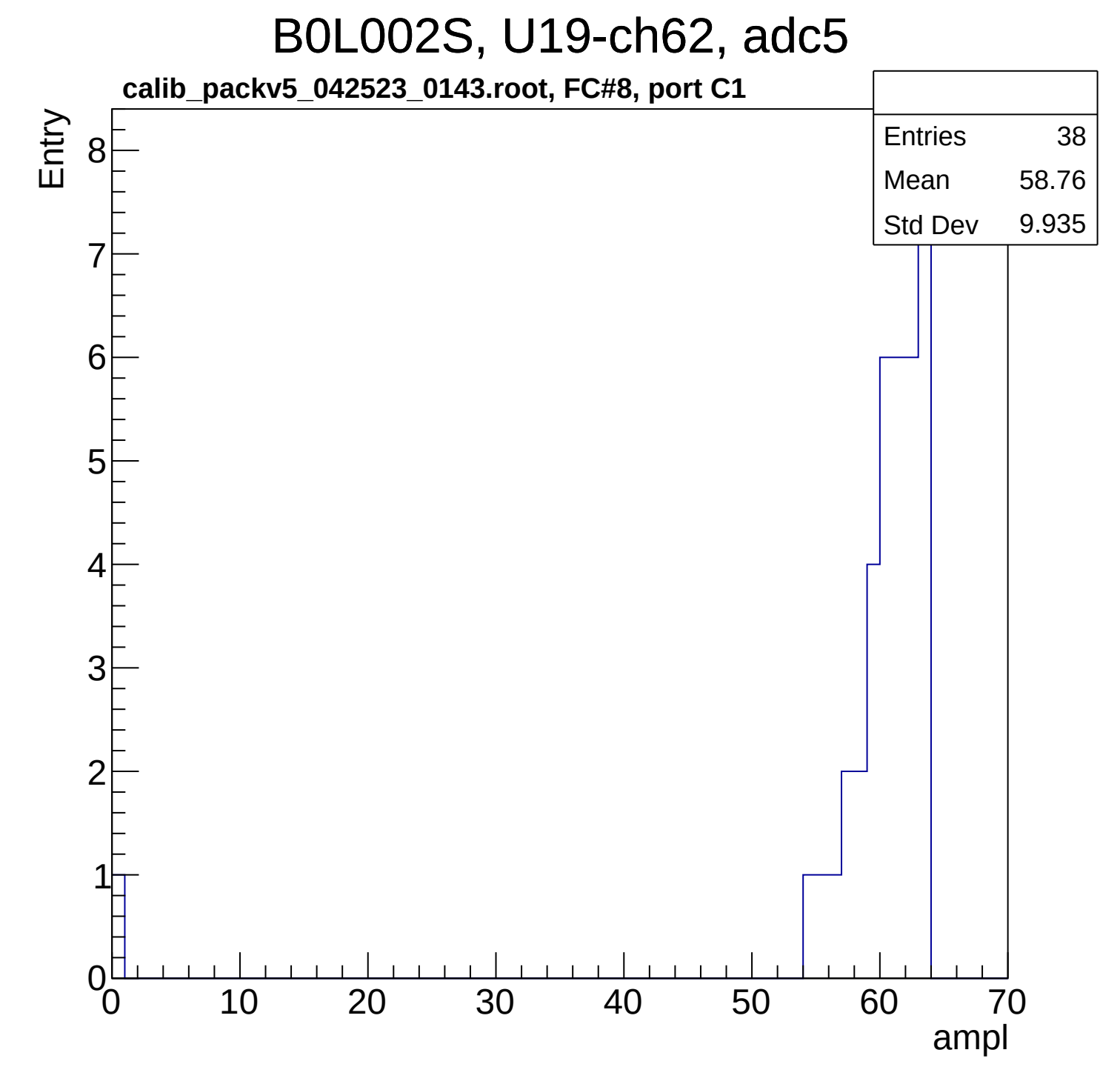

B0L002S, U19-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.21
Std Dev 3.639
Gaus mean : 30.5929
Gaus Width: 3.6116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

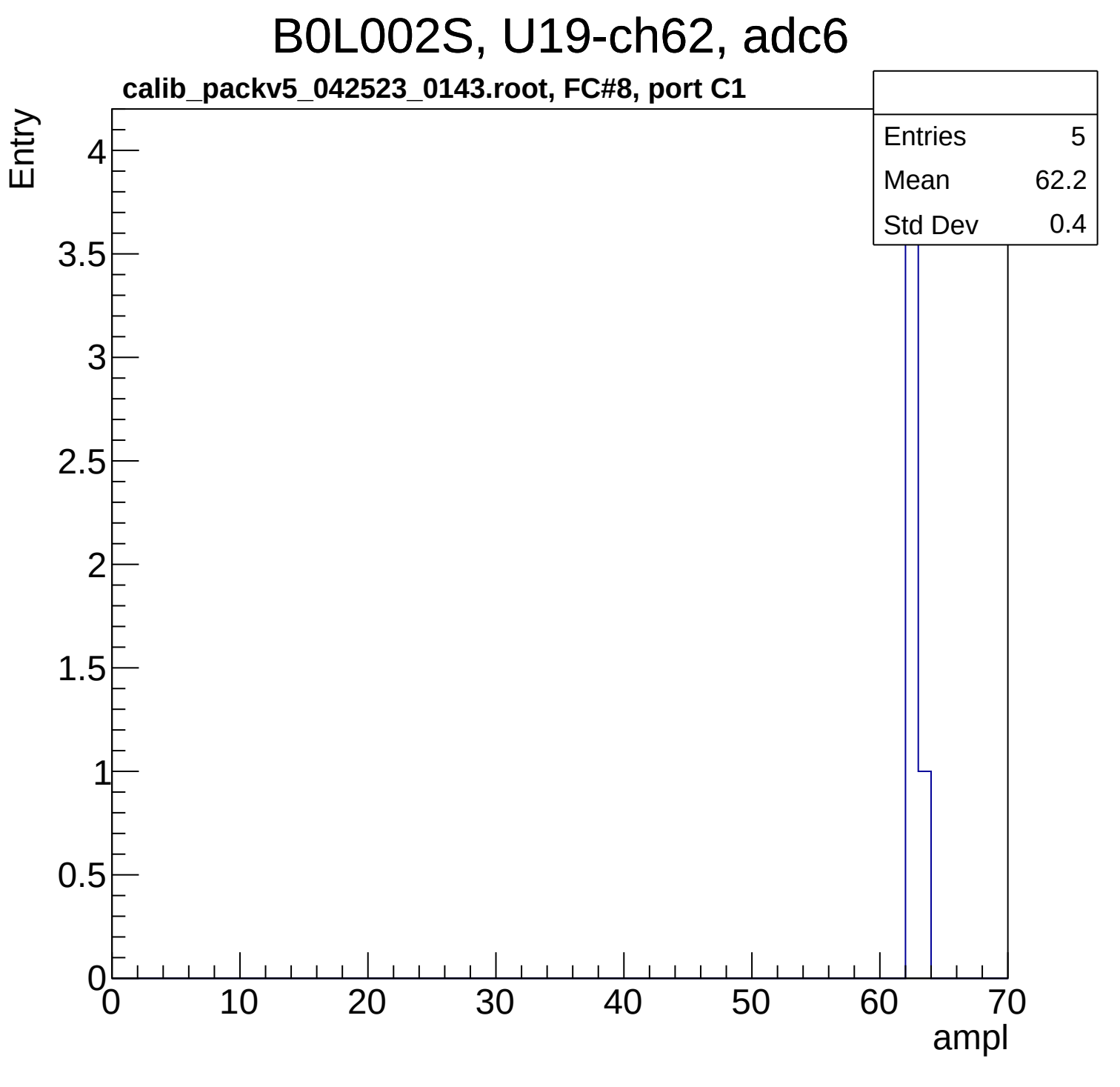

B0L002S, U19-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.14
Std Dev 3.367
Gaus mean : 38.3536
Gaus Width: 3.8480
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

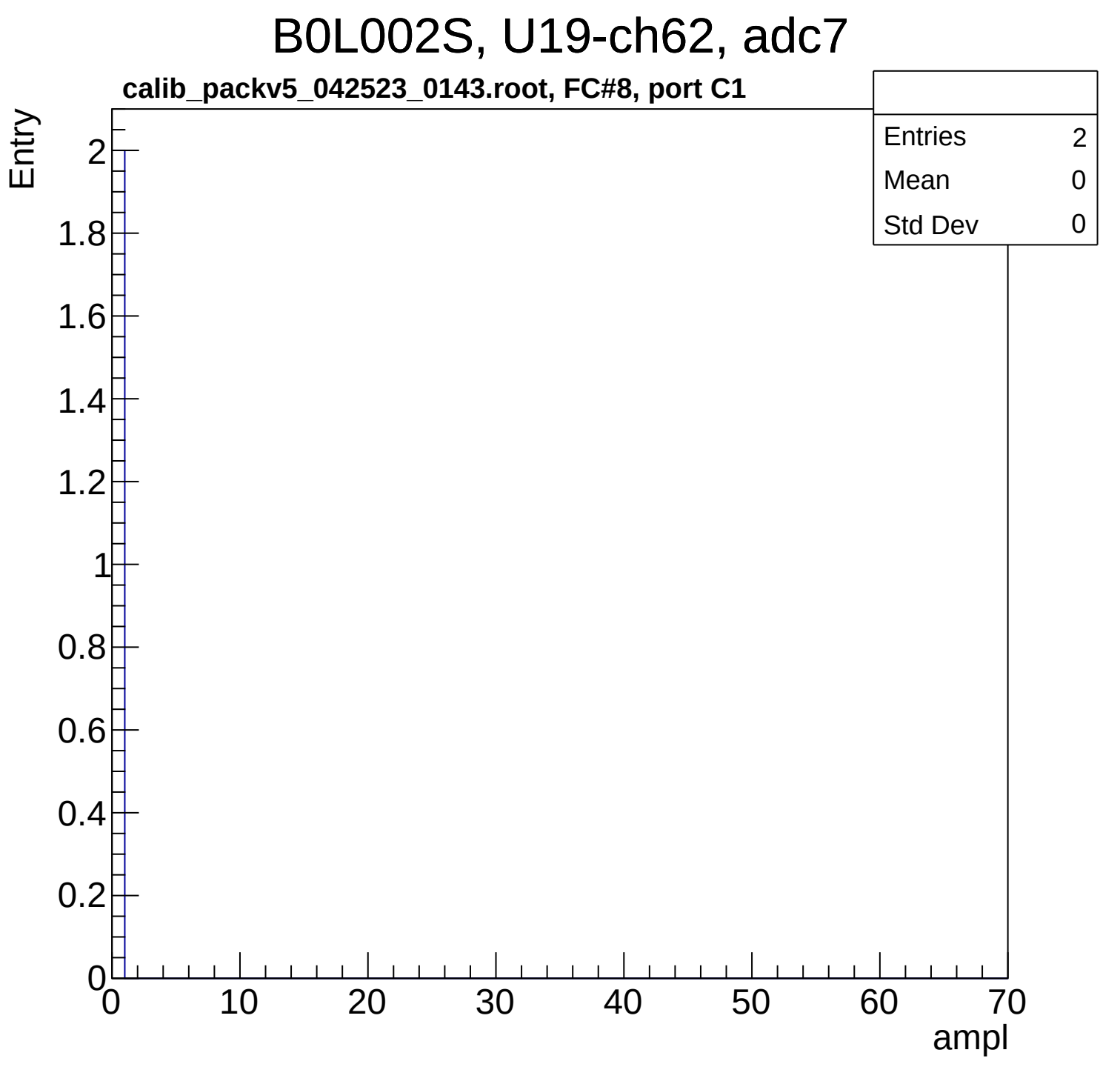

B0L002S, U19-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.74
Std Dev 3.684
Gaus mean : 43.8765
Gaus Width: 3.6859
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

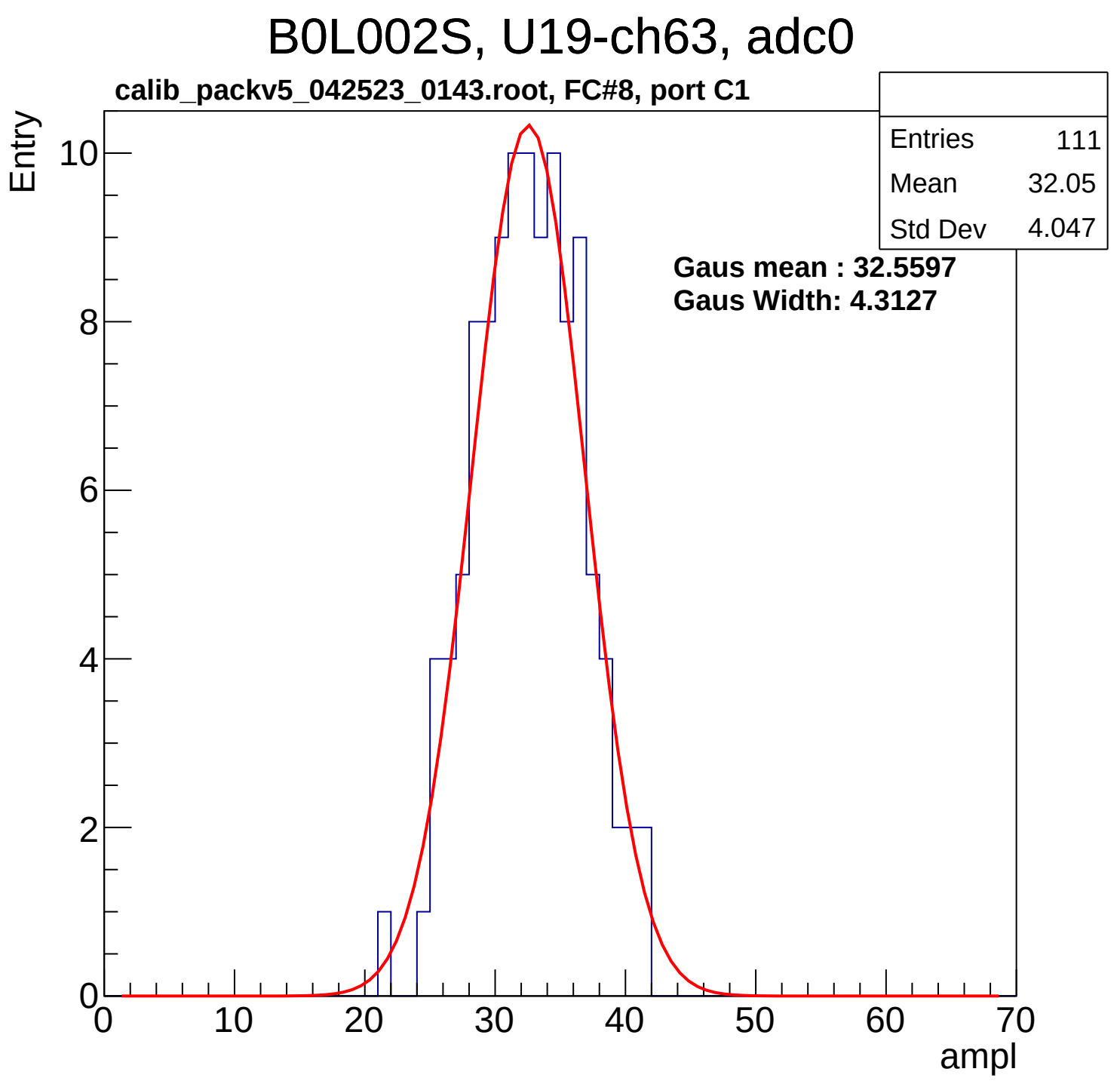

B0L002S, U19-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.56
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

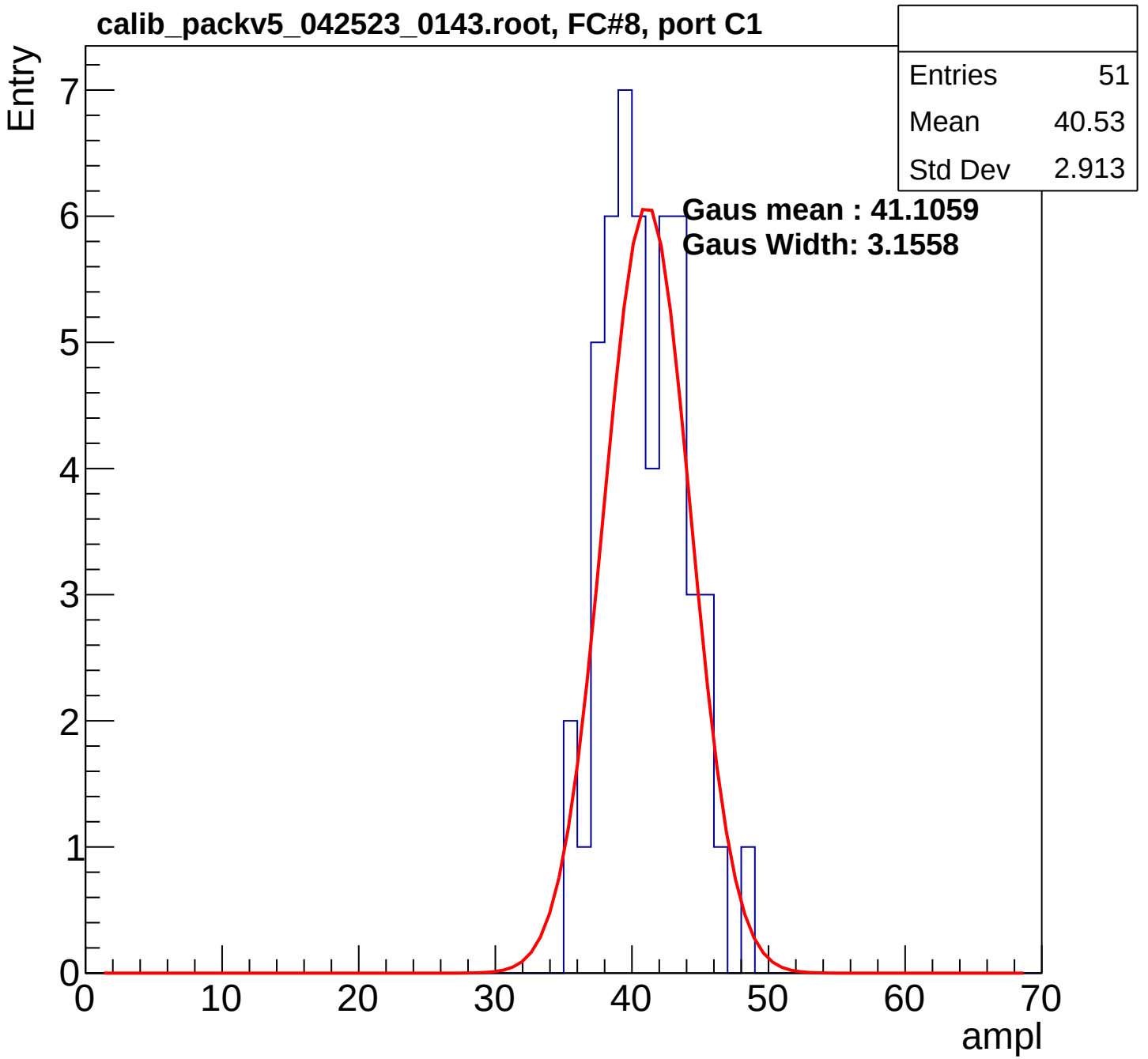

B0L002S, U19-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.8
Std Dev 2.89
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

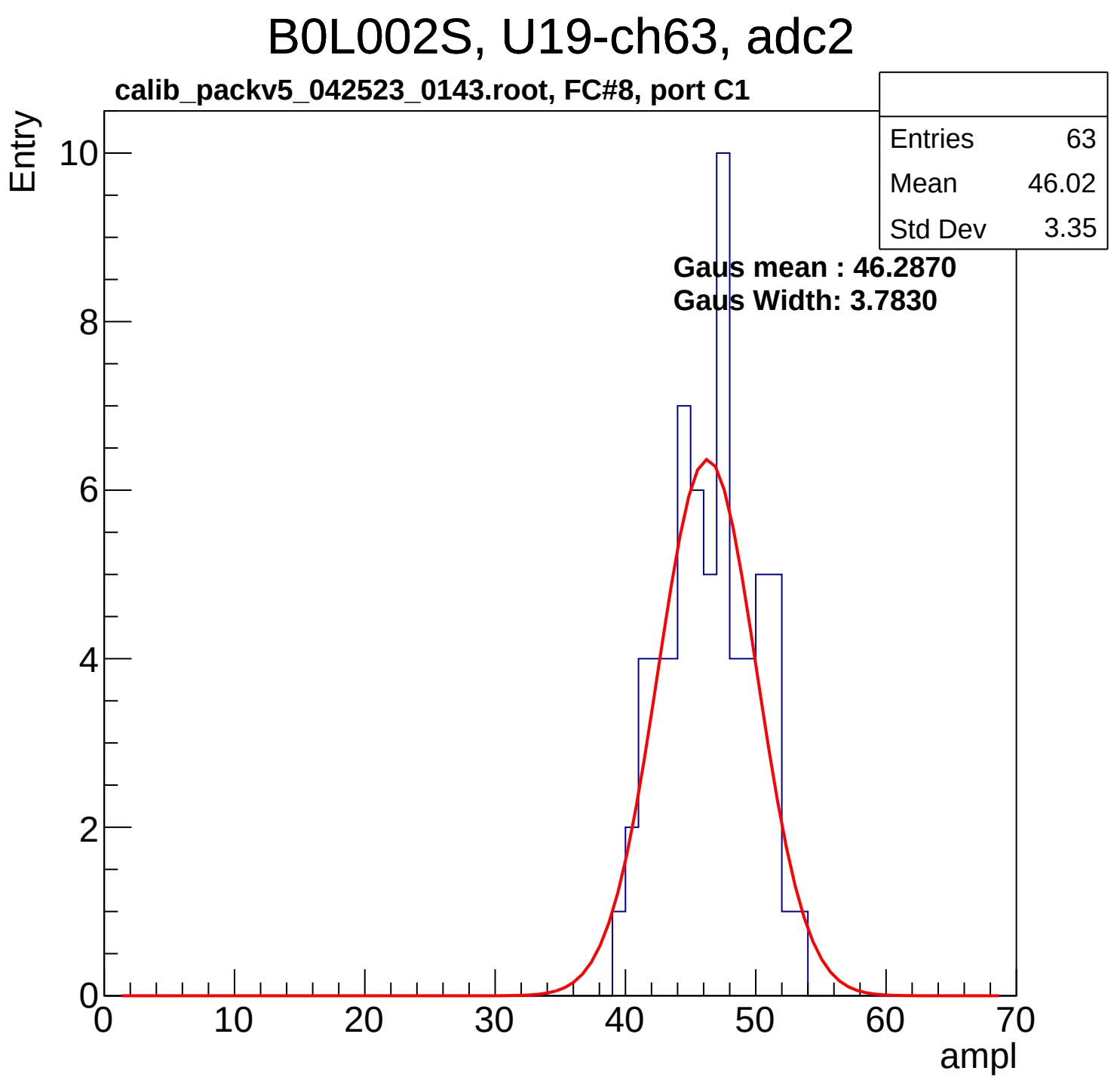

B0L002S, U19-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 58.76
Std Dev 9.935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

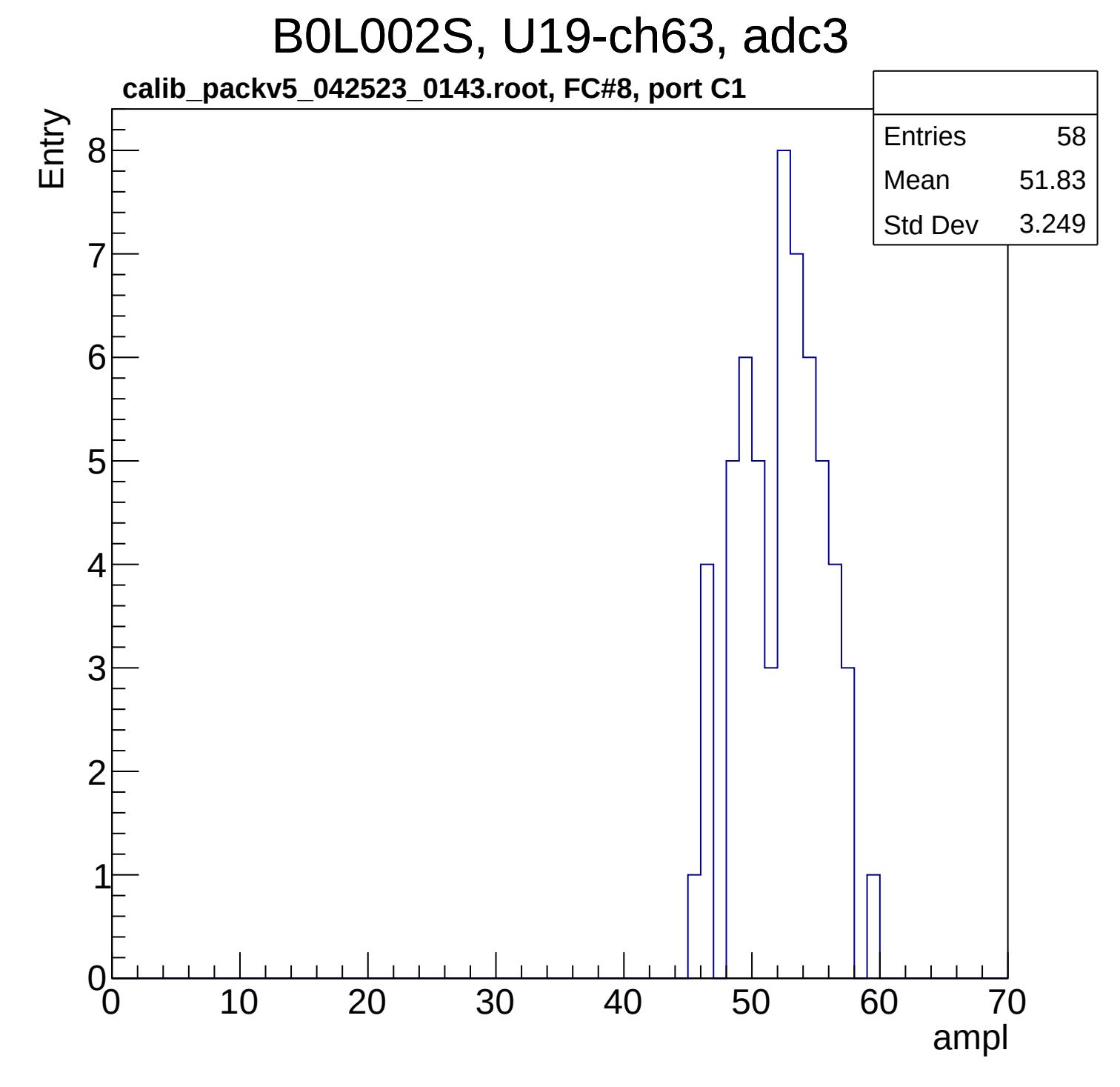

B0L002S, U19-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.2
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

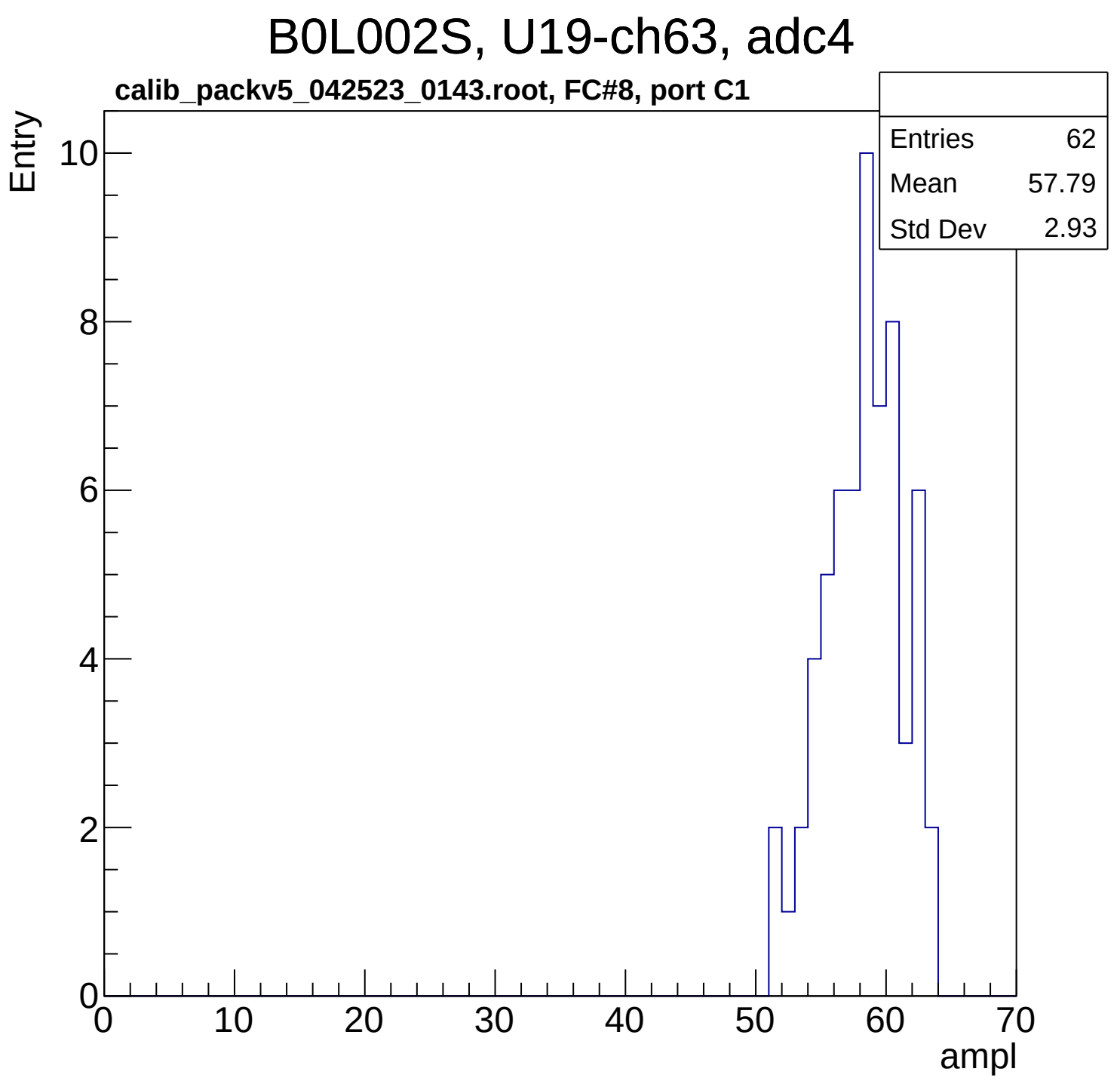

B0L002S, U19-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

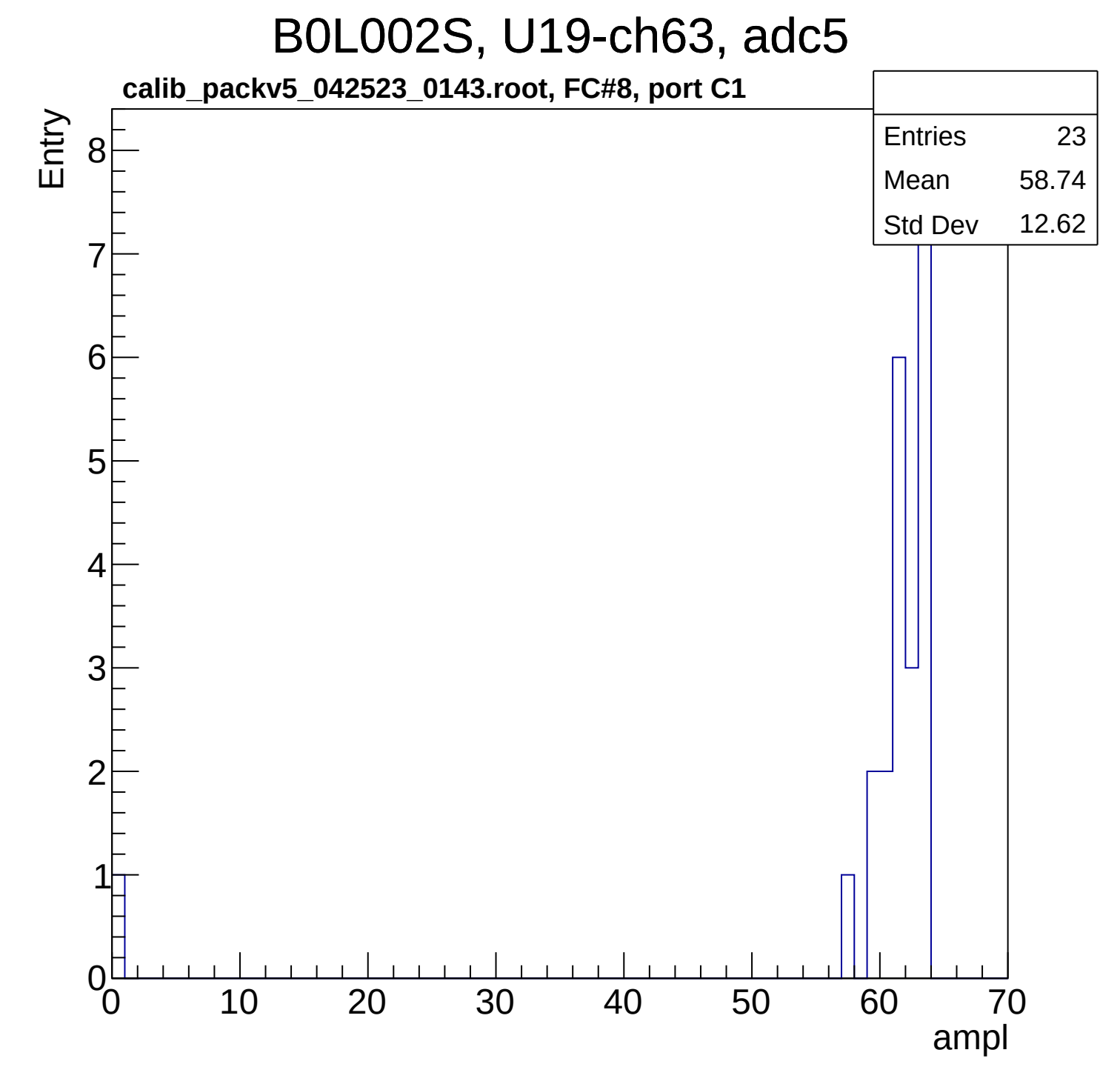

B0L002S, U19-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 111
Mean 32.05
Std Dev 4.047
Gaus mean : 32.5597
Gaus Width: 4.3127
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

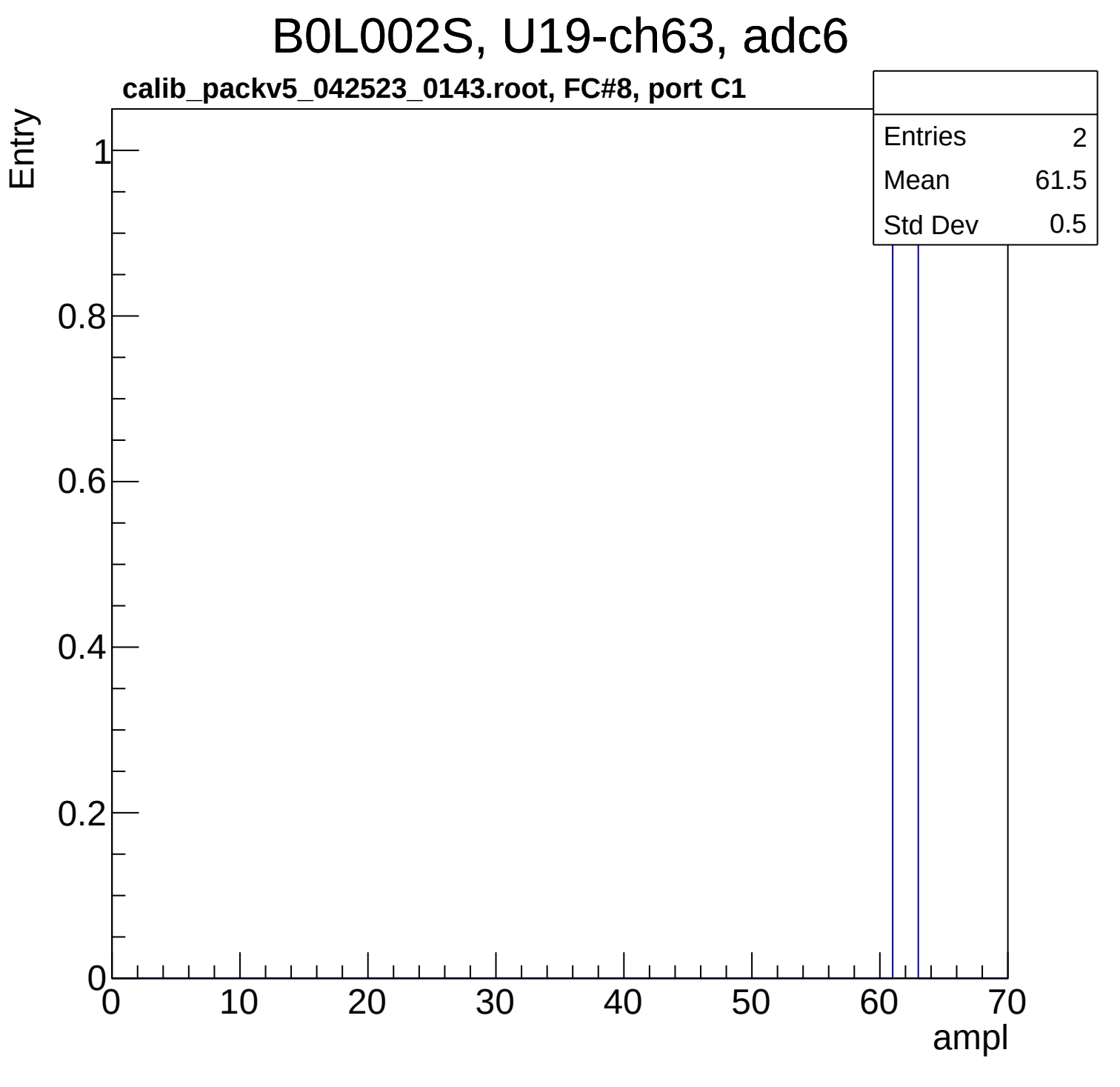

B0L002S, U19-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 40.53
Std Dev 2.913
Gaus mean : 41.1059
Gaus Width: 3.1558
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

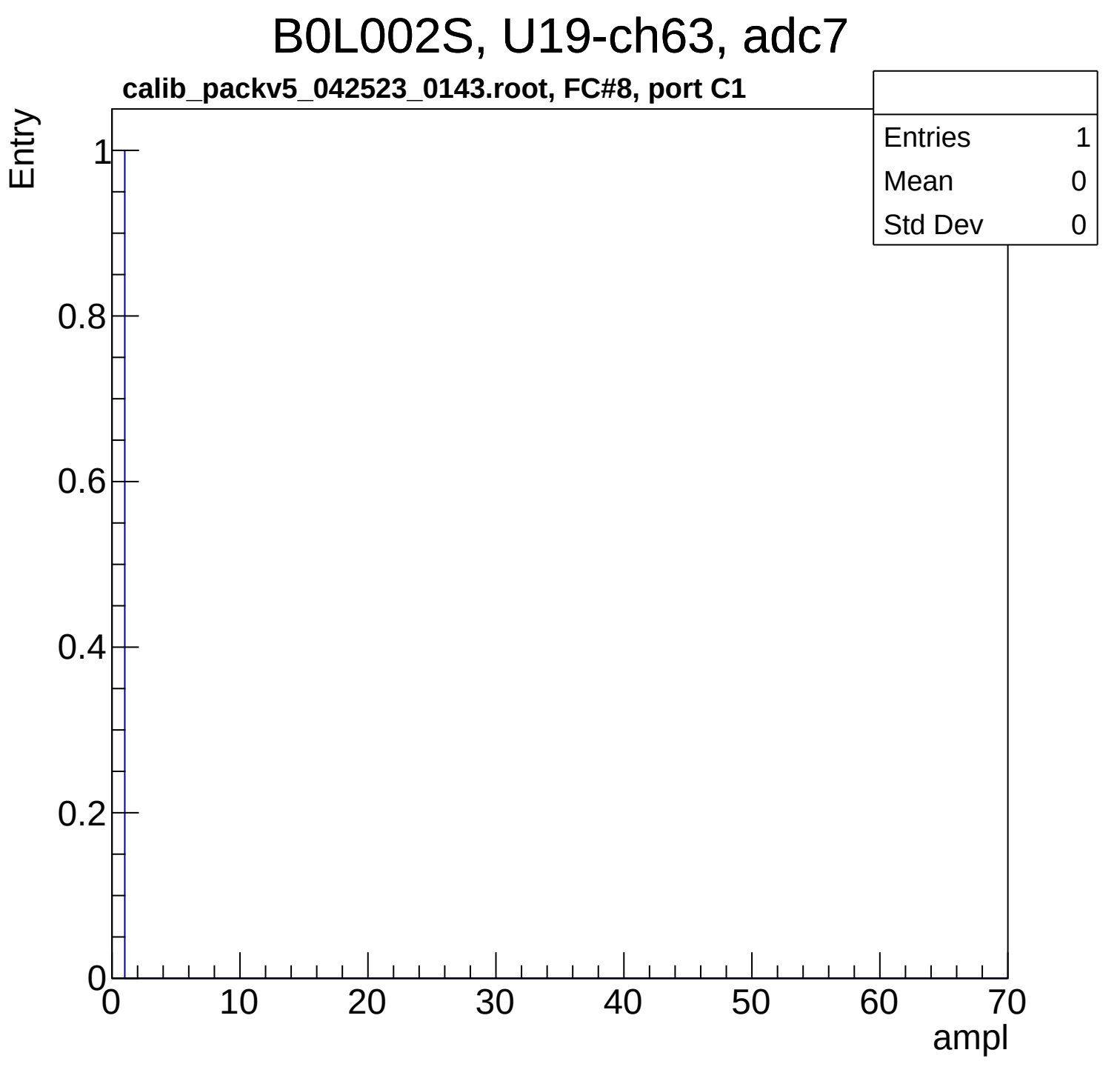

B0L002S, U19-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 46.02
Std Dev 3.35
Gaus mean : 46.2870
Gaus Width: 3.7830
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

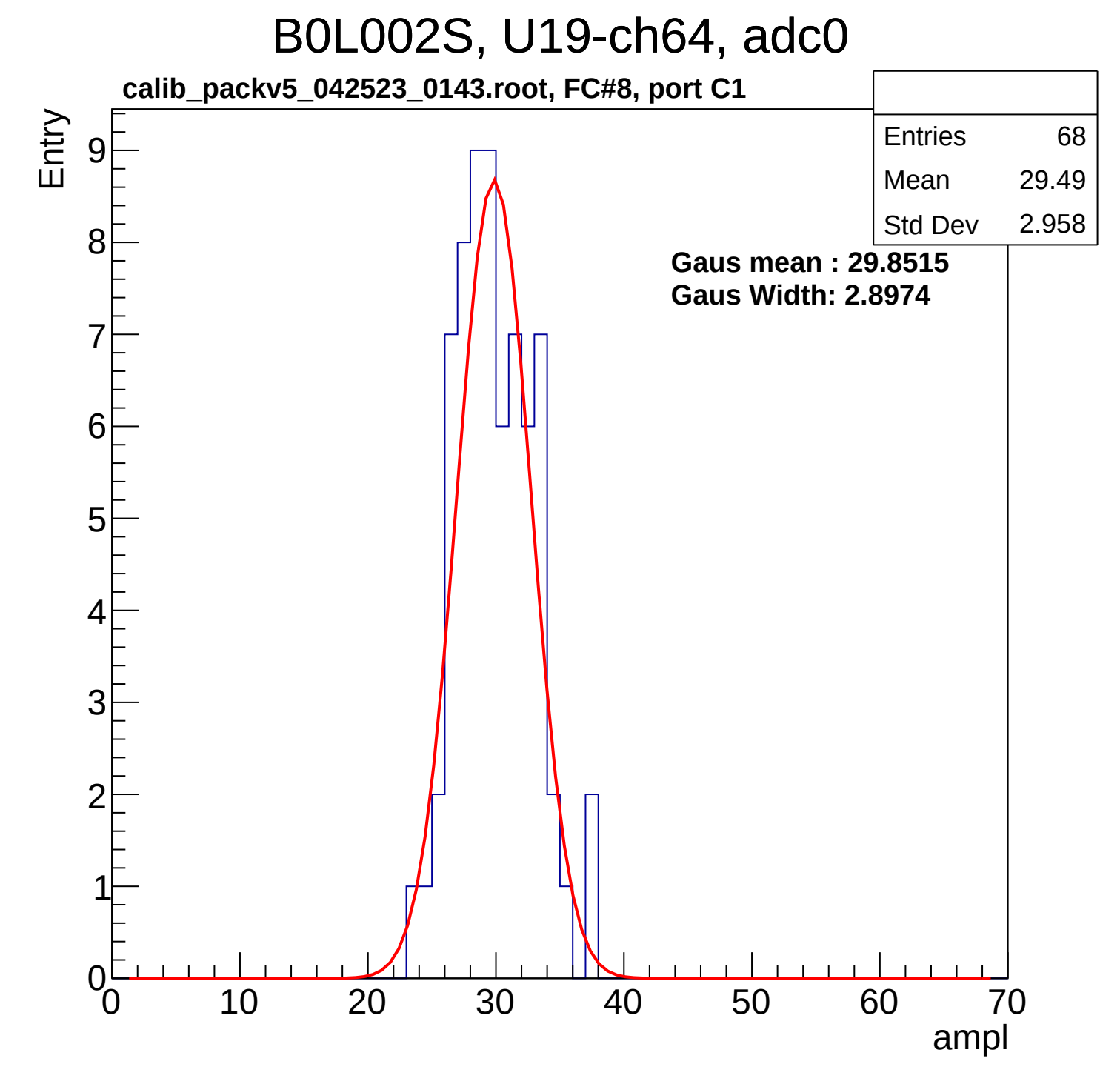

B0L002S, U19-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.83
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

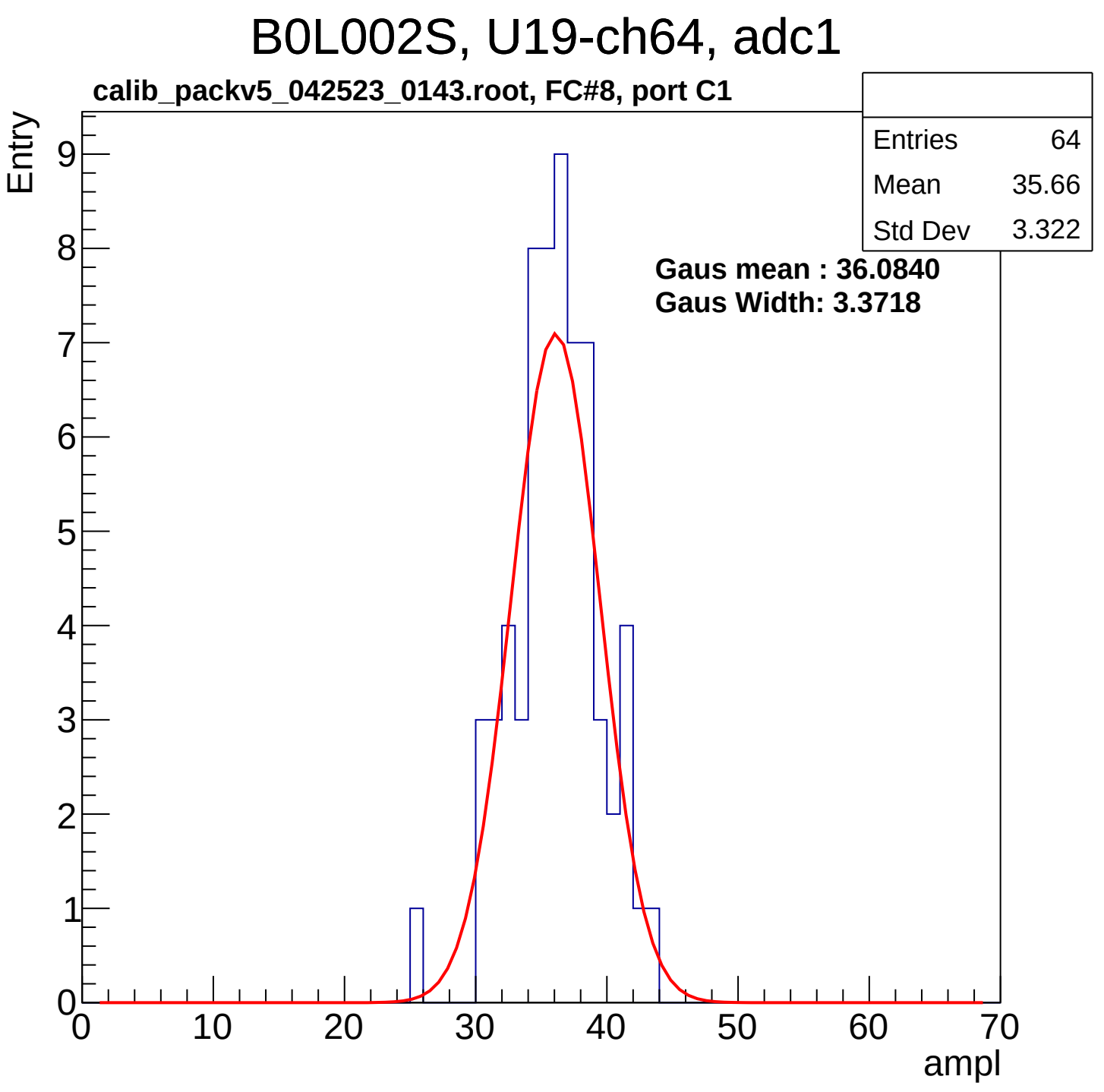

B0L002S, U19-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.79
Std Dev 2.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

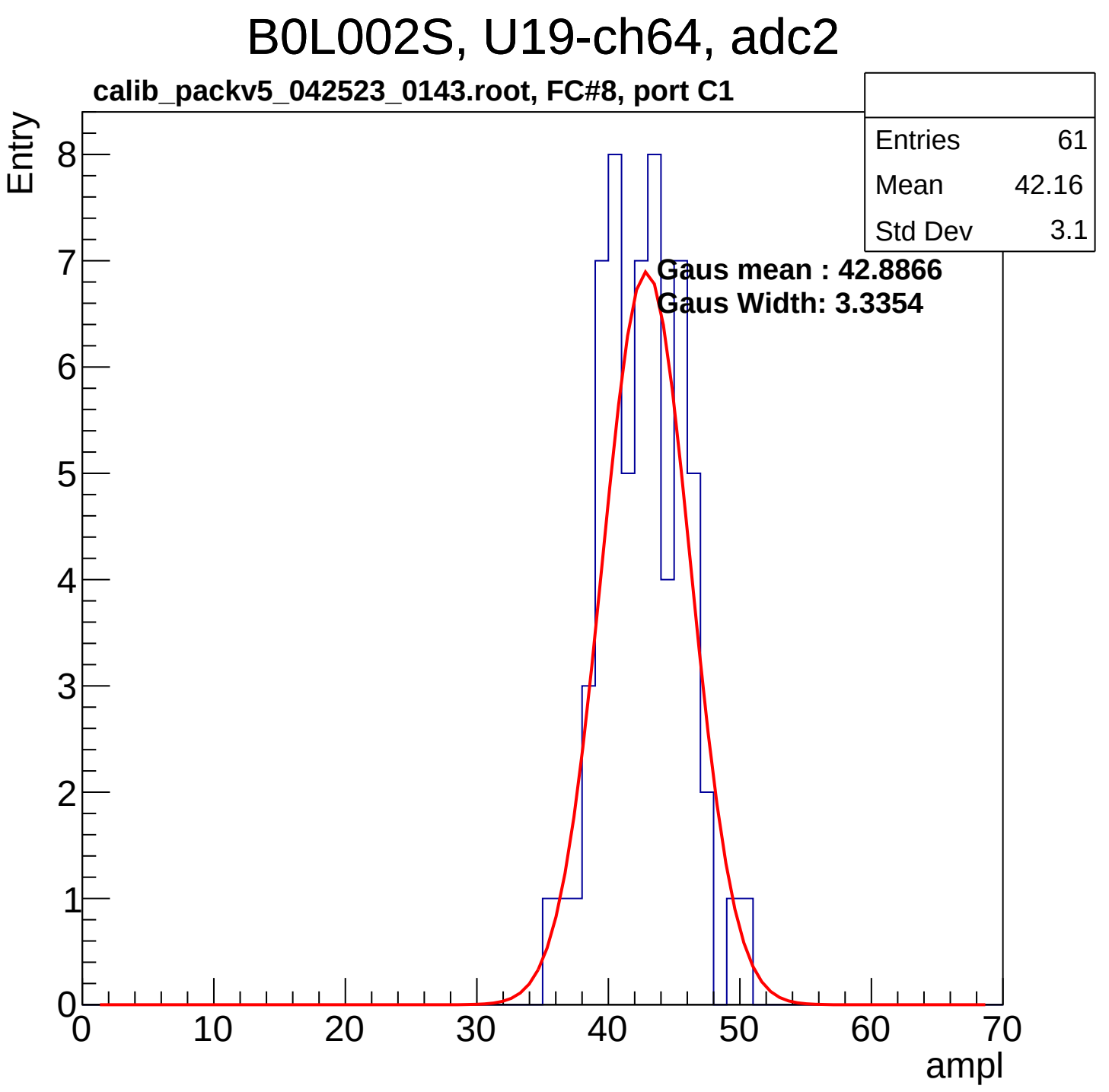

B0L002S, U19-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.74
Std Dev 12.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

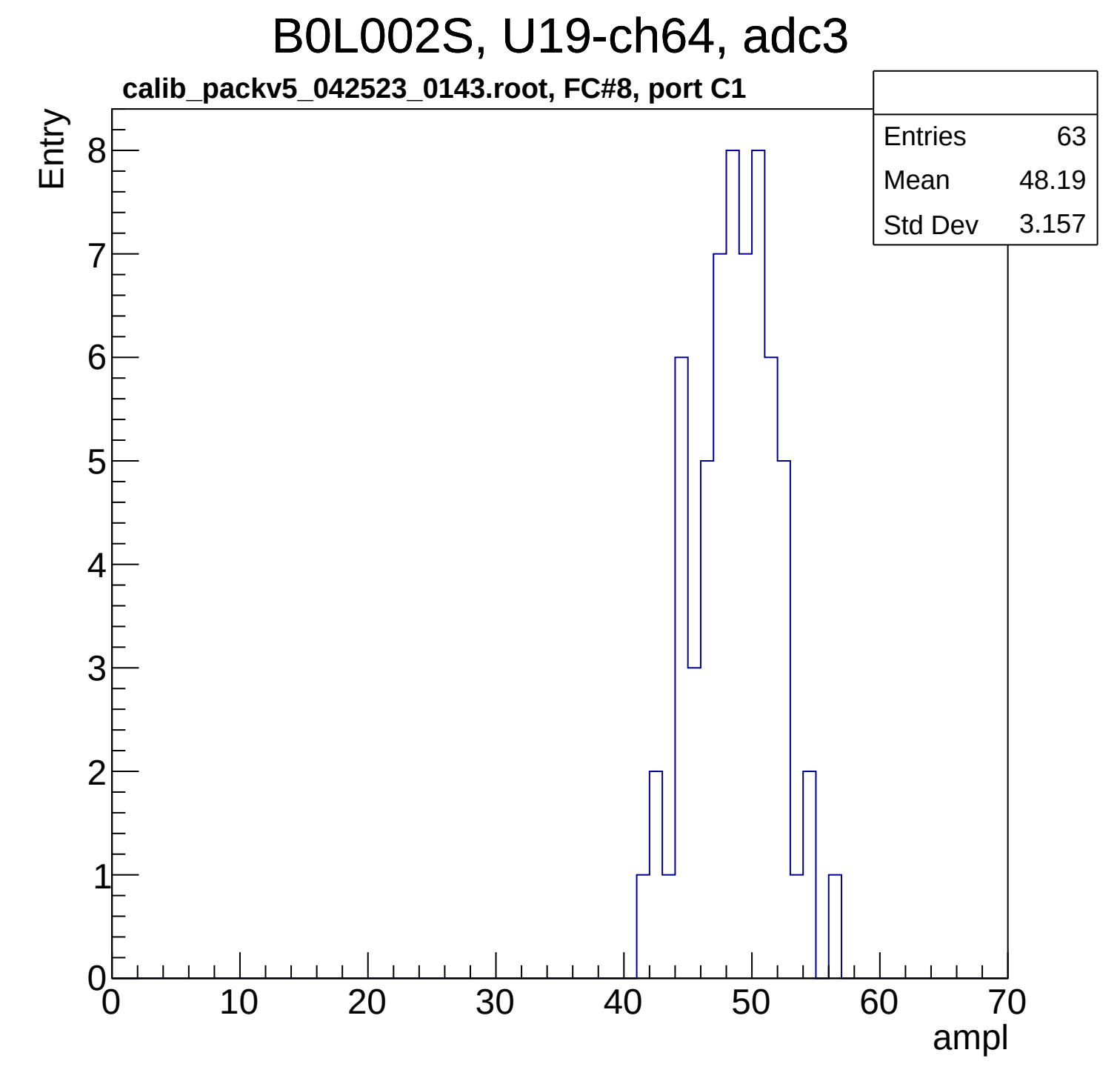

B0L002S, U19-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

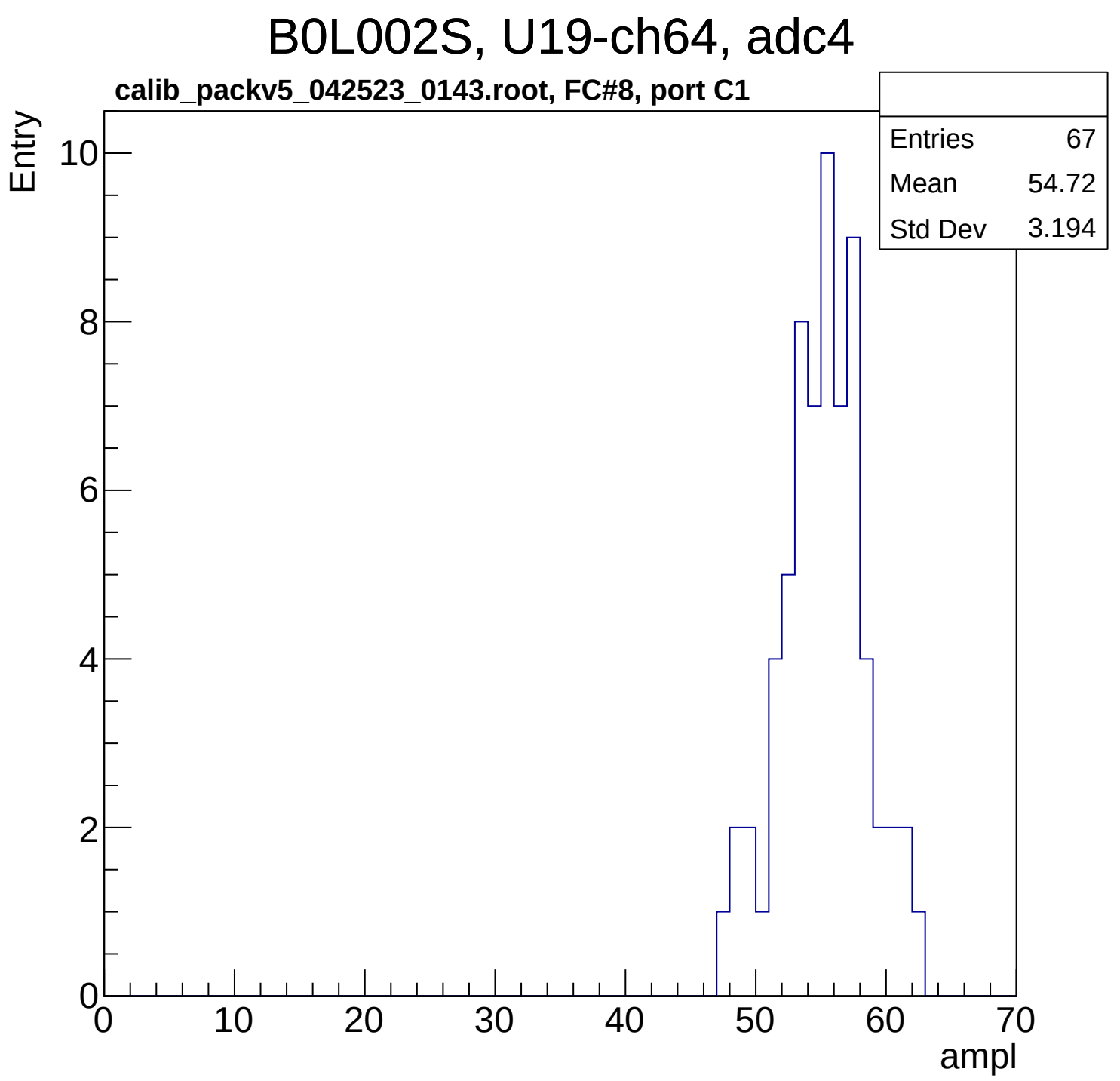

B0L002S, U19-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

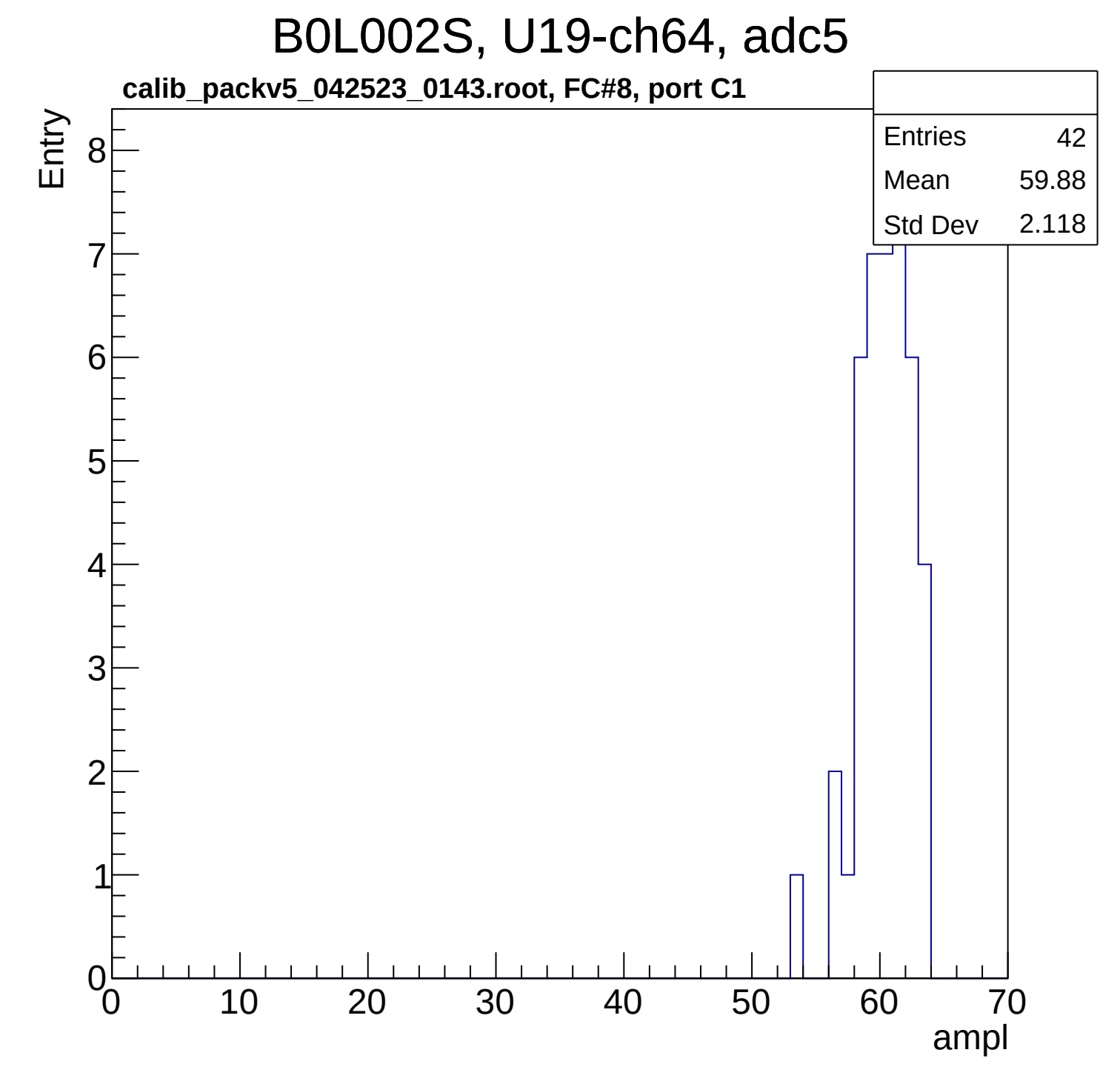

B0L002S, U19-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.49
Std Dev 2.958
Gaus mean : 29.8515
Gaus Width: 2.8974
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

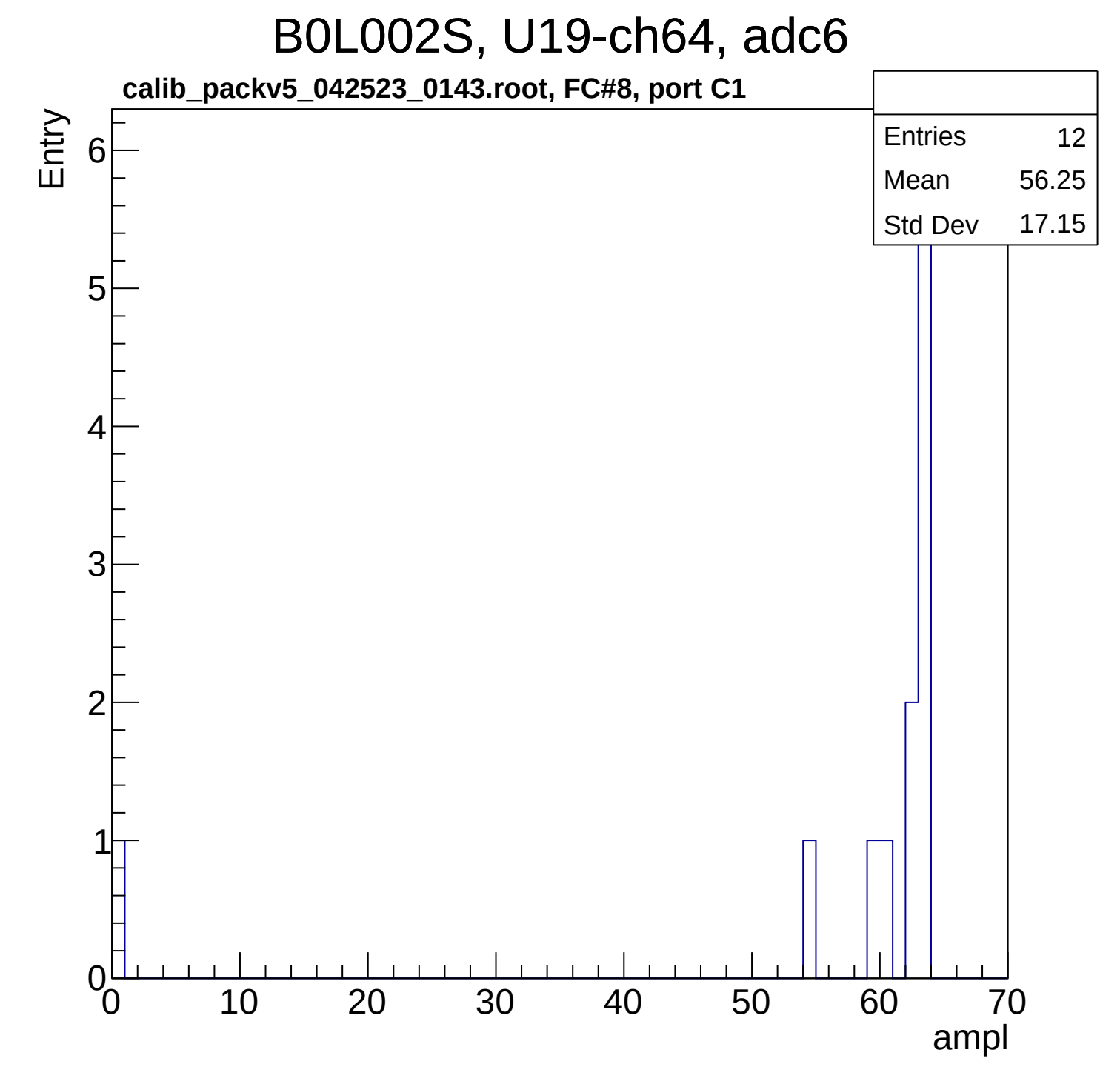

B0L002S, U19-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 35.66
Std Dev 3.322
Gaus mean : 36.0840
Gaus Width: 3.3718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

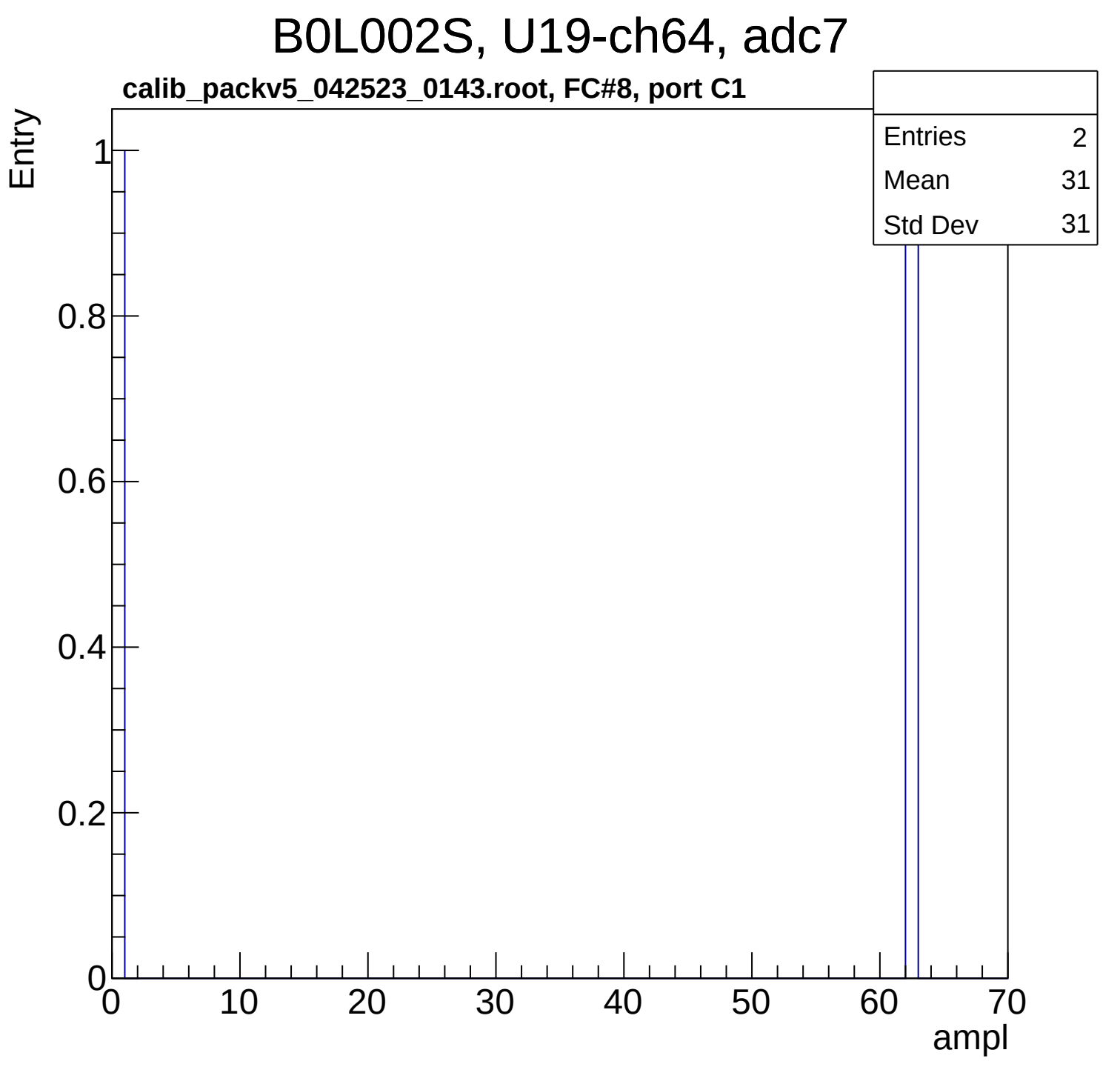

B0L002S, U19-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 42.16
Std Dev 3.1
Gaus mean : 42.8866
Gaus Width: 3.3354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

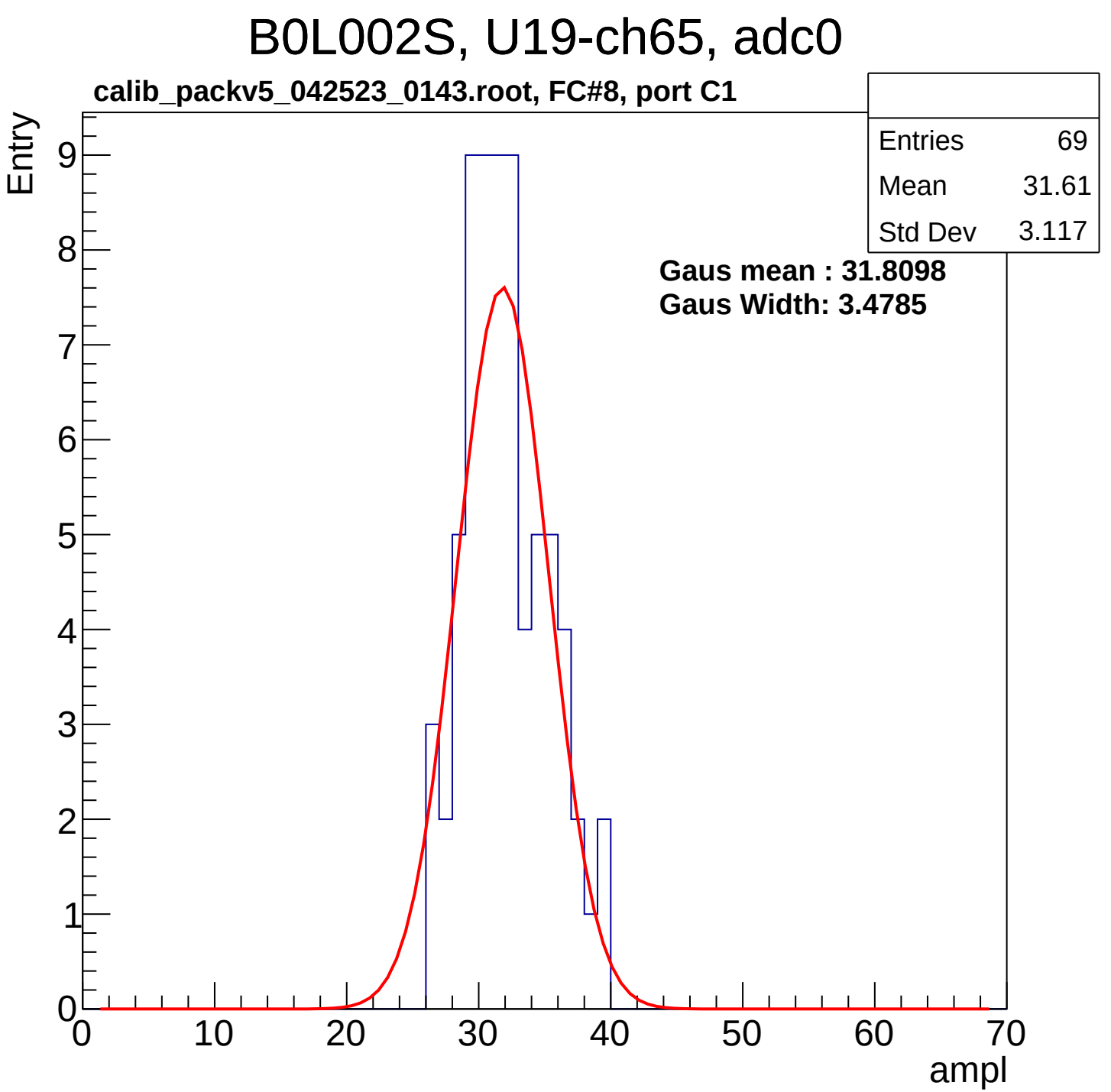

B0L002S, U19-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 48.19
Std Dev 3.157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

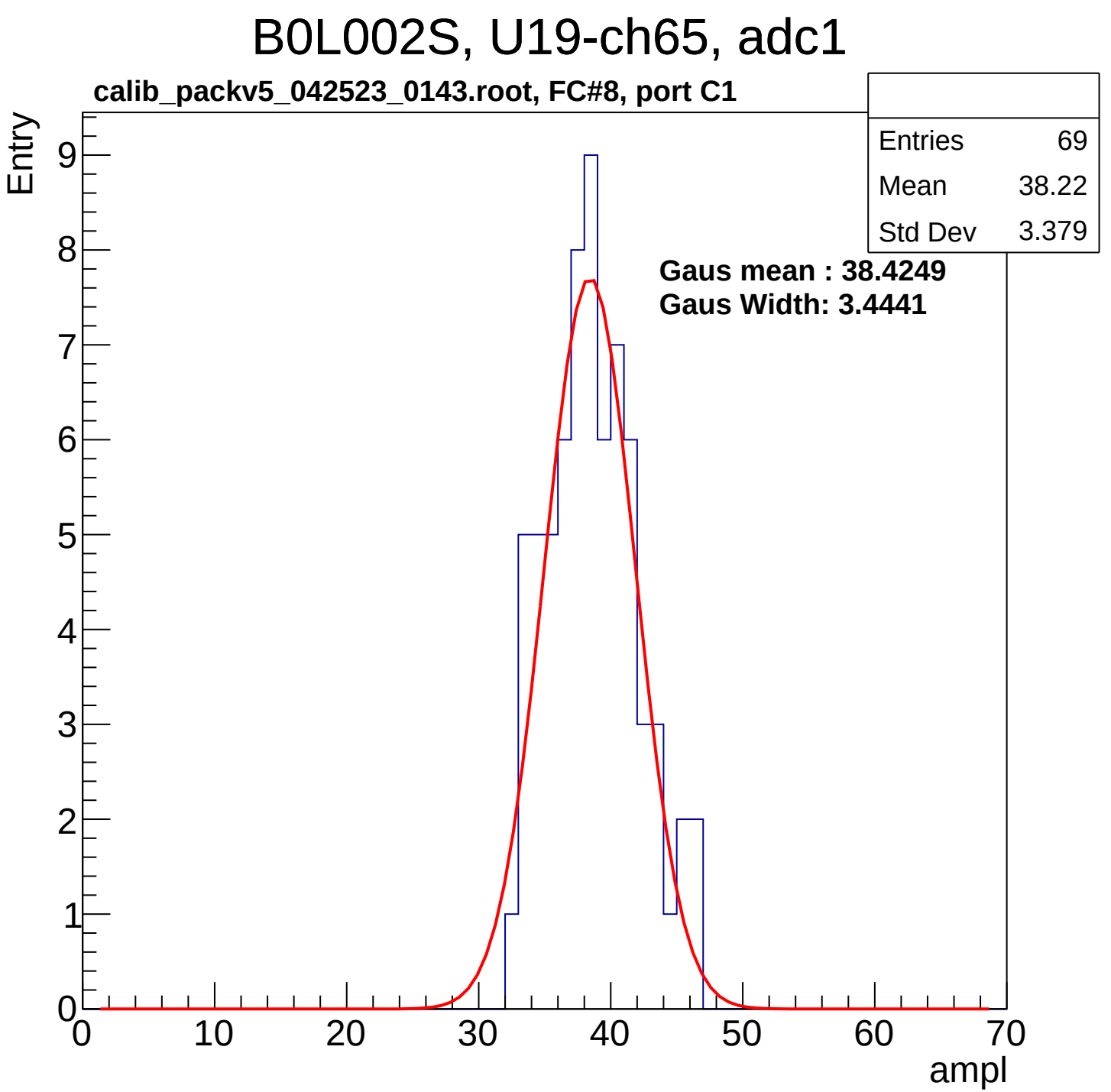

B0L002S, U19-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 54.72
Std Dev 3.194
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

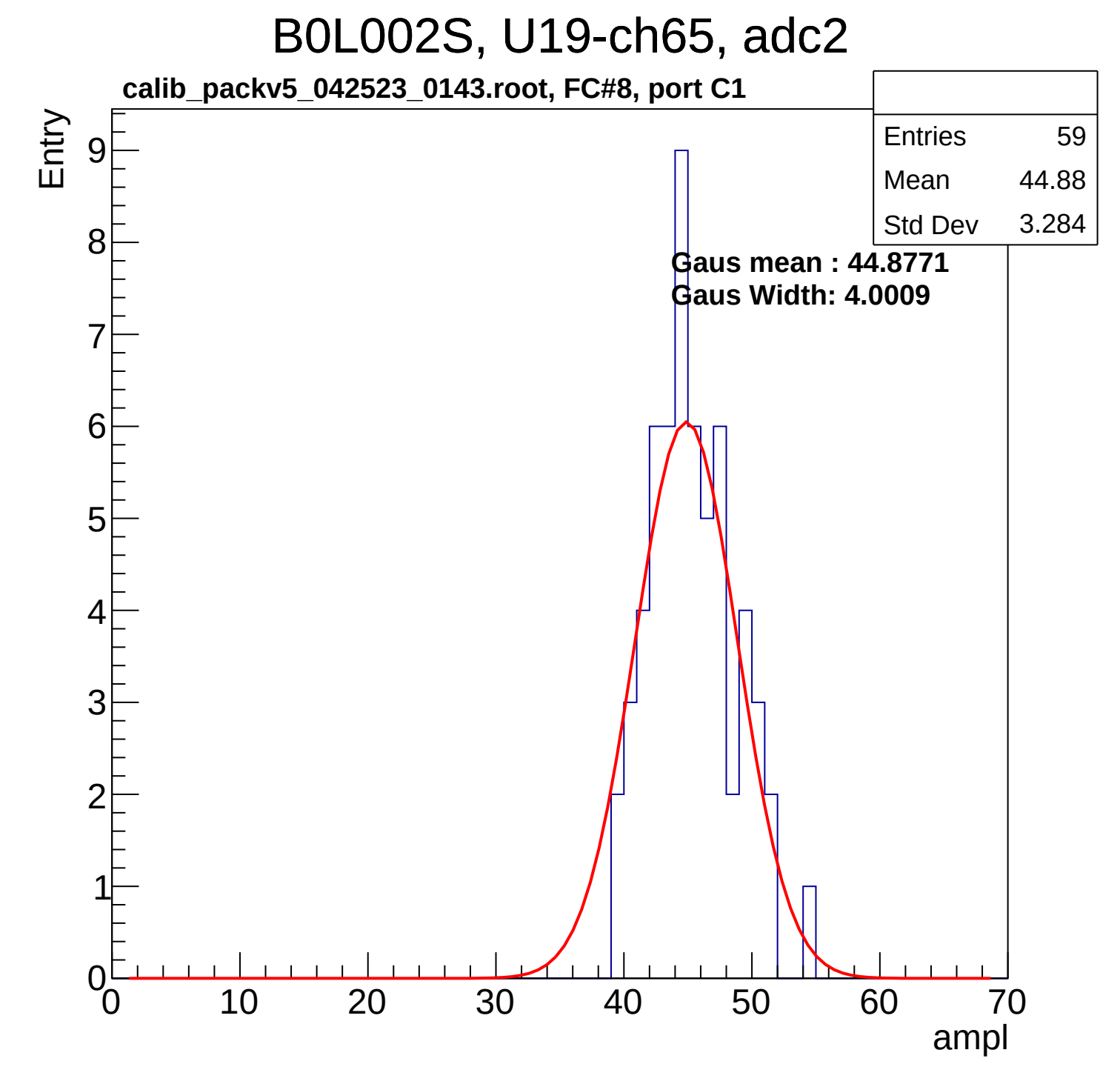

B0L002S, U19-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.88
Std Dev 2.118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

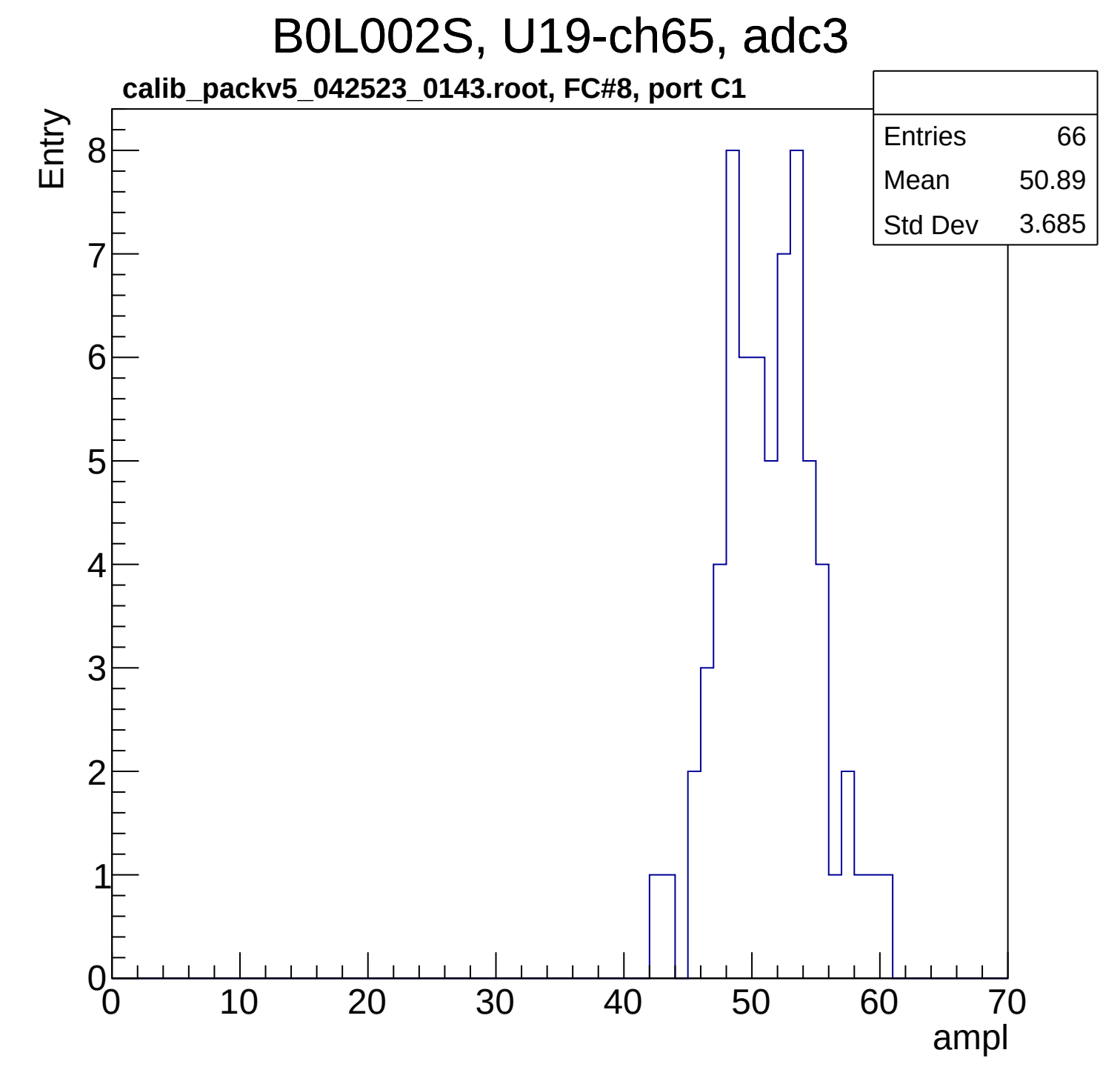

B0L002S, U19-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 56.25
Std Dev 17.15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

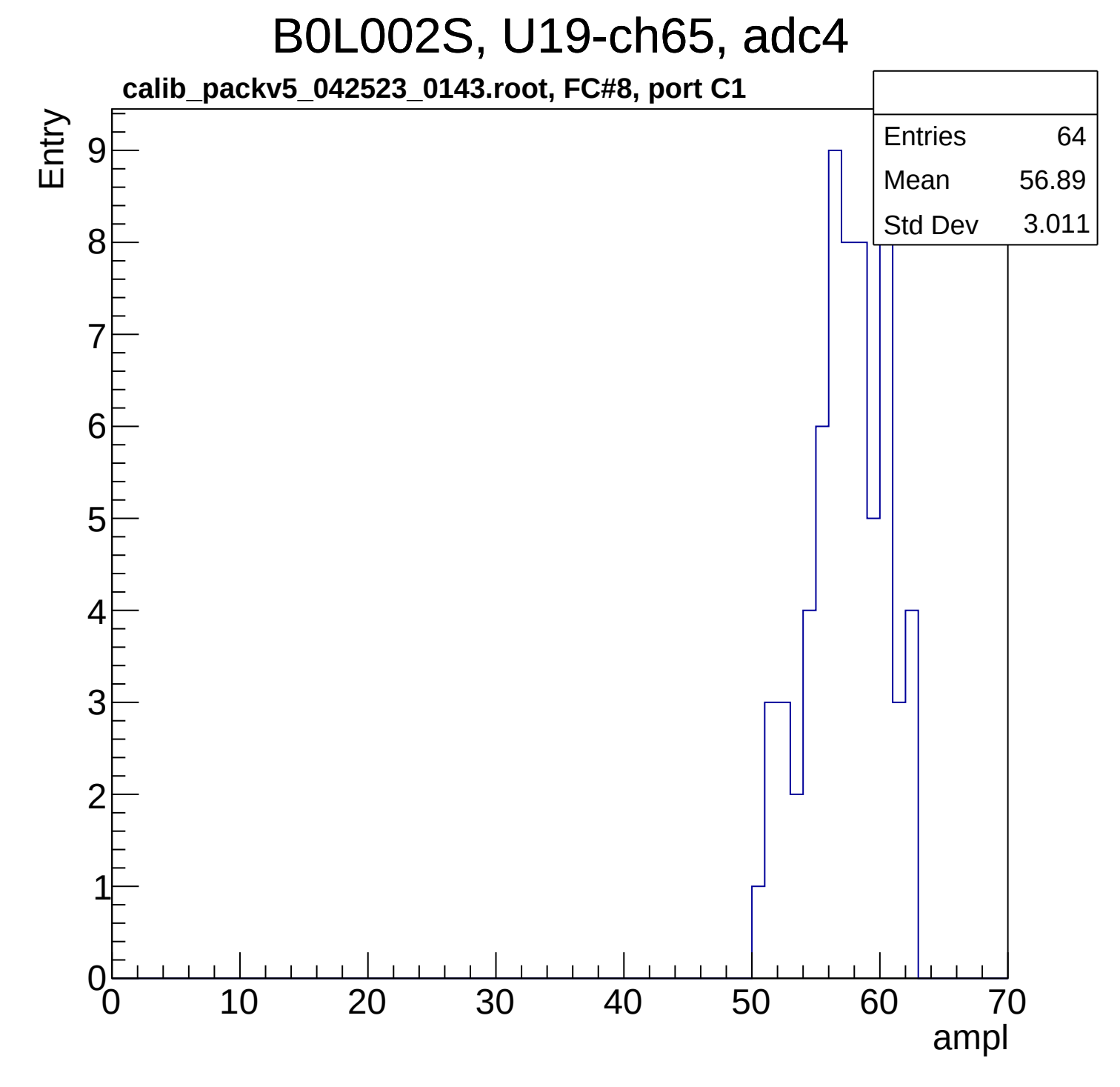

B0L002S, U19-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

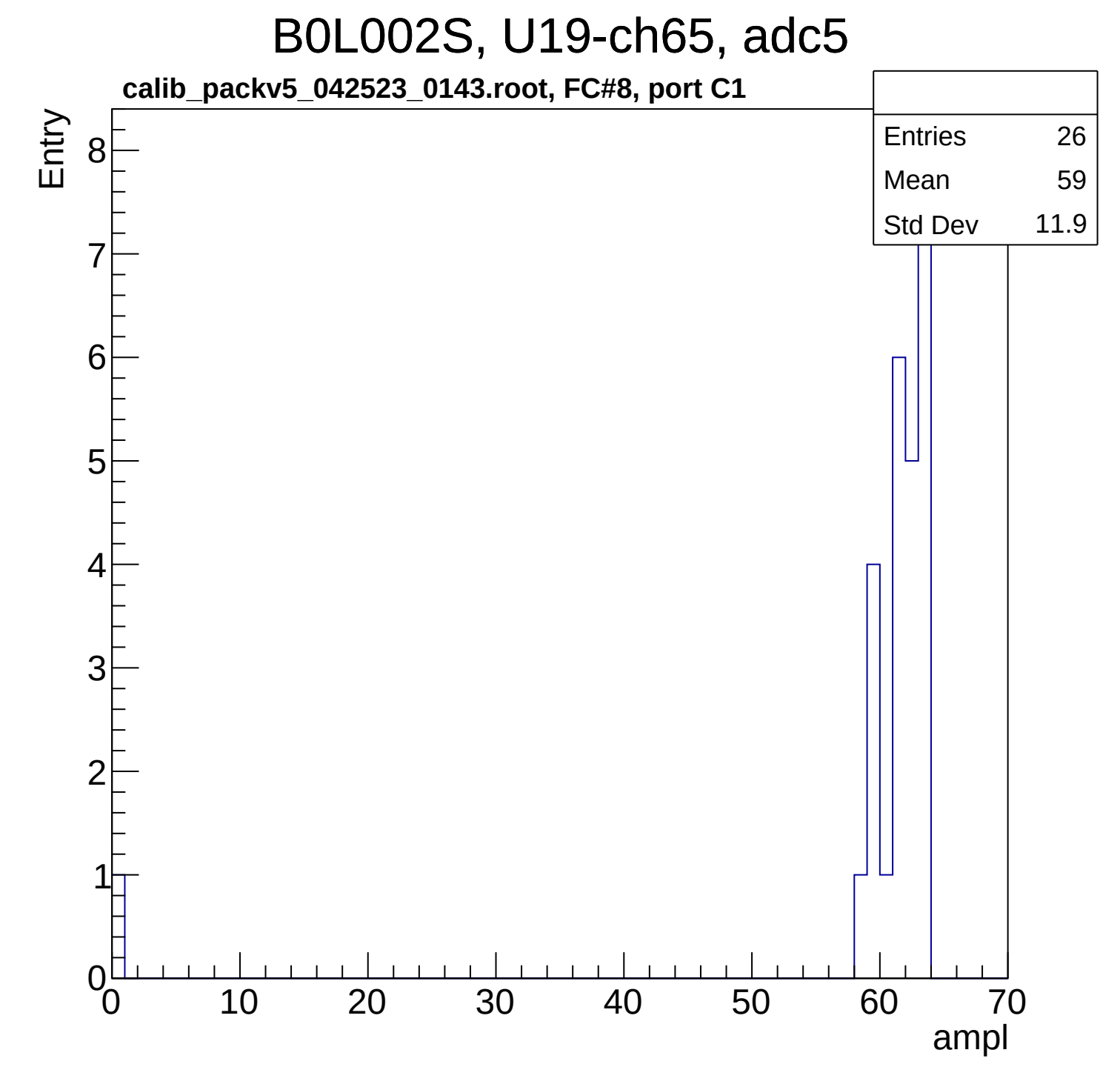

B0L002S, U19-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 31.61
Std Dev 3.117
Gaus mean : 31.8098
Gaus Width: 3.4785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

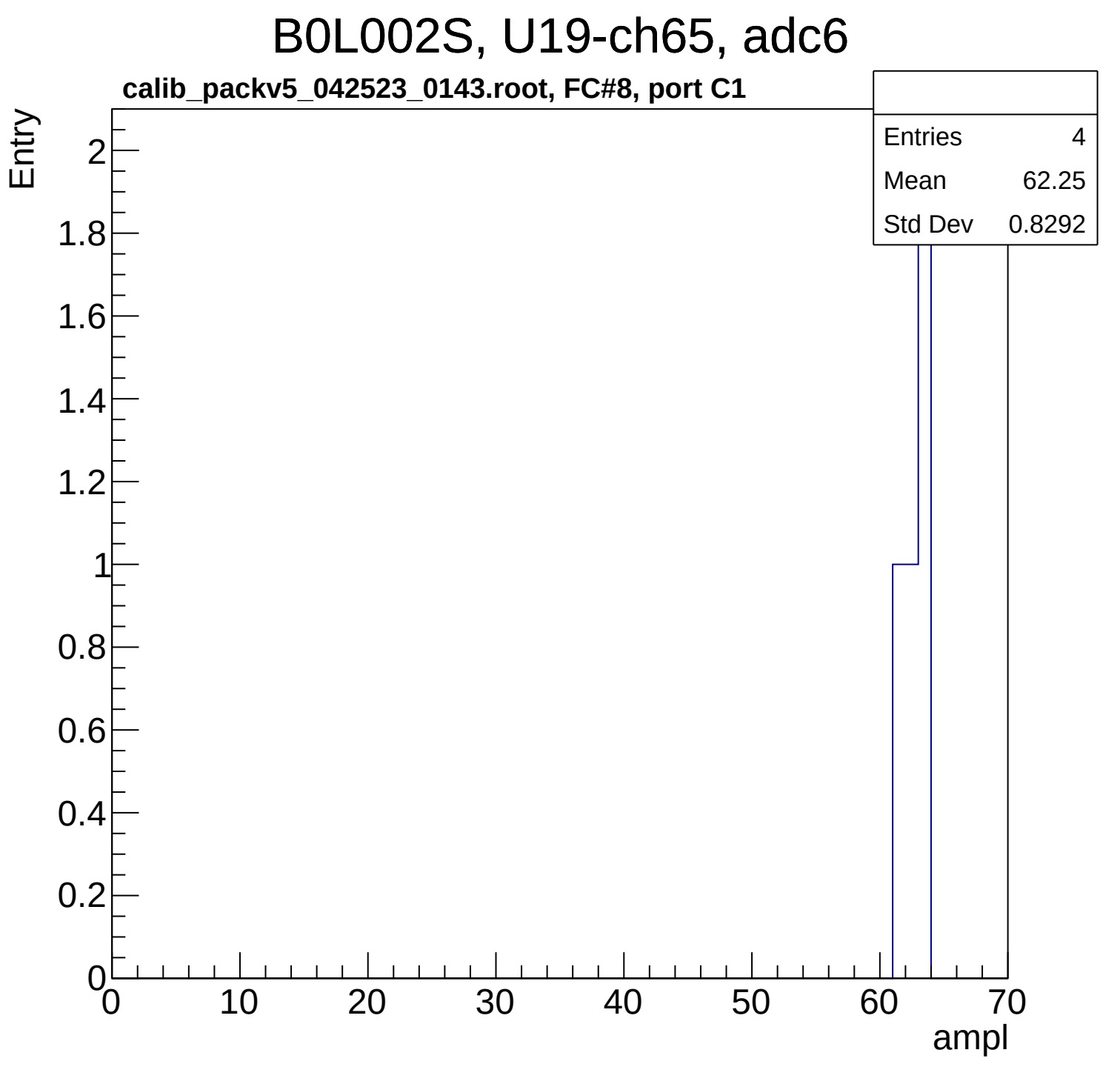

B0L002S, U19-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.22
Std Dev 3.379
Gaus mean : 38.4249
Gaus Width: 3.4441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

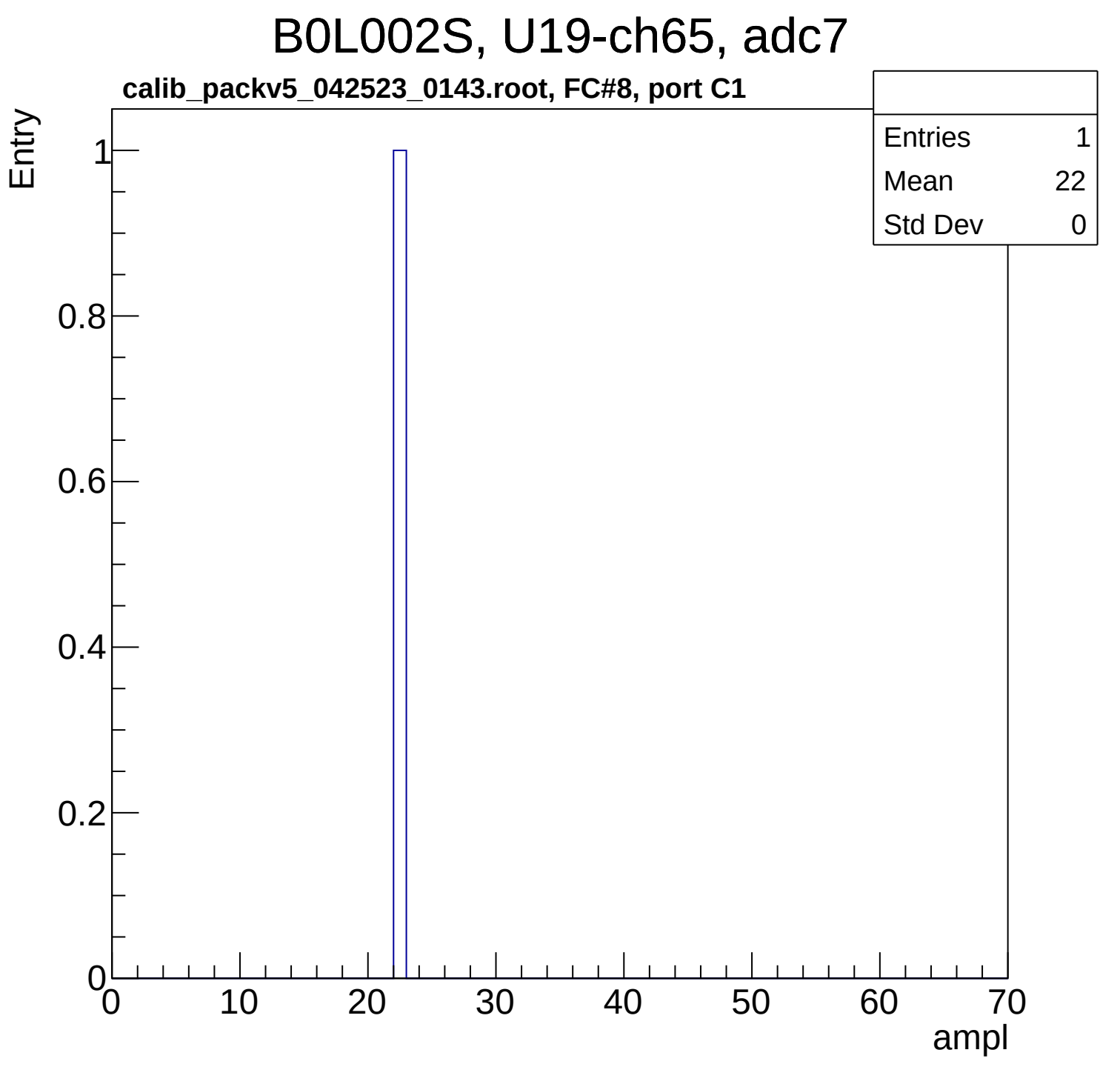

B0L002S, U19-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 44.88
Std Dev 3.284
Gaus mean : 44.8771
Gaus Width: 4.0009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

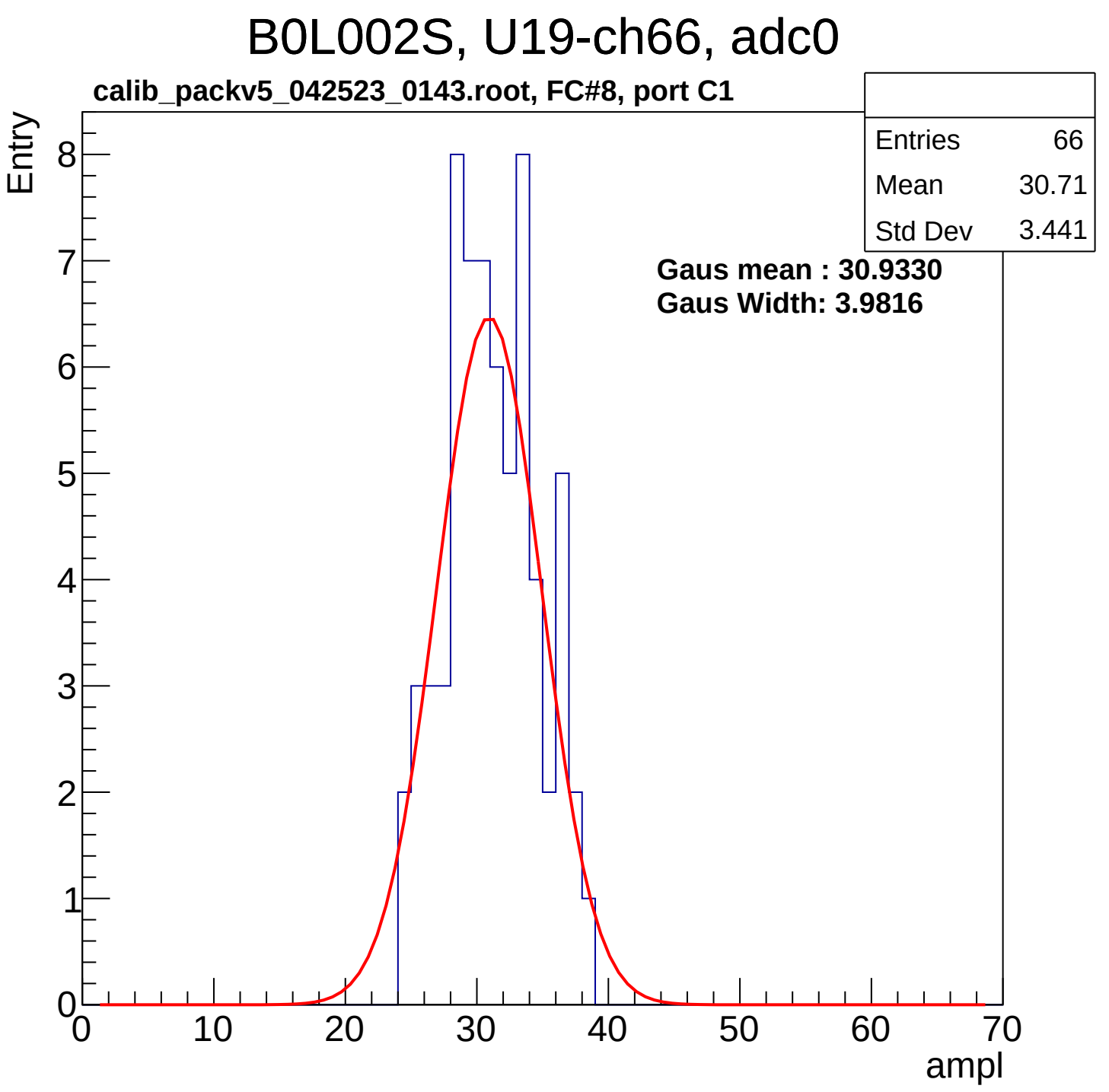

B0L002S, U19-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.89
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

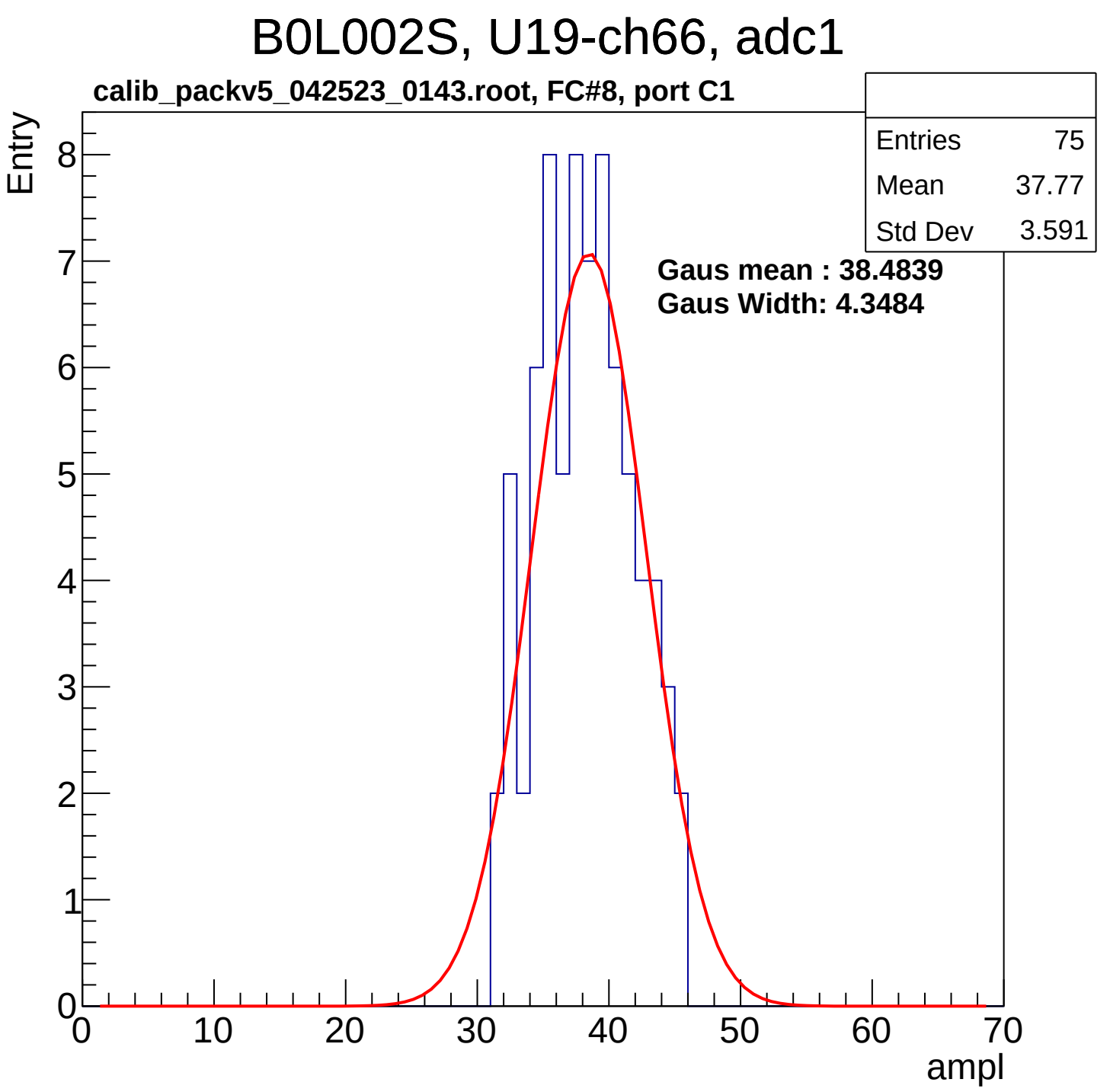

B0L002S, U19-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.89
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

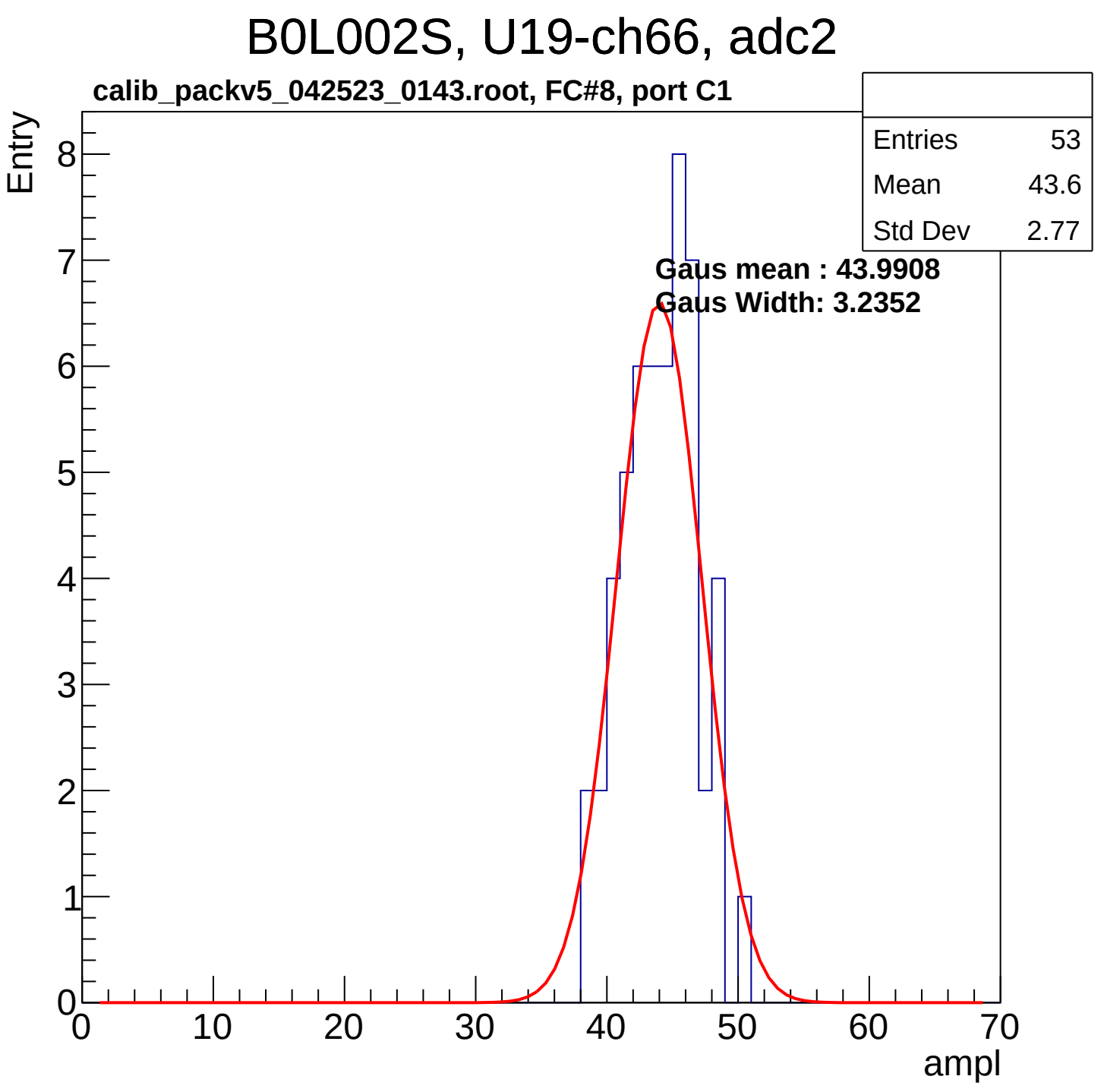

B0L002S, U19-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

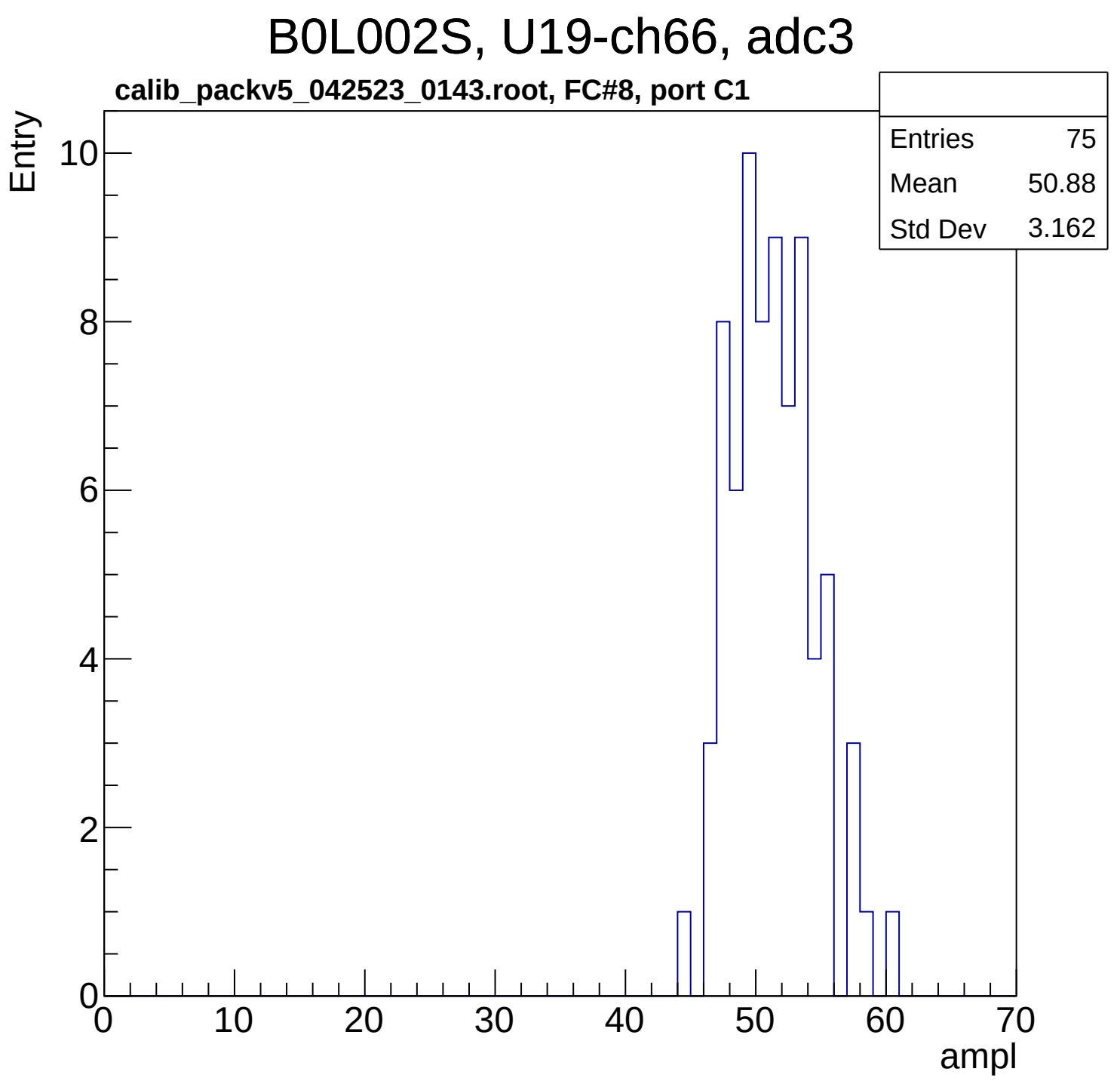

B0L002S, U19-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

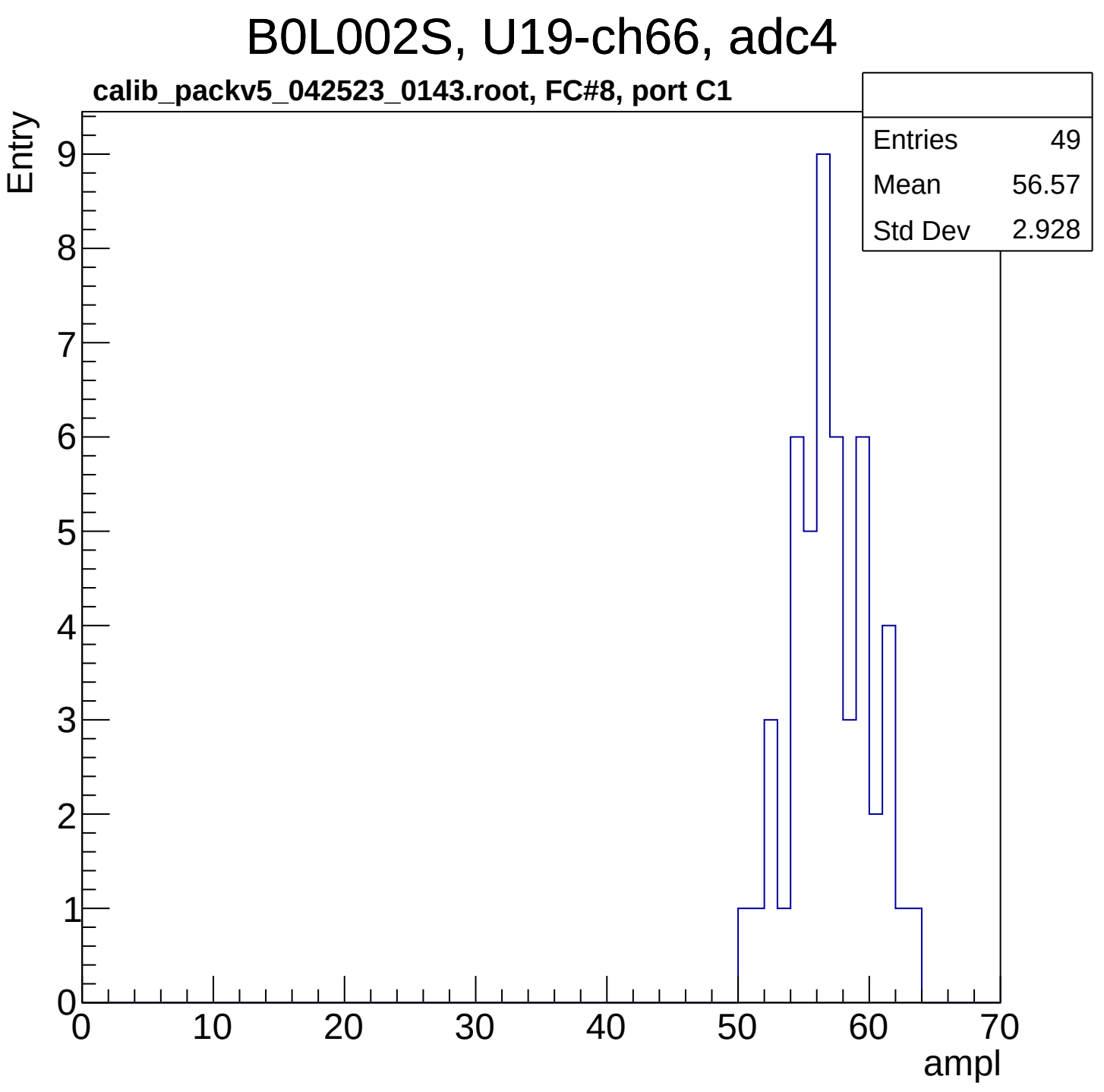

B0L002S, U19-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

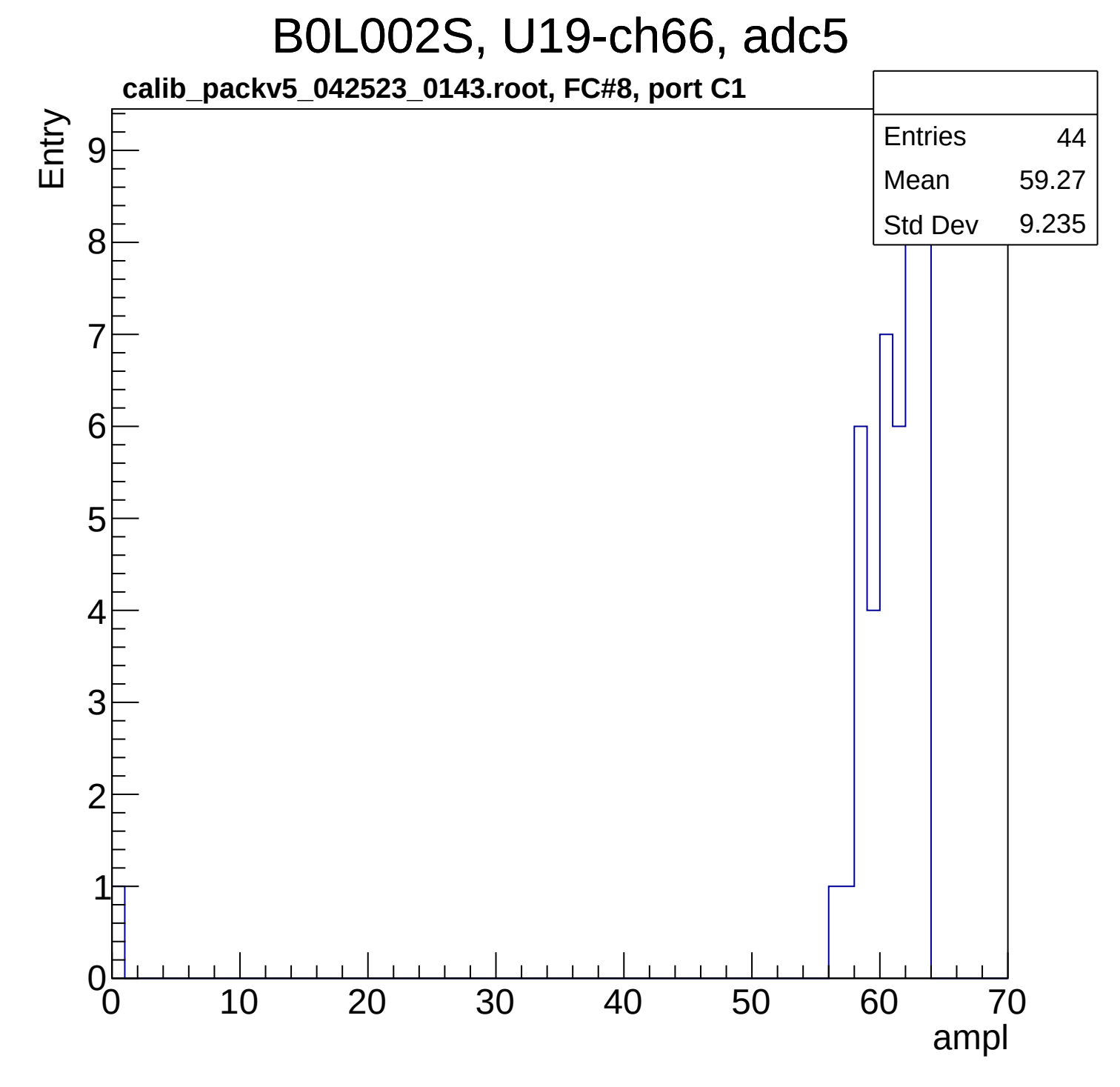

B0L002S, U19-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.71
Std Dev 3.441
Gaus mean : 30.9330
Gaus Width: 3.9816
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

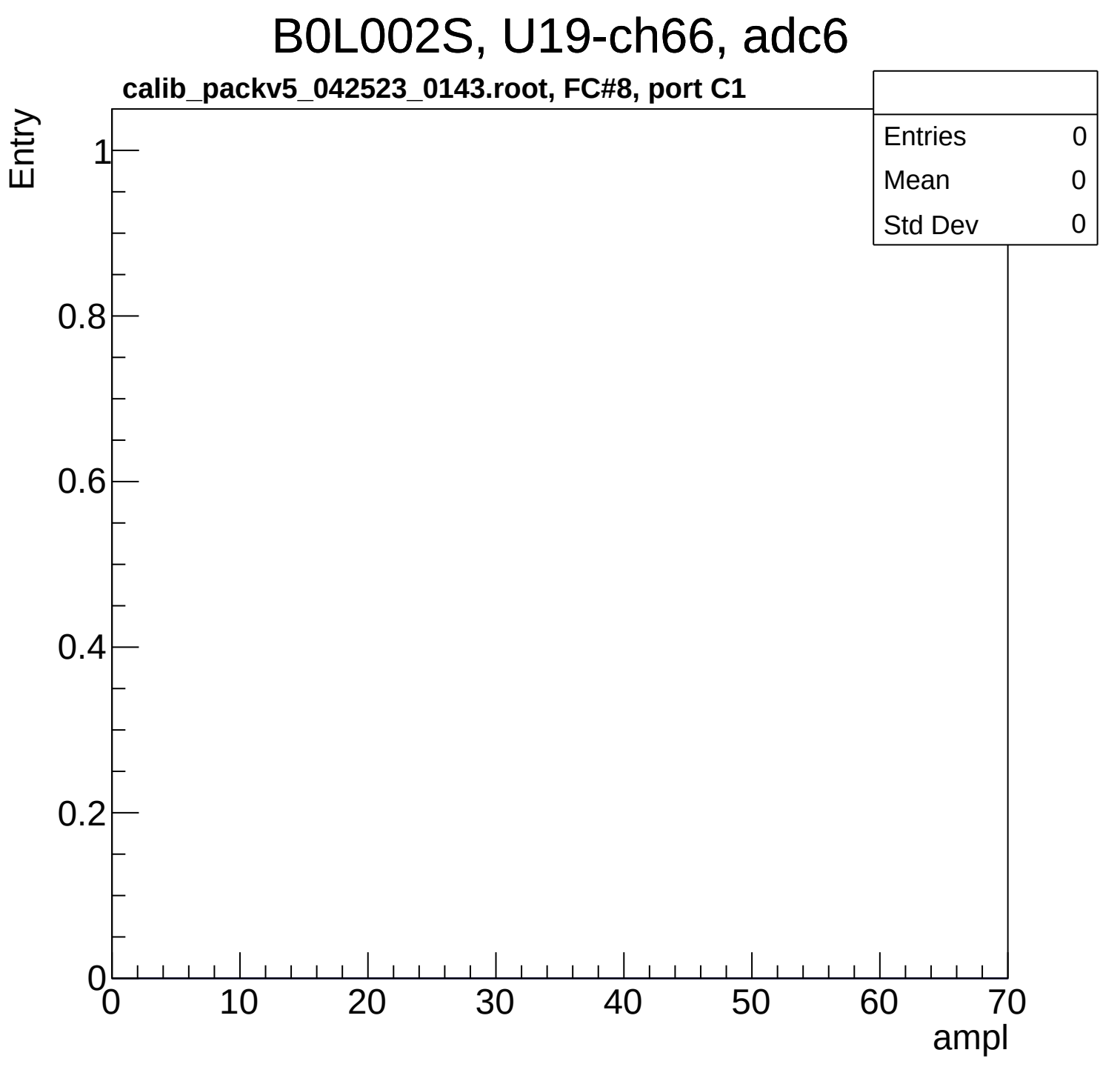

B0L002S, U19-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.77
Std Dev 3.591
Gaus mean : 38.4839
Gaus Width: 4.3484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

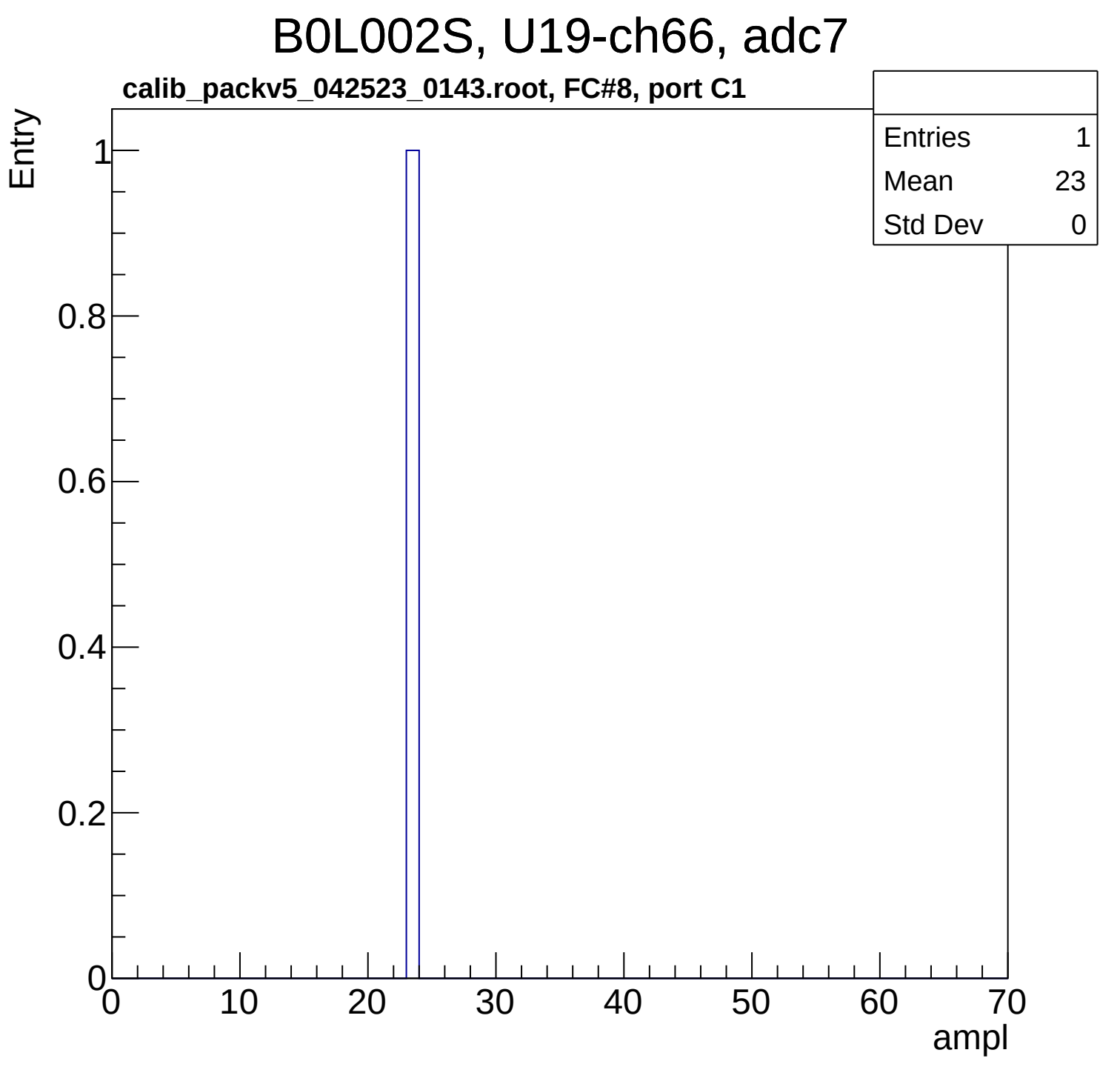

B0L002S, U19-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 43.6
Std Dev 2.77
Gaus mean : 43.9908
Gaus Width: 3.2352
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

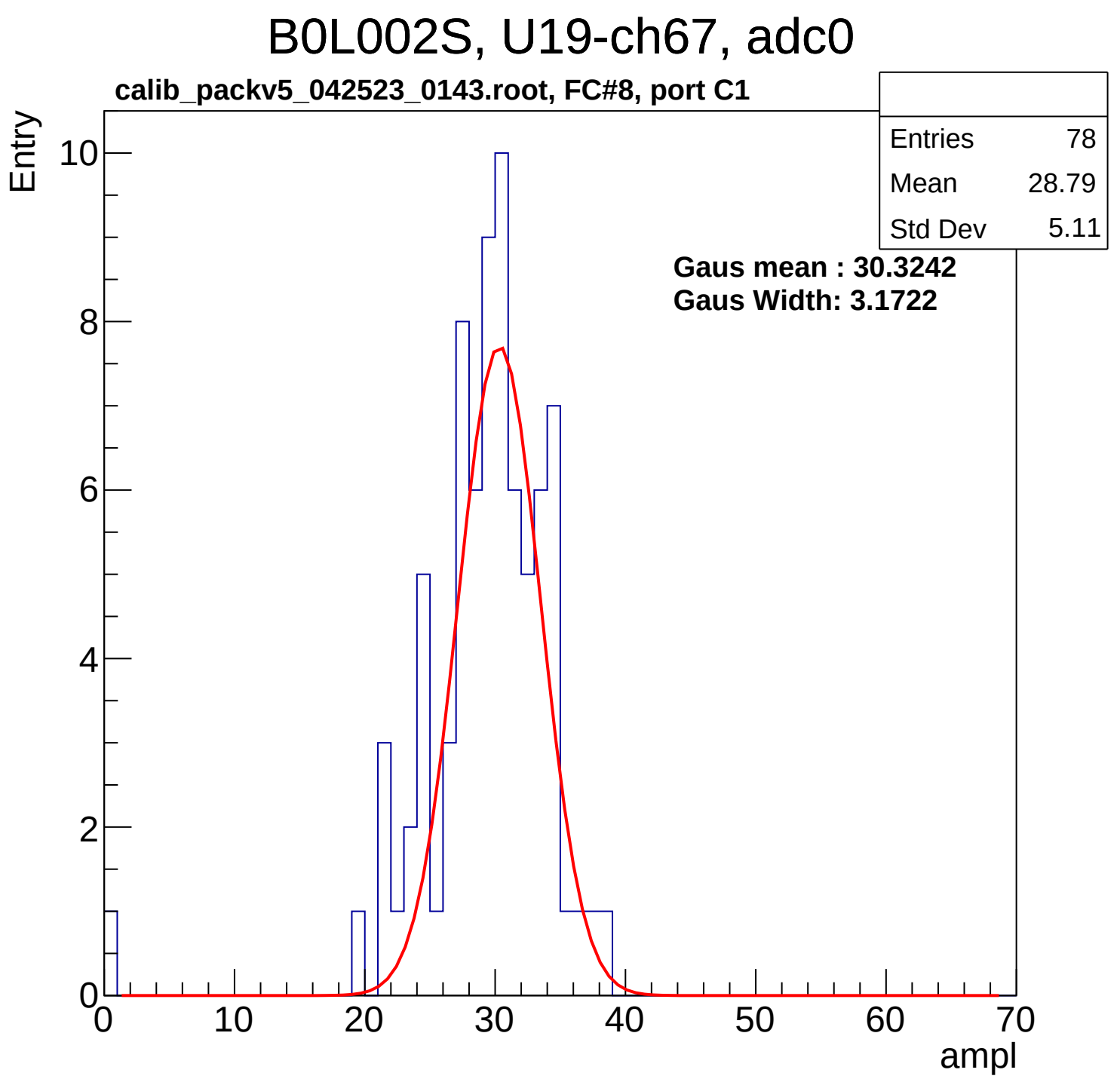

B0L002S, U19-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.88
Std Dev 3.162
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

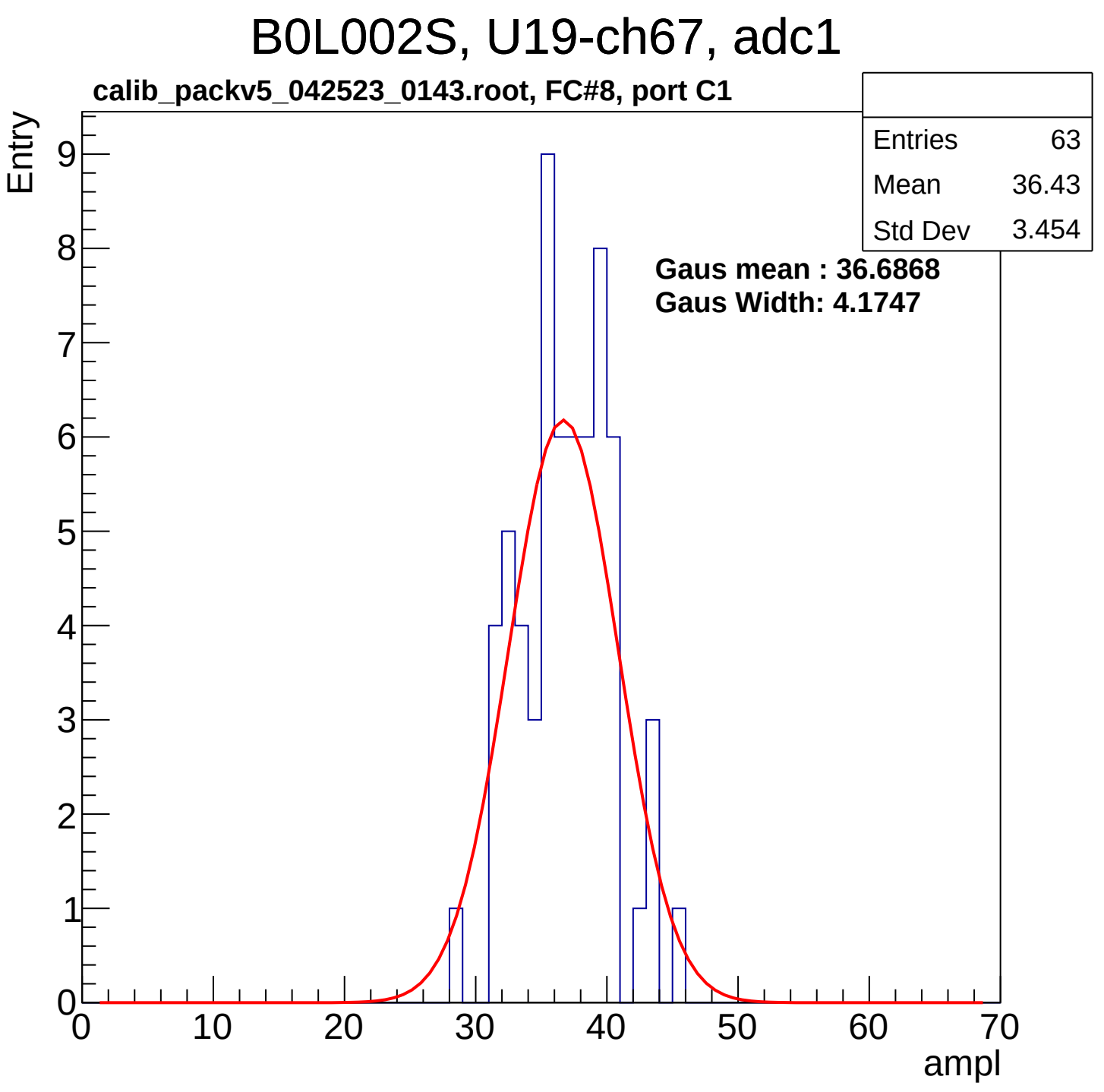

B0L002S, U19-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 56.57
Std Dev 2.928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

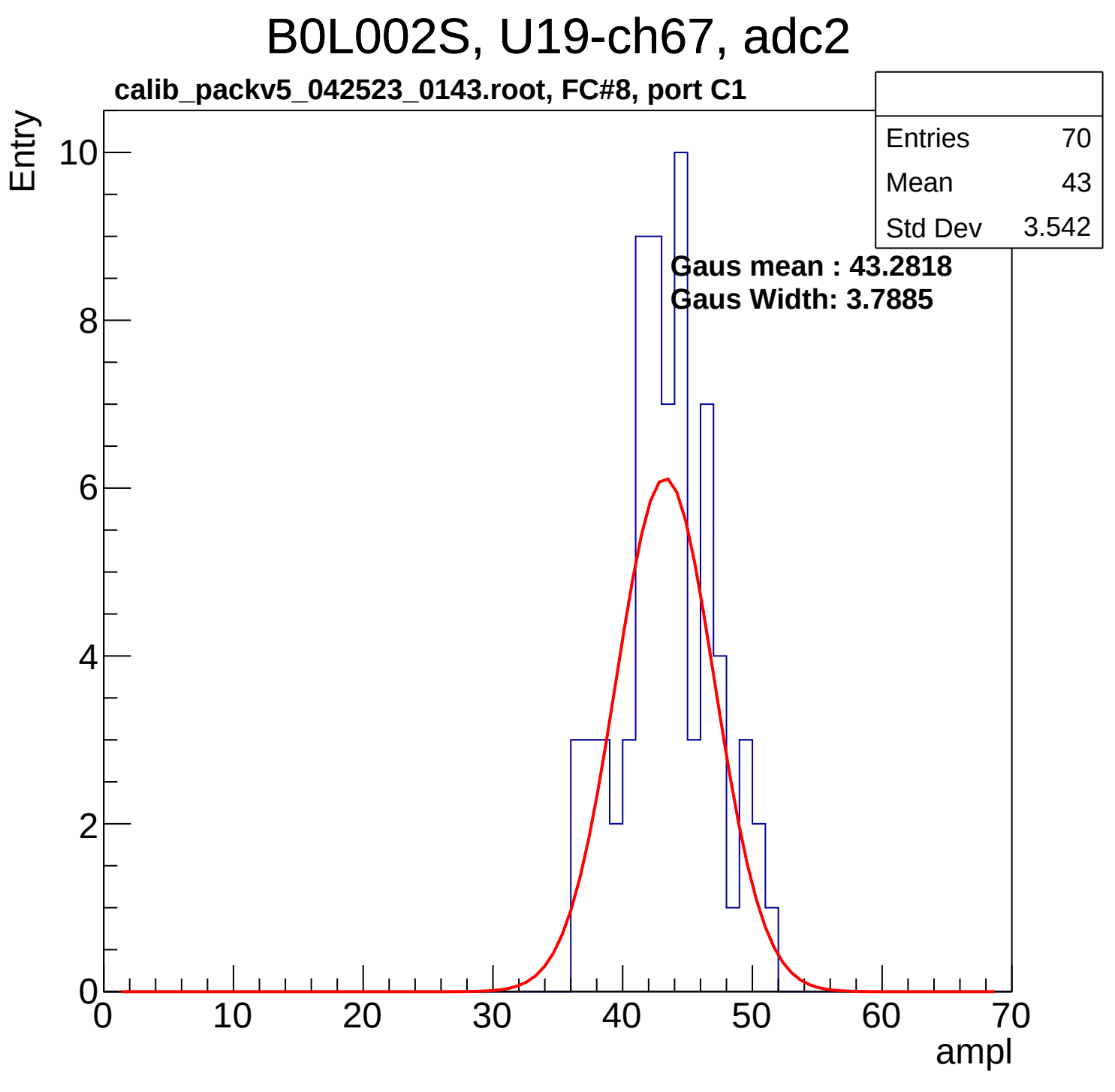

B0L002S, U19-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.27
Std Dev 9.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

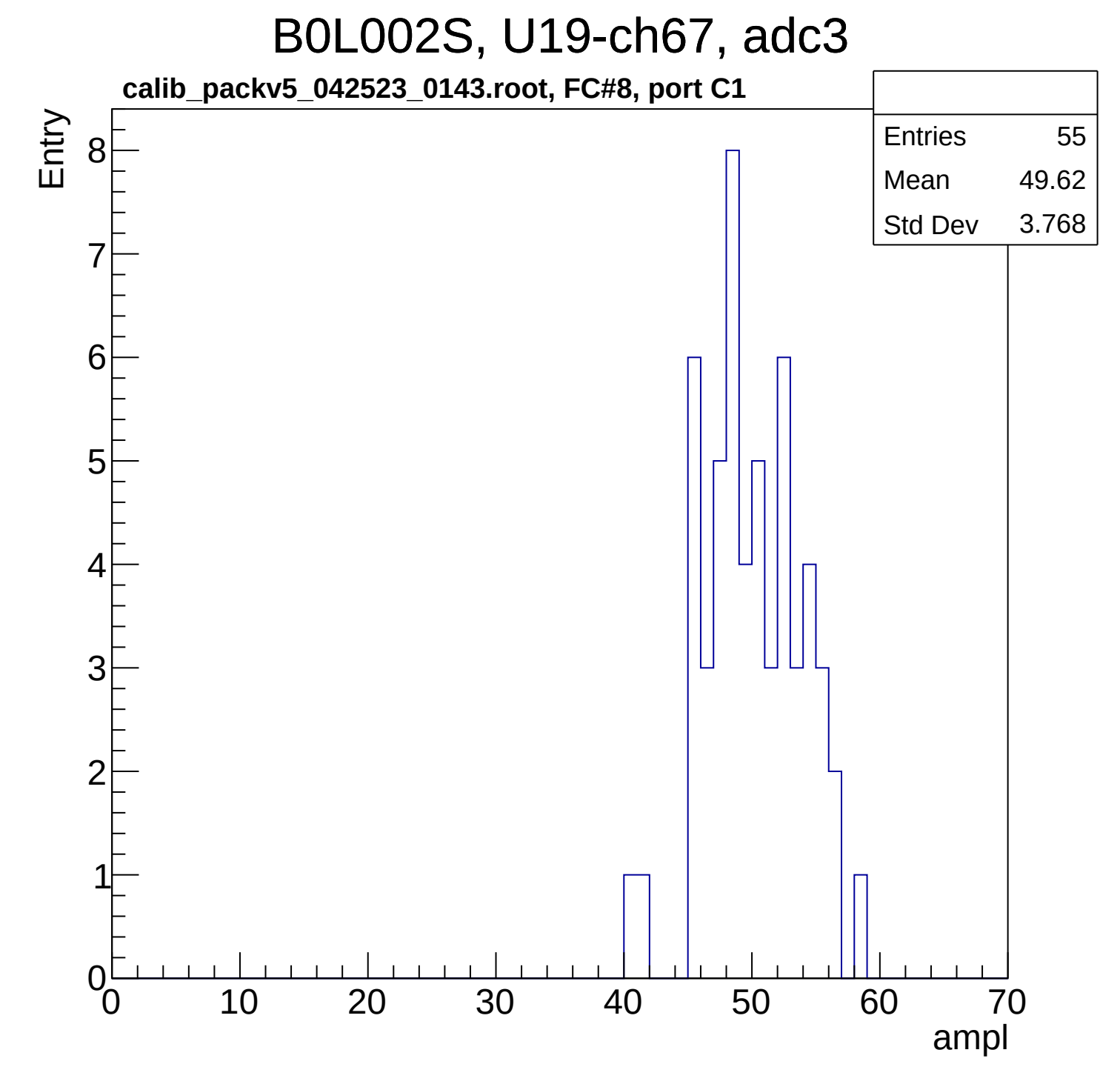

B0L002S, U19-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

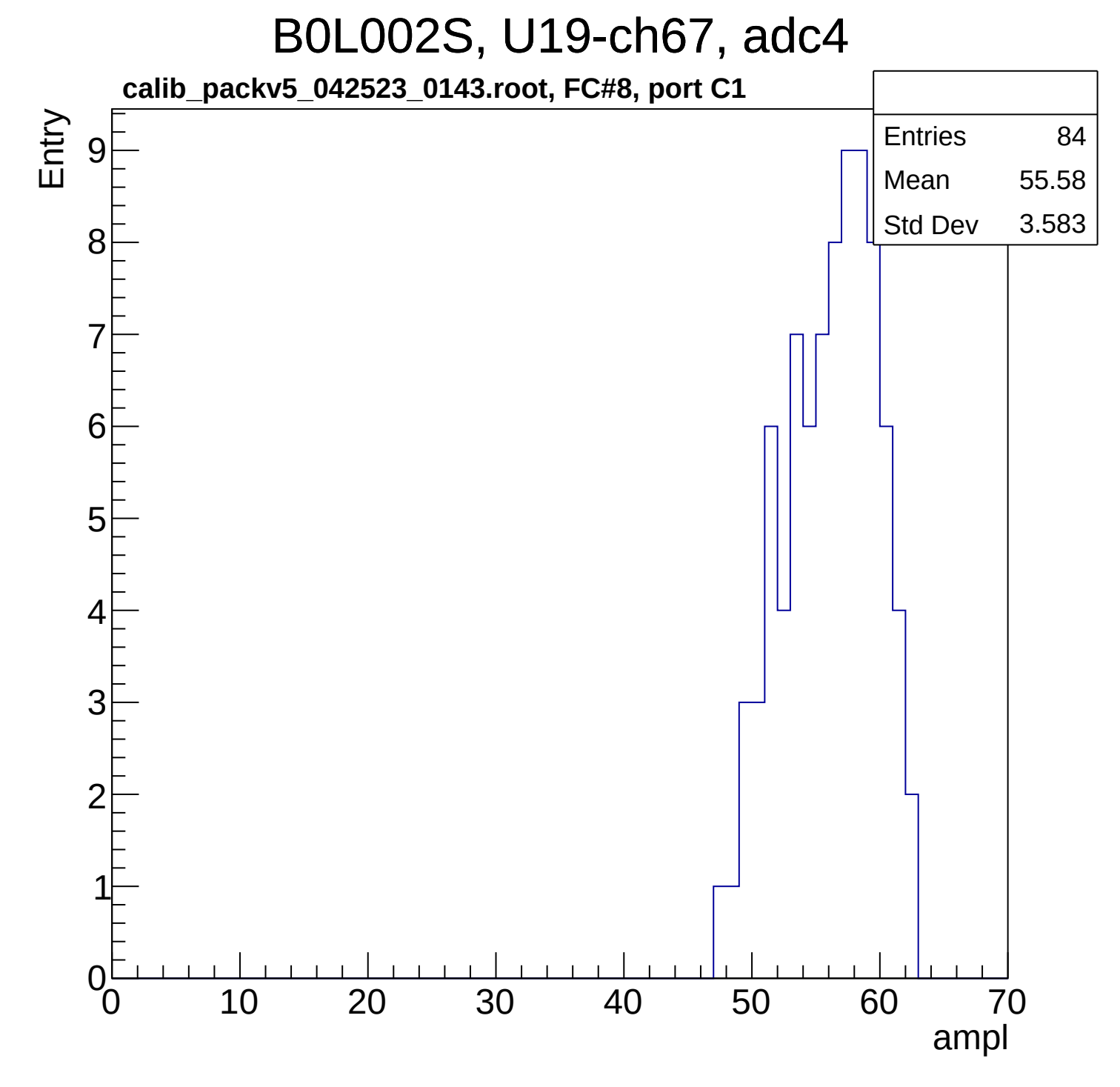

B0L002S, U19-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

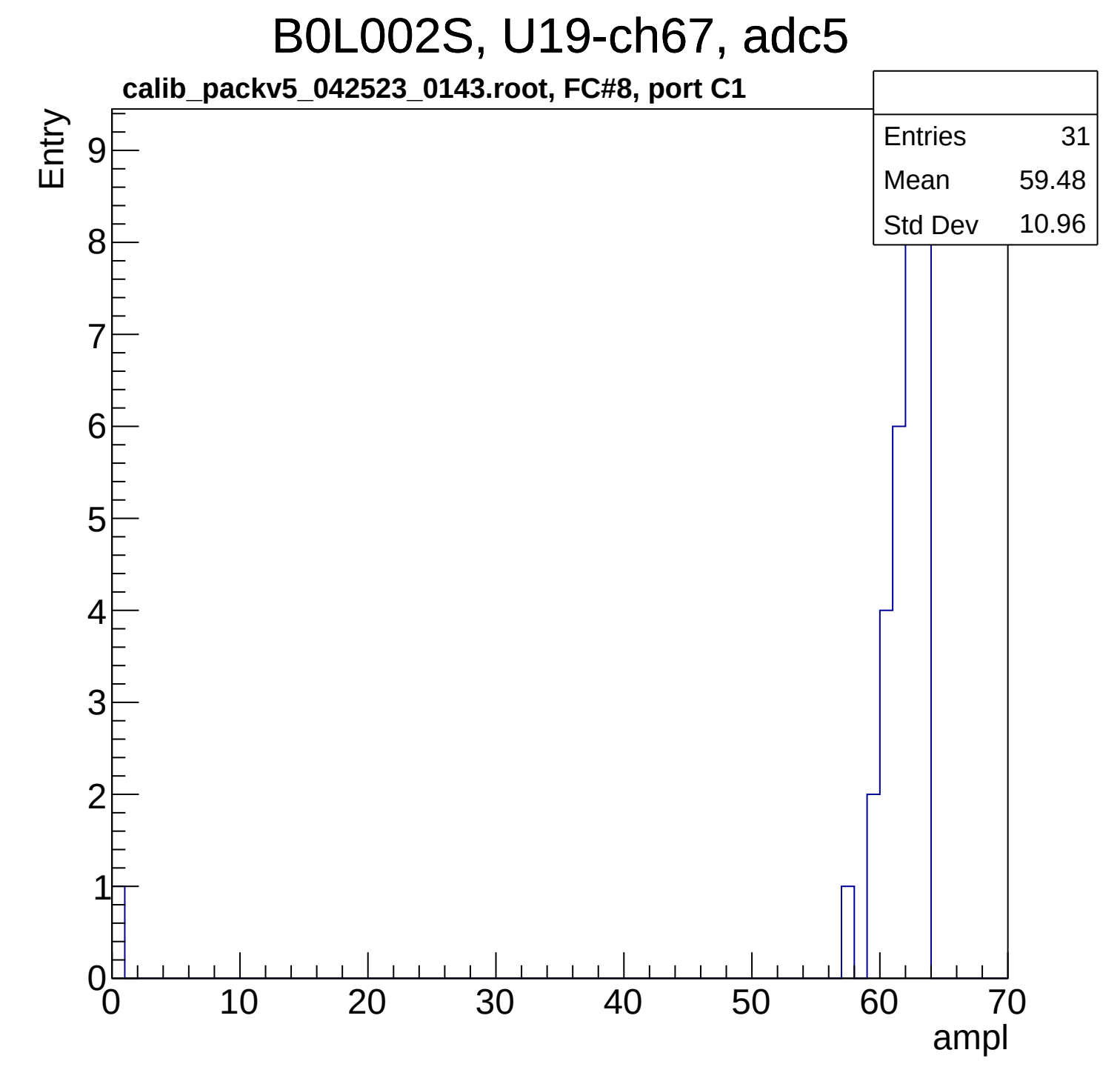

B0L002S, U19-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 28.79
Std Dev 5.11
Gaus mean : 30.3242
Gaus Width: 3.1722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

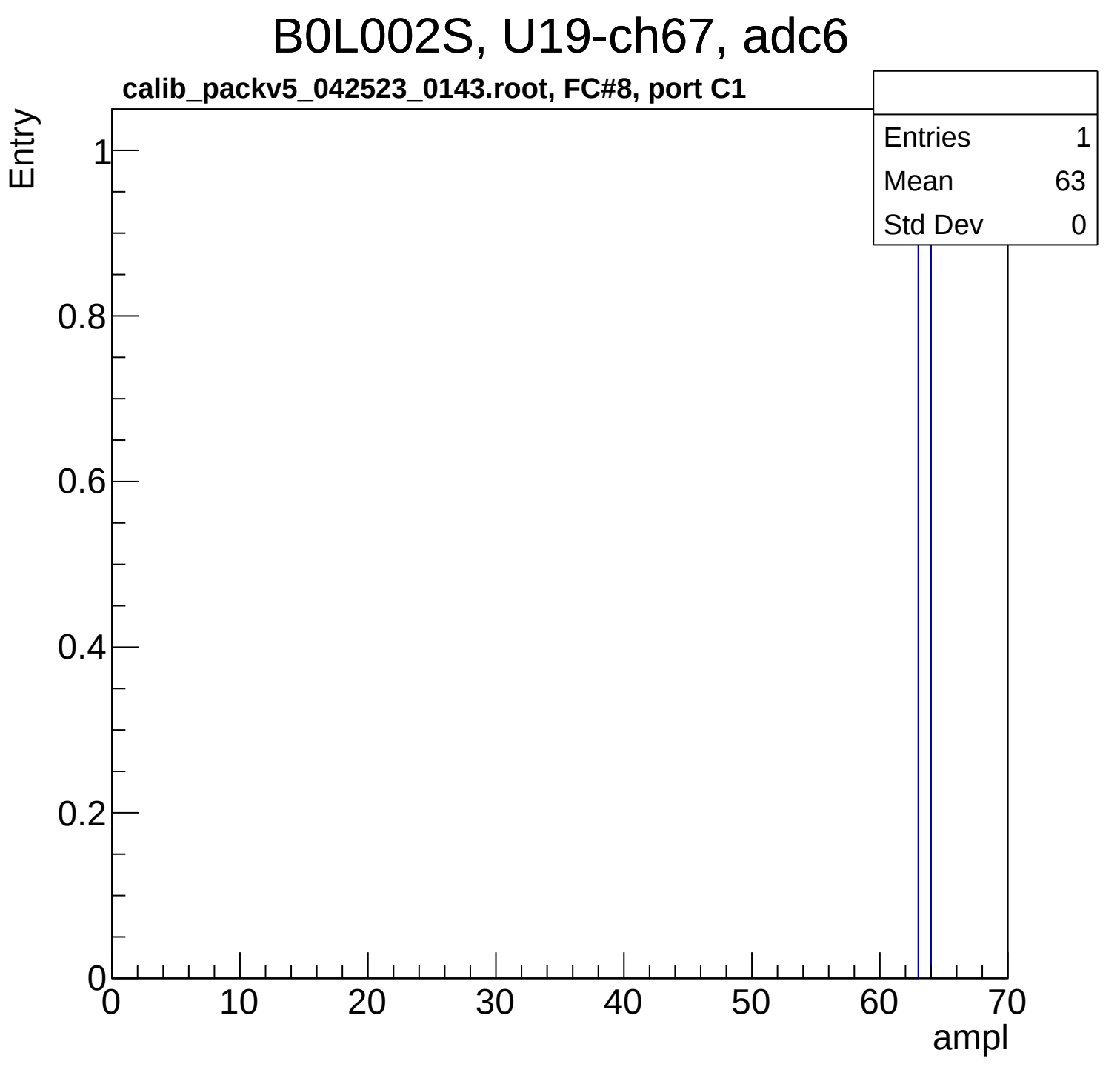

B0L002S, U19-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 36.43
Std Dev 3.454
Gaus mean : 36.6868
Gaus Width: 4.1747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

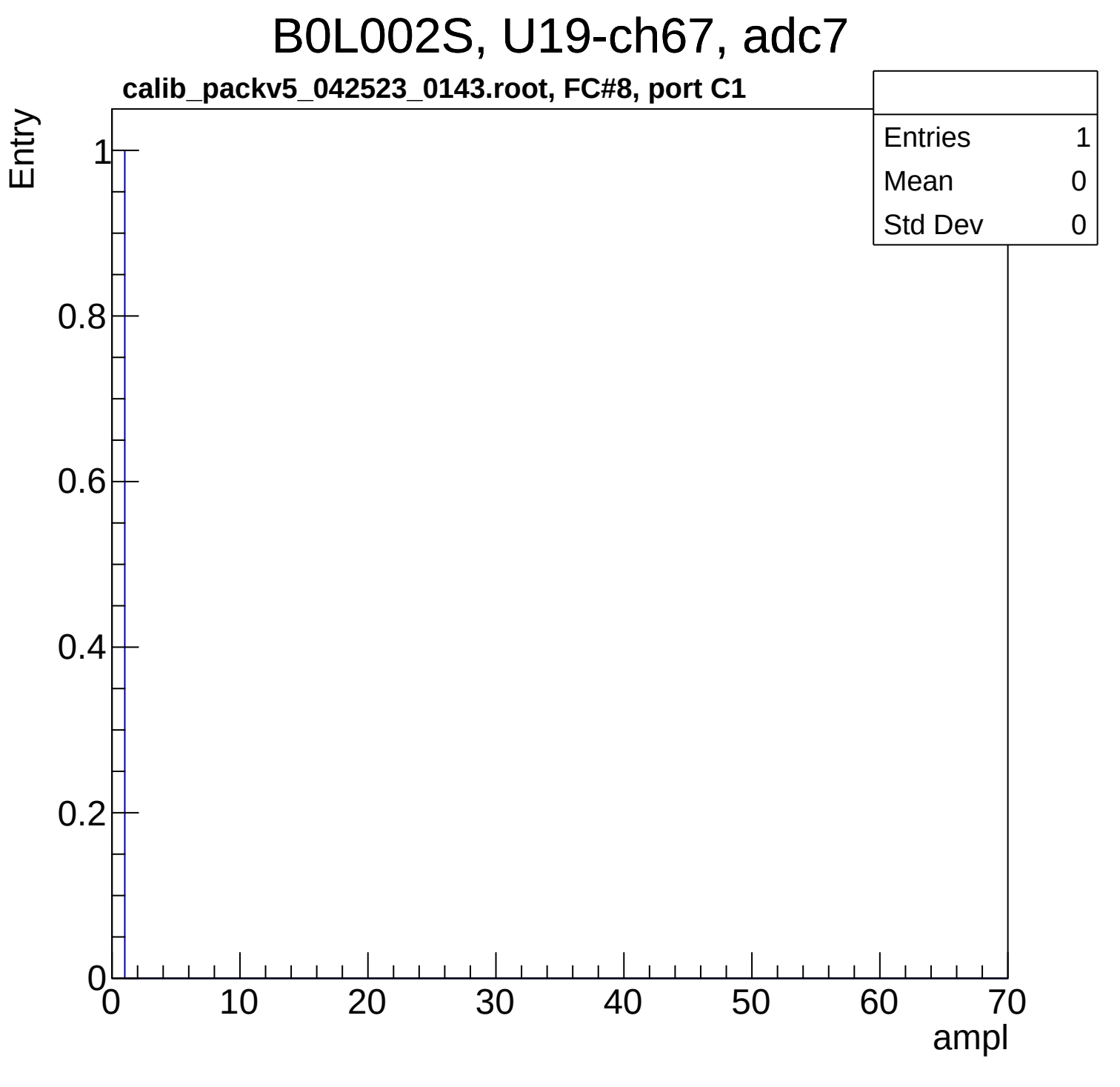

B0L002S, U19-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43
Std Dev 3.542
Gaus mean : 43.2818
Gaus Width: 3.7885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

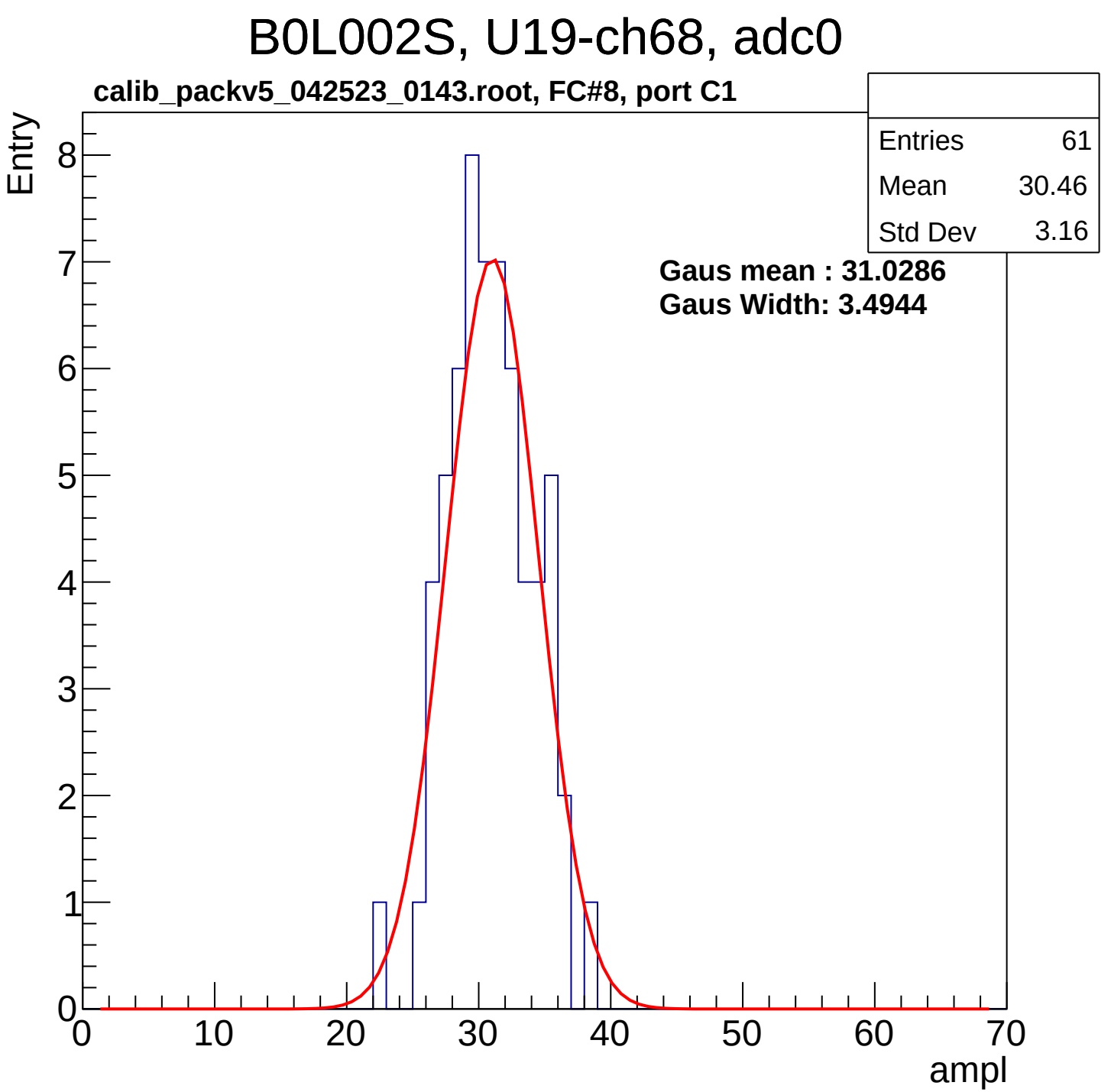

B0L002S, U19-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 49.62
Std Dev 3.768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

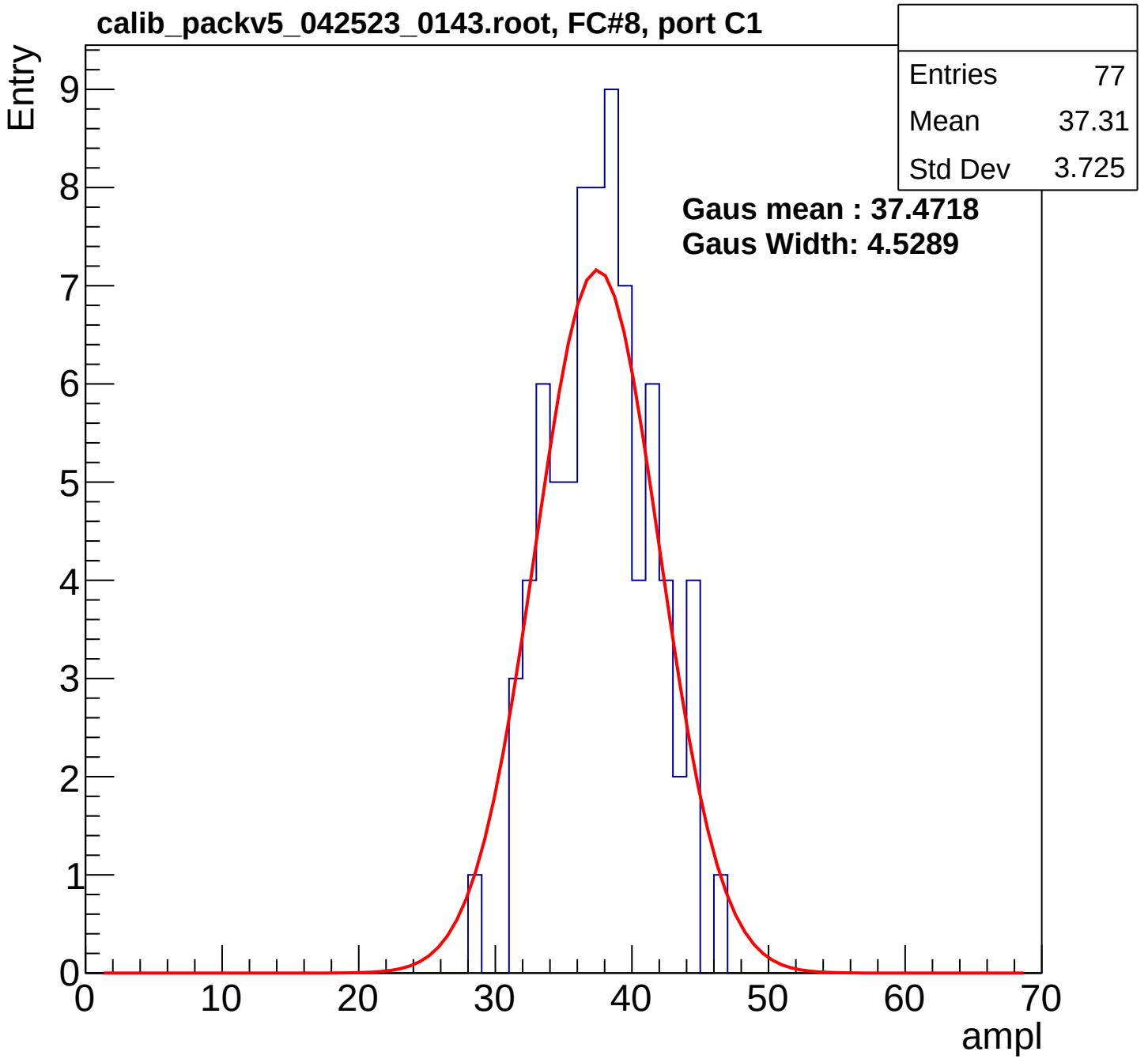

B0L002S, U19-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 55.58
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

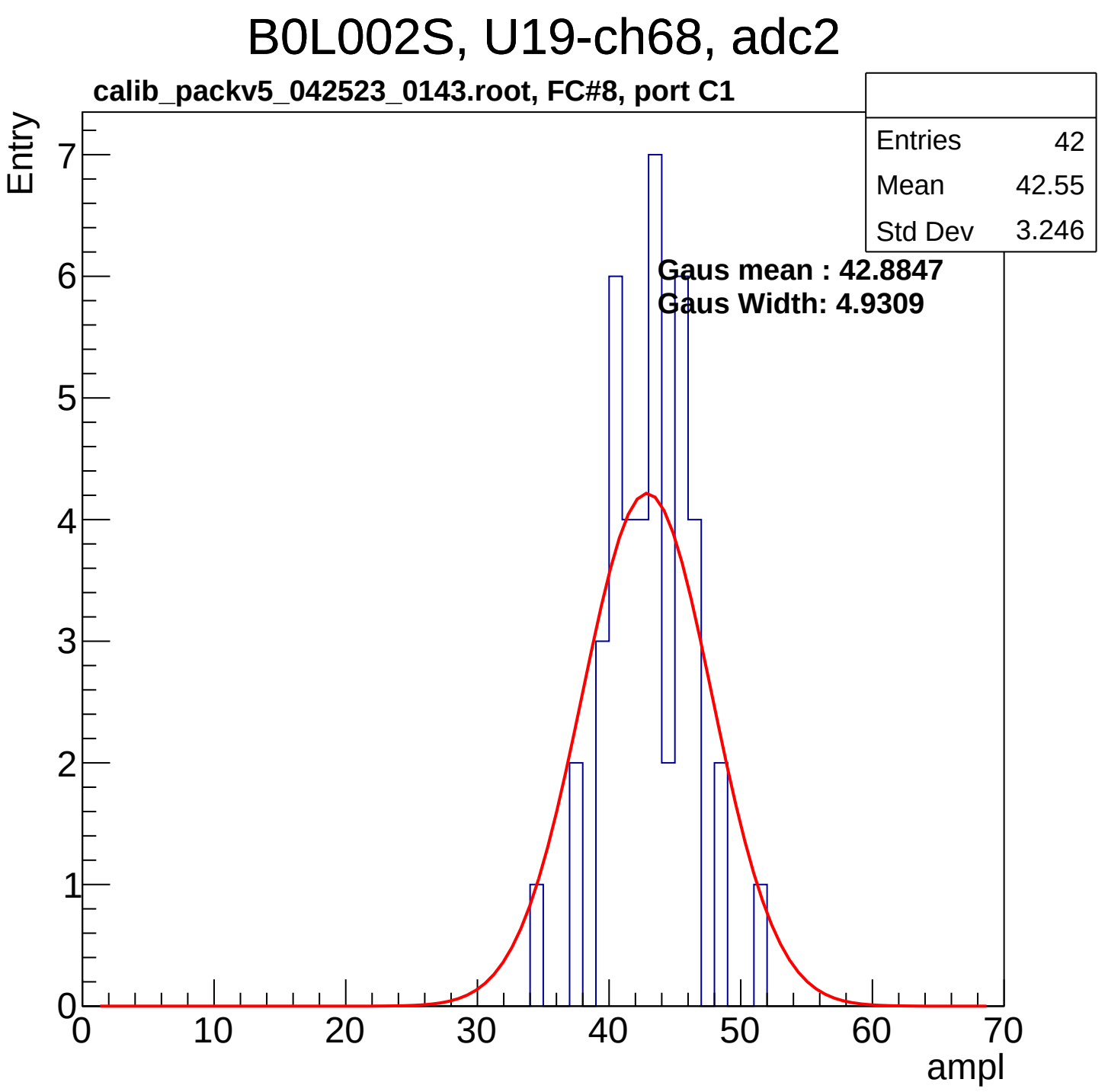

B0L002S, U19-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.48
Std Dev 10.96
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

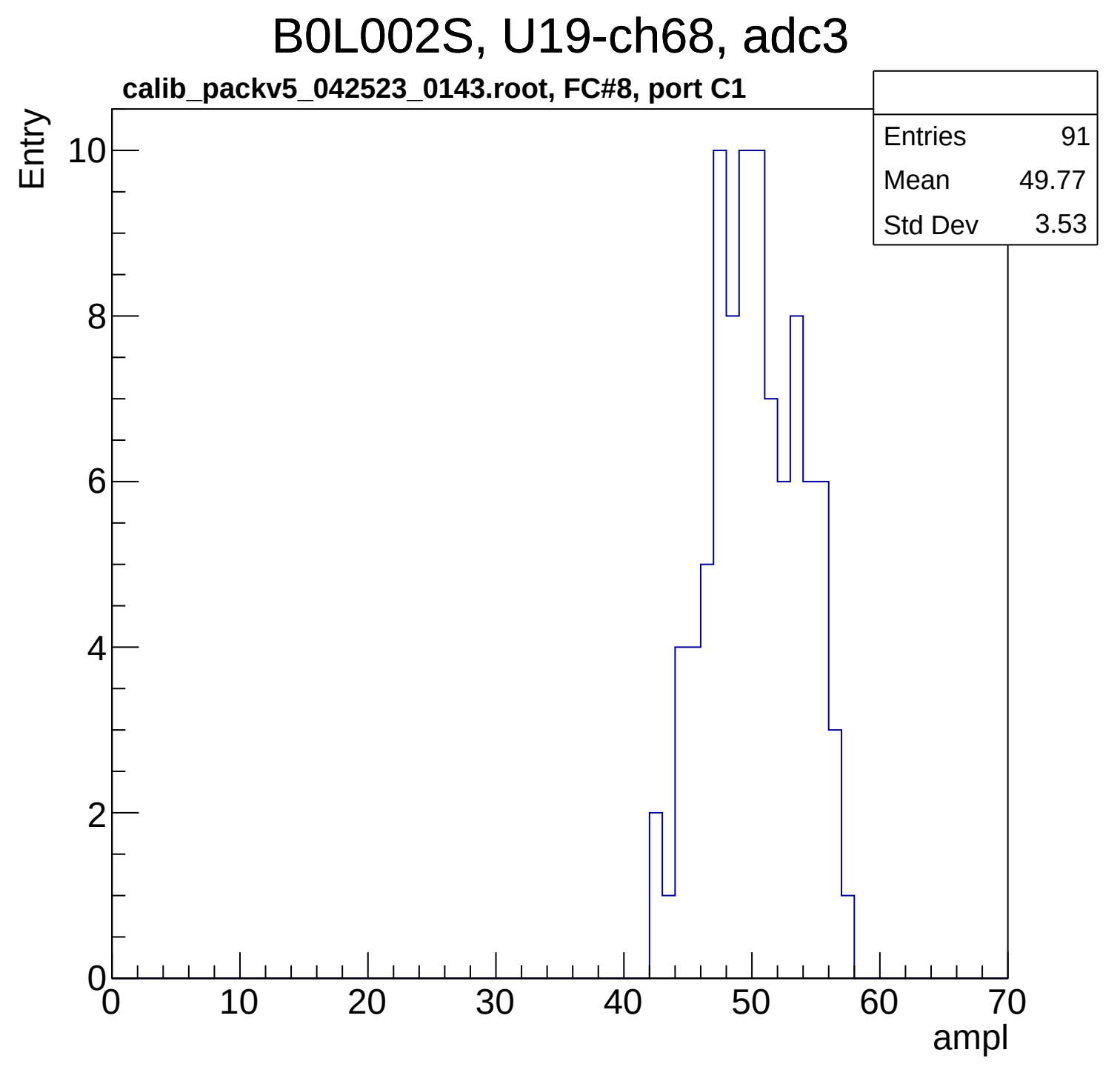

B0L002S, U19-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

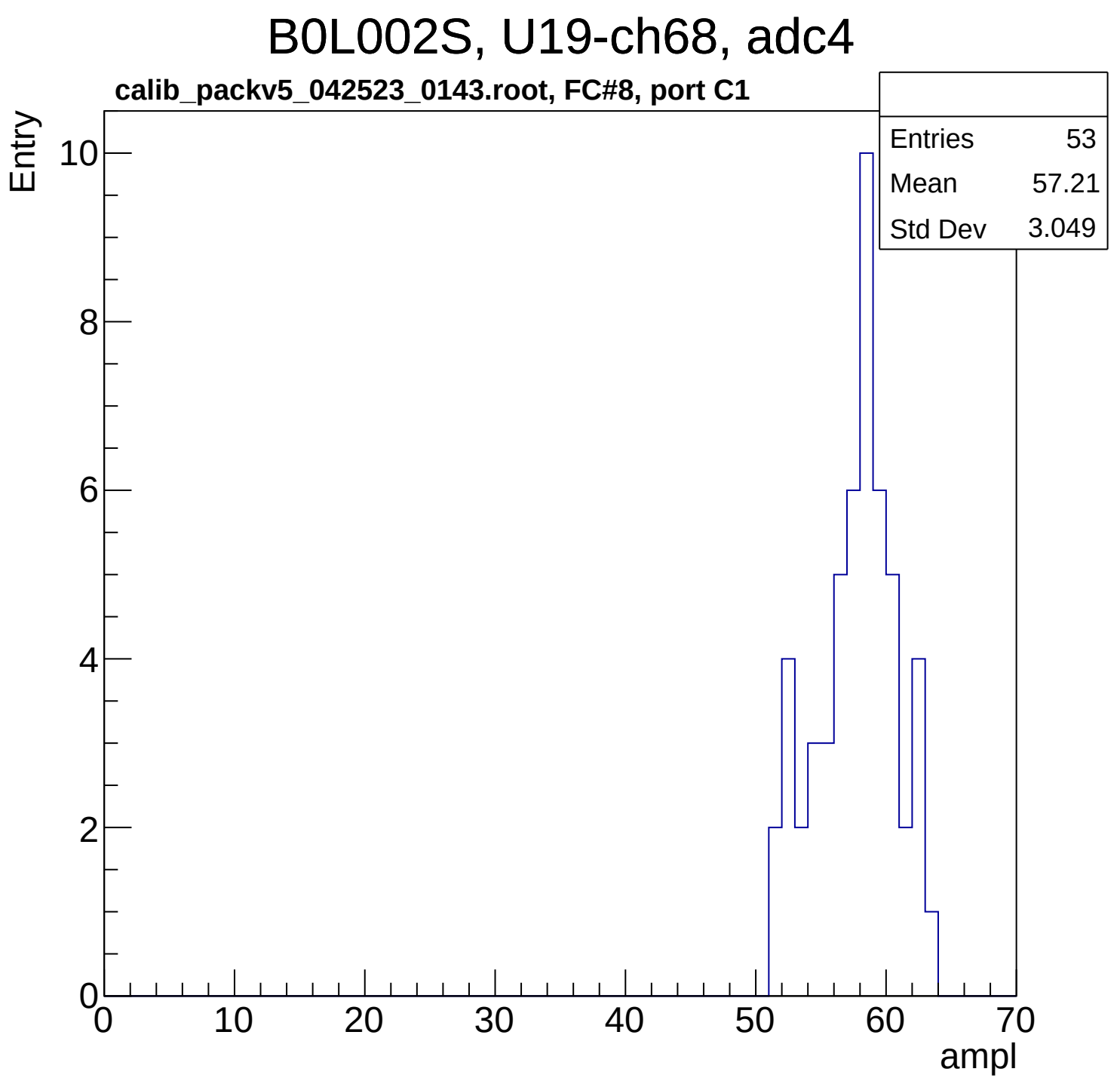

B0L002S, U19-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

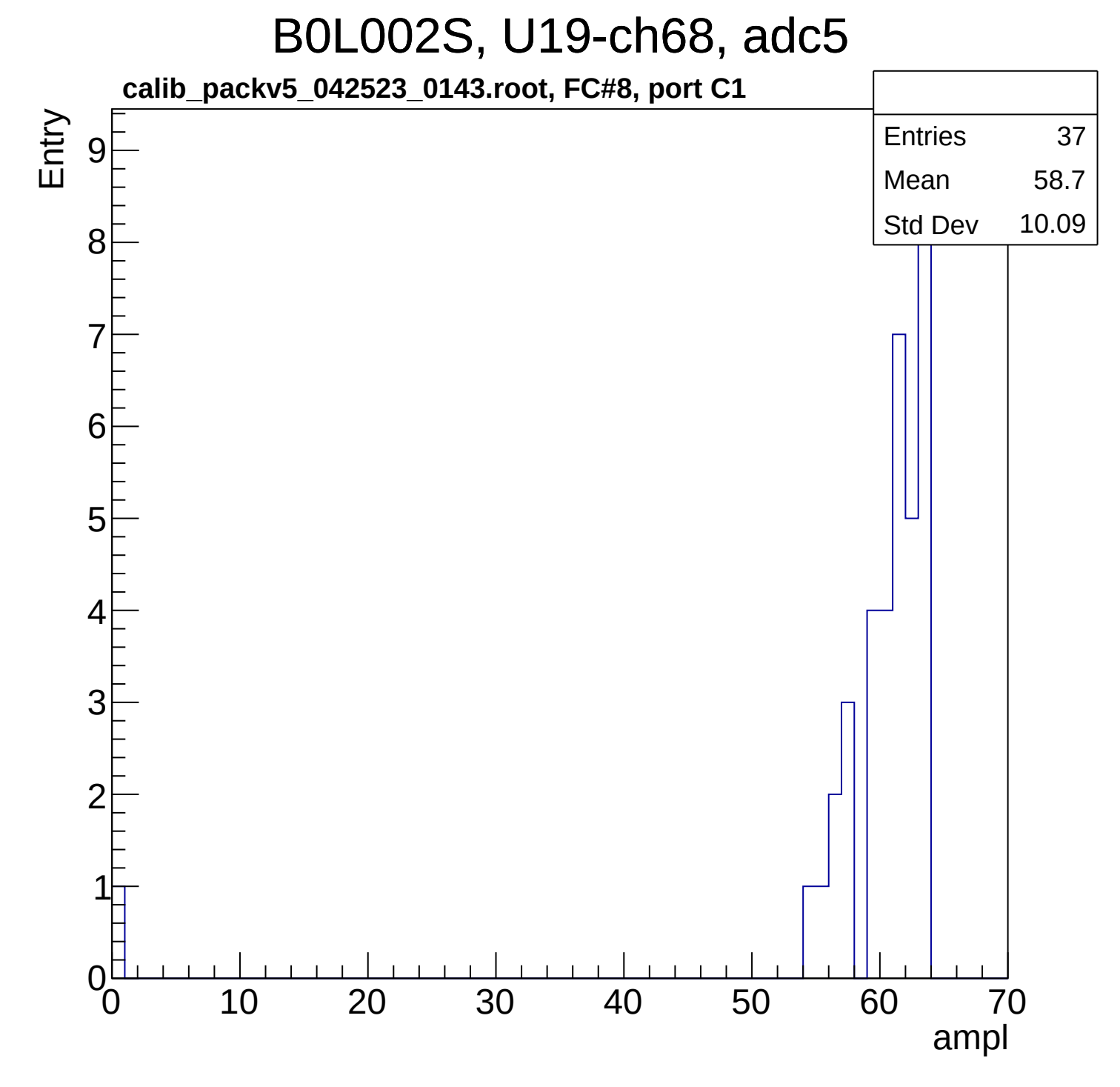

B0L002S, U19-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.46
Std Dev 3.16
Gaus mean : 31.0286
Gaus Width: 3.4944
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

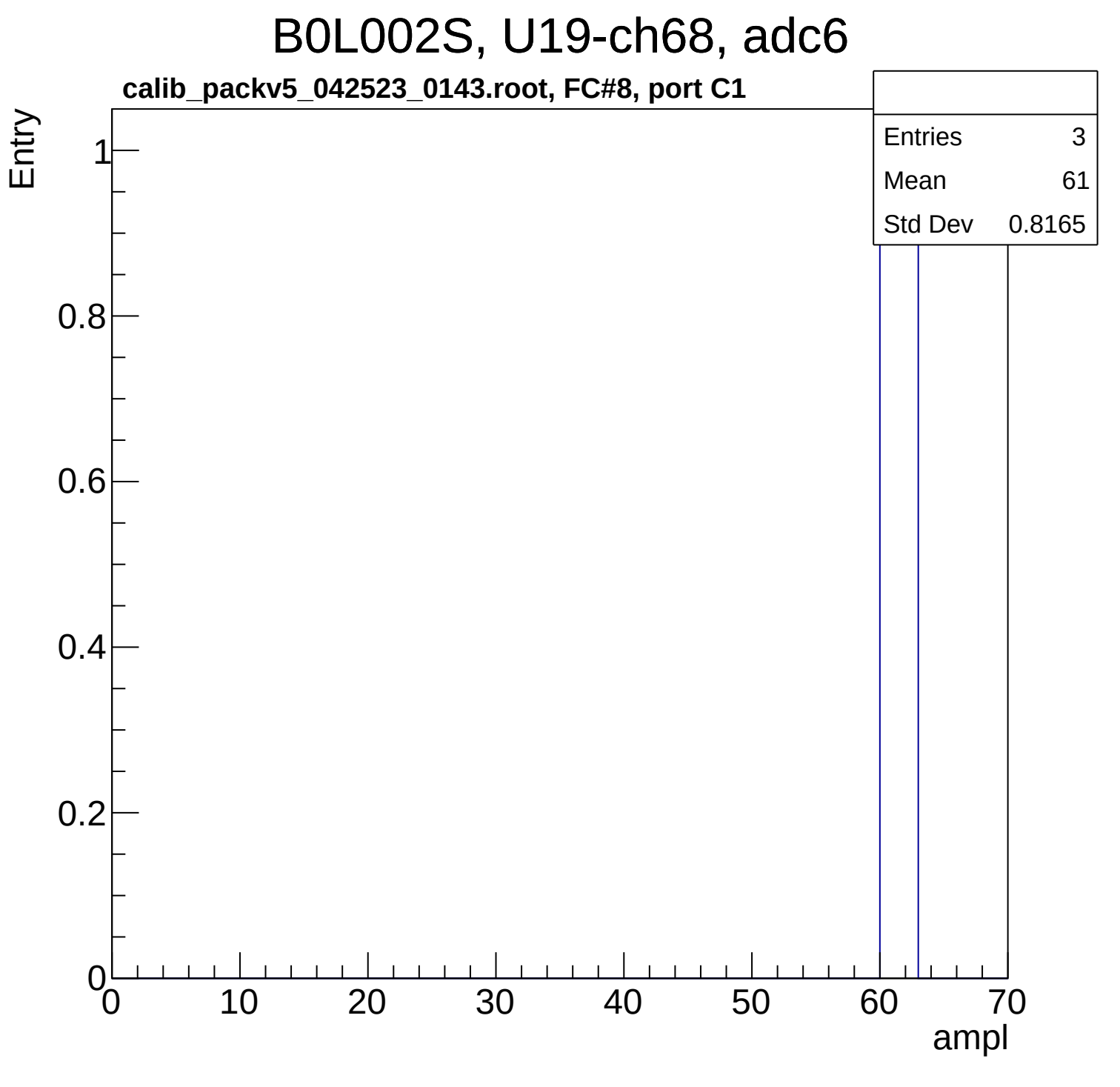

B0L002S, U19-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.31
Std Dev 3.725
Gaus mean : 37.4718
Gaus Width: 4.5289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

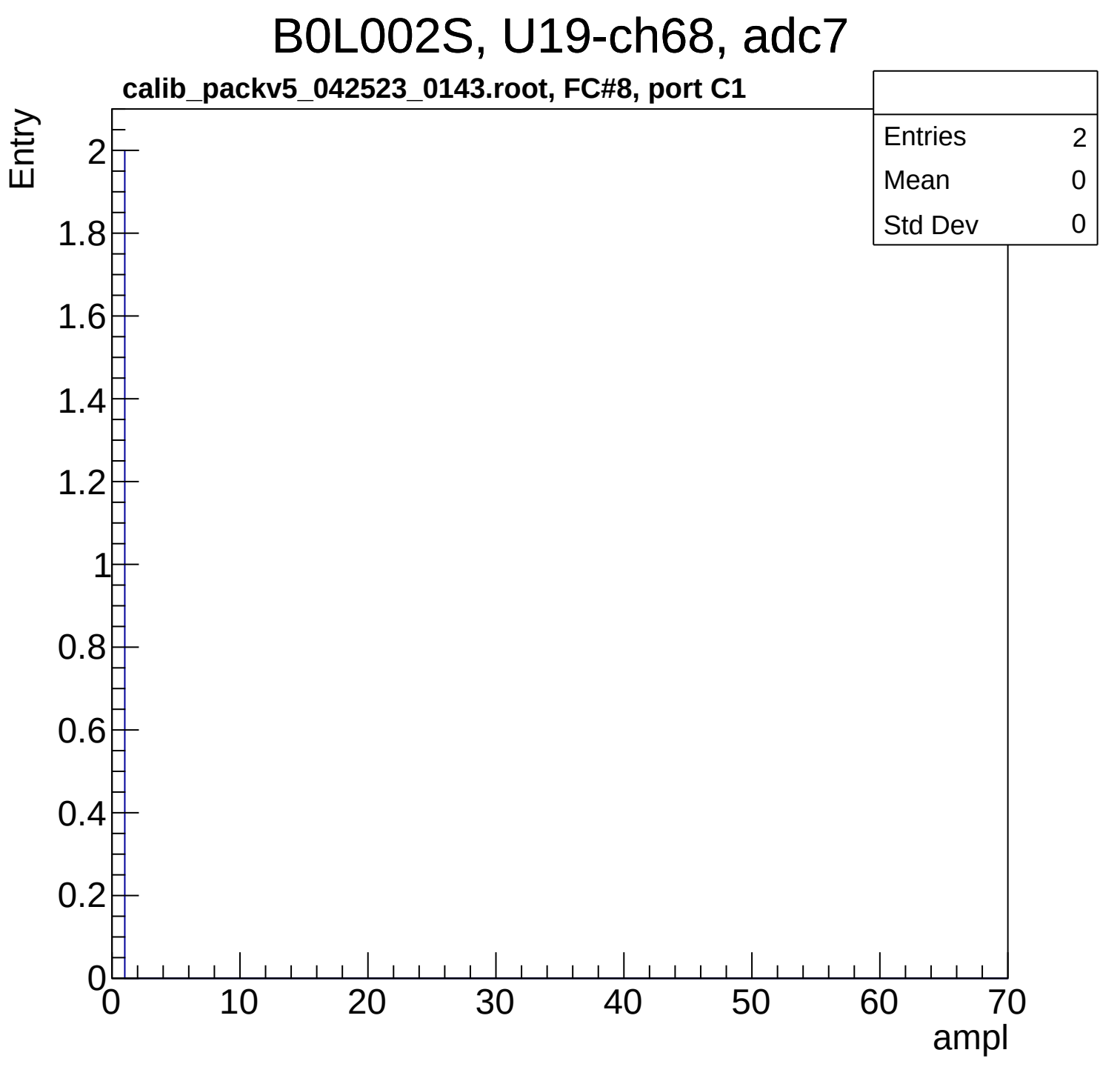

B0L002S, U19-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 42.55
Std Dev 3.246
Gaus mean : 42.8847
Gaus Width: 4.9309
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

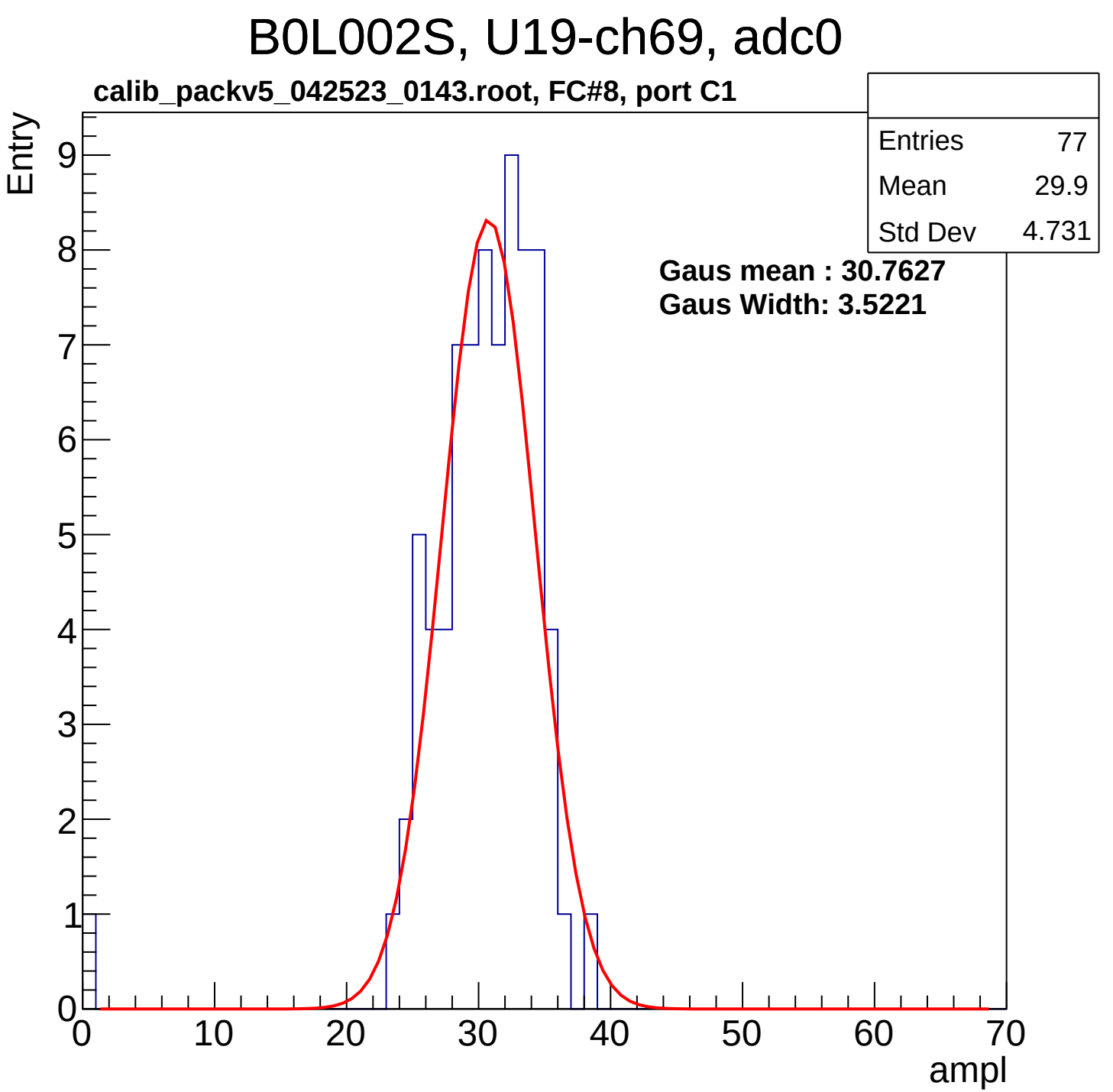

B0L002S, U19-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 49.77
Std Dev 3.53
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

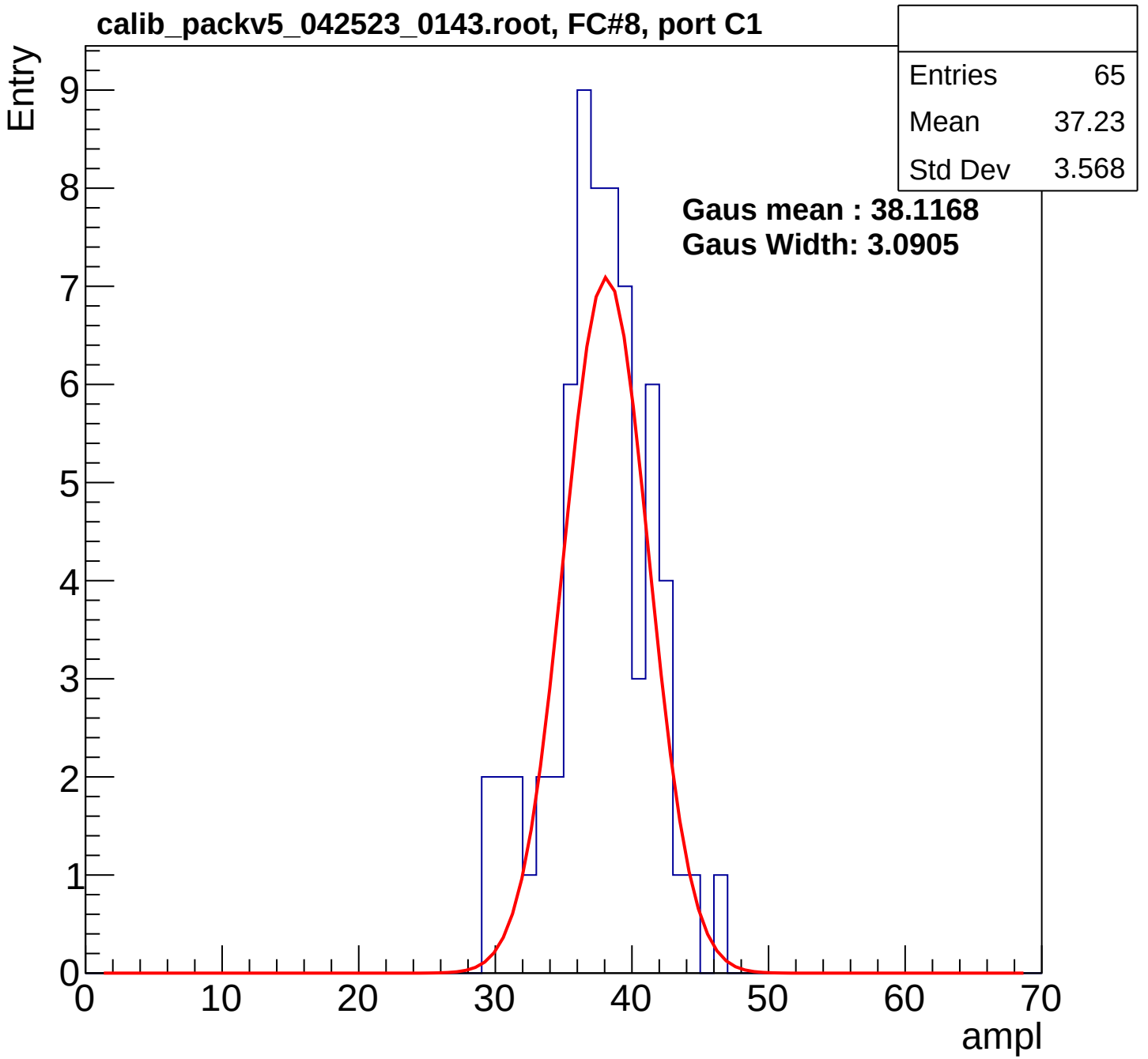

B0L002S, U19-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.21
Std Dev 3.049
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

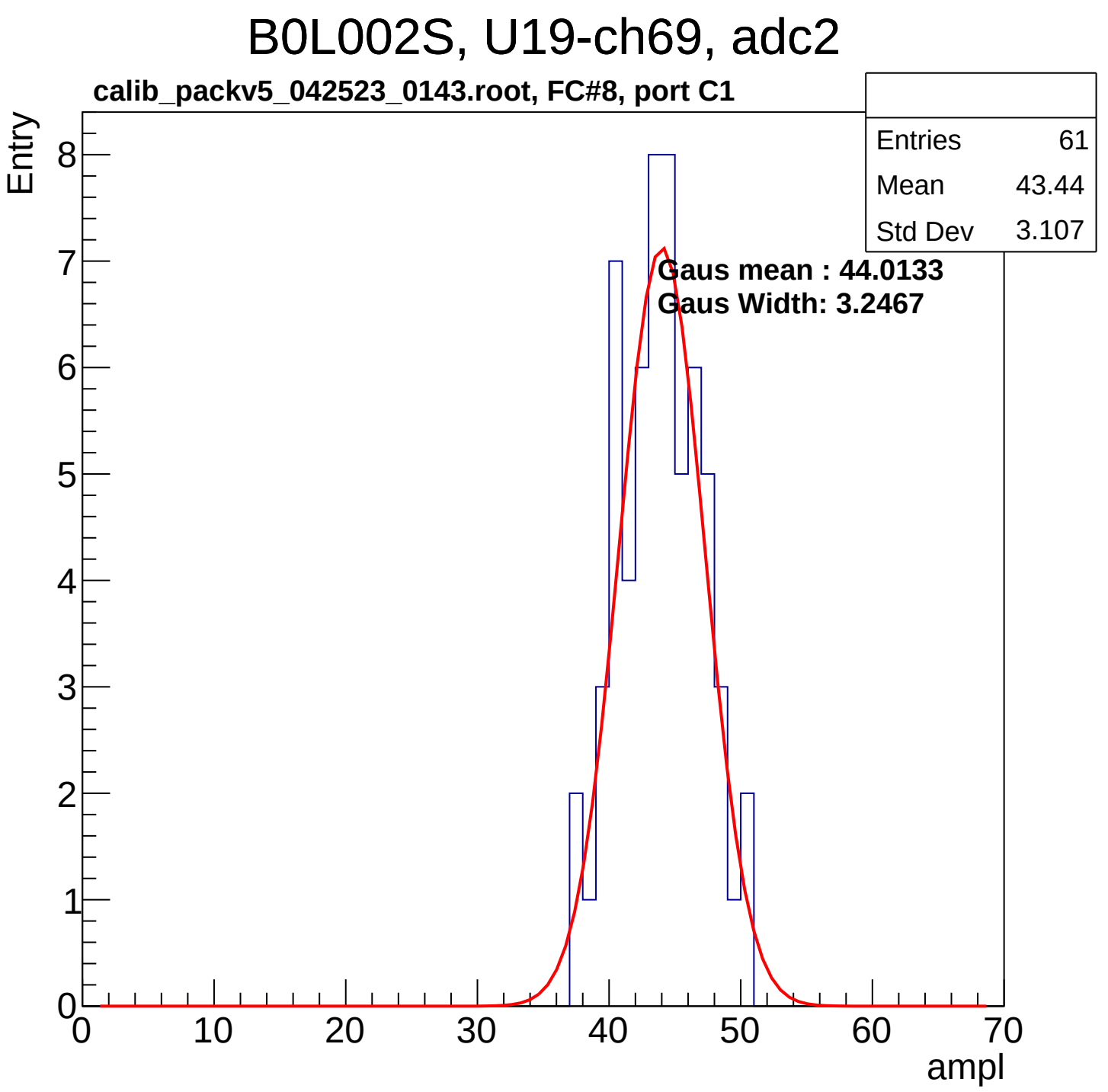

B0L002S, U19-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.7
Std Dev 10.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

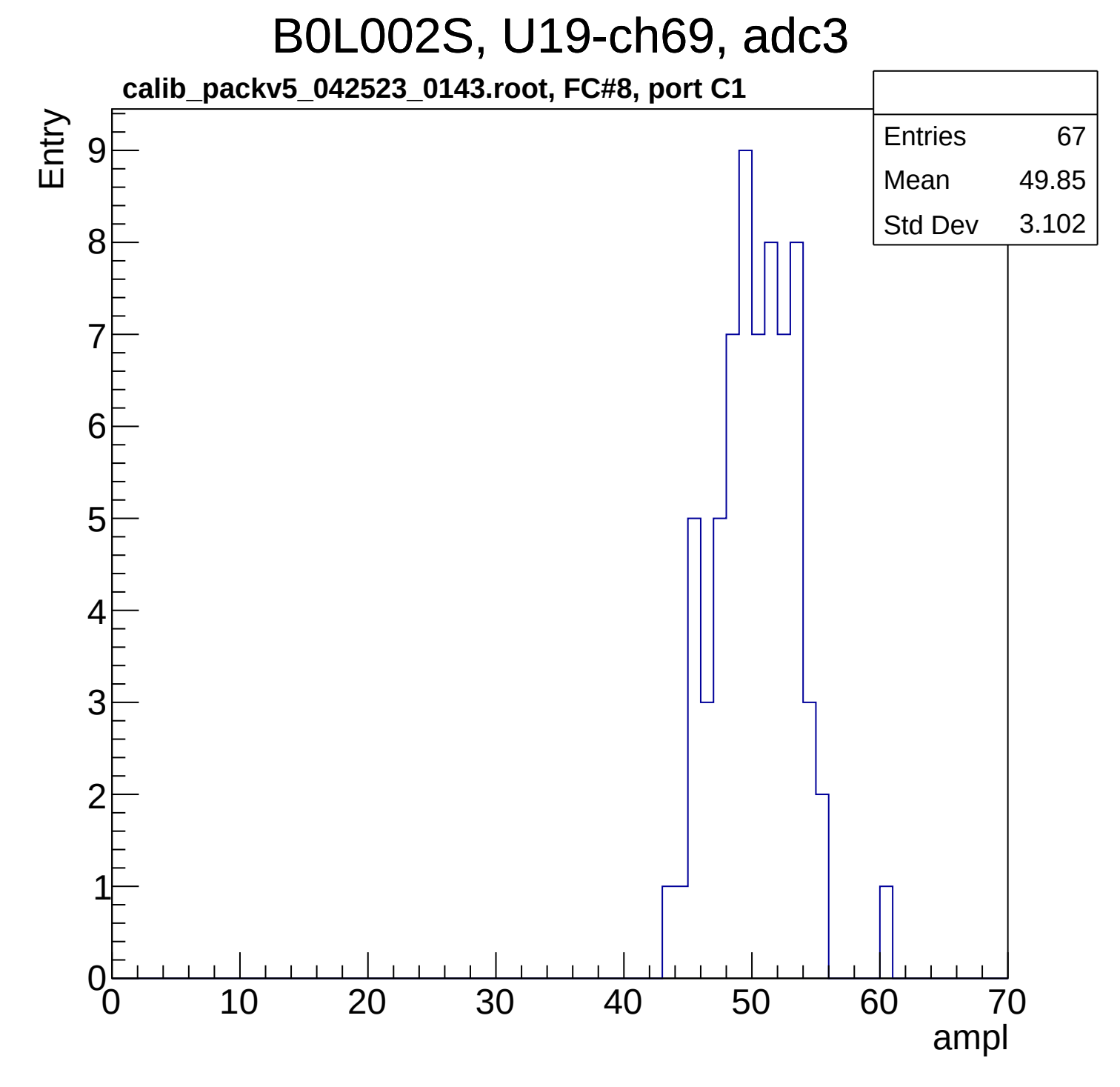

B0L002S, U19-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

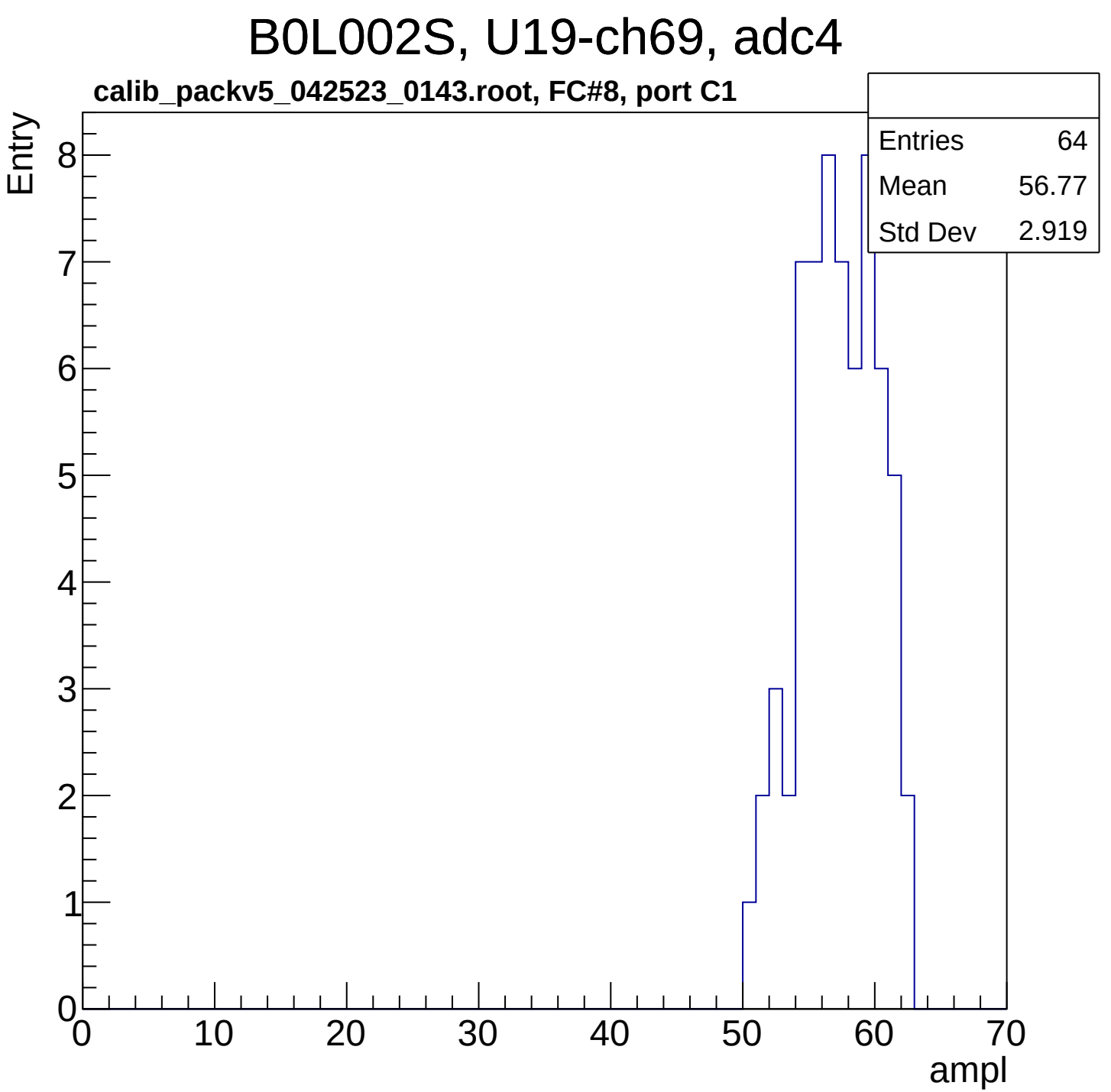

B0L002S, U19-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

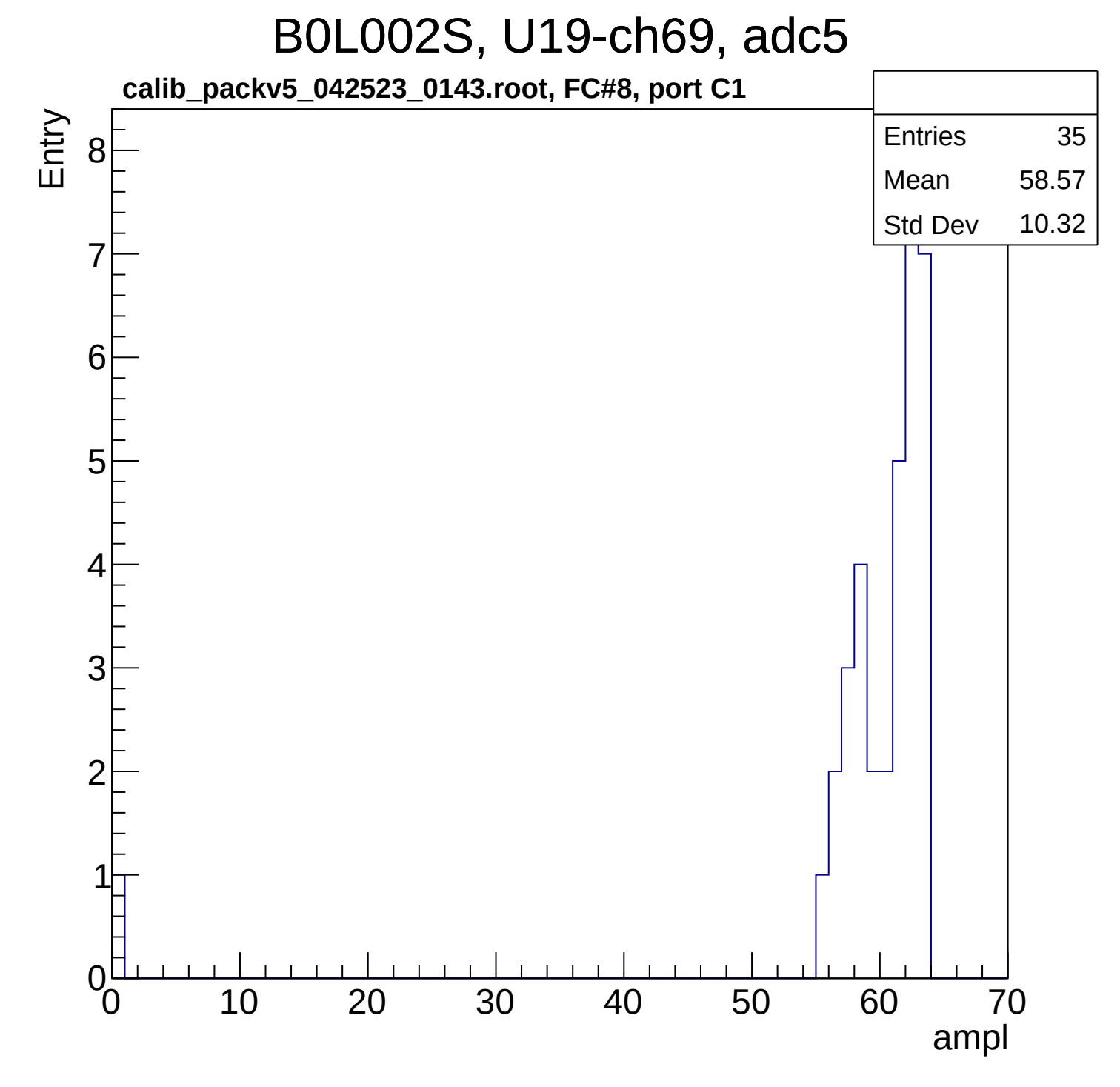

B0L002S, U19-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 29.9
Std Dev 4.731
Gaus mean : 30.7627
Gaus Width: 3.5221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

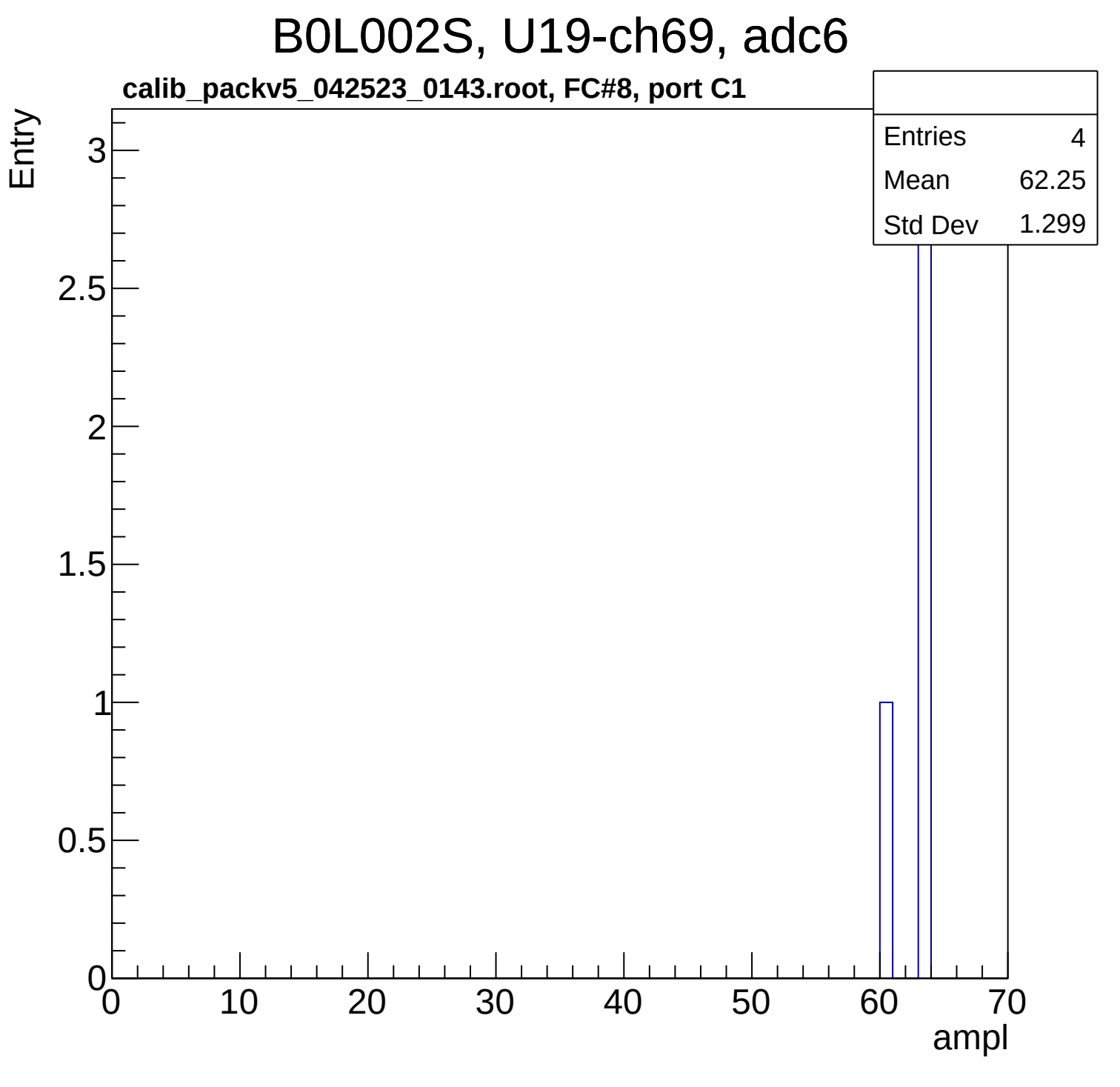

B0L002S, U19-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.23
Std Dev 3.568
Gaus mean : 38.1168
Gaus Width: 3.0905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

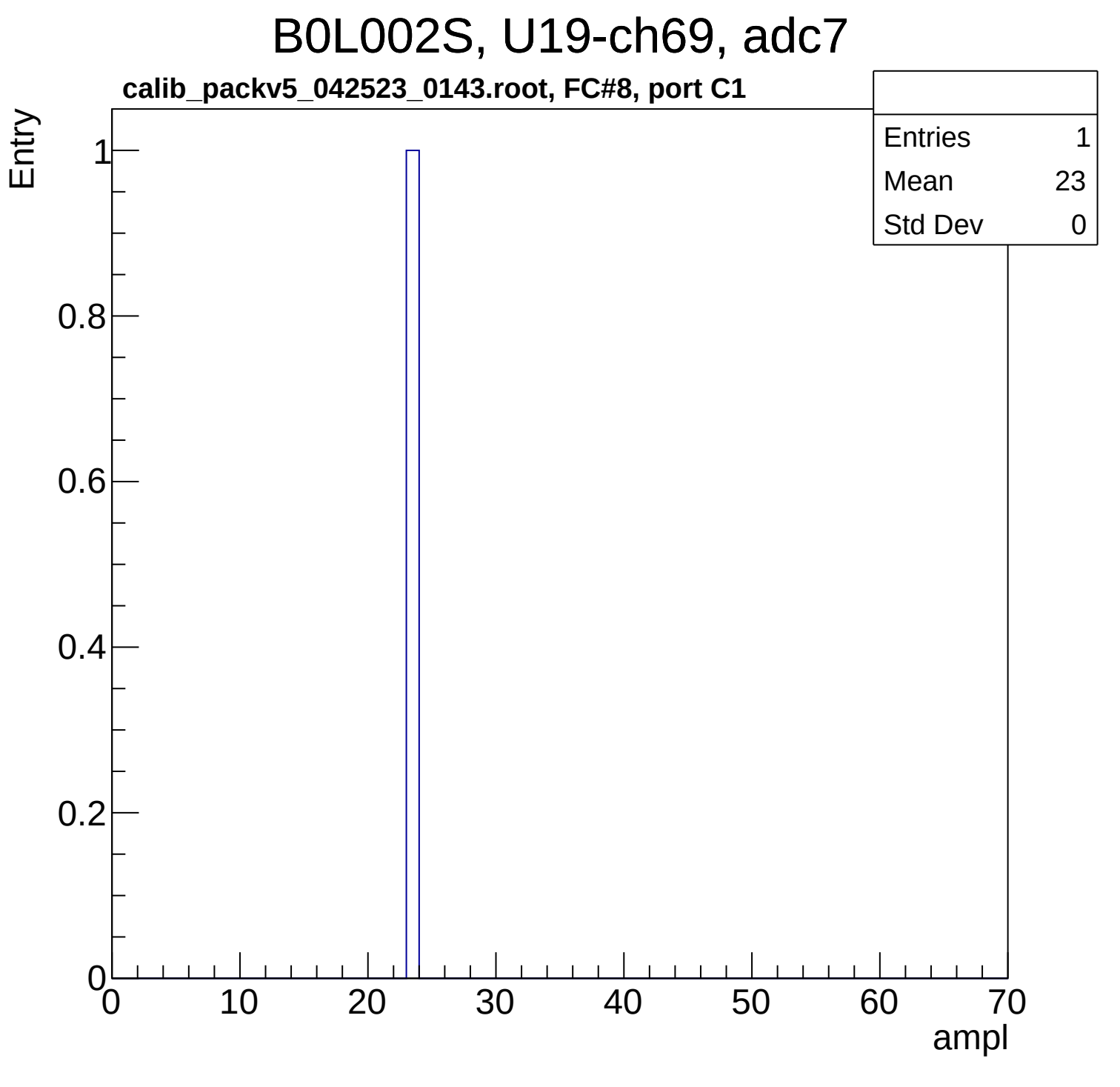

B0L002S, U19-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.44
Std Dev 3.107
Gaus mean : 44.0133
Gaus Width: 3.2467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

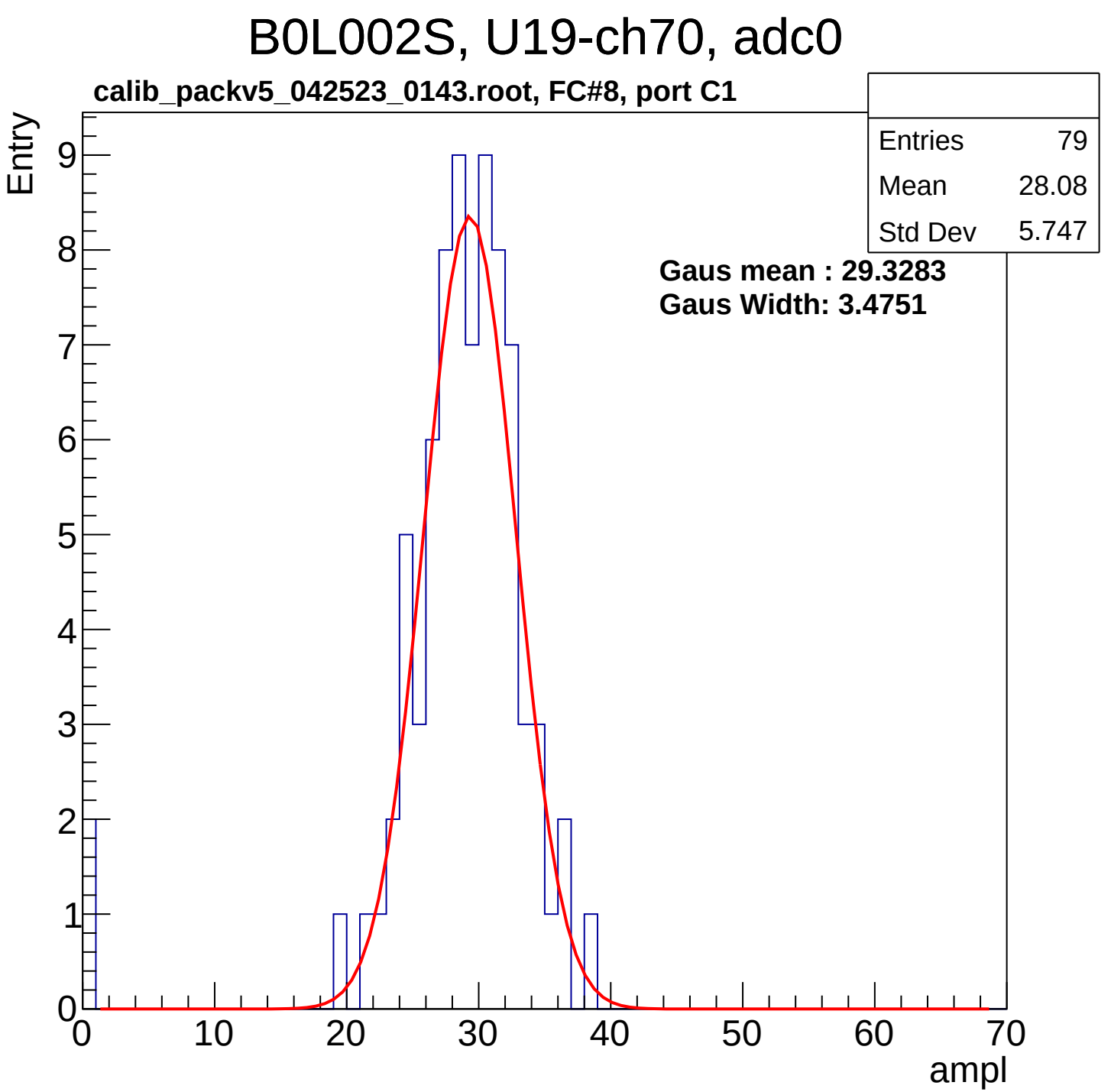

B0L002S, U19-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 49.85
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

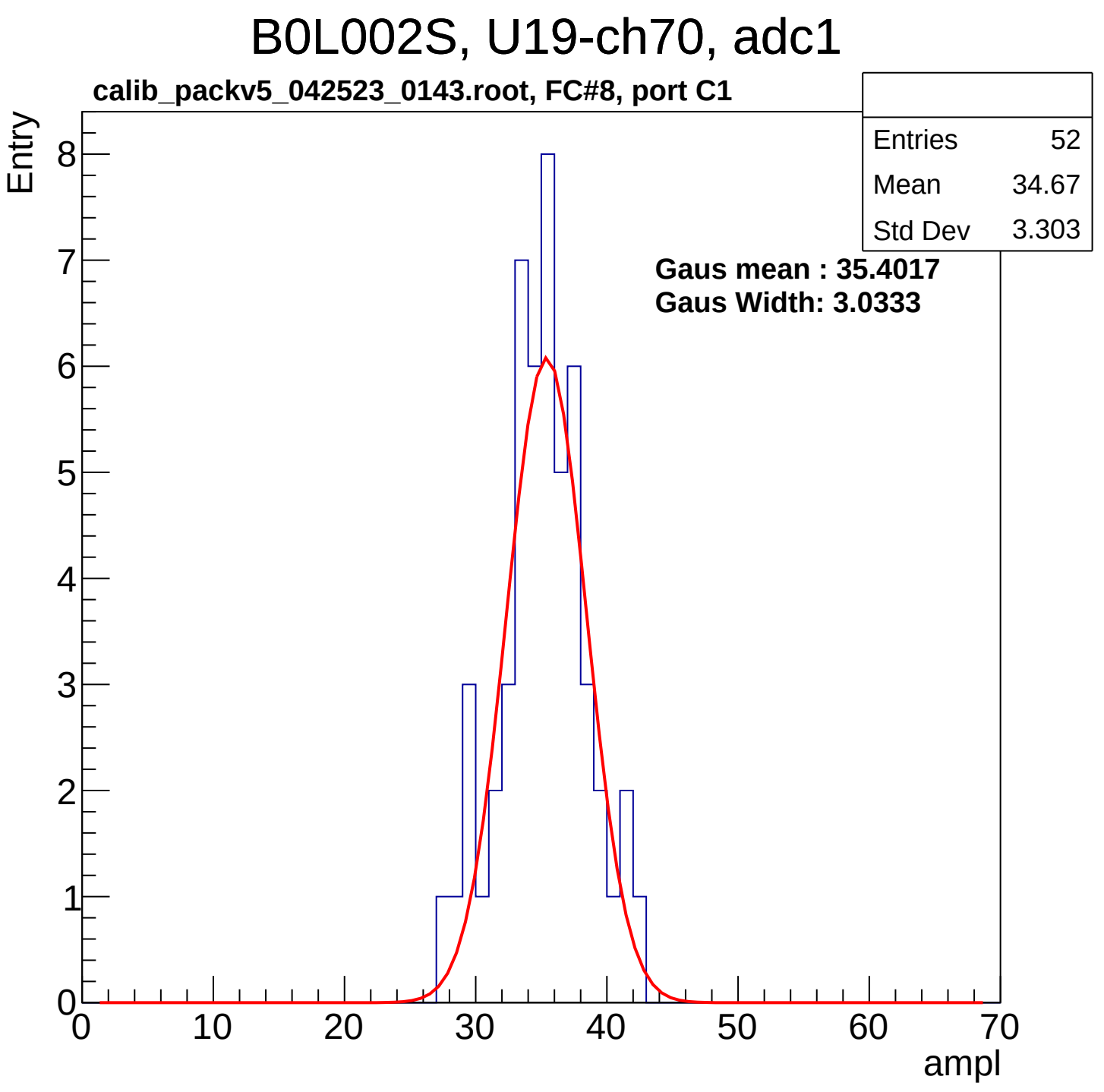

B0L002S, U19-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.77
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

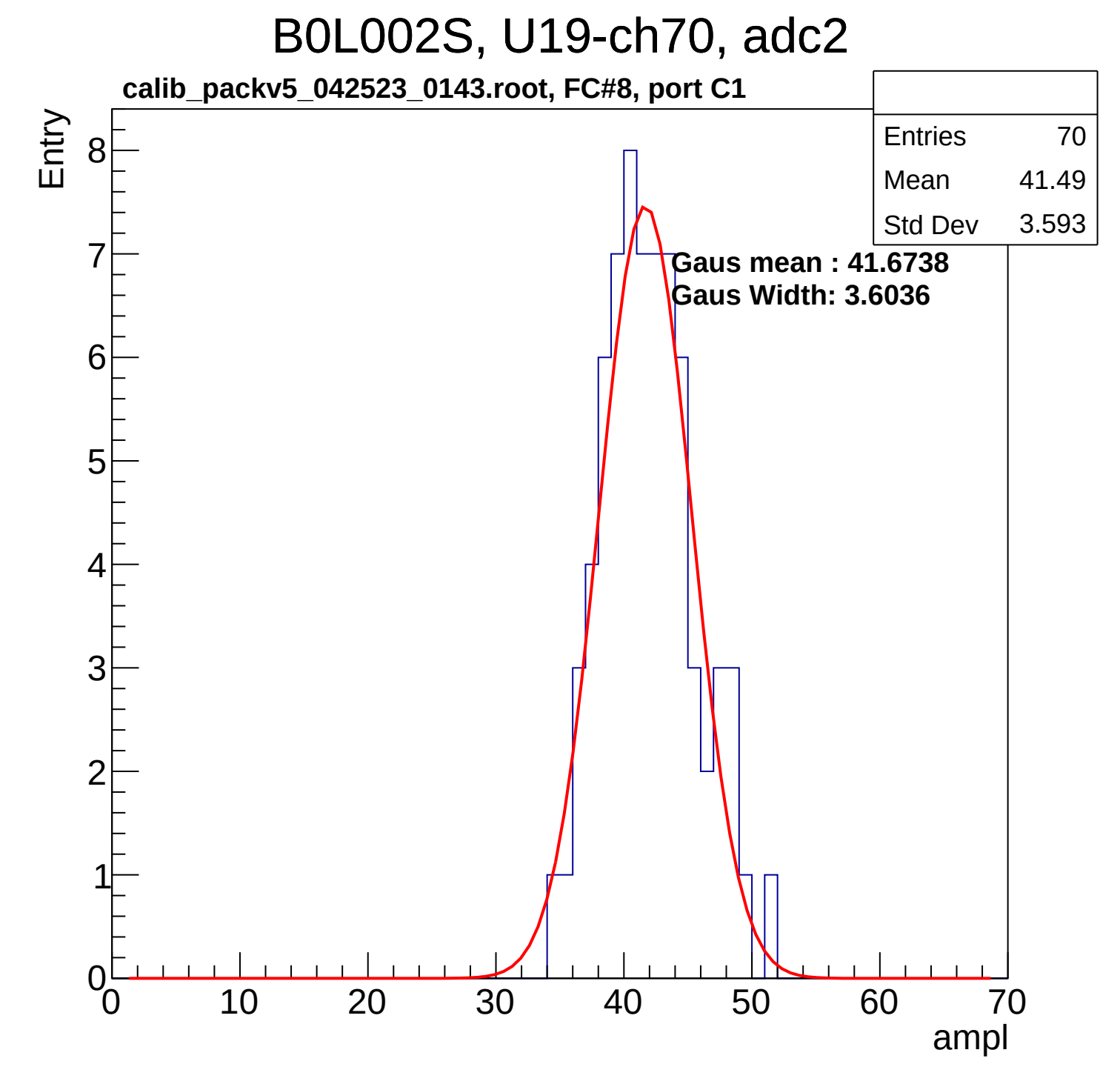

B0L002S, U19-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.57
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

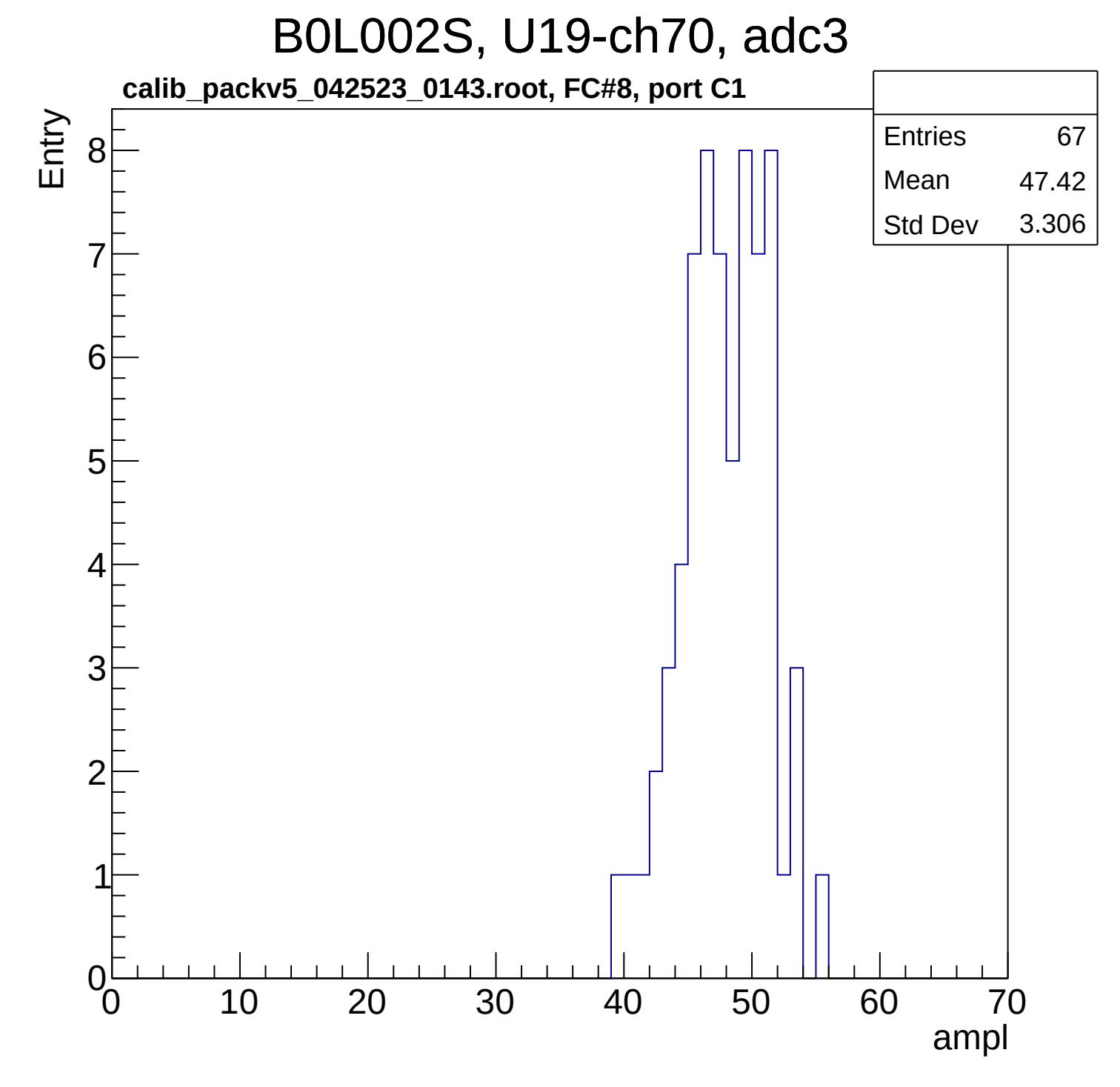

B0L002S, U19-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

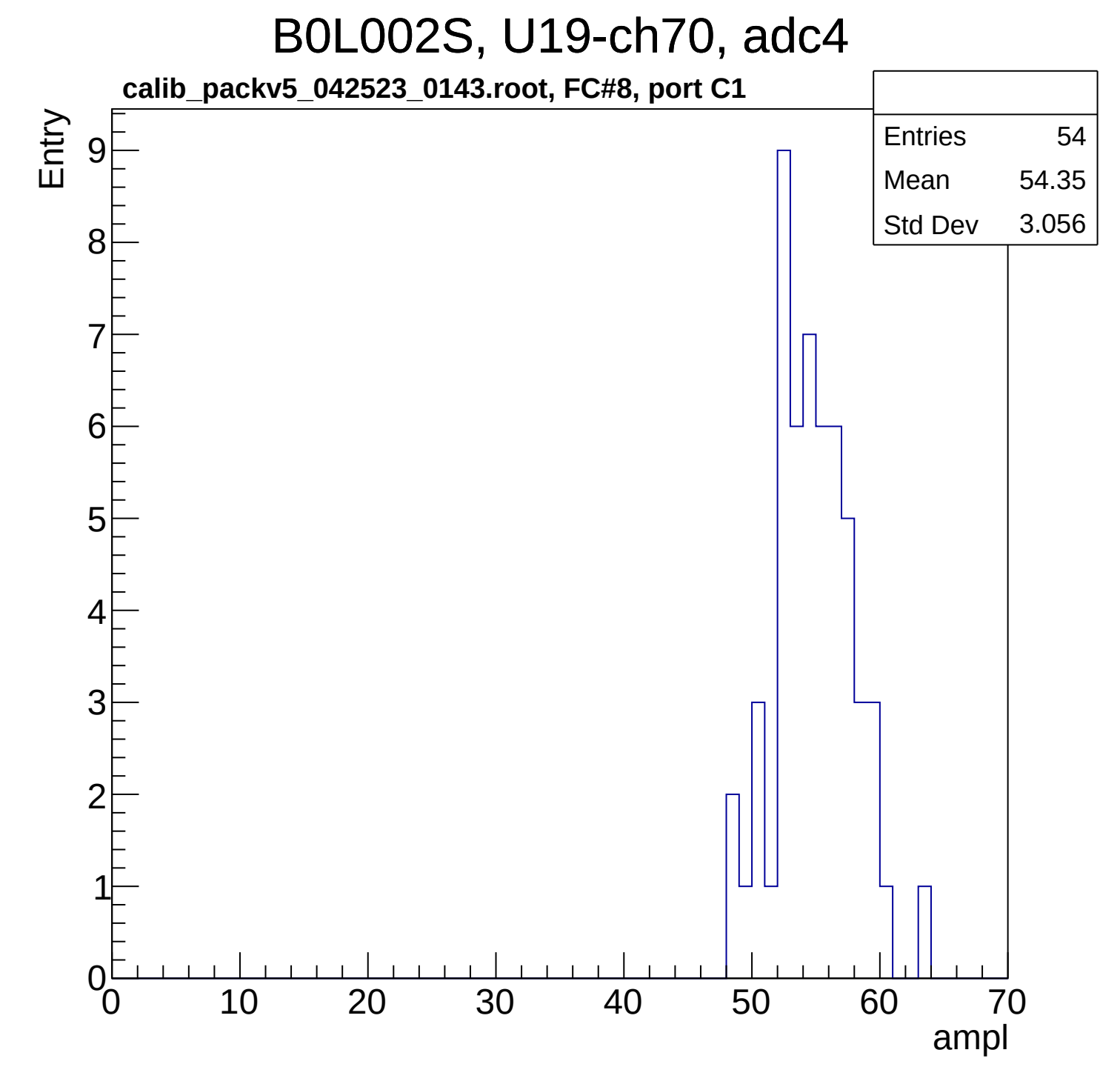

B0L002S, U19-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

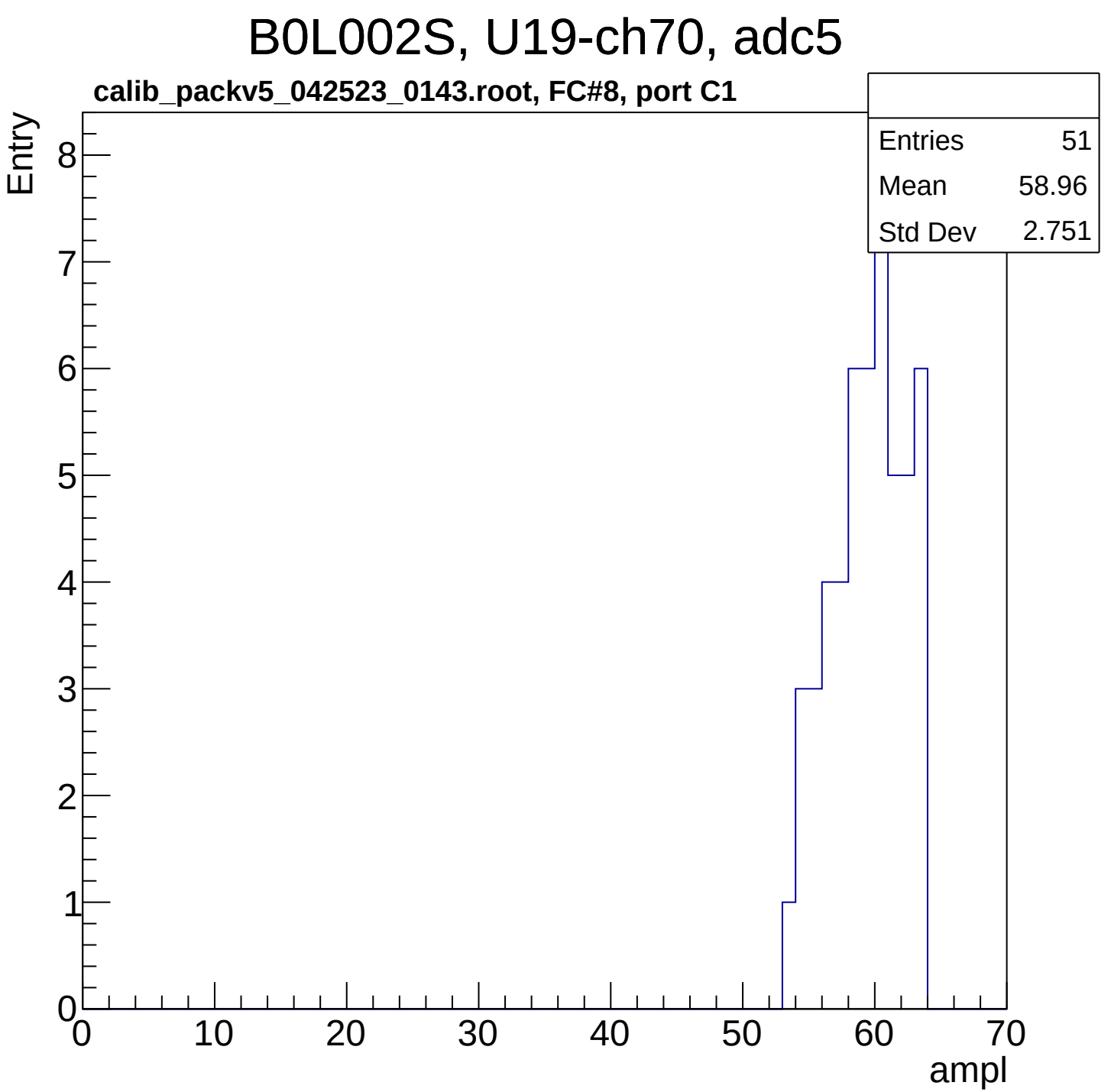

B0L002S, U19-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 28.08
Std Dev 5.747
Gaus mean : 29.3283
Gaus Width: 3.4751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

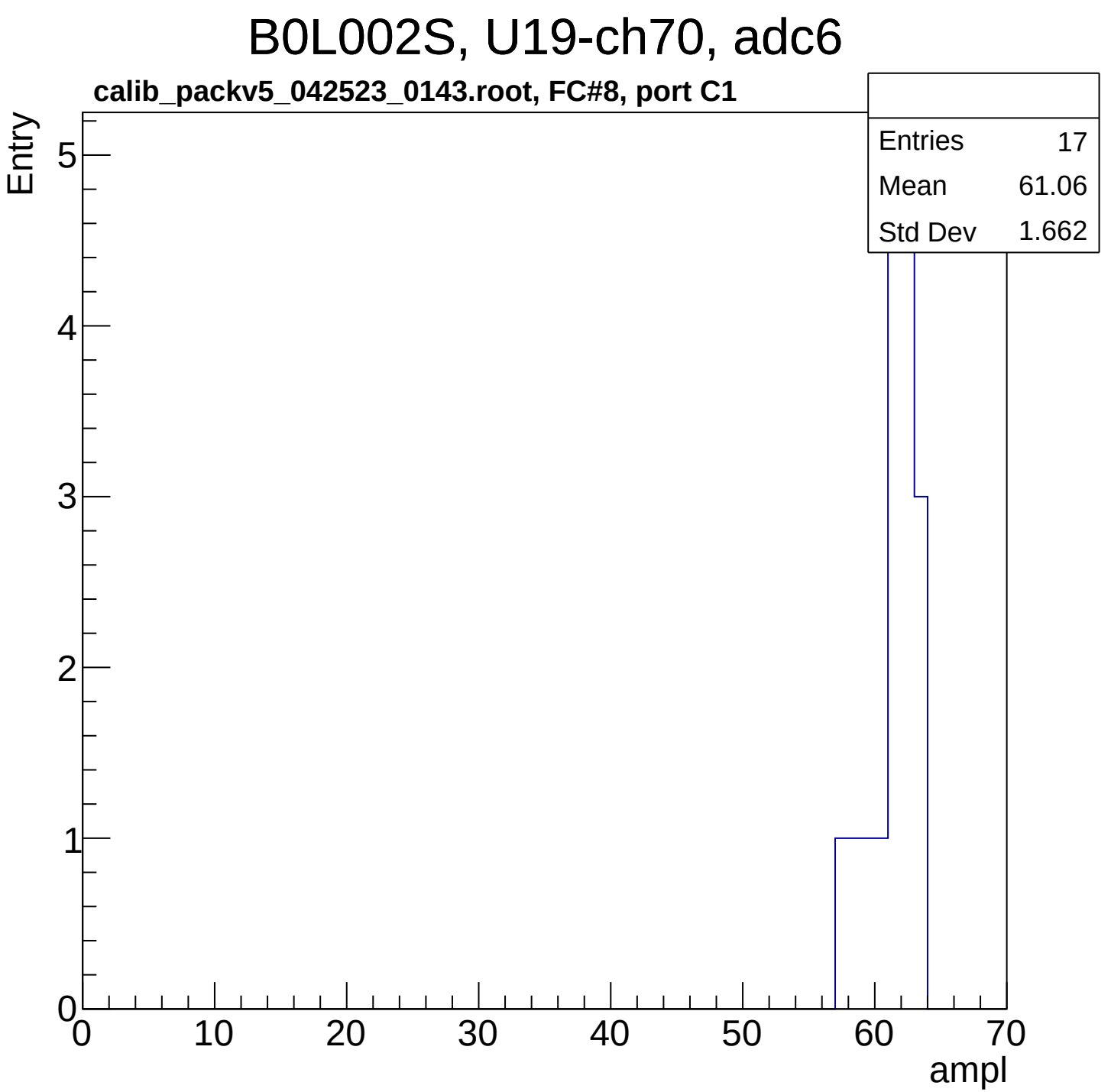

B0L002S, U19-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 34.67
Std Dev 3.303
Gaus mean : 35.4017
Gaus Width: 3.0333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

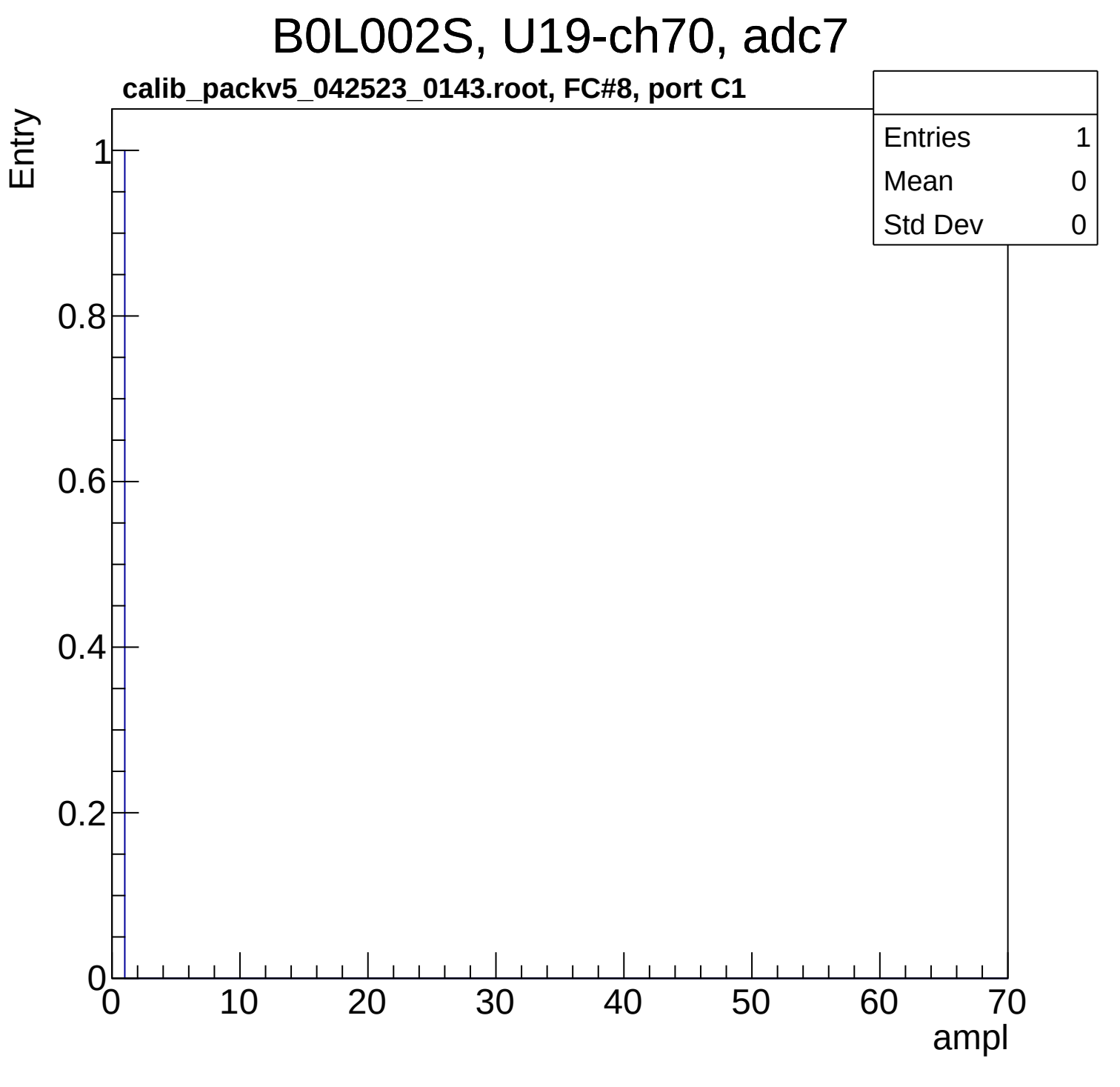

B0L002S, U19-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 41.49
Std Dev 3.593
Gaus mean : 41.6738
Gaus Width: 3.6036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

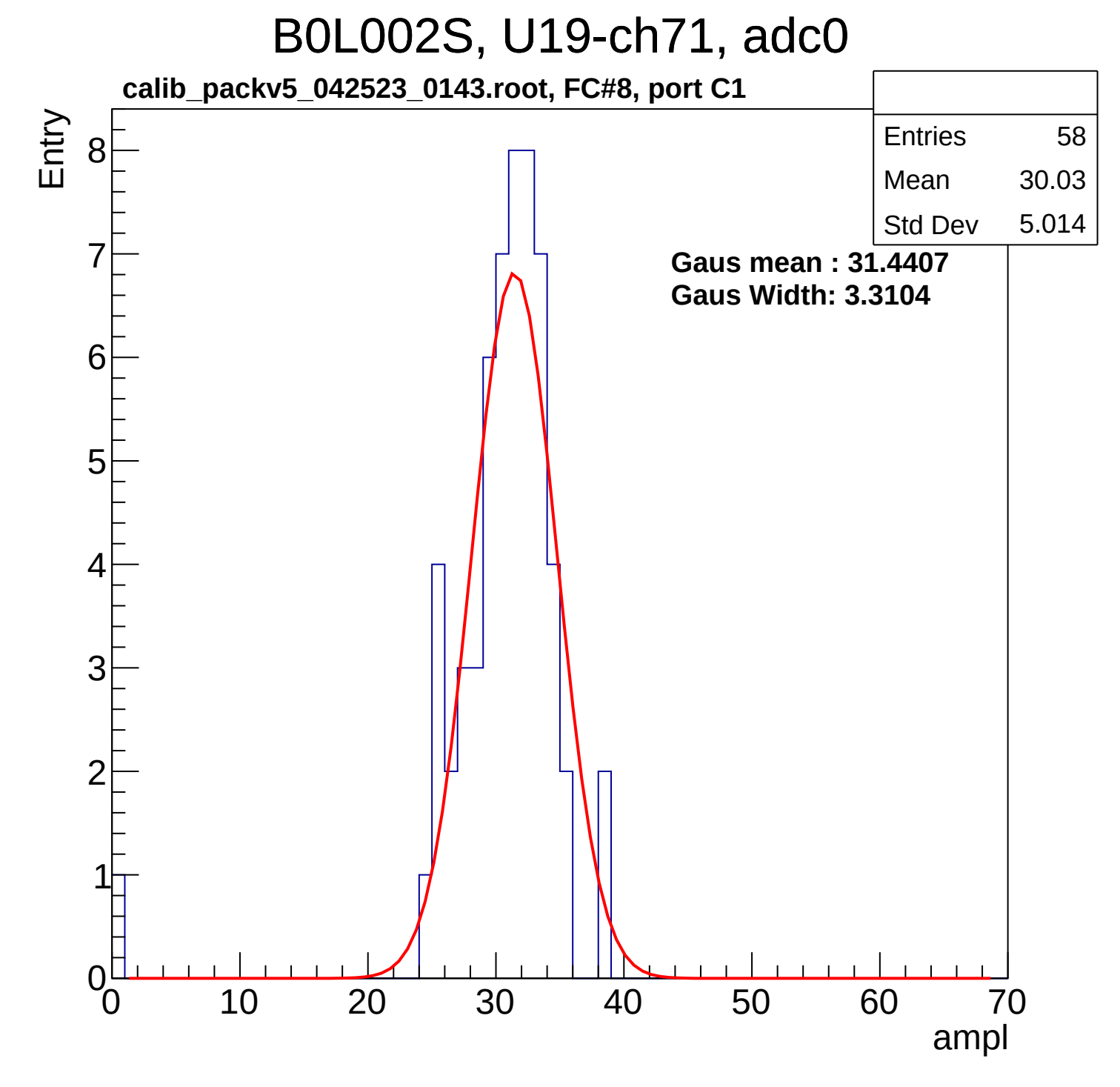

B0L002S, U19-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 47.42
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

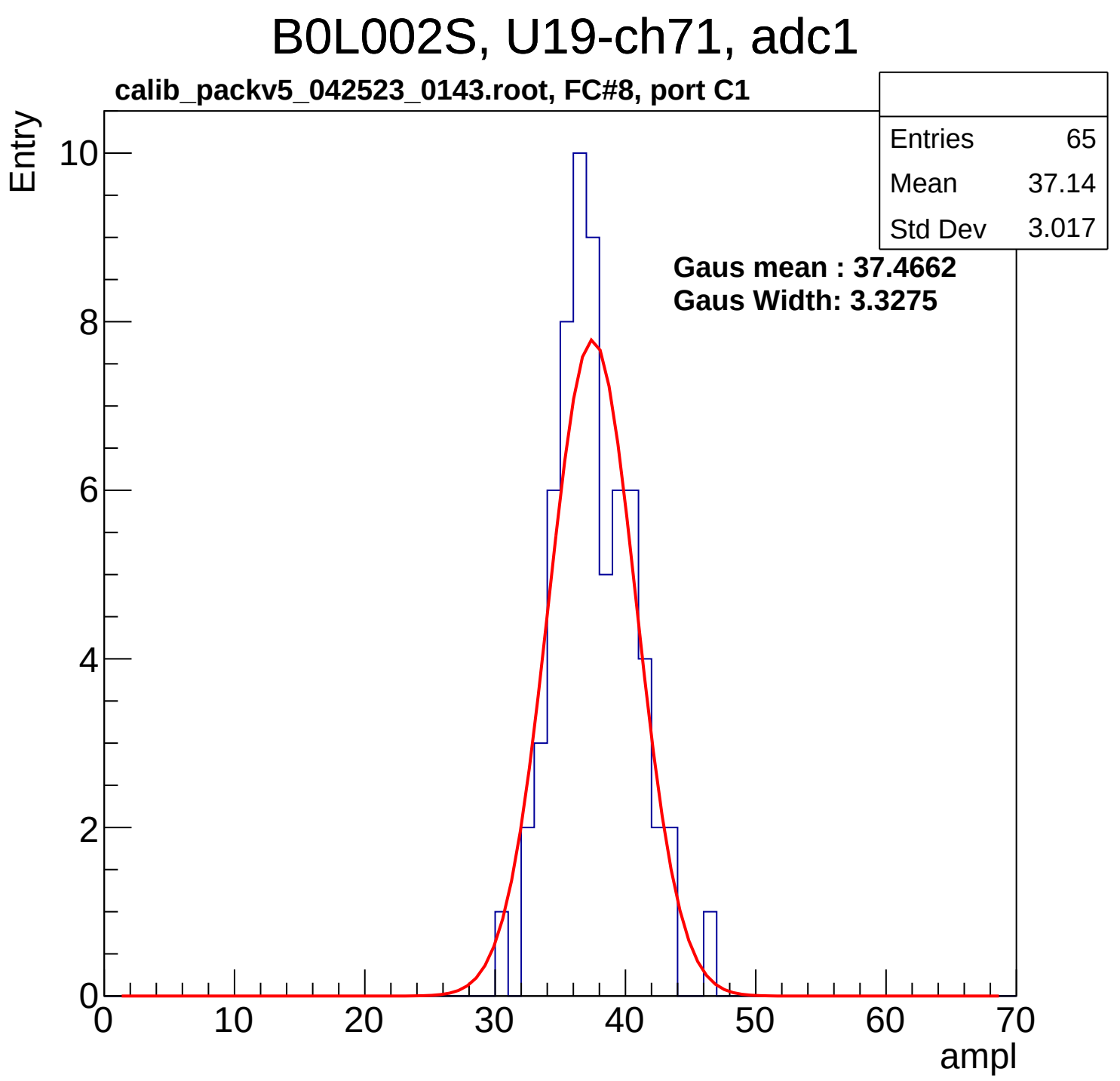

B0L002S, U19-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 54.35
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

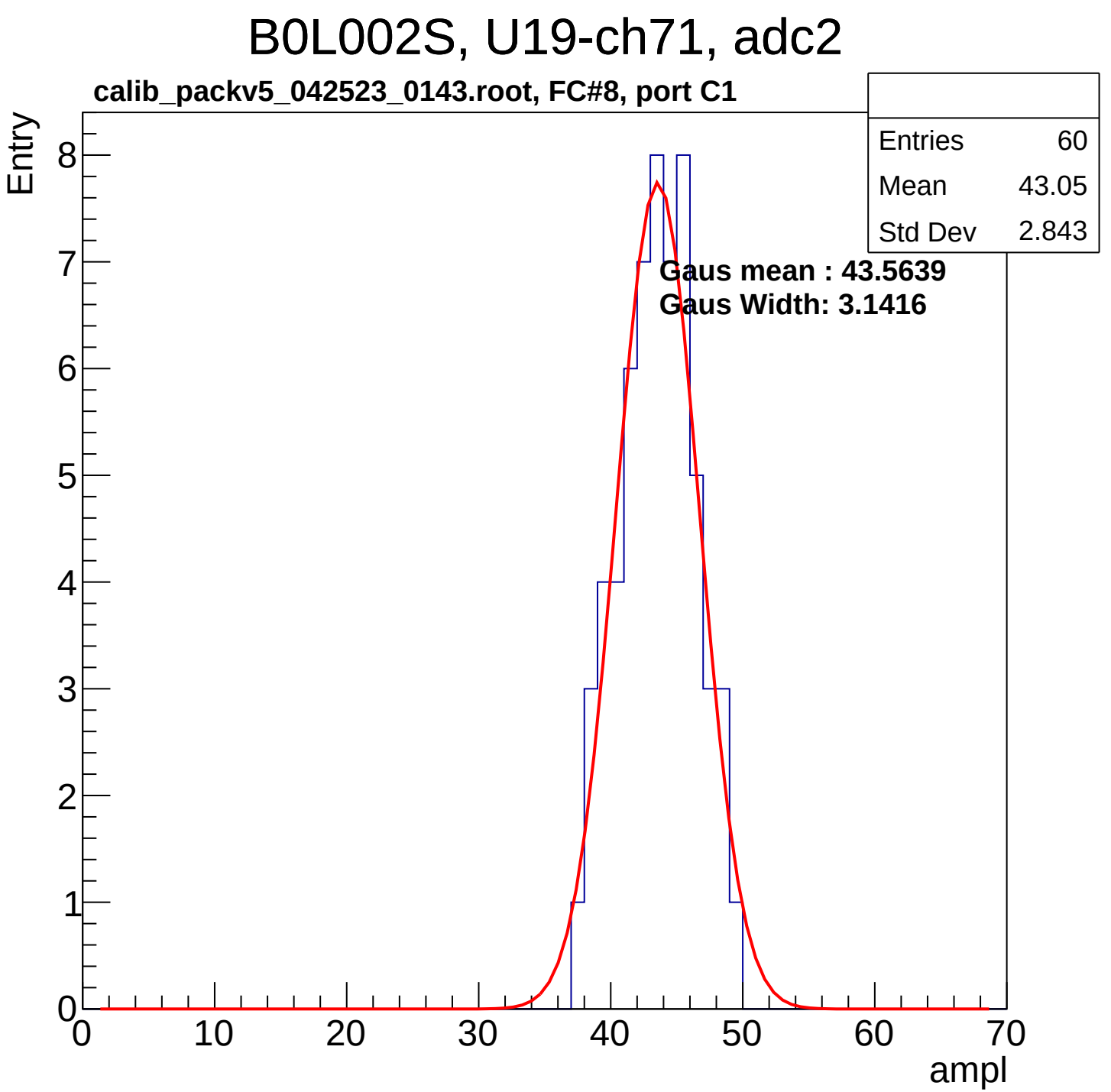

B0L002S, U19-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.96
Std Dev 2.751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

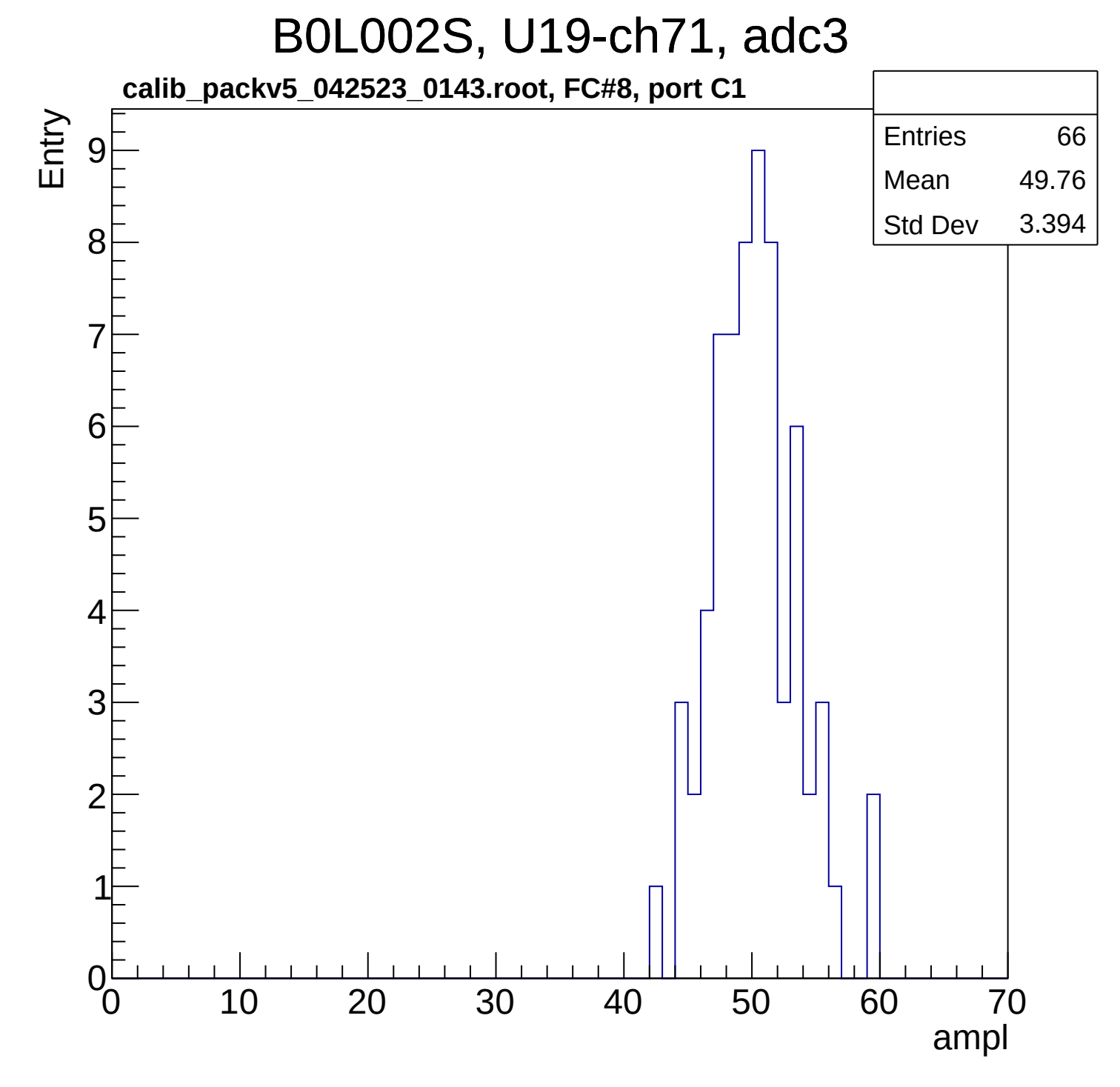

B0L002S, U19-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 61.06
Std Dev 1.662
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

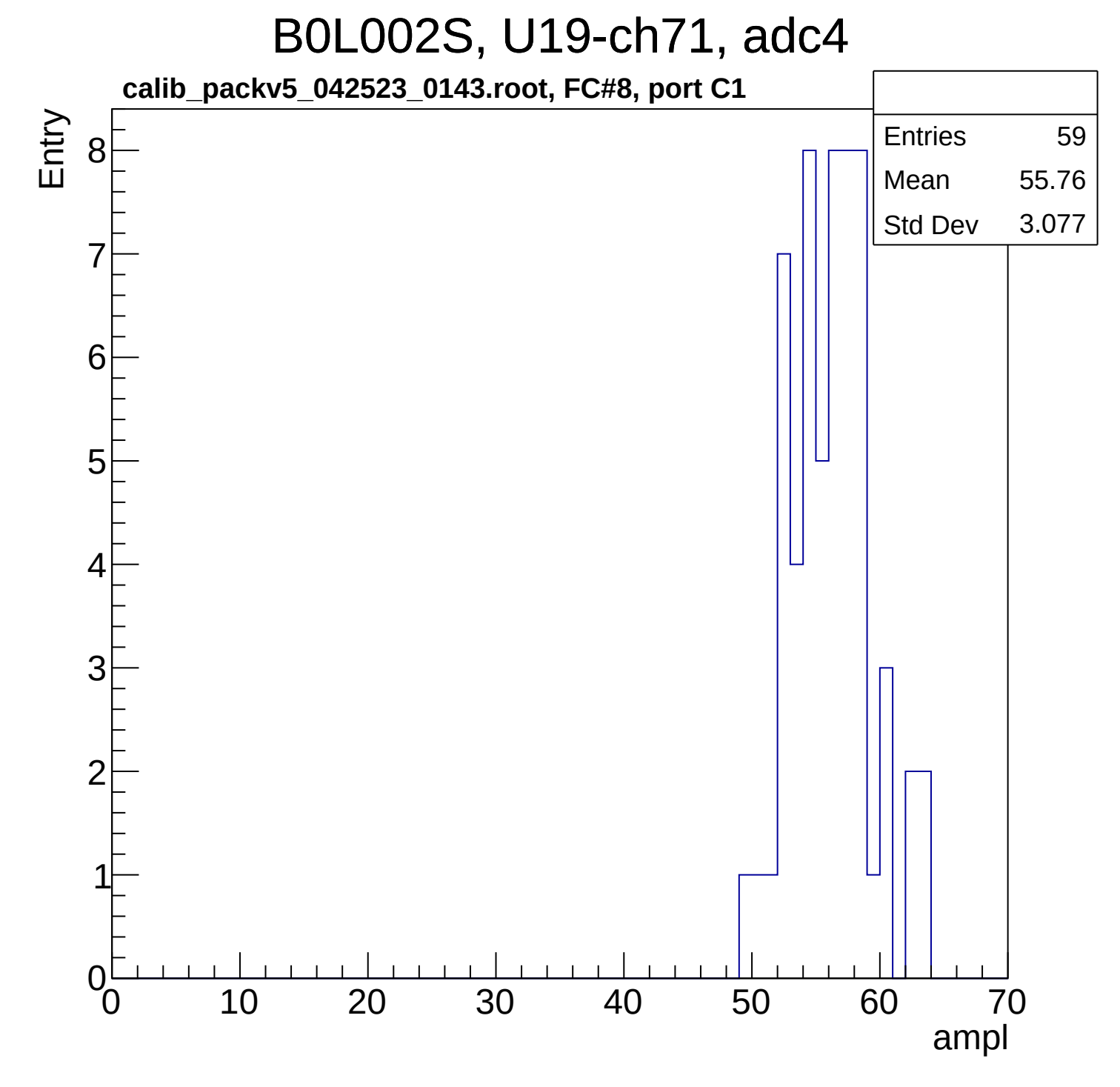

B0L002S, U19-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

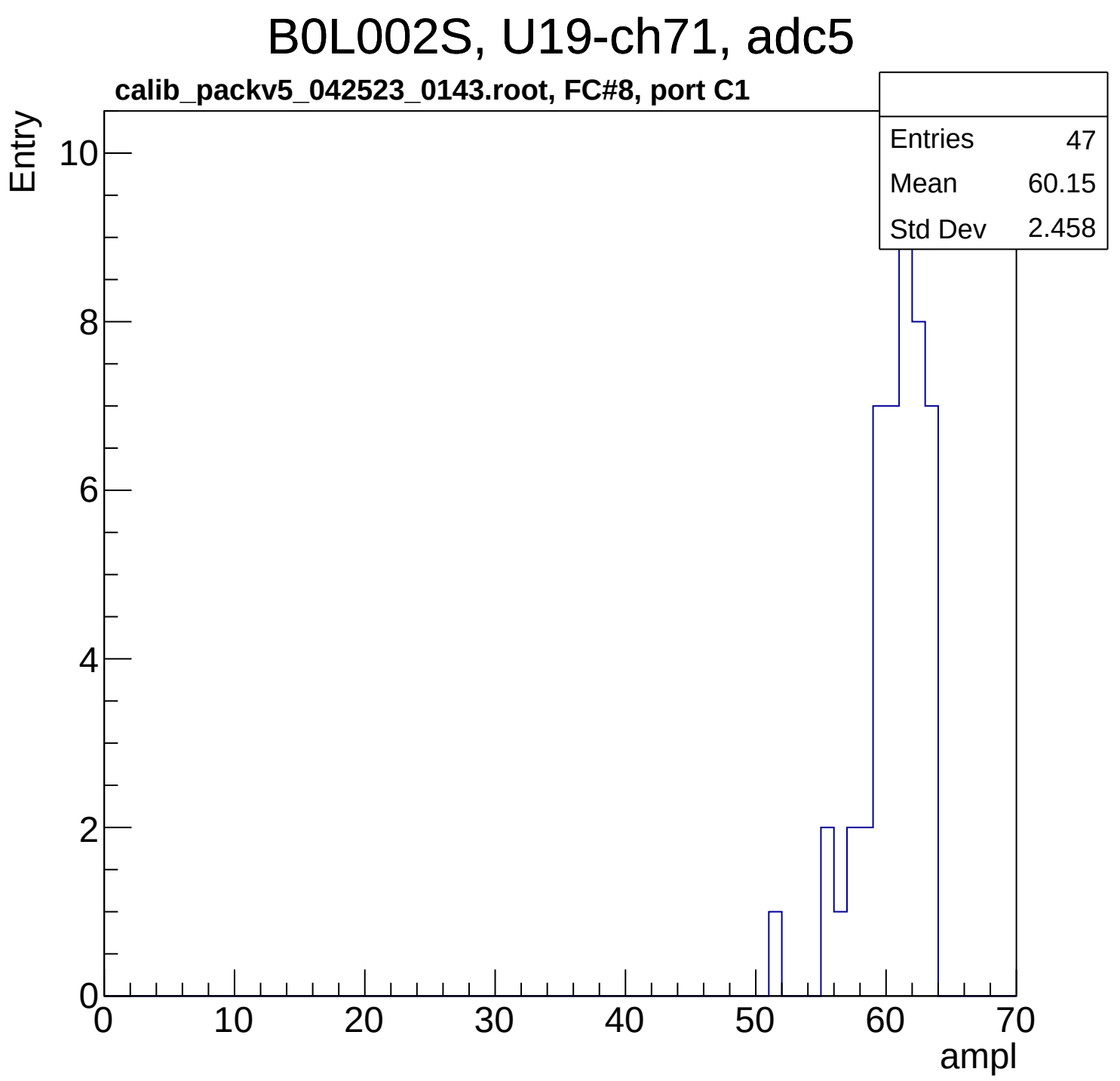

B0L002S, U19-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.03
Std Dev 5.014
Gaus mean : 31.4407
Gaus Width: 3.3104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

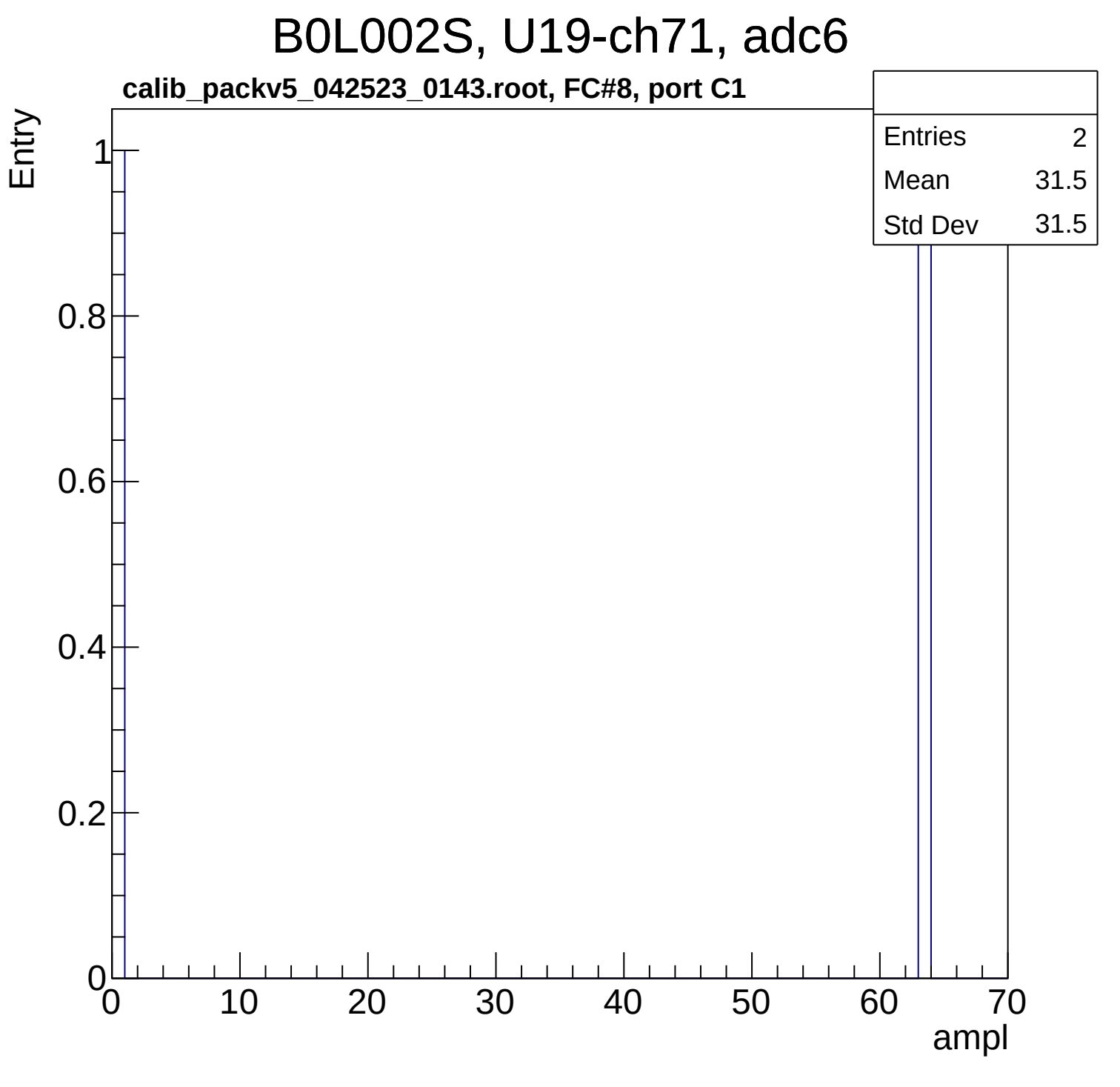

B0L002S, U19-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.14
Std Dev 3.017
Gaus mean : 37.4662
Gaus Width: 3.3275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

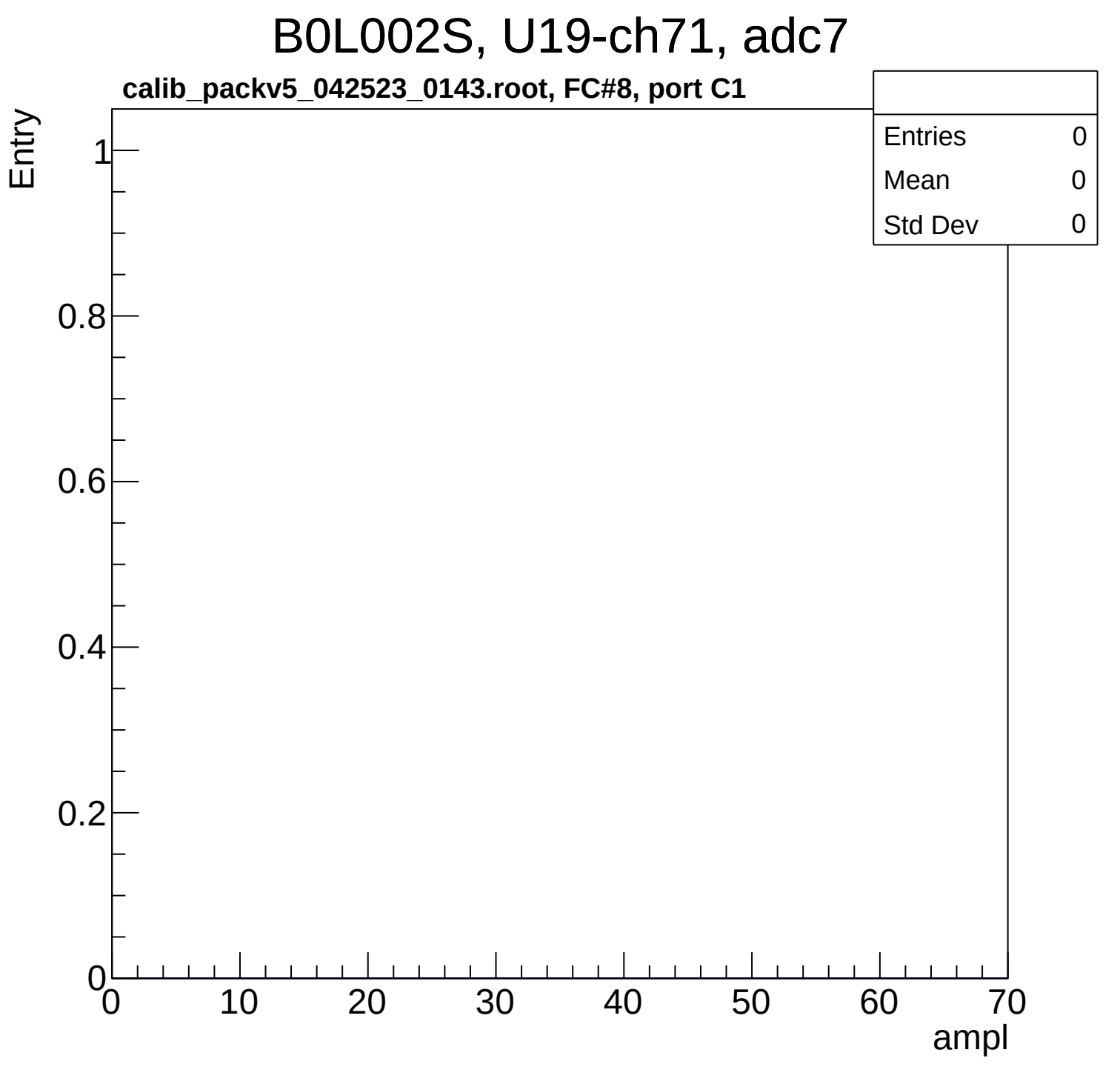

B0L002S, U19-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.05
Std Dev 2.843
Gaus mean : 43.5639
Gaus Width: 3.1416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

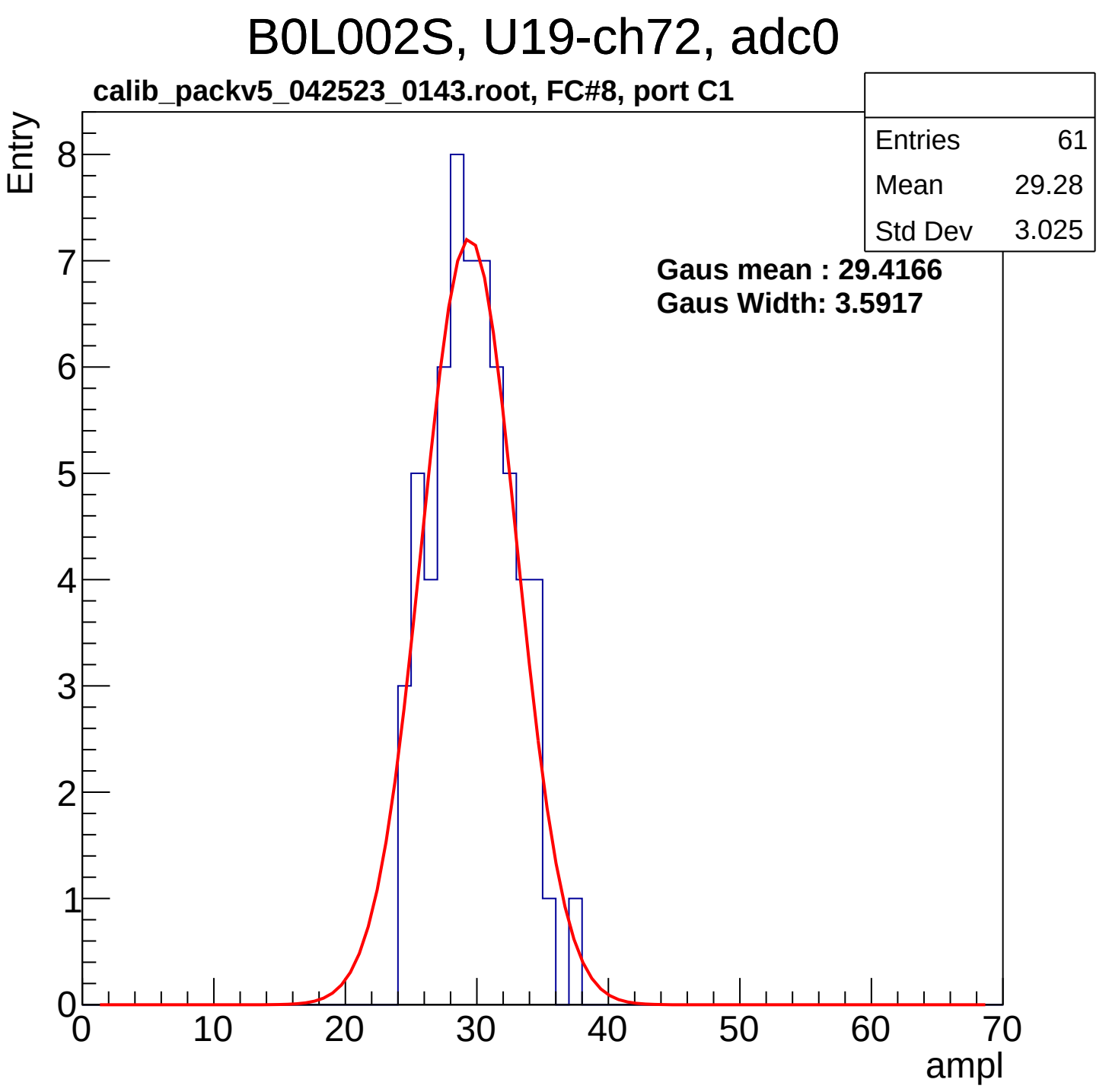

B0L002S, U19-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.76
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

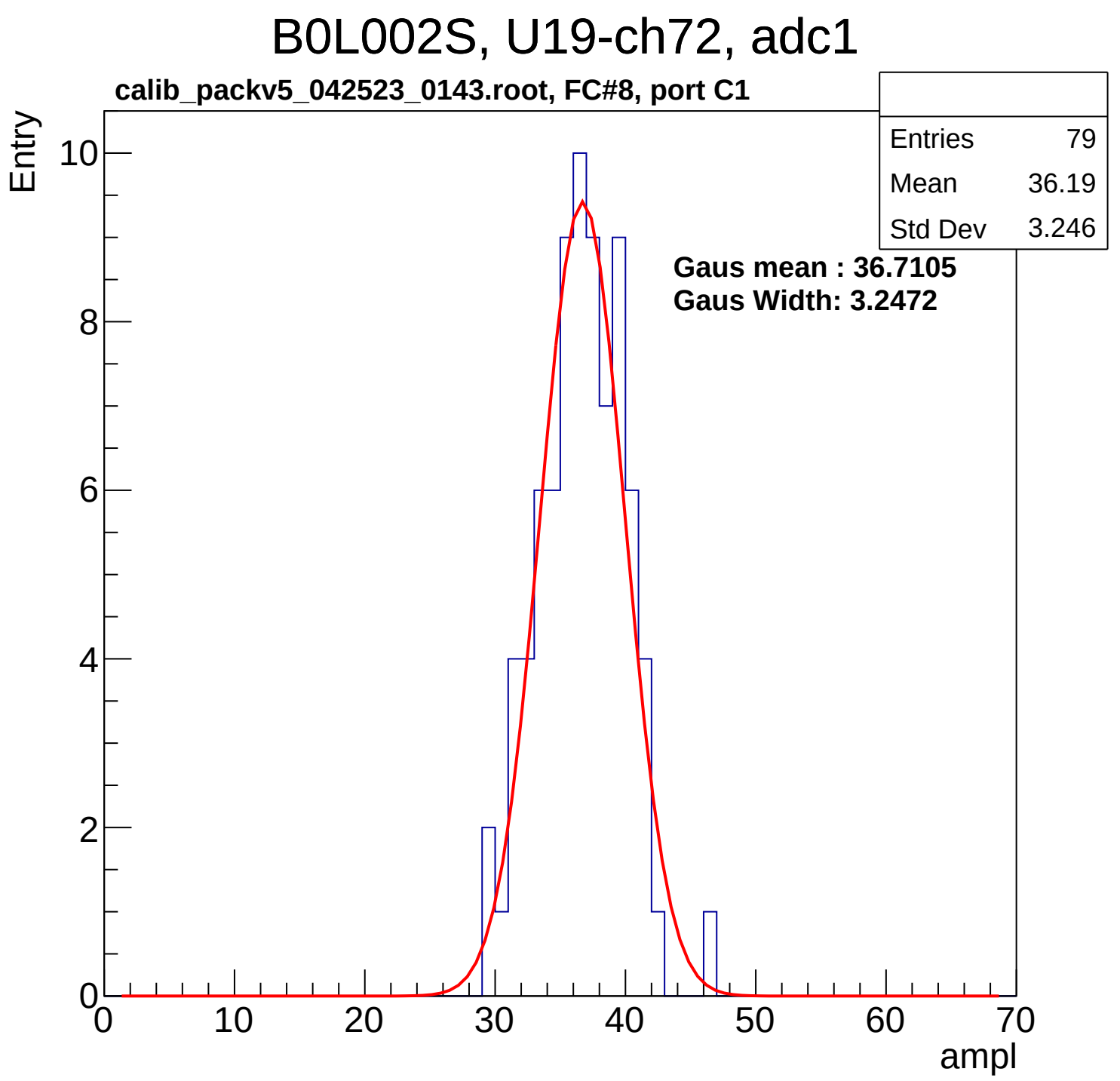

B0L002S, U19-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.76
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

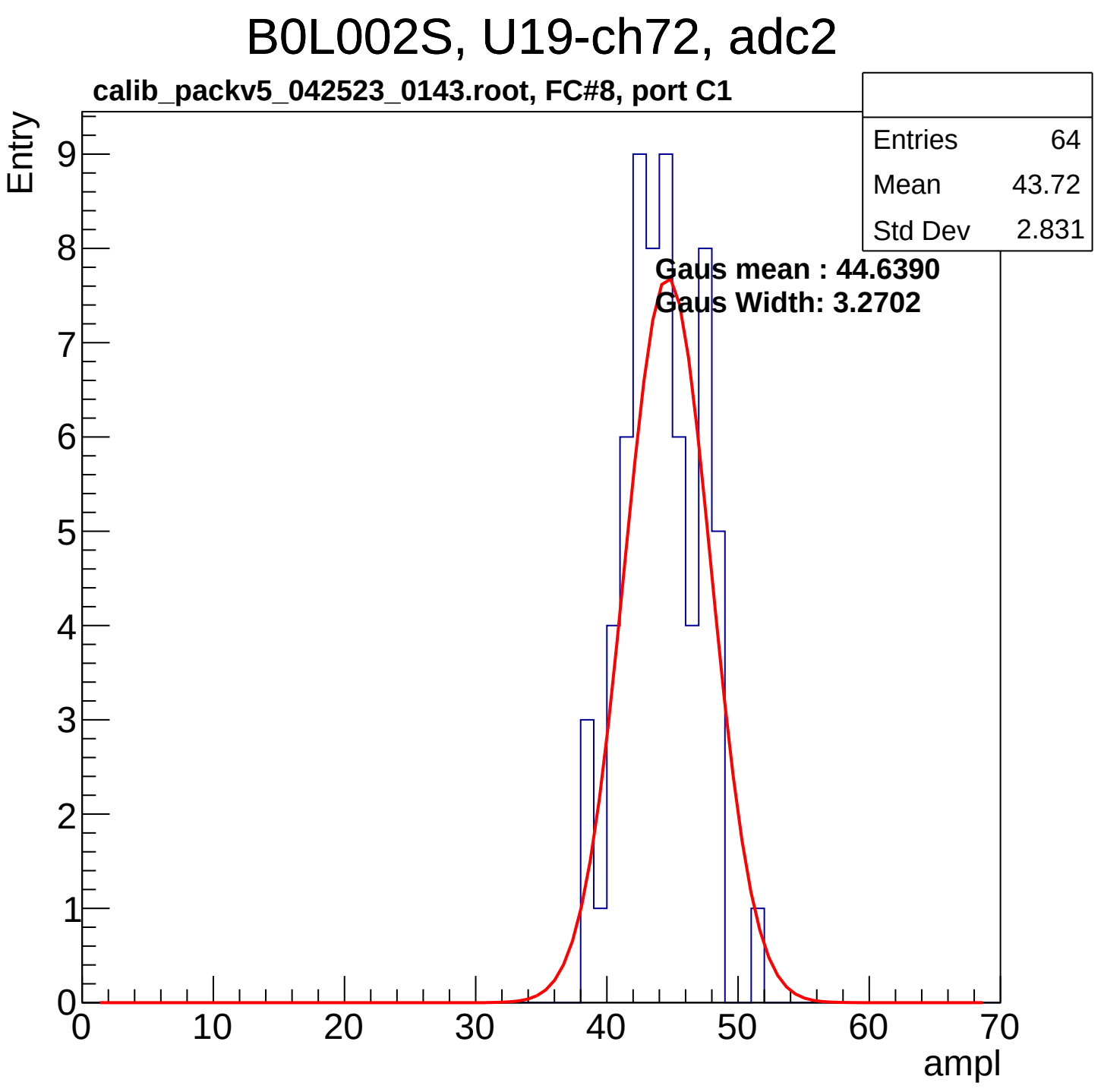

B0L002S, U19-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 60.15
Std Dev 2.458
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

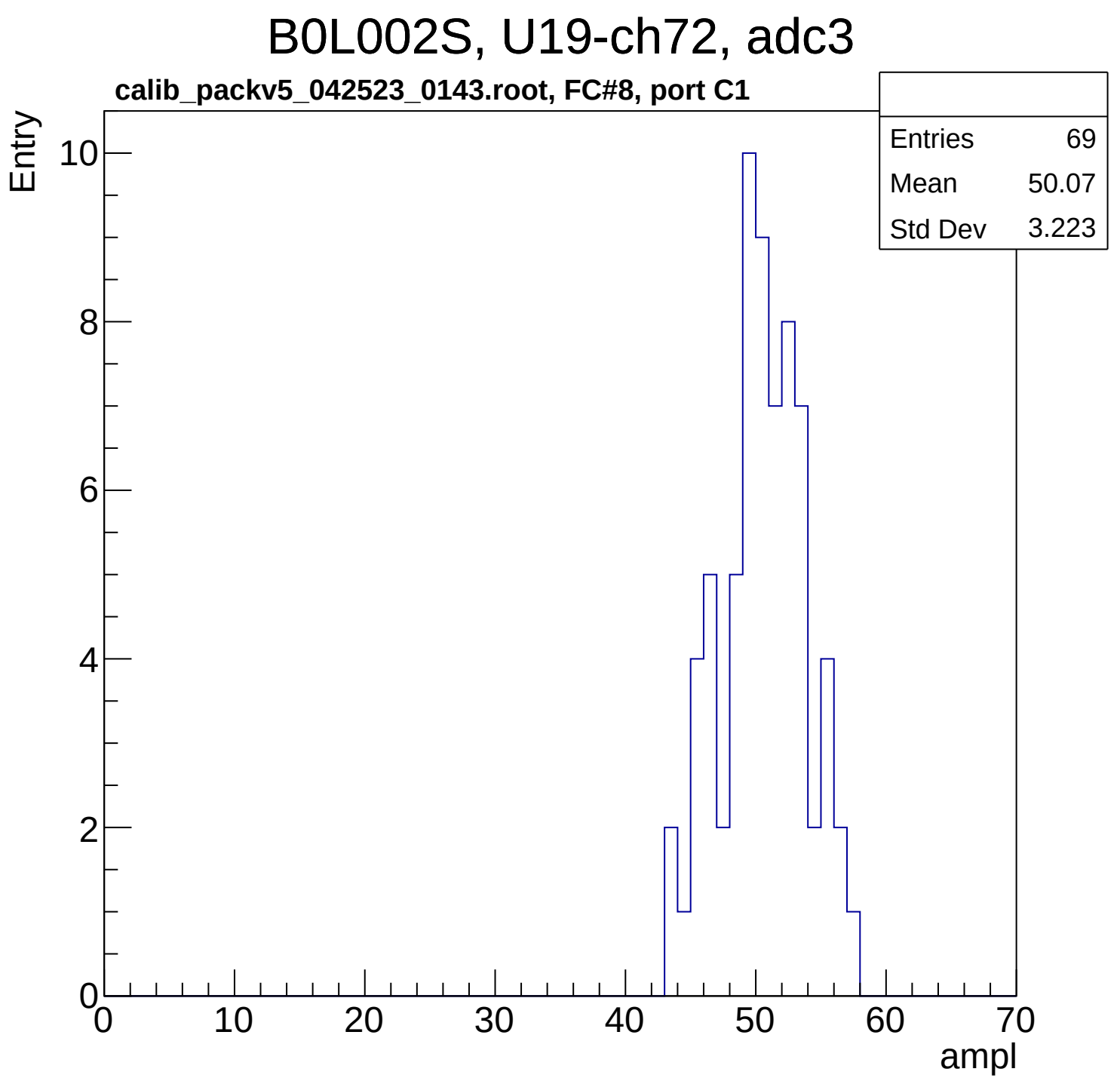

B0L002S, U19-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

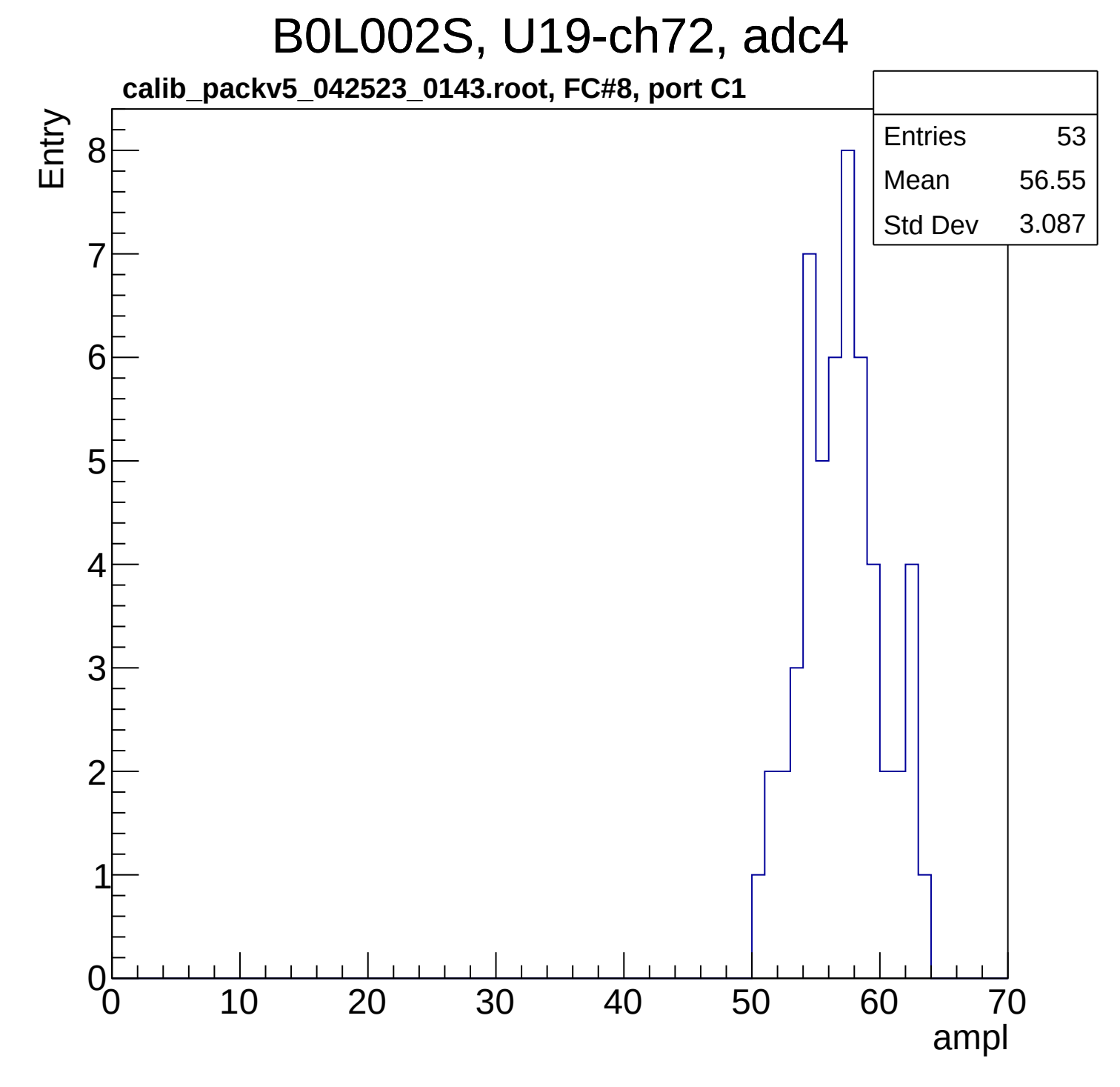

B0L002S, U19-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

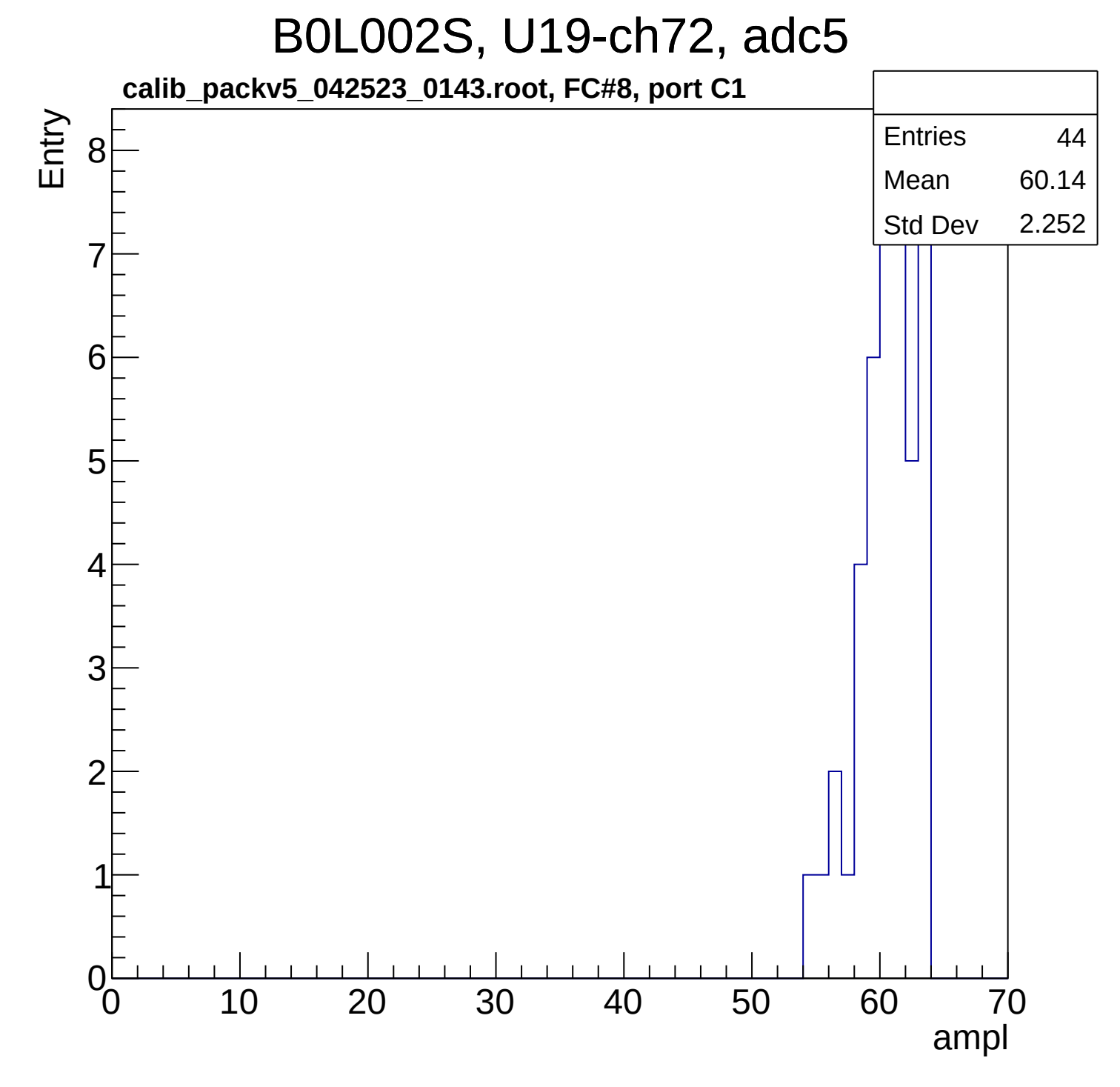

B0L002S, U19-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.28
Std Dev 3.025
Gaus mean : 29.4166
Gaus Width: 3.5917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

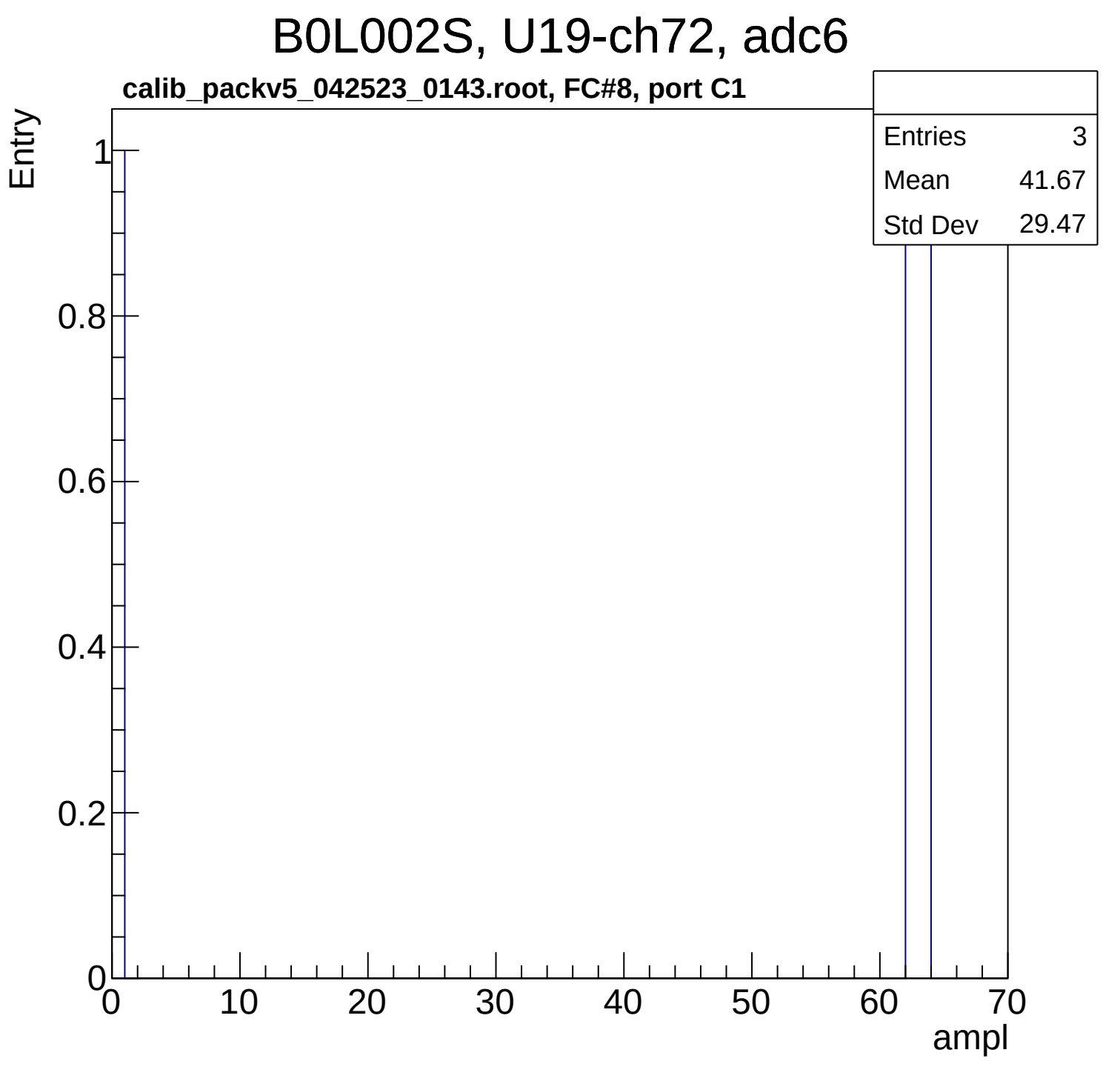

B0L002S, U19-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 36.19
Std Dev 3.246
Gaus mean : 36.7105
Gaus Width: 3.2472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

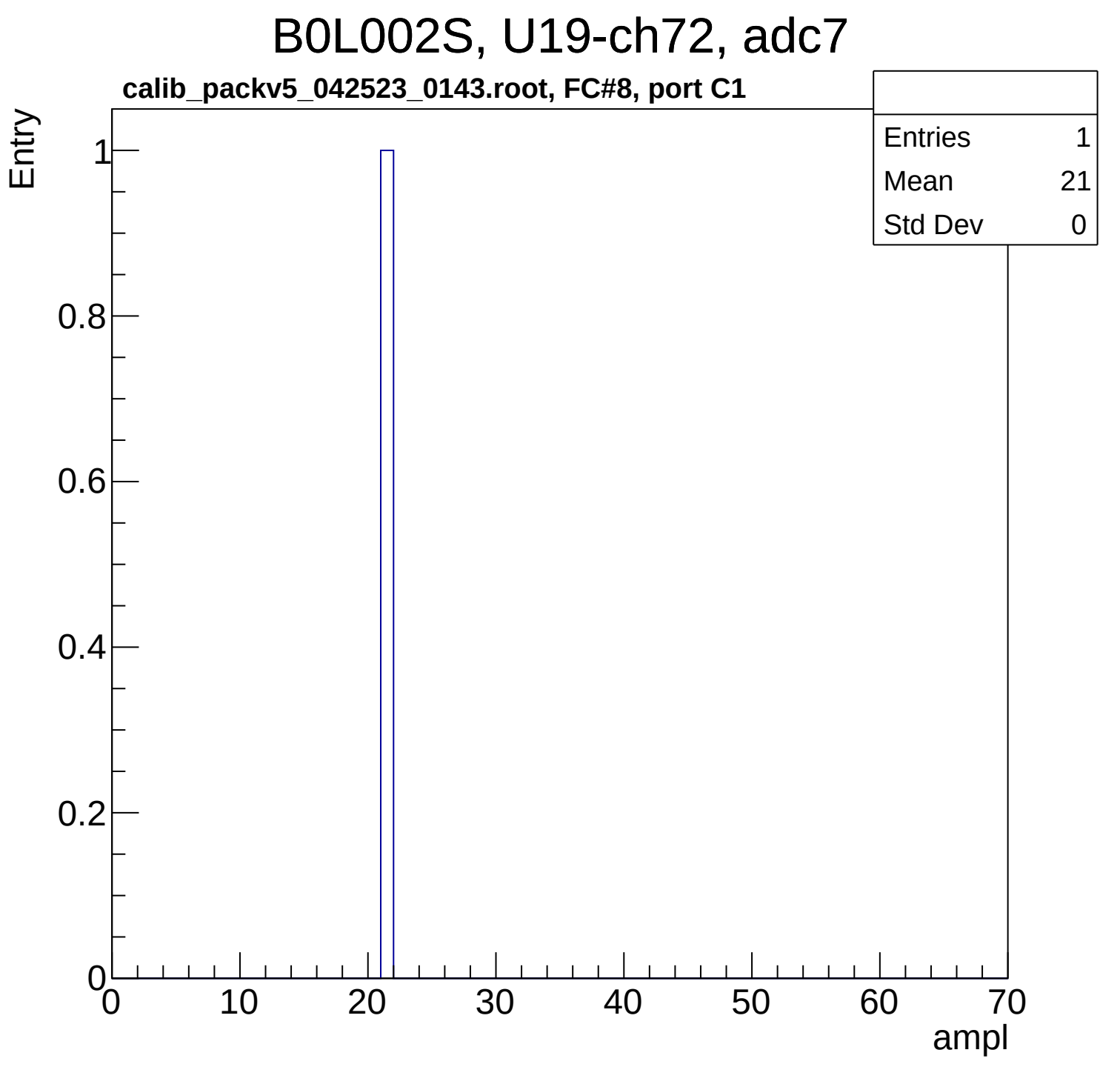

B0L002S, U19-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.72
Std Dev 2.831
Gaus mean : 44.6390
Gaus Width: 3.2702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

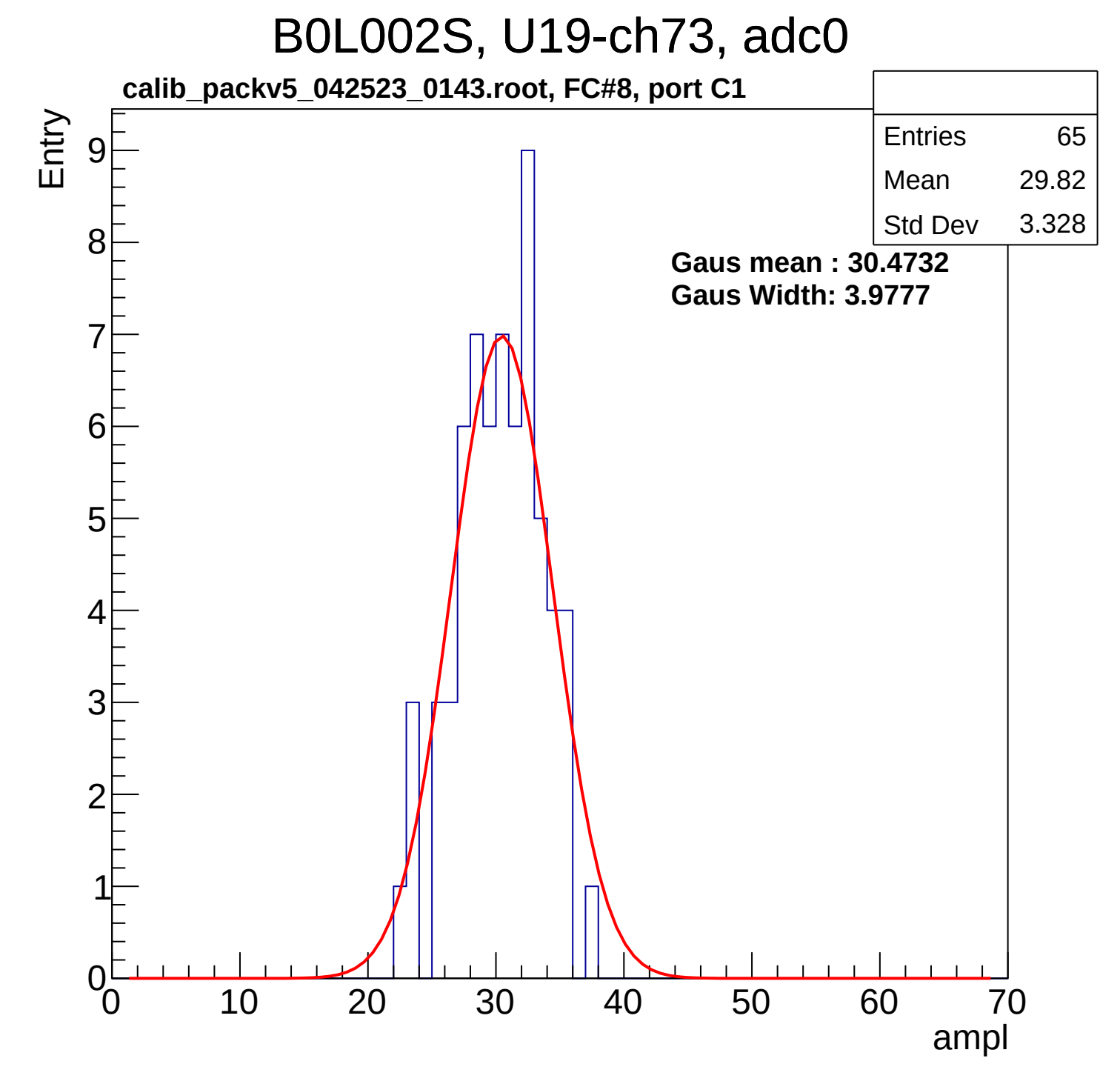

B0L002S, U19-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 50.07
Std Dev 3.223
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

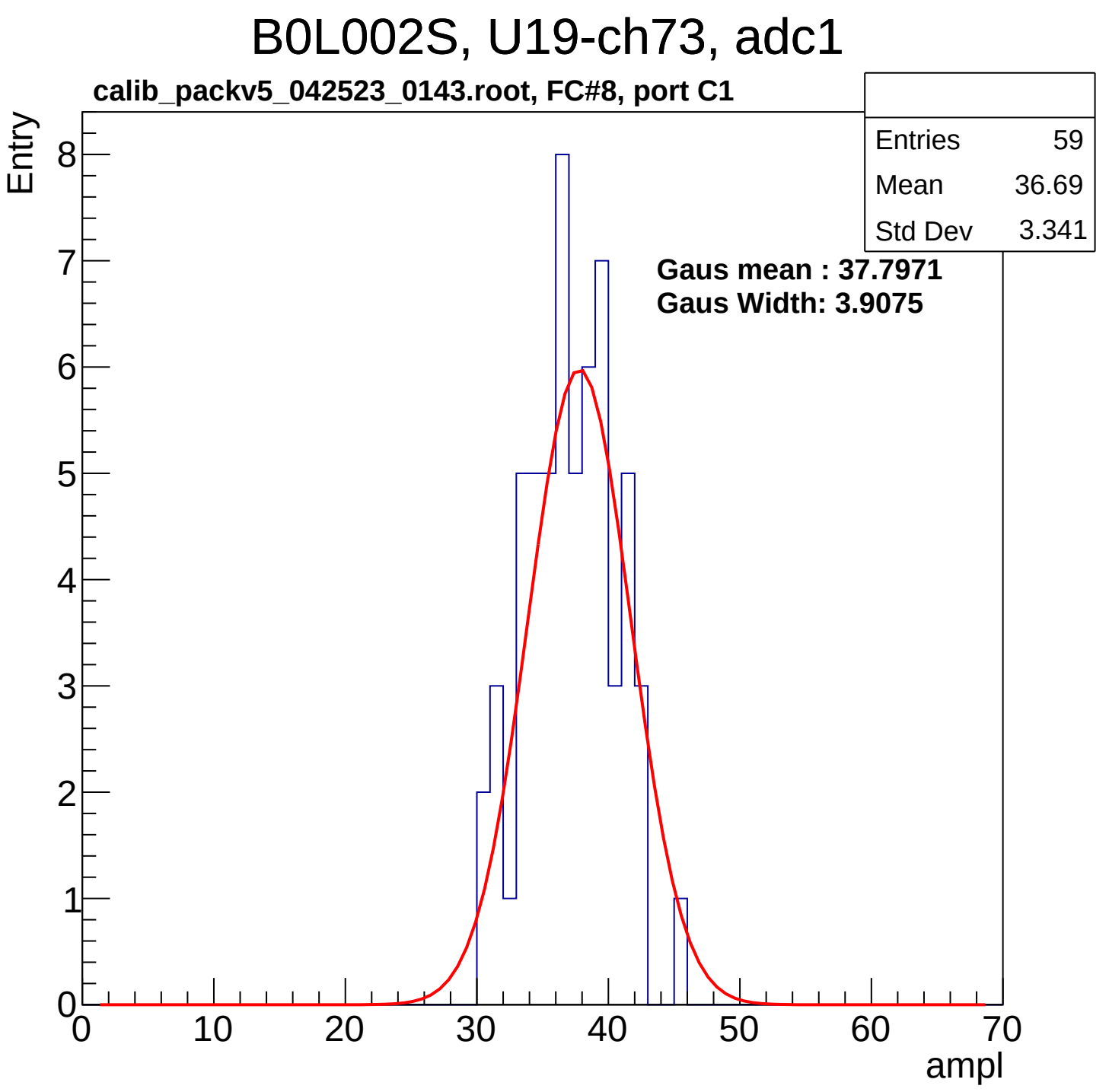

B0L002S, U19-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.55
Std Dev 3.087
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

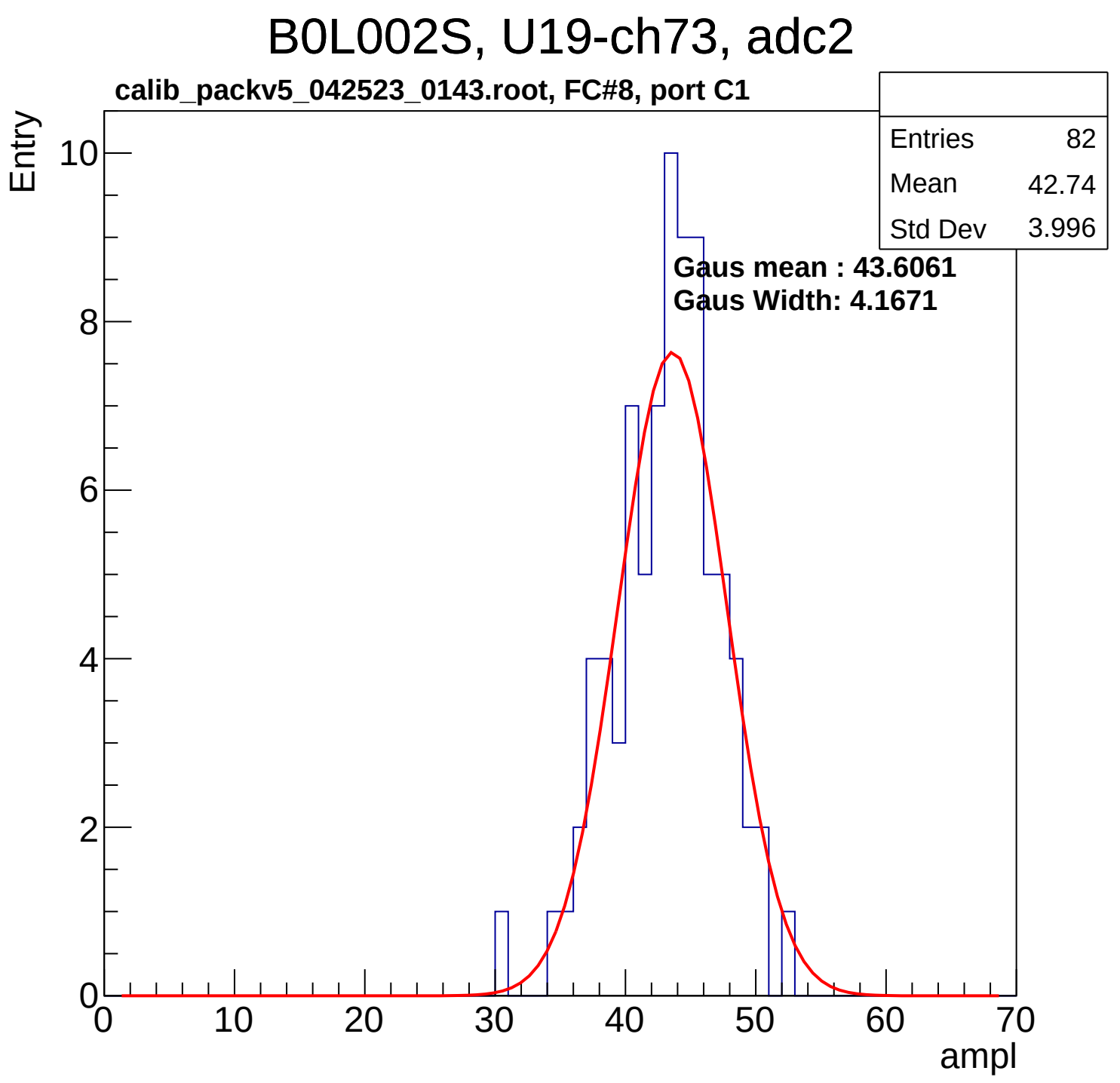

B0L002S, U19-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 60.14
Std Dev 2.252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

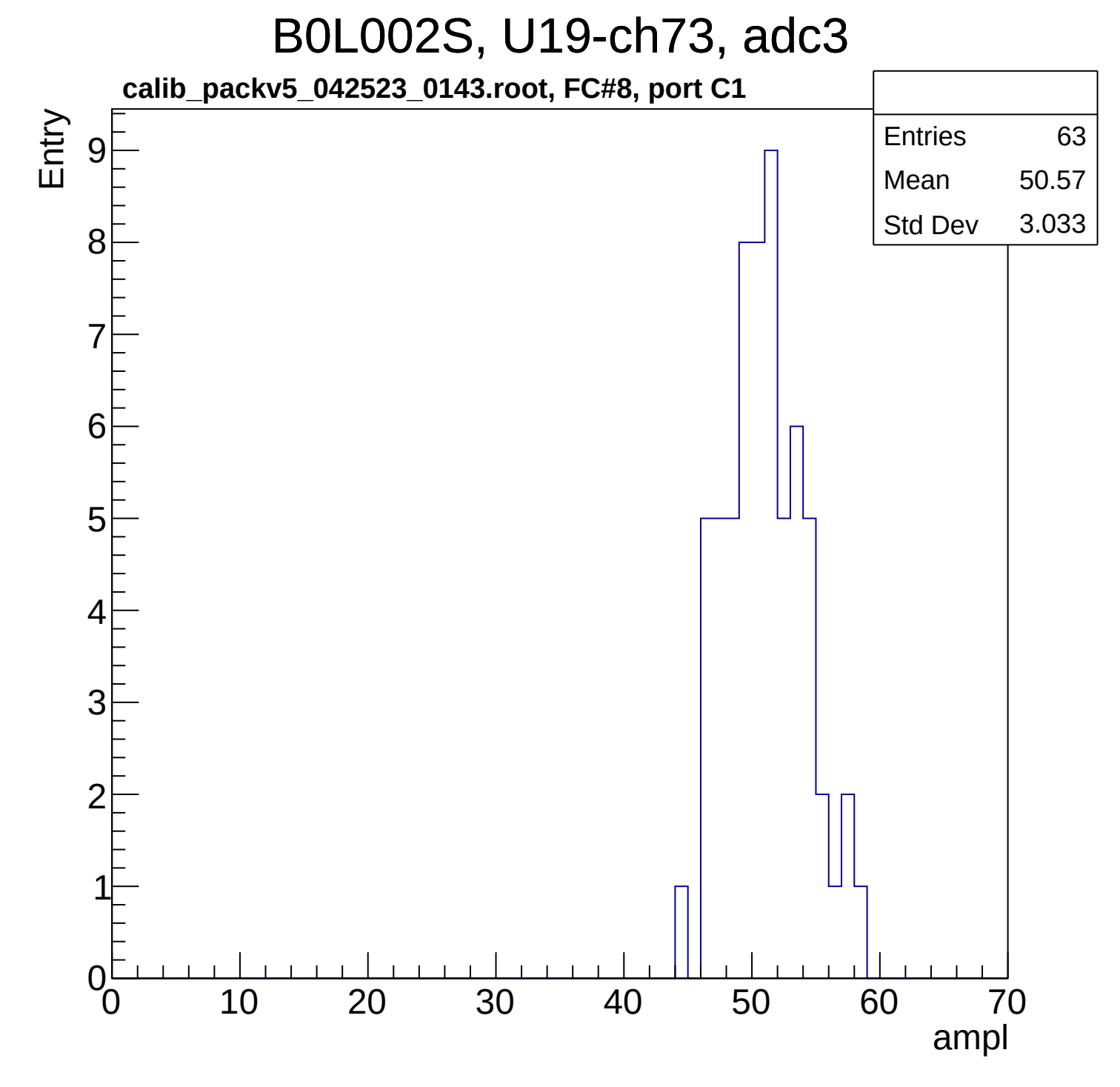

B0L002S, U19-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

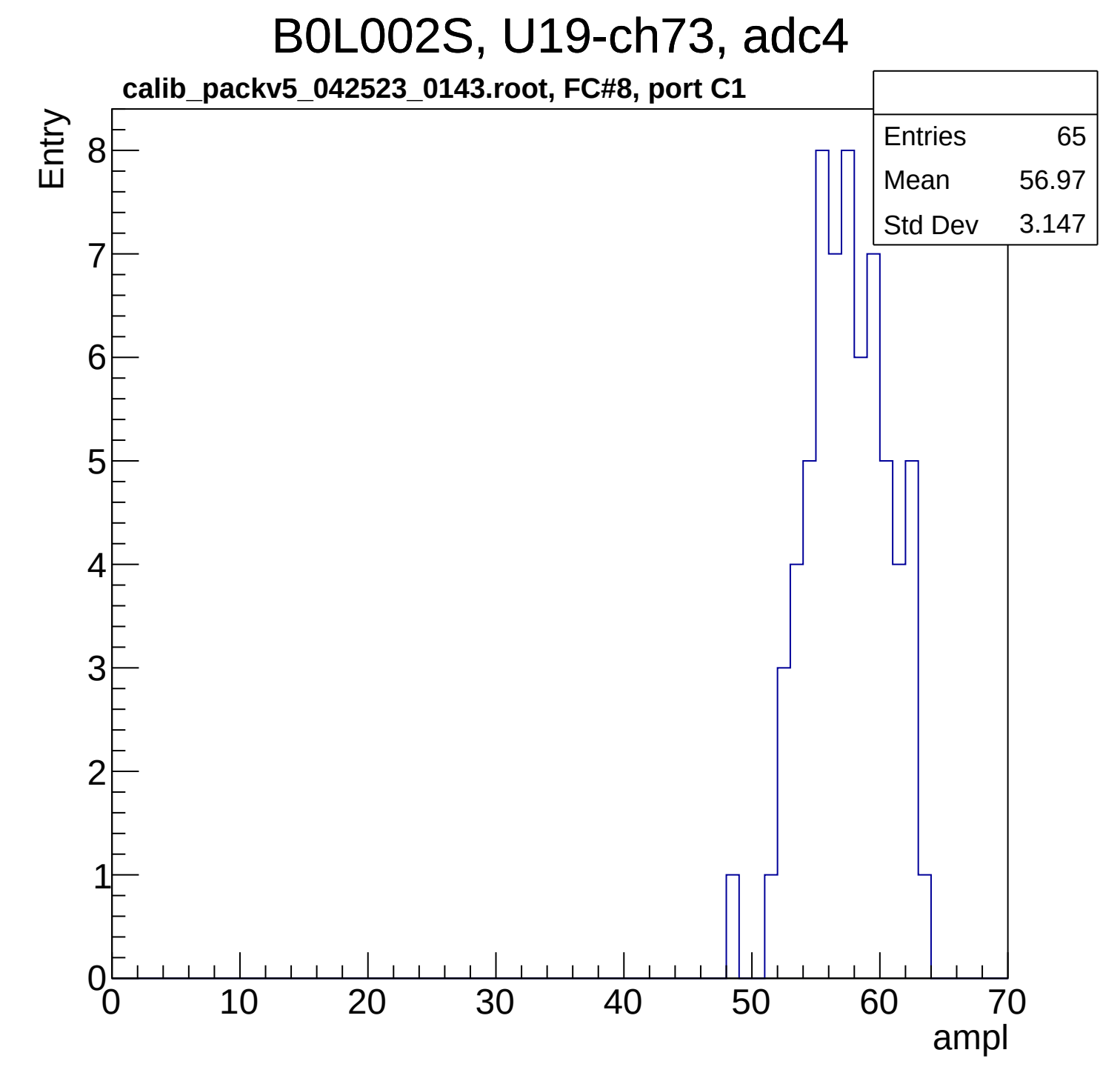

B0L002S, U19-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

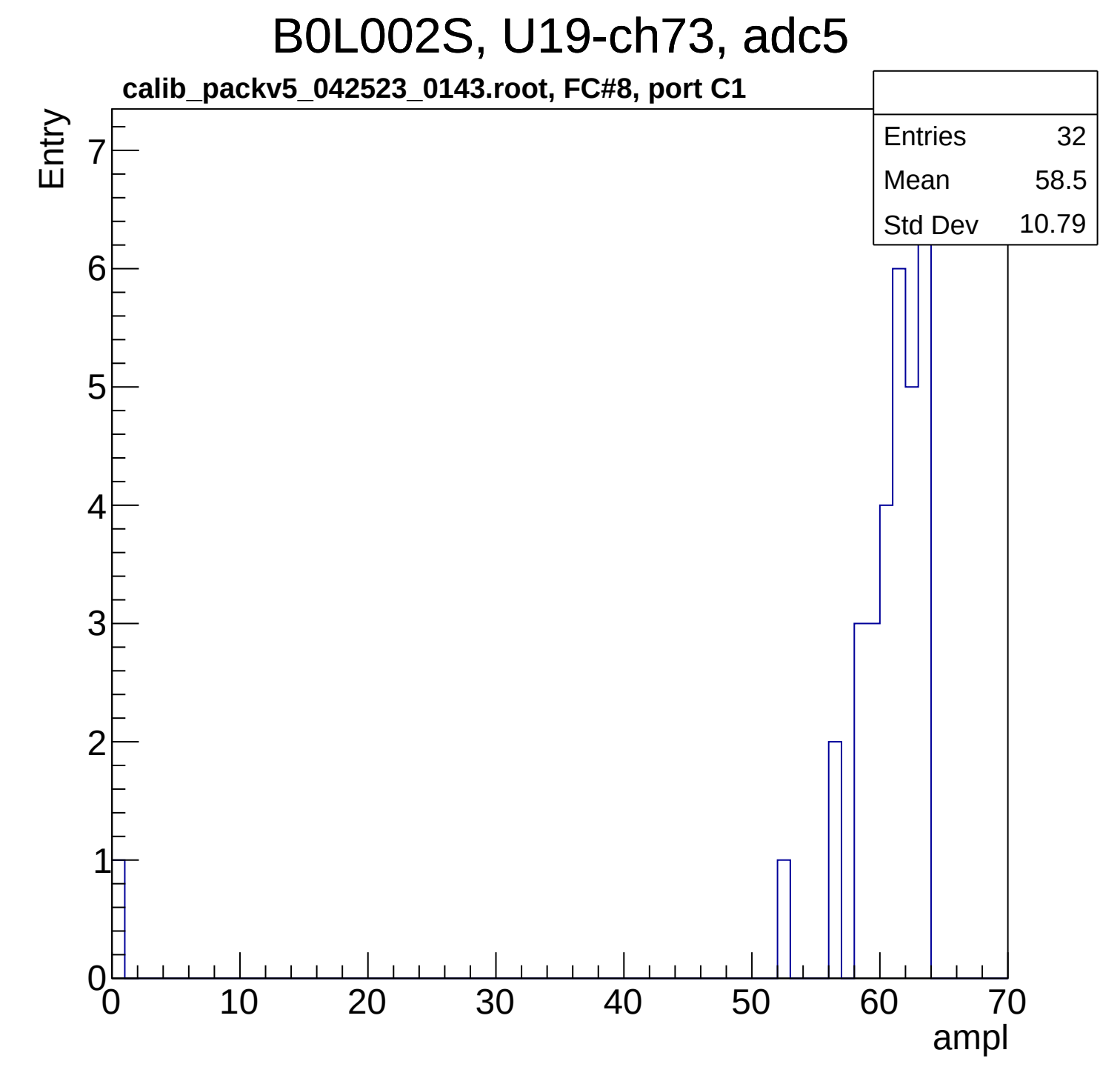

B0L002S, U19-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.82
Std Dev 3.328
Gaus mean : 30.4732
Gaus Width: 3.9777
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

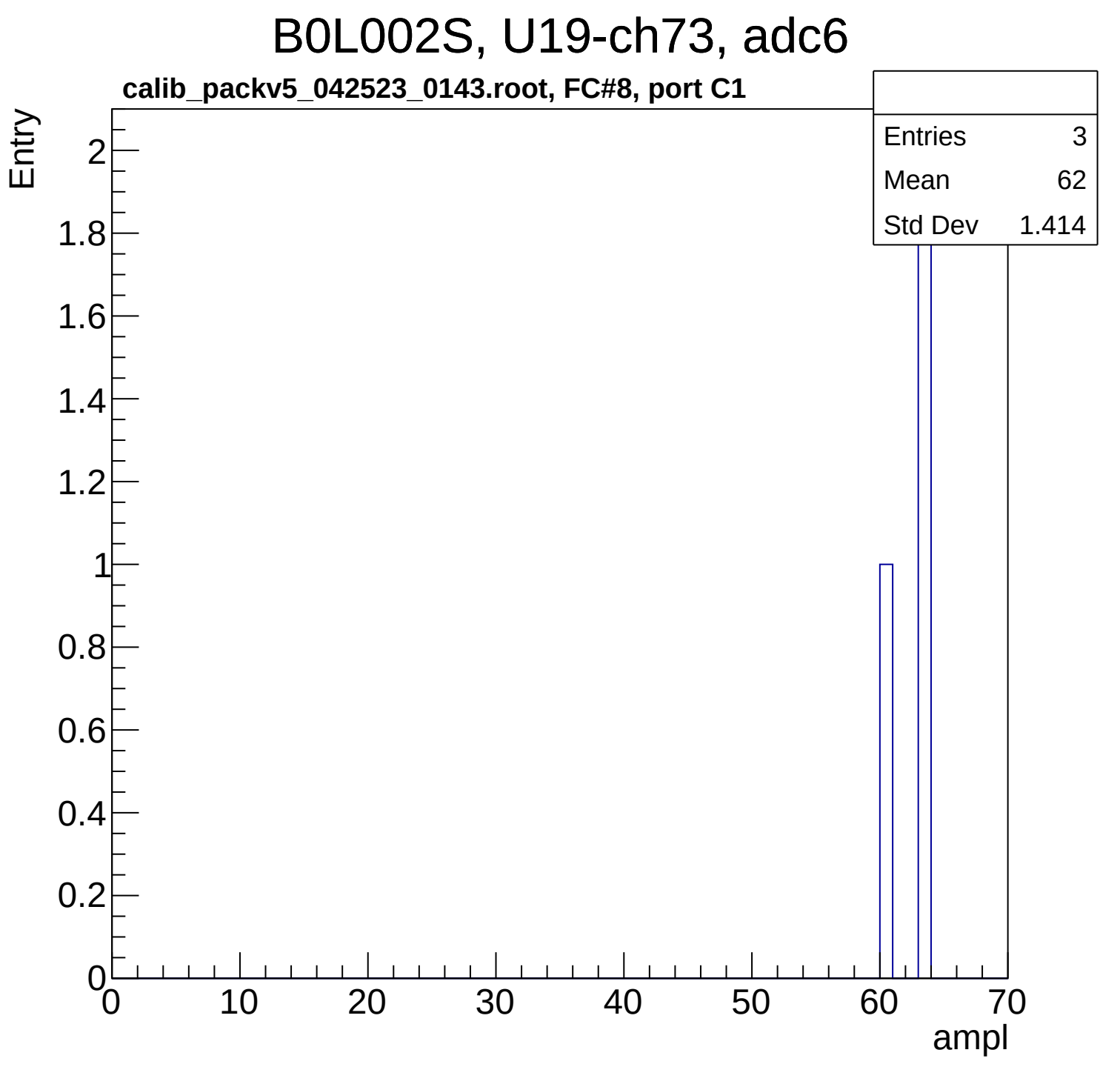

B0L002S, U19-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 36.69
Std Dev 3.341
Gaus mean : 37.7971
Gaus Width: 3.9075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

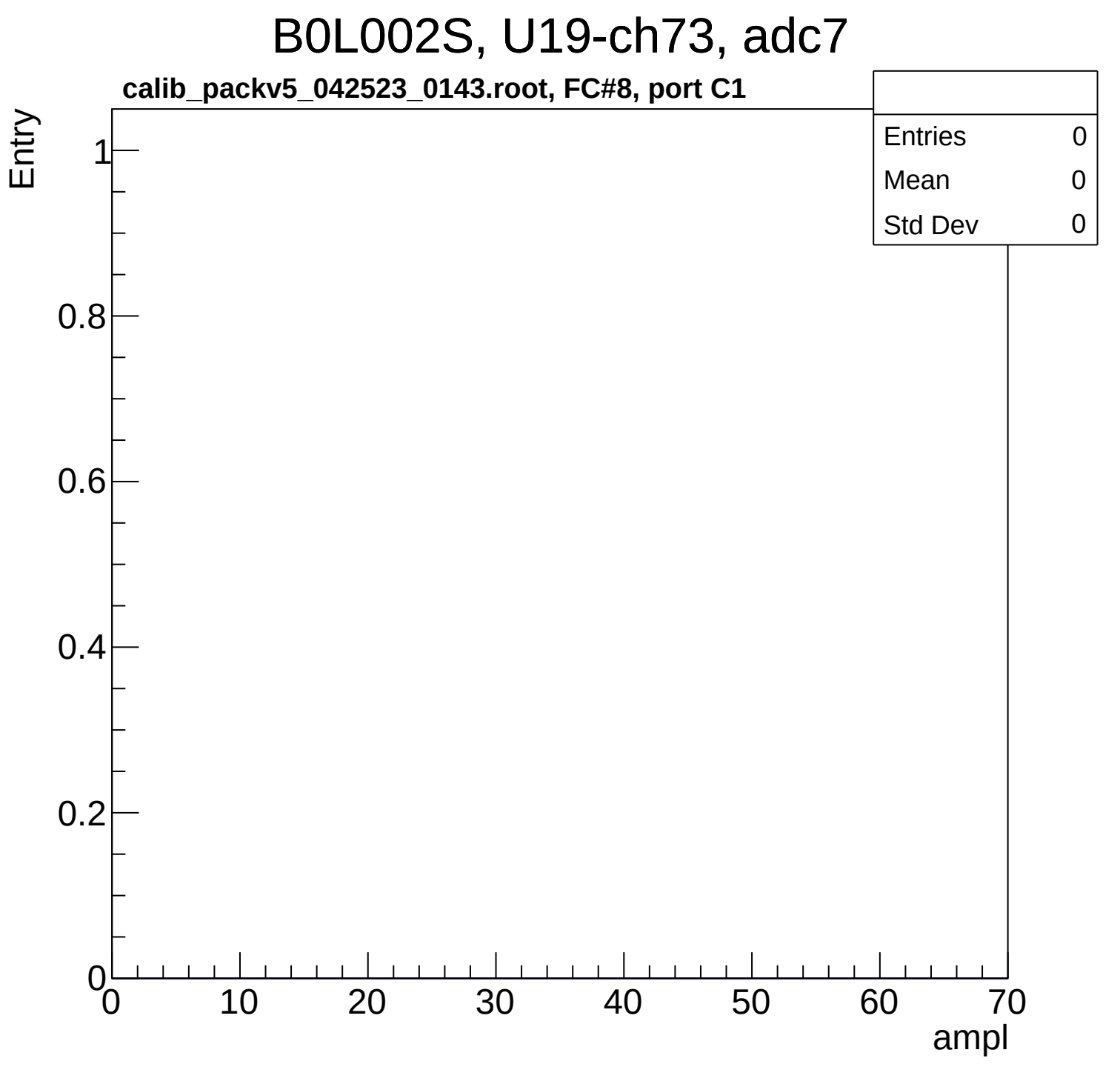

B0L002S, U19-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 42.74
Std Dev 3.996
Gaus mean : 43.6061
Gaus Width: 4.1671
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

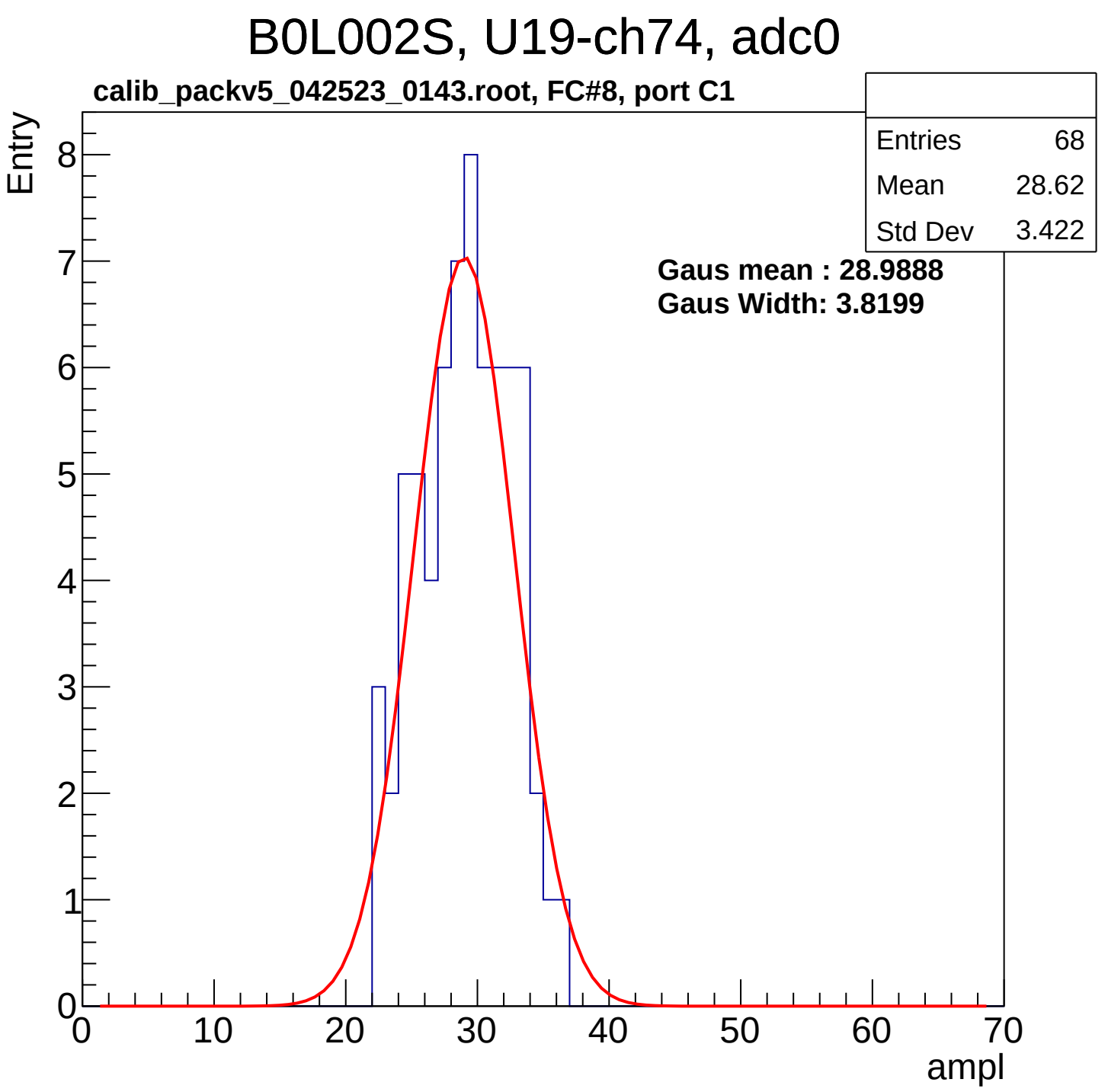

B0L002S, U19-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.57
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

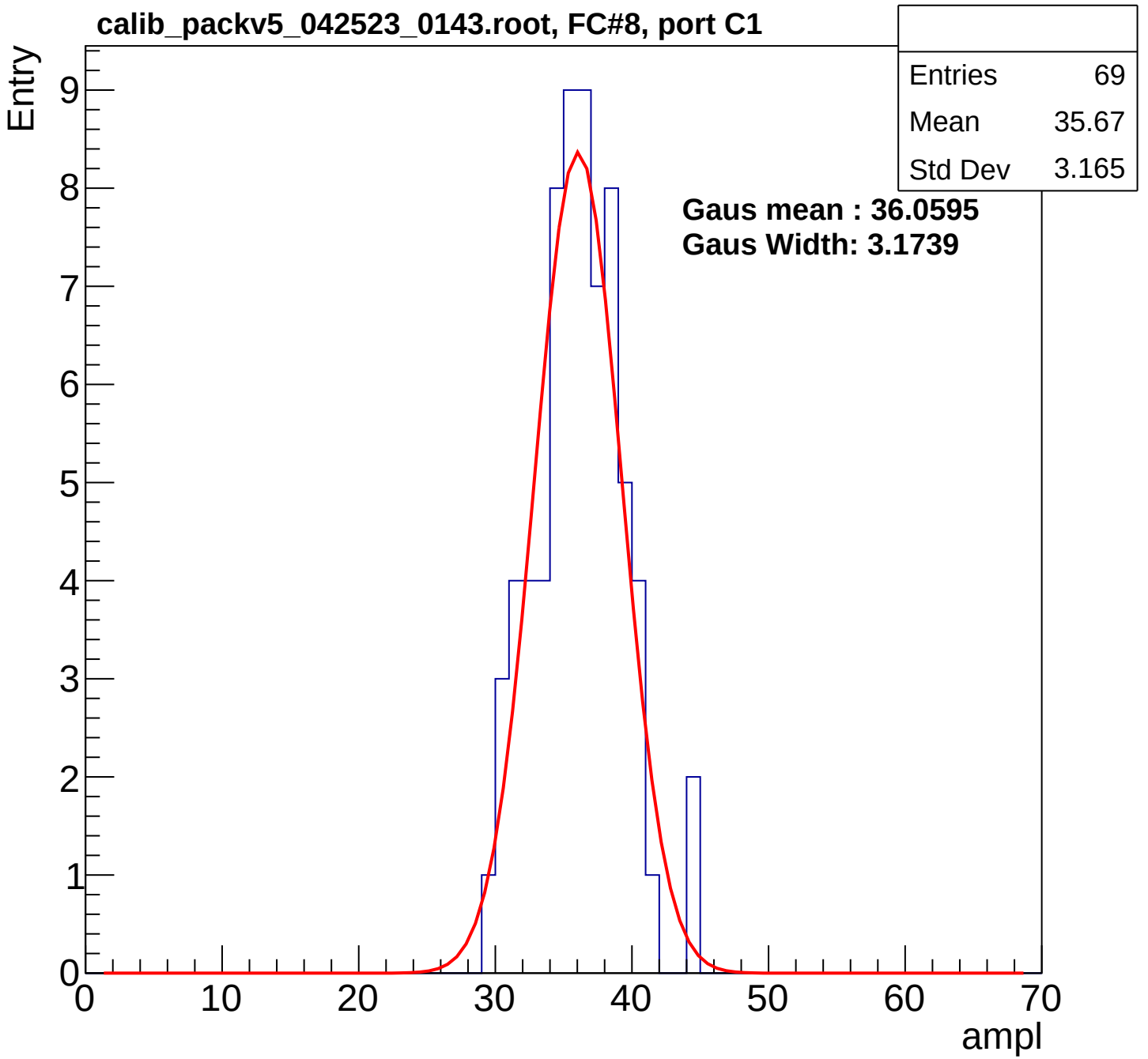

B0L002S, U19-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.97
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

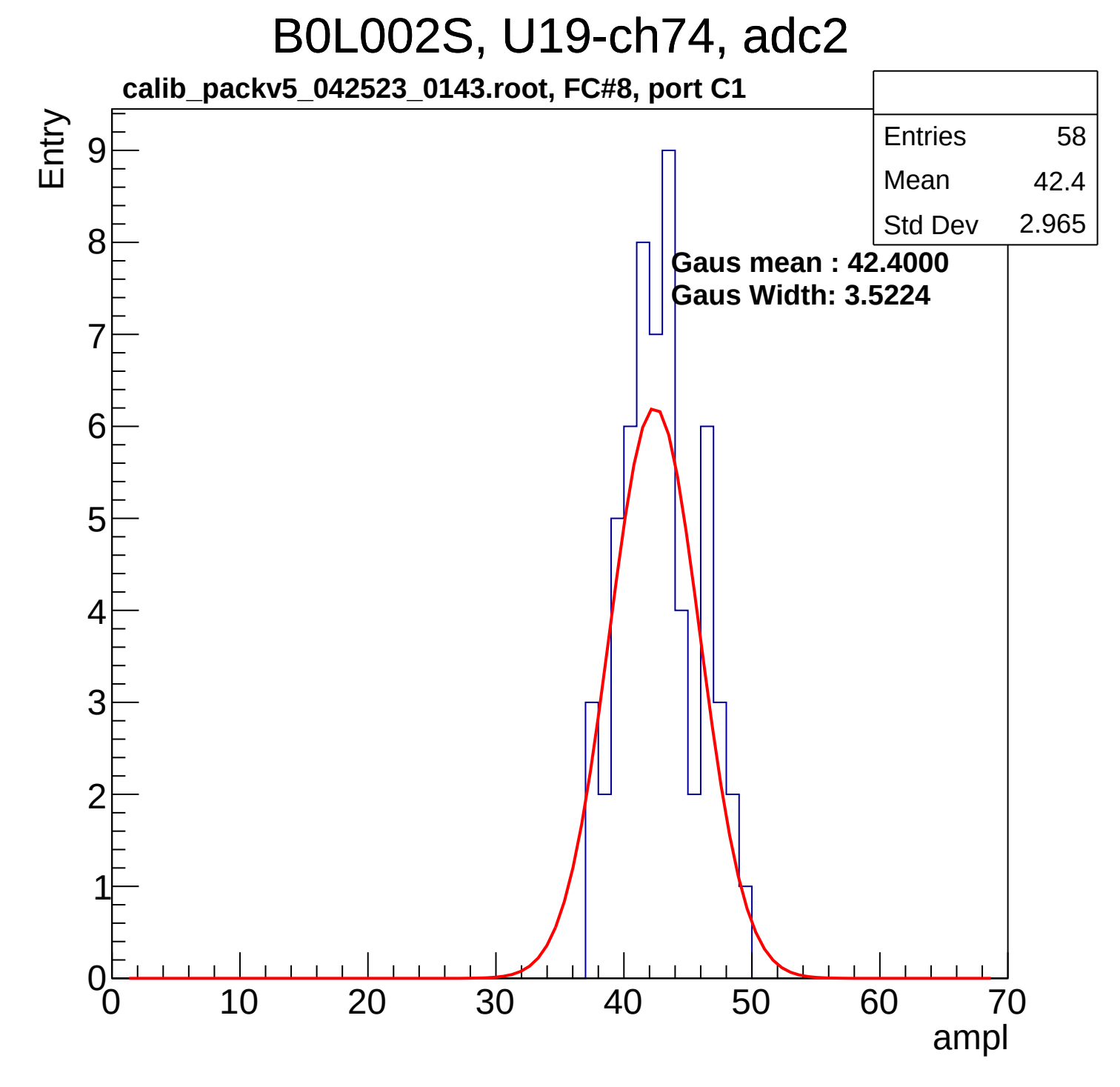

B0L002S, U19-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.5
Std Dev 10.79
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

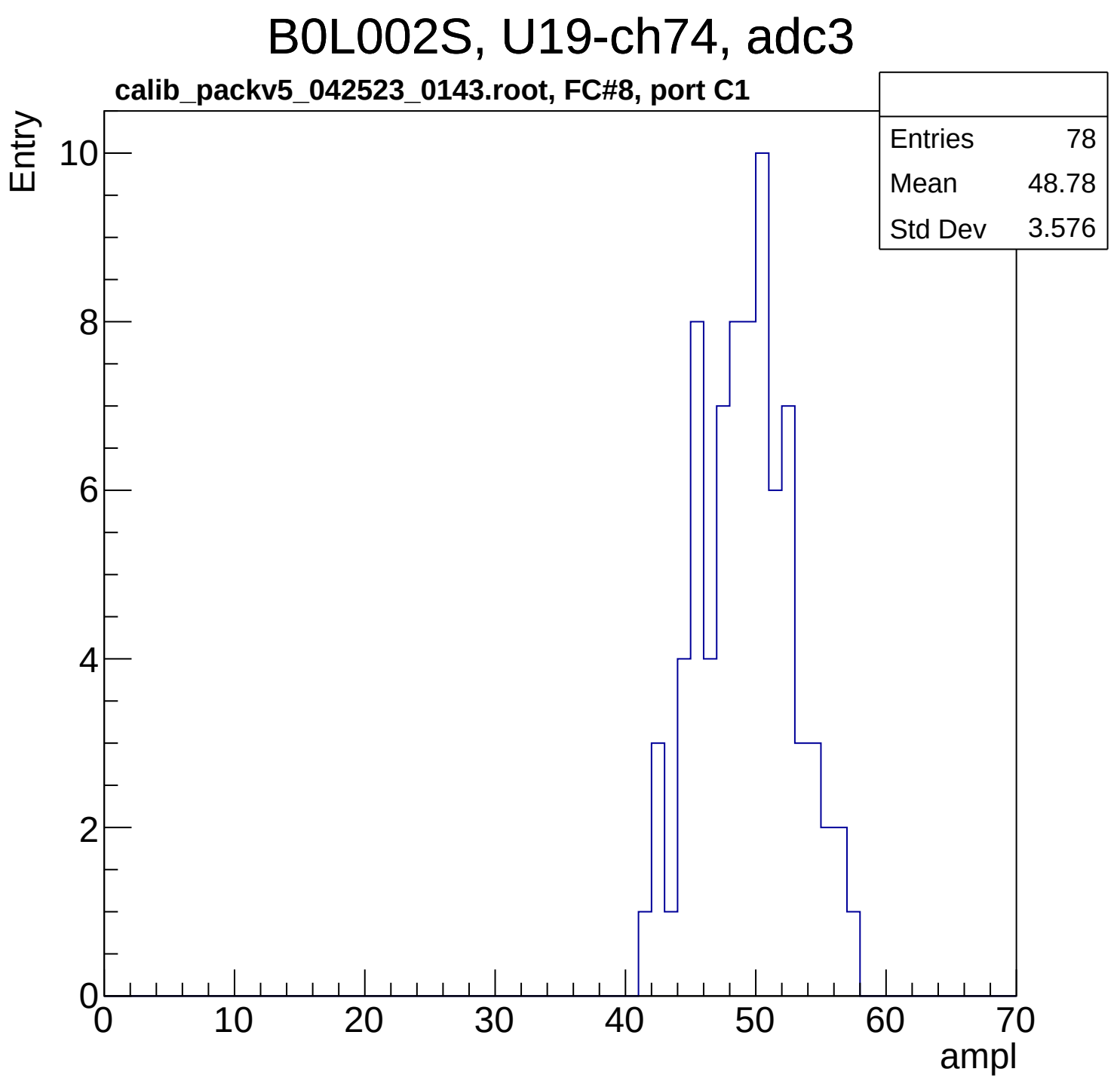

B0L002S, U19-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

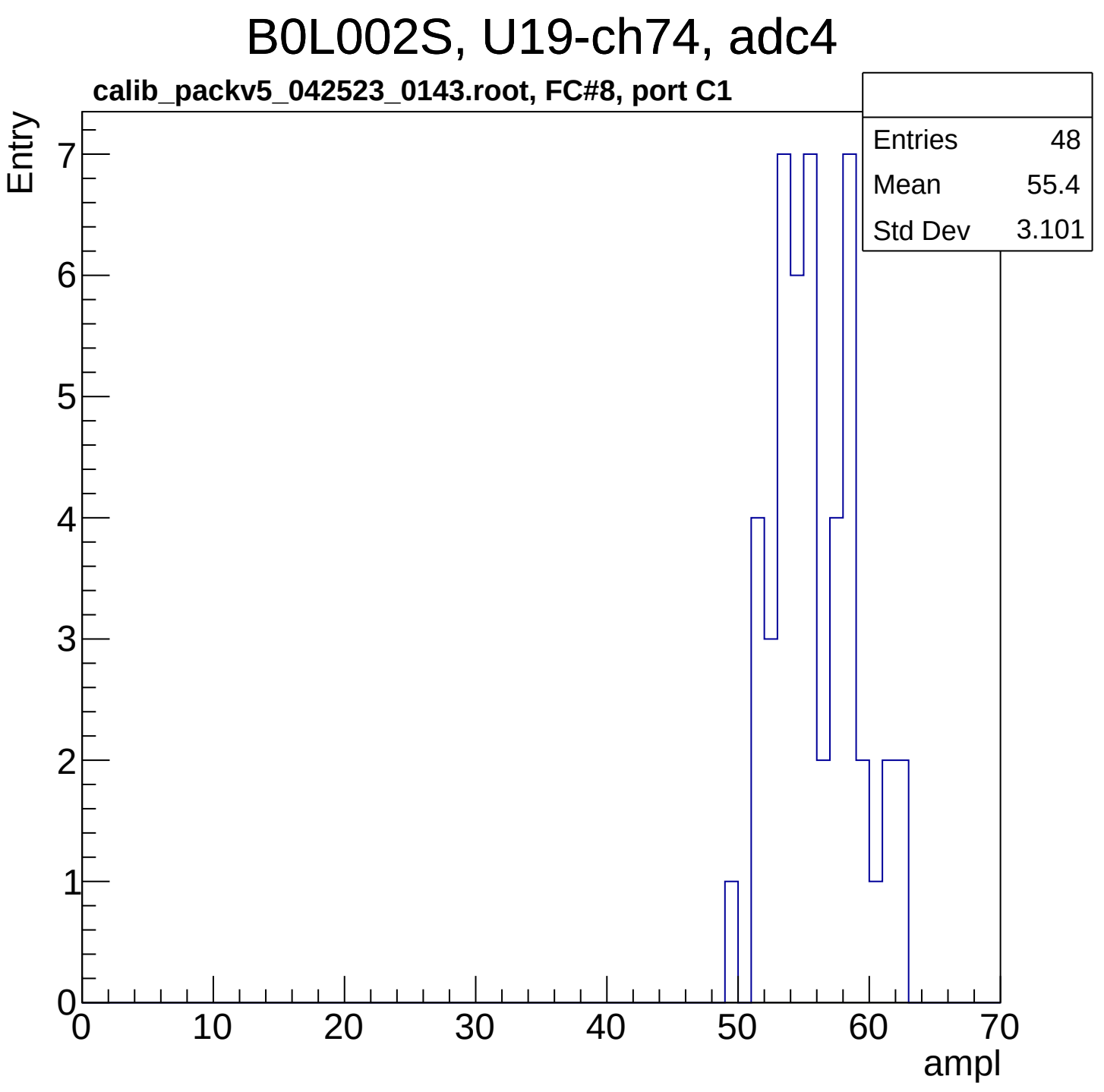

B0L002S, U19-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

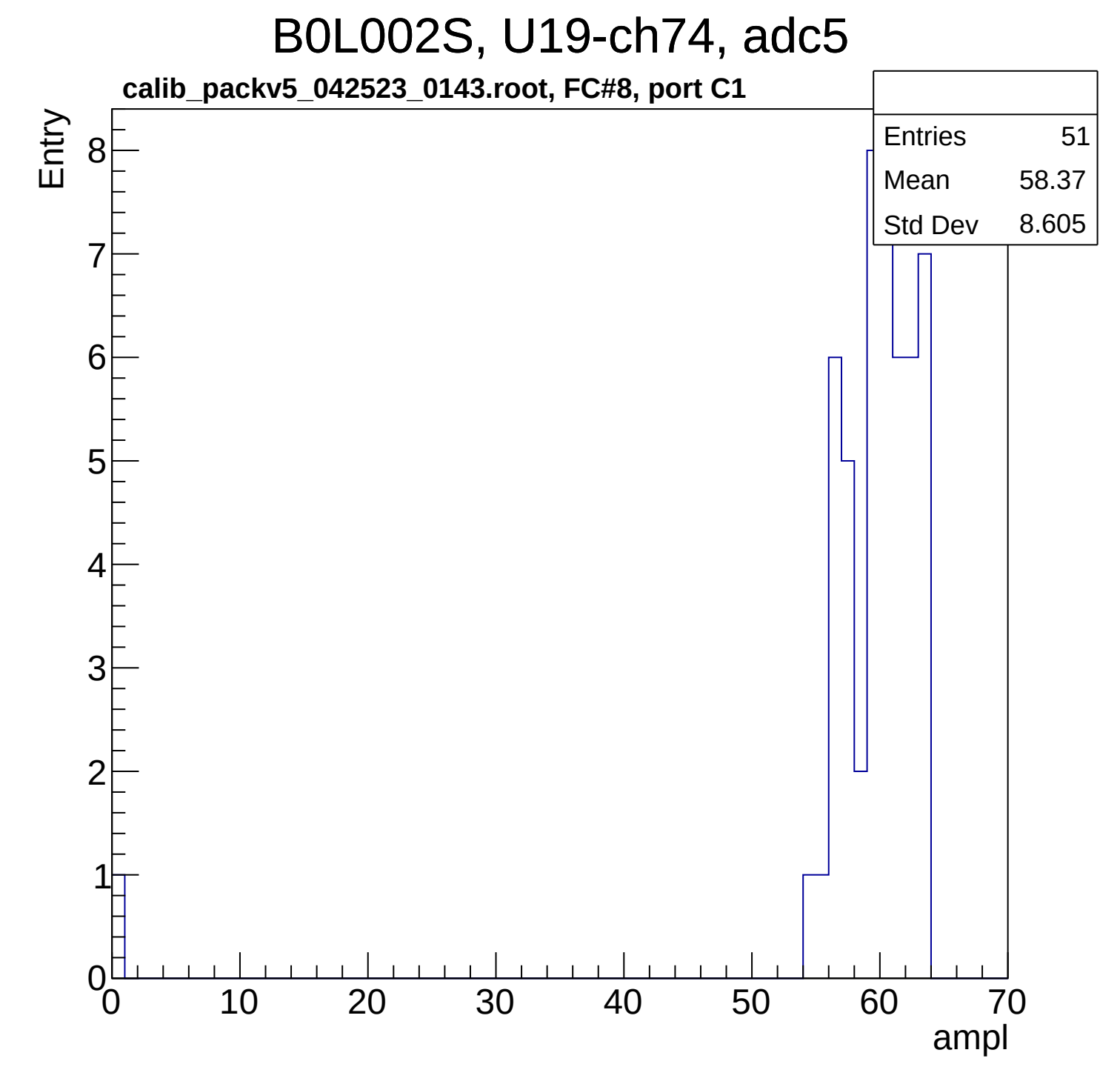

B0L002S, U19-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 28.62
Std Dev 3.422
Gaus mean : 28.9888
Gaus Width: 3.8199
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

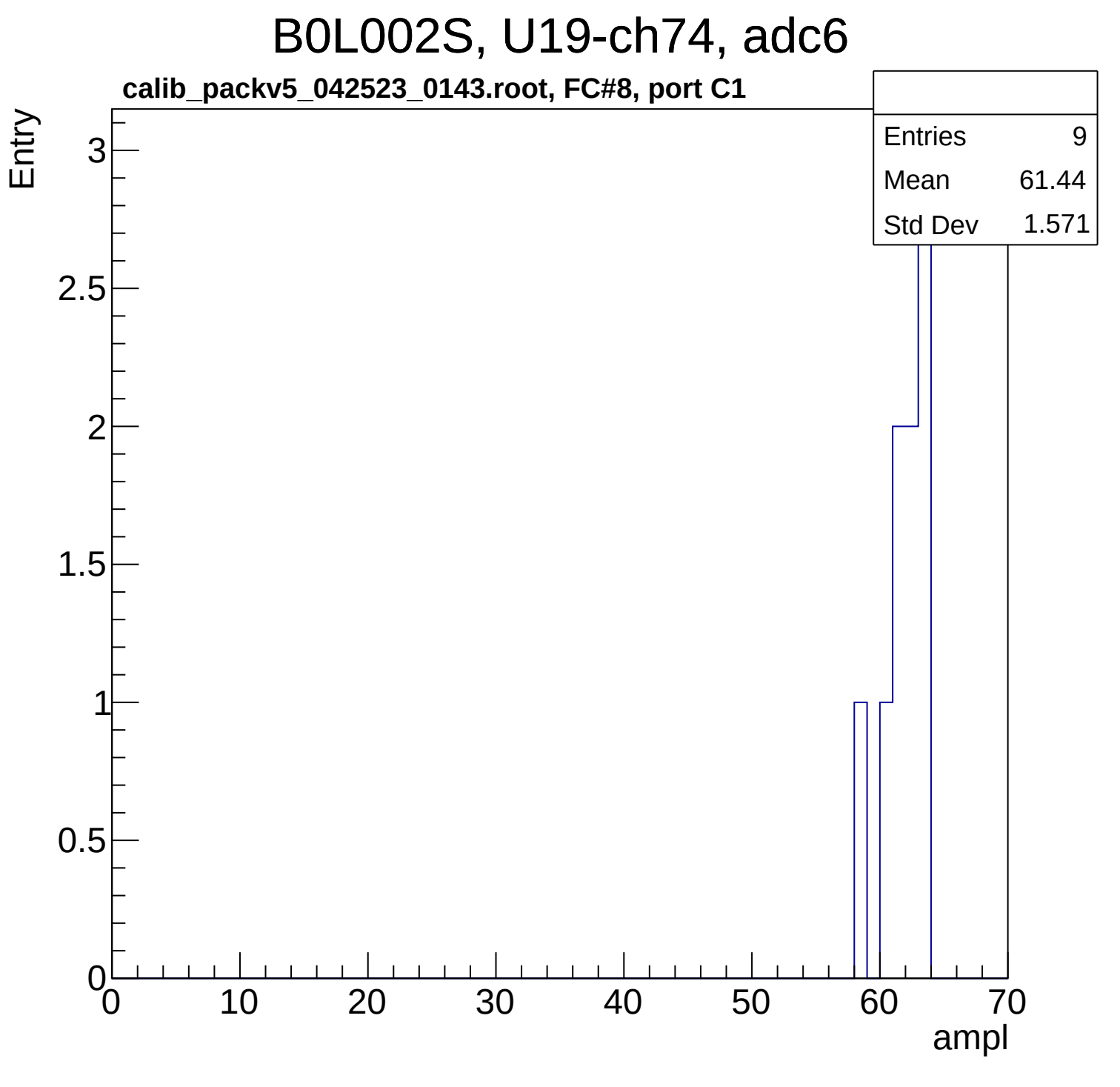

B0L002S, U19-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 35.67
Std Dev 3.165
Gaus mean : 36.0595
Gaus Width: 3.1739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

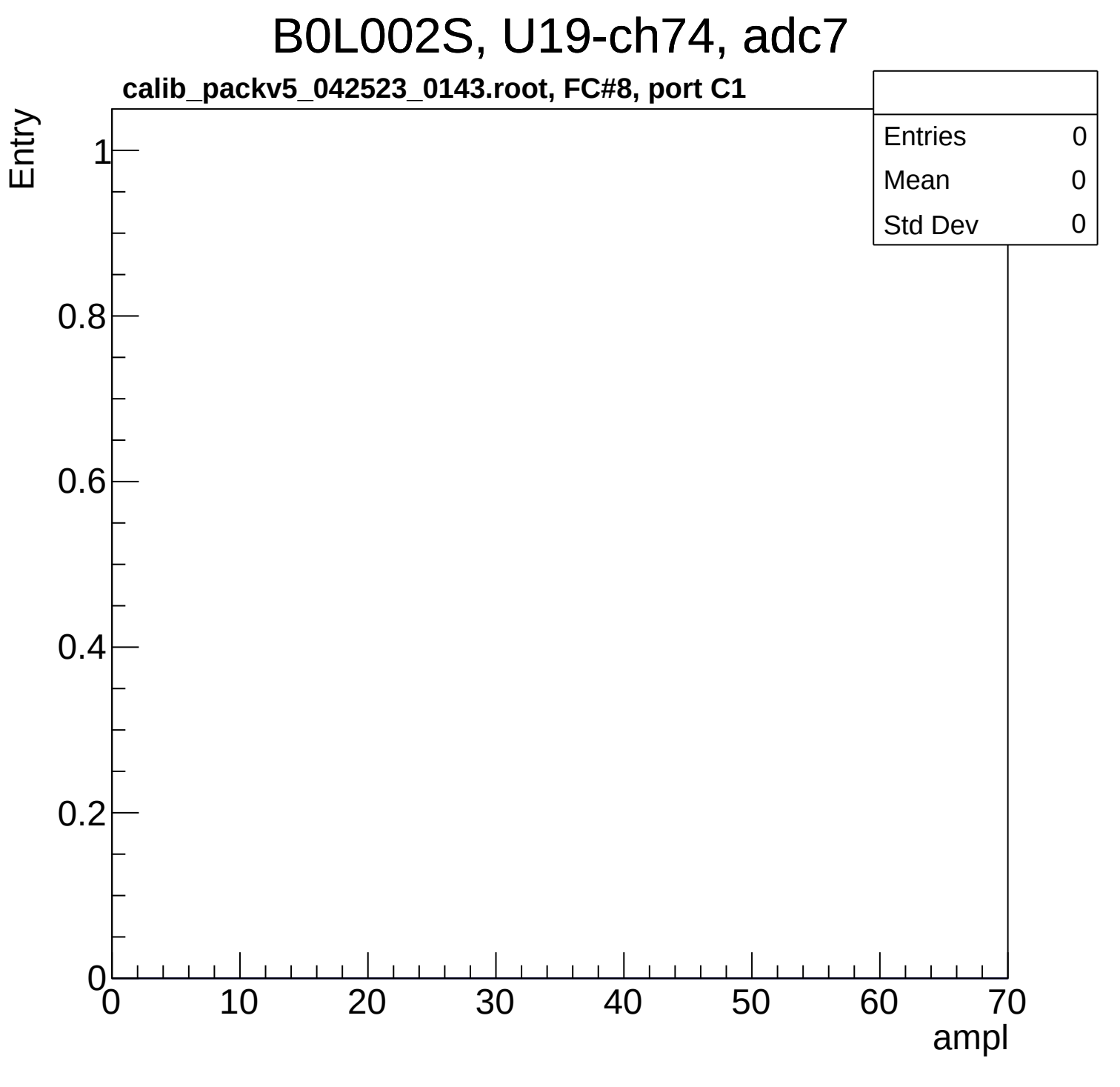

B0L002S, U19-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 42.4
Std Dev 2.965
Gaus mean : 42.4000
Gaus Width: 3.5224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

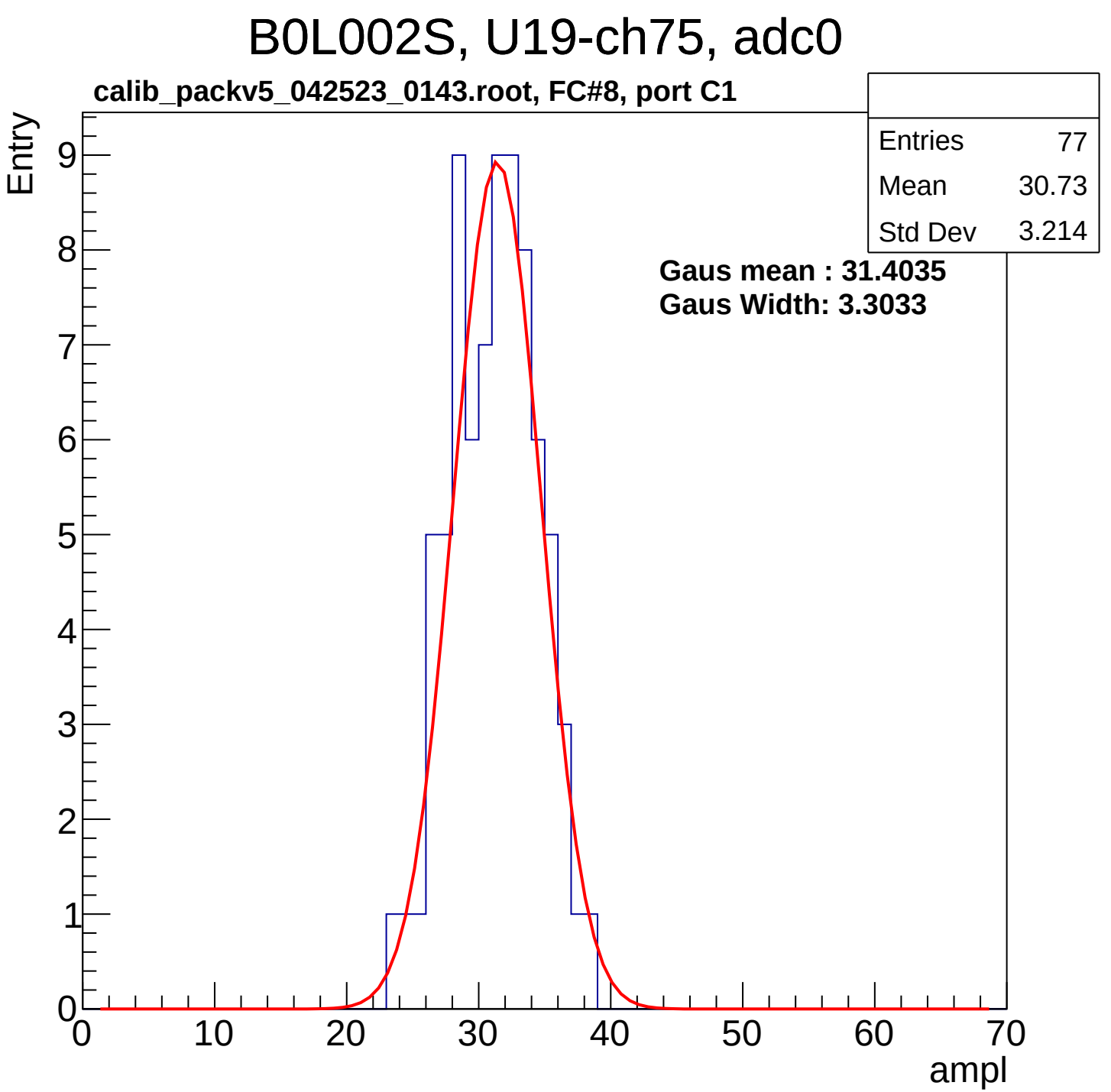

B0L002S, U19-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 48.78
Std Dev 3.576
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

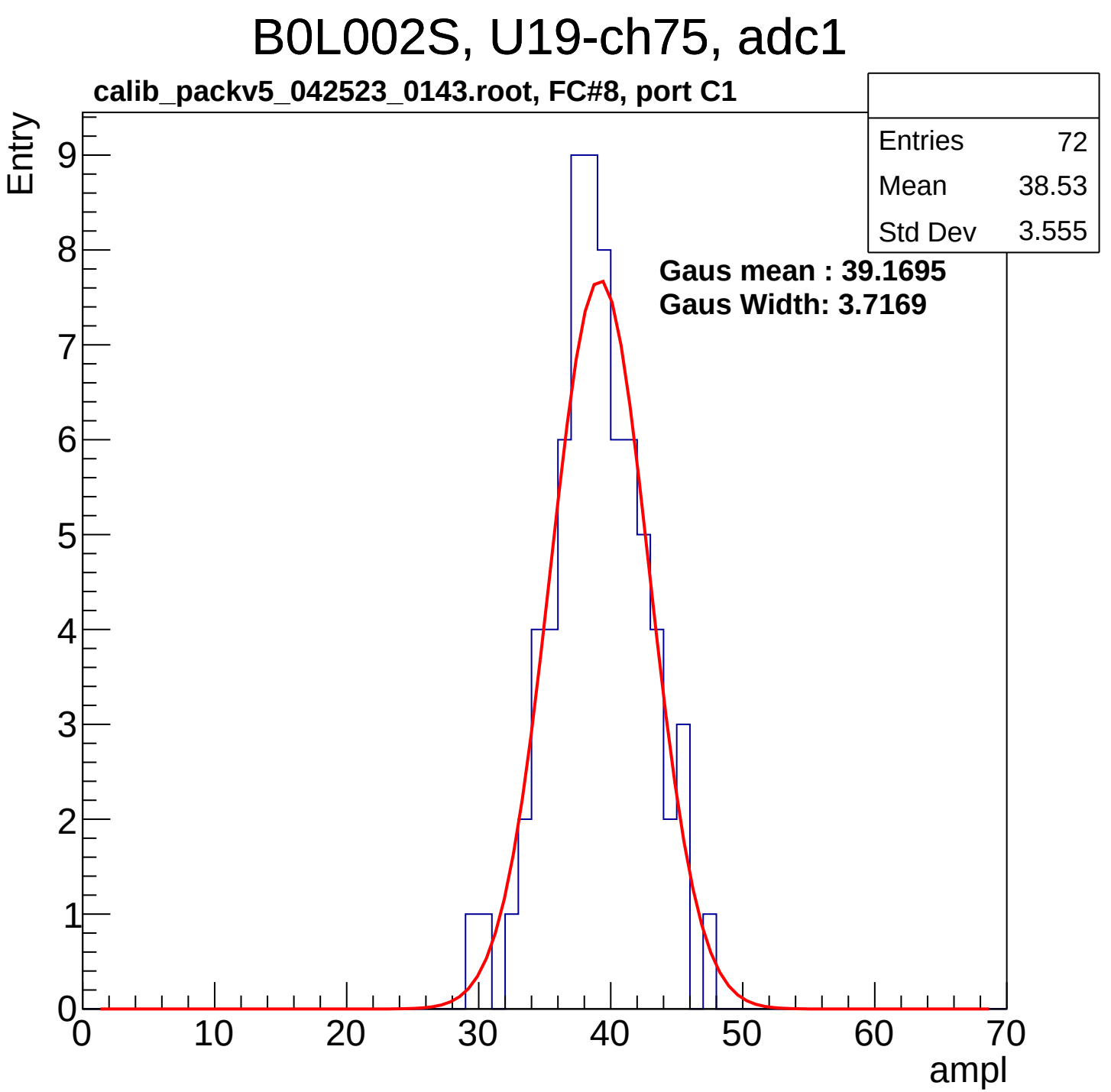

B0L002S, U19-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 55.4
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

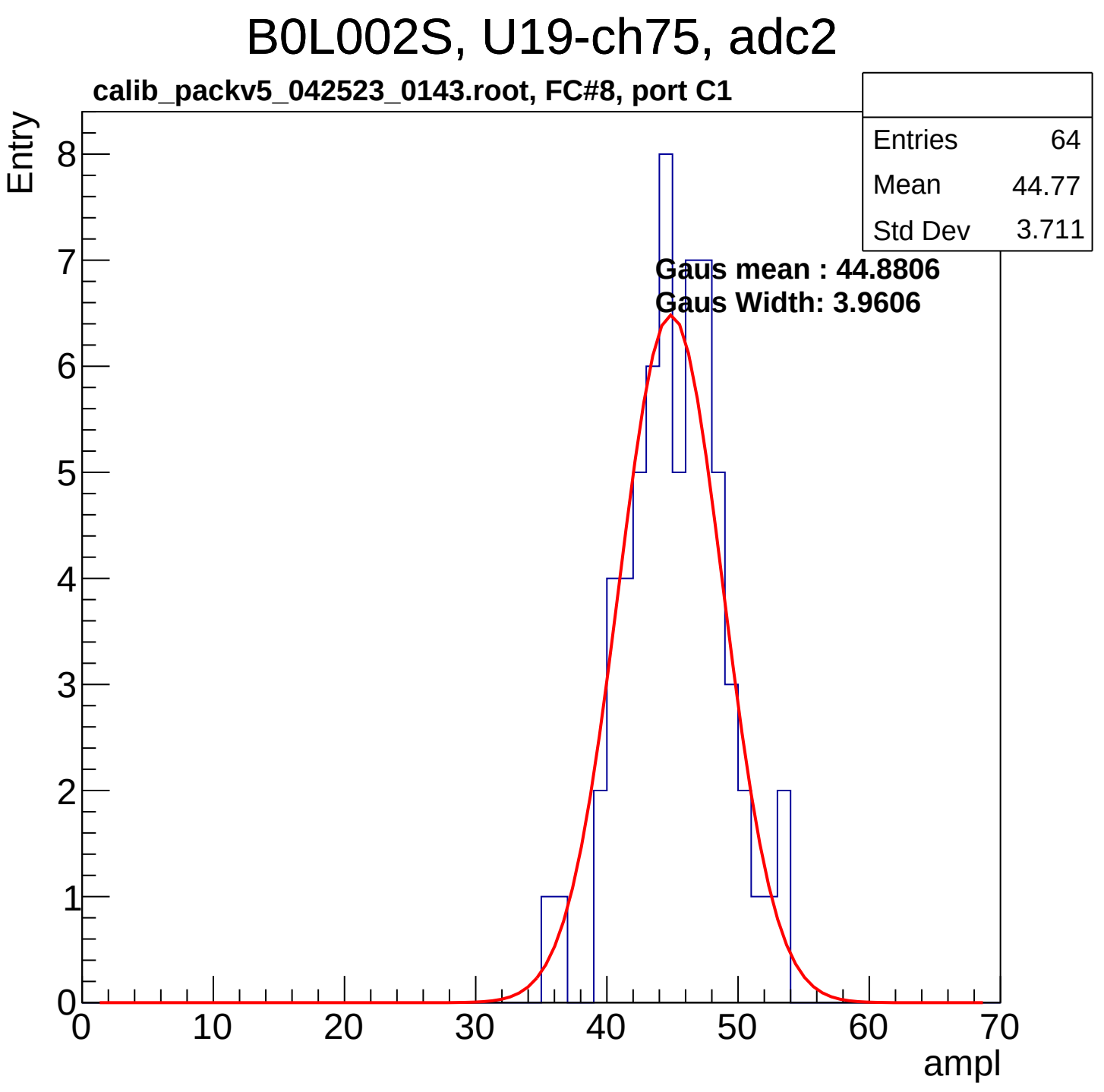

B0L002S, U19-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.37
Std Dev 8.605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

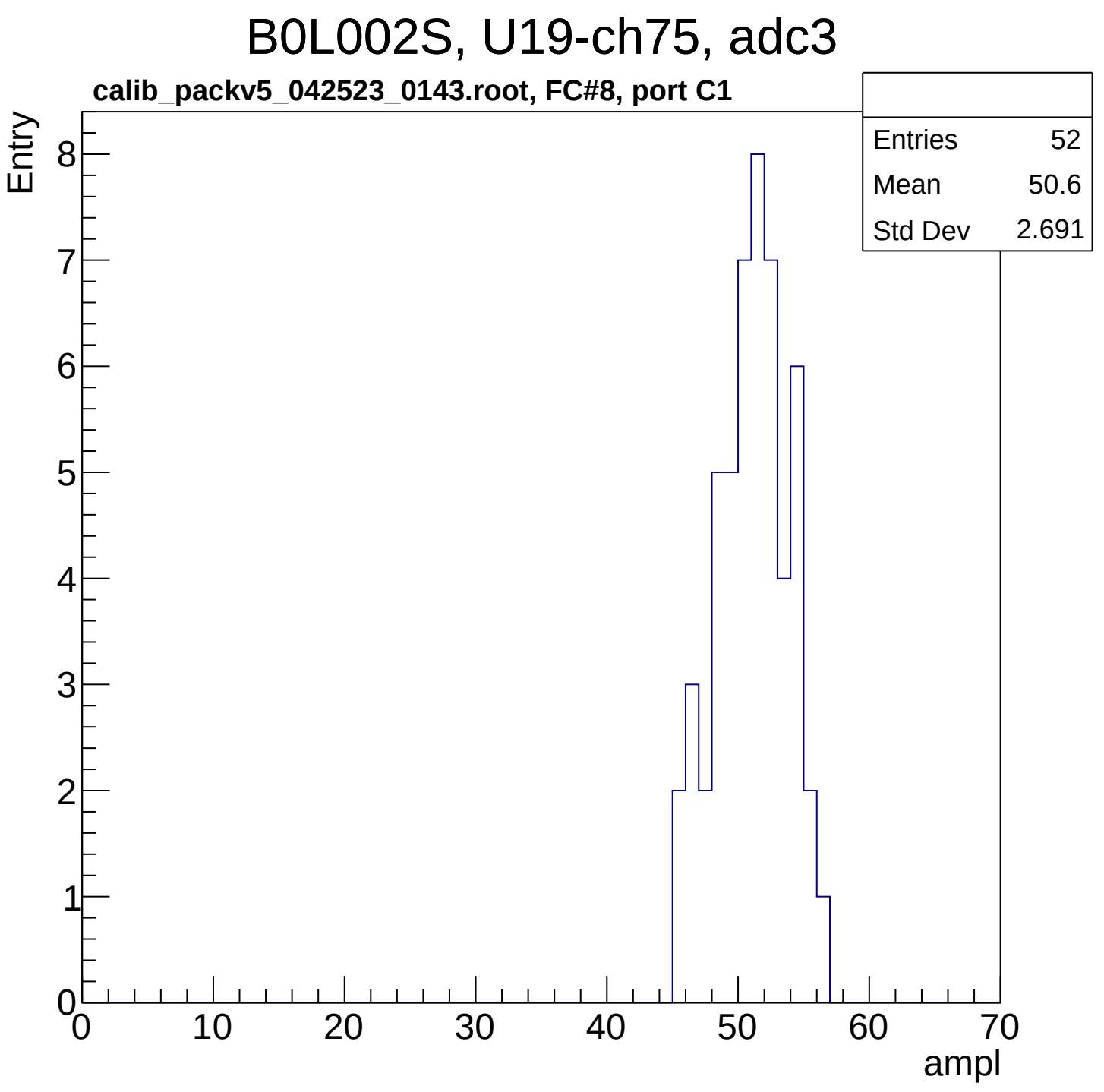

B0L002S, U19-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.44
Std Dev 1.571
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

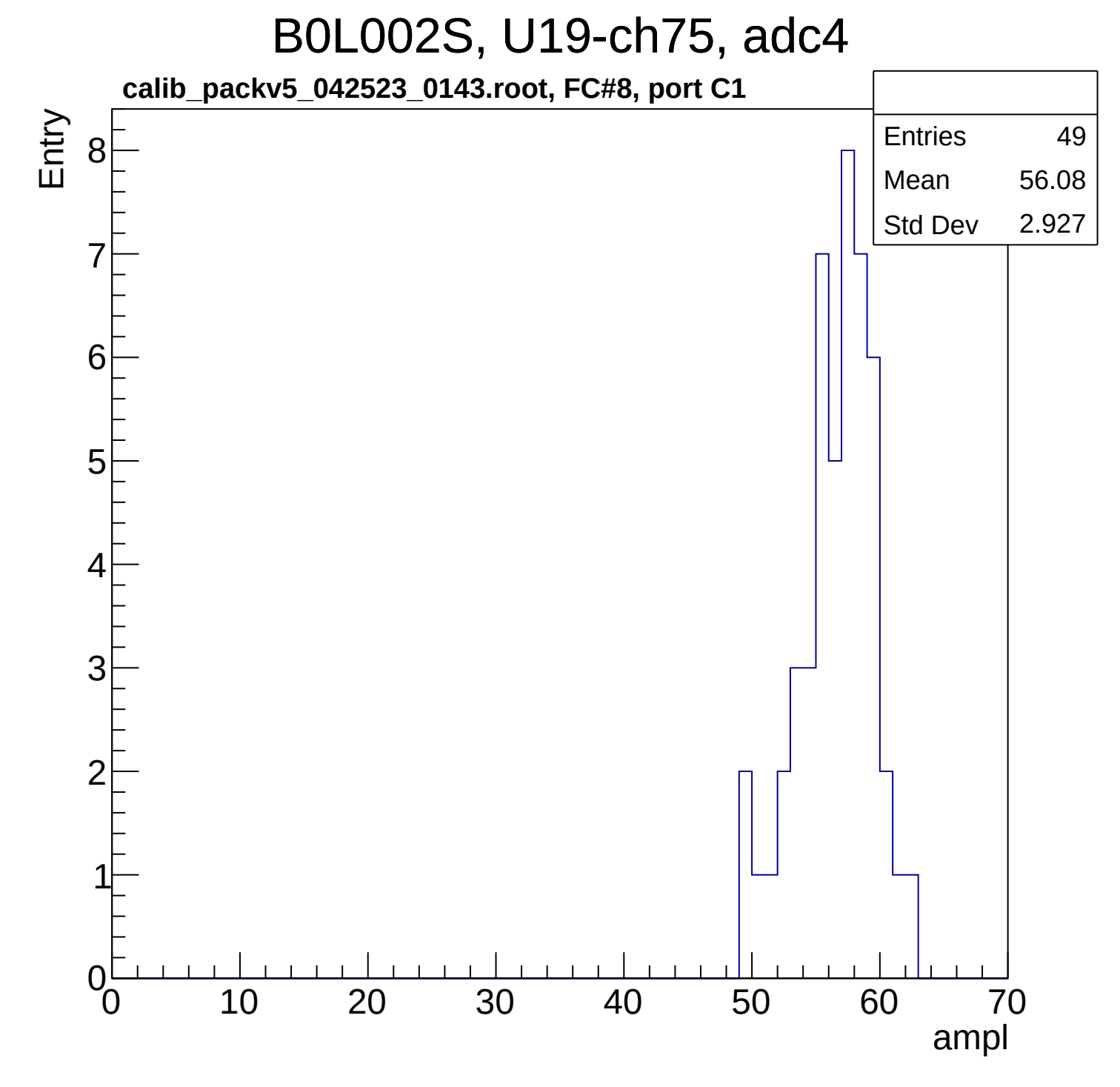

B0L002S, U19-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

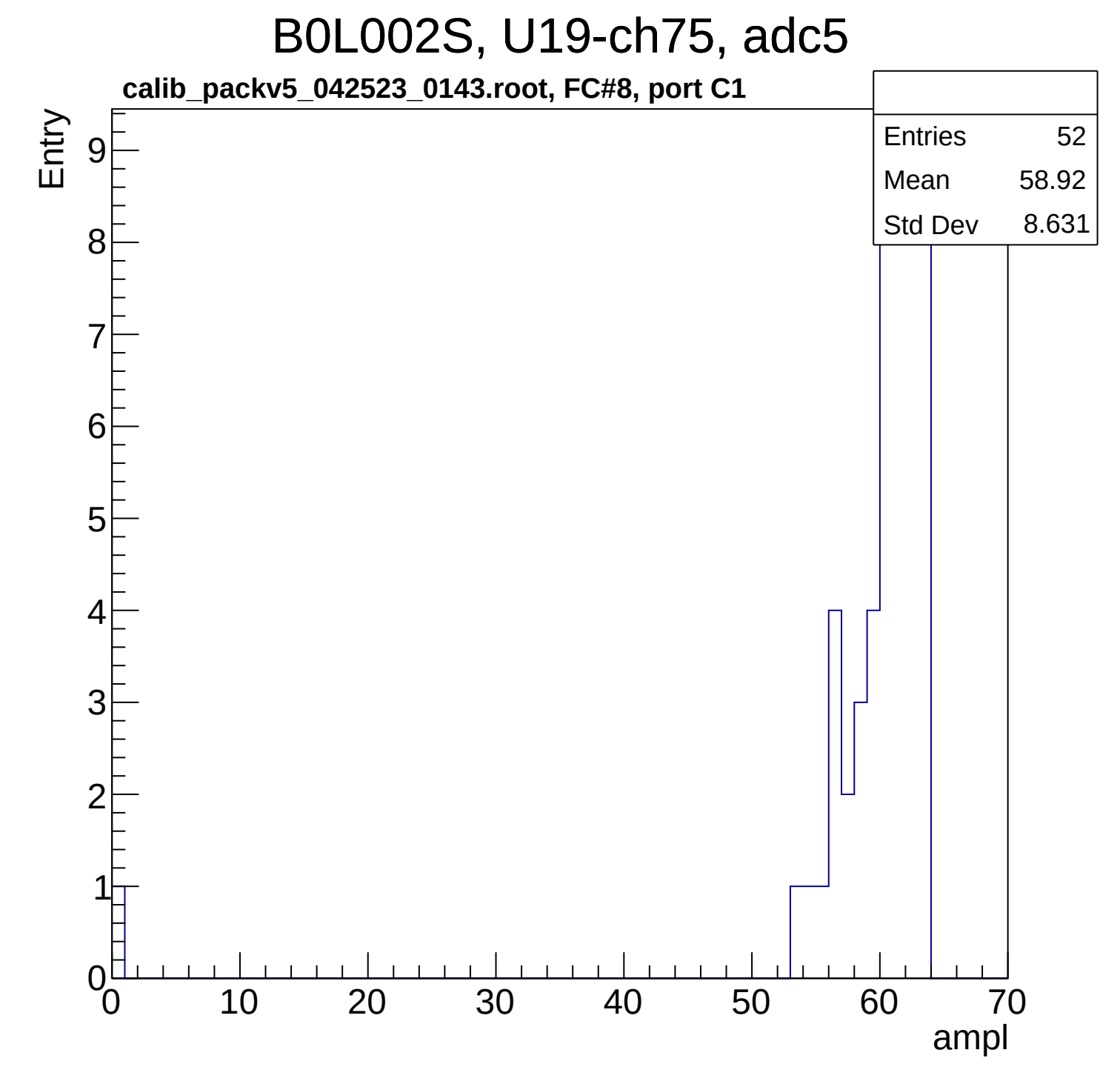

B0L002S, U19-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.73
Std Dev 3.214
Gaus mean : 31.4035
Gaus Width: 3.3033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

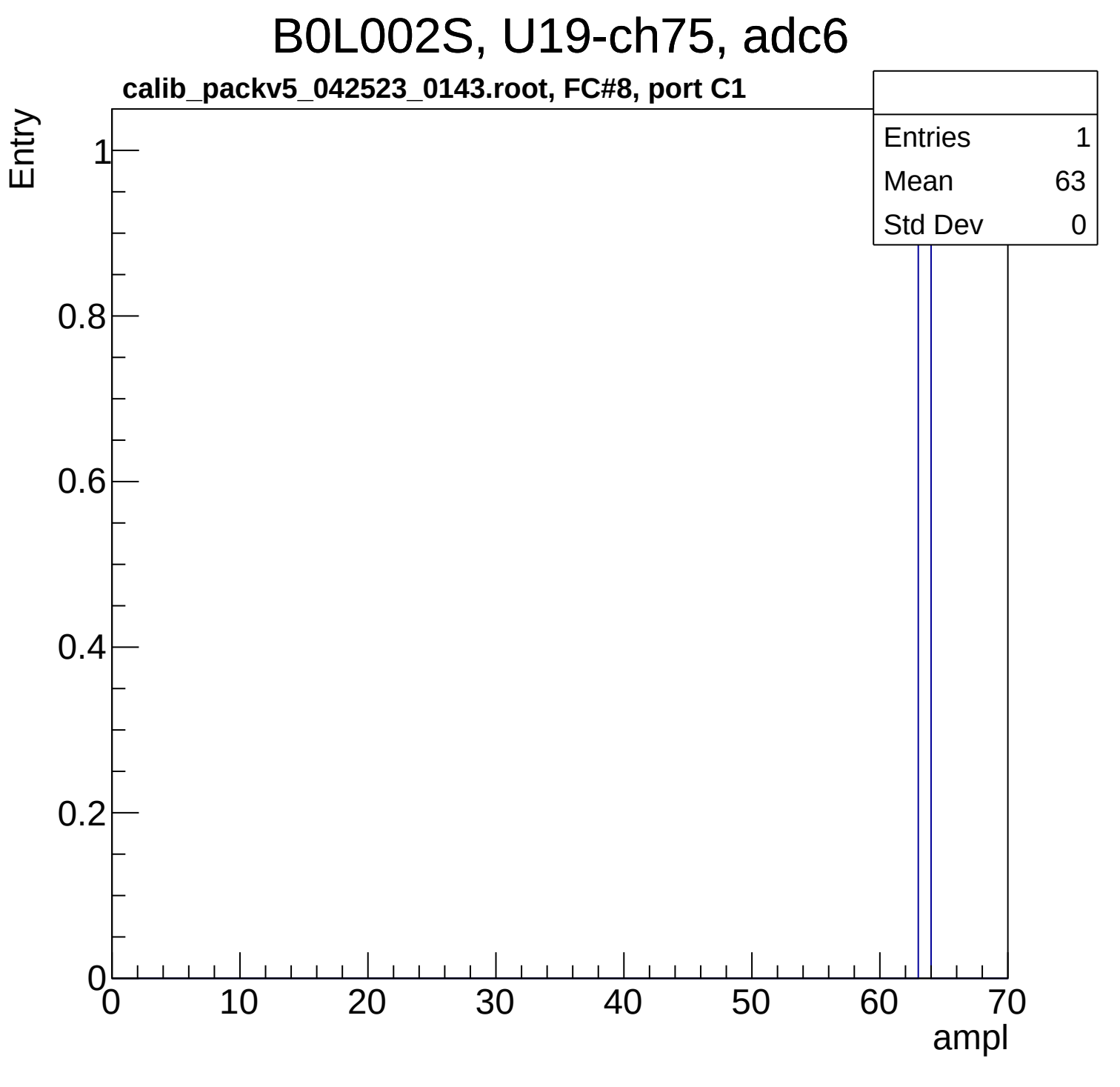

B0L002S, U19-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.53
Std Dev 3.555
Gaus mean : 39.1695
Gaus Width: 3.7169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

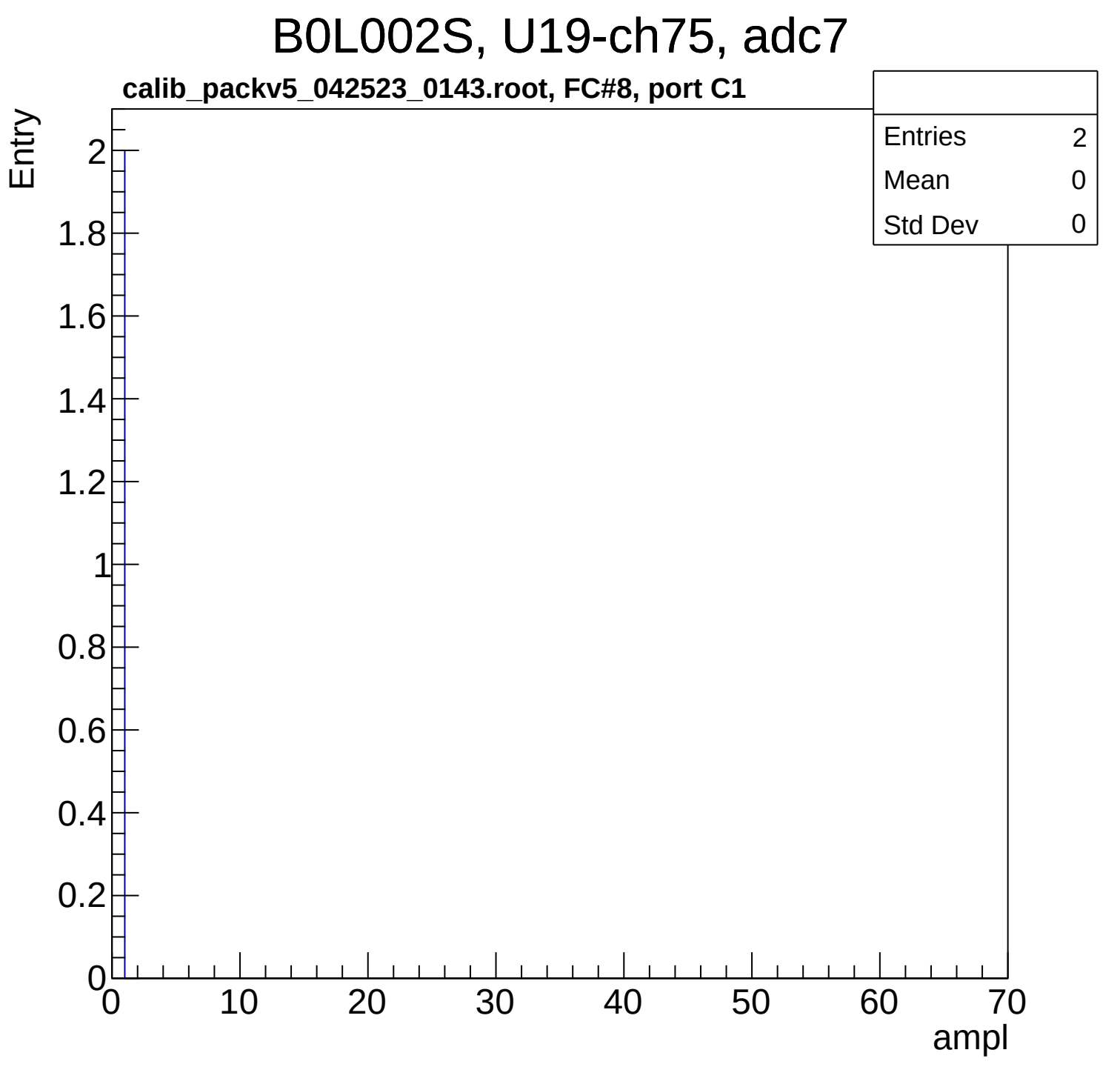

B0L002S, U19-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.77
Std Dev 3.711
Gaus mean : 44.8806
Gaus Width: 3.9606
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

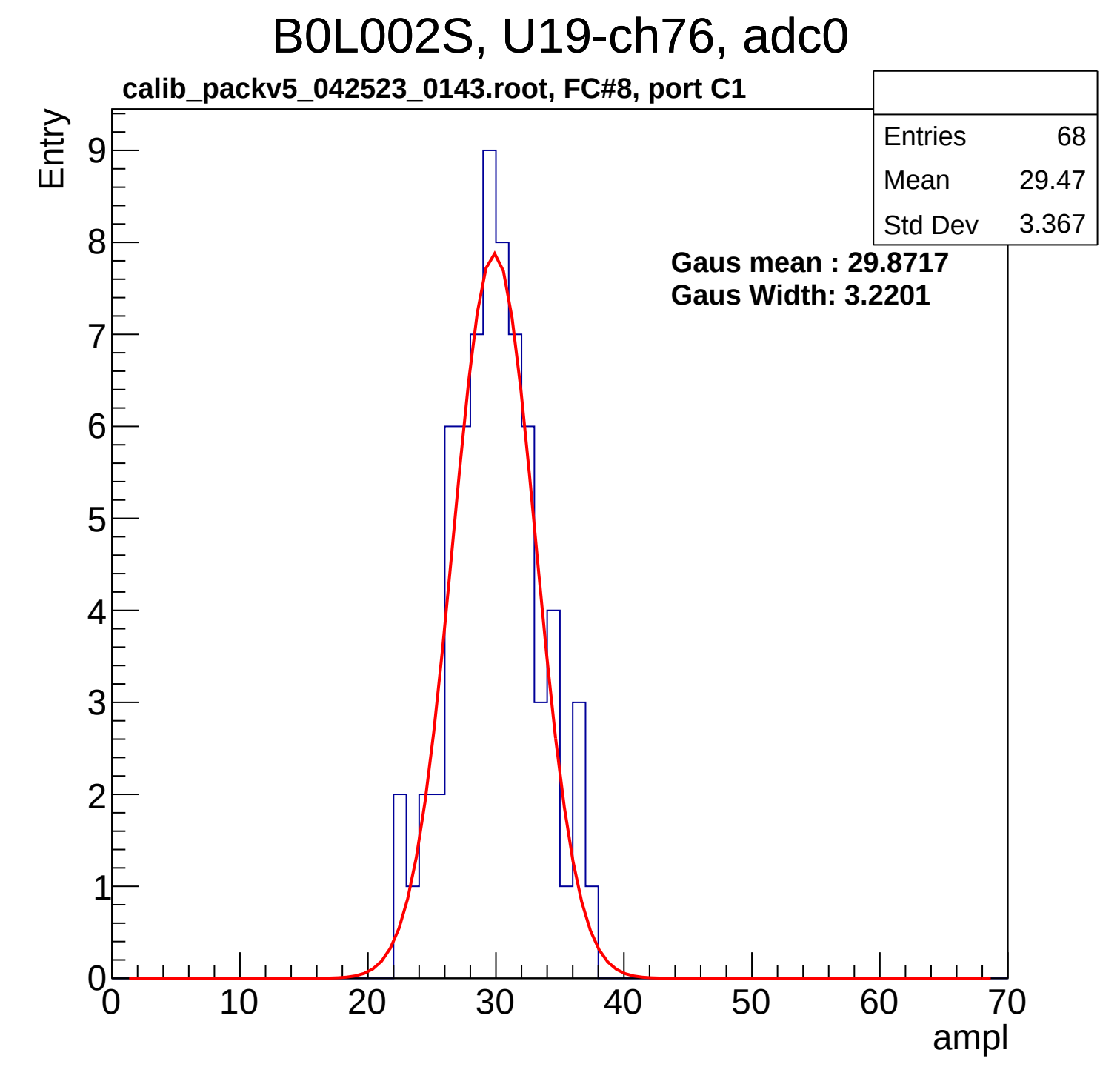

B0L002S, U19-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.6
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

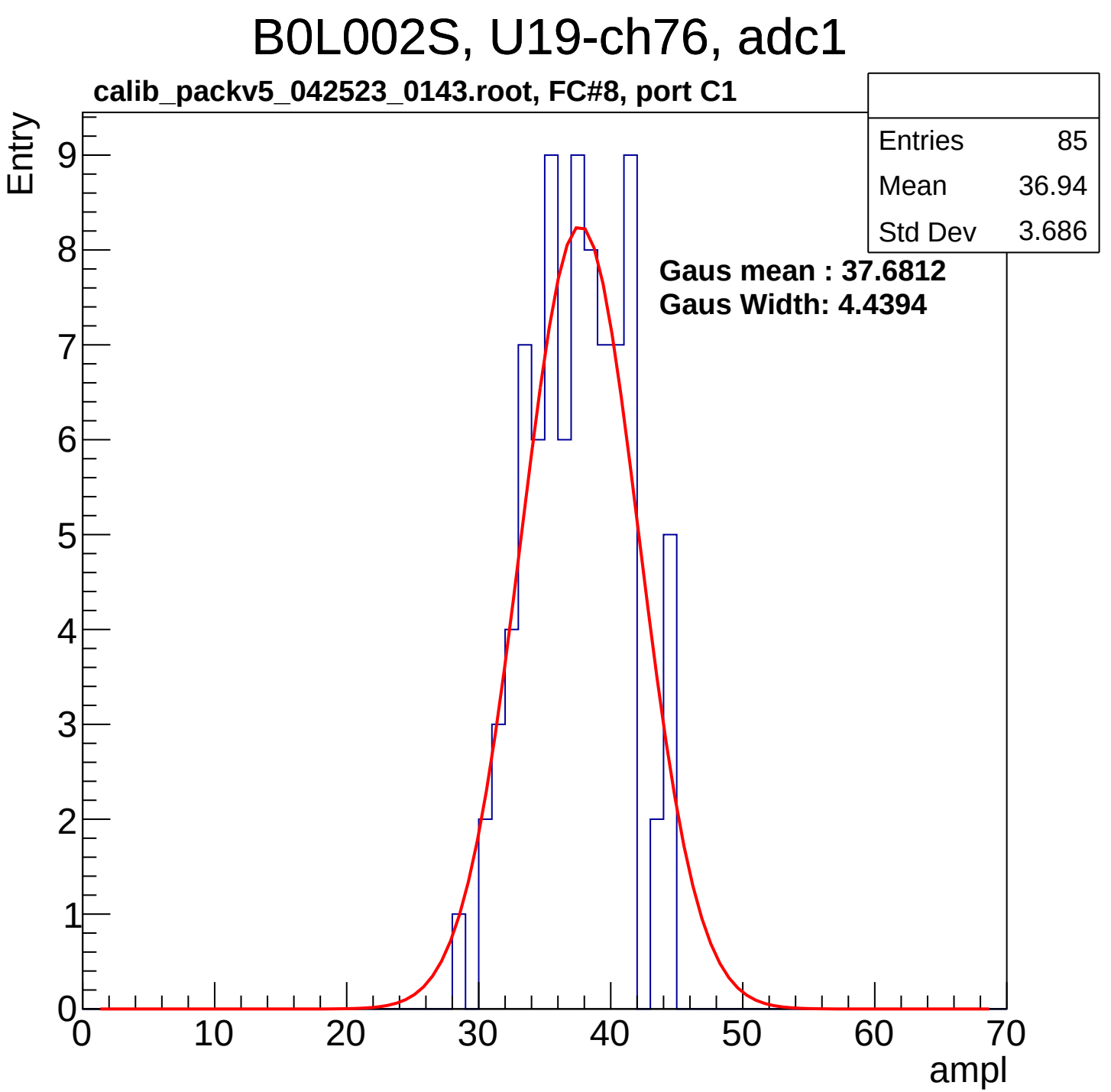

B0L002S, U19-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 56.08
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

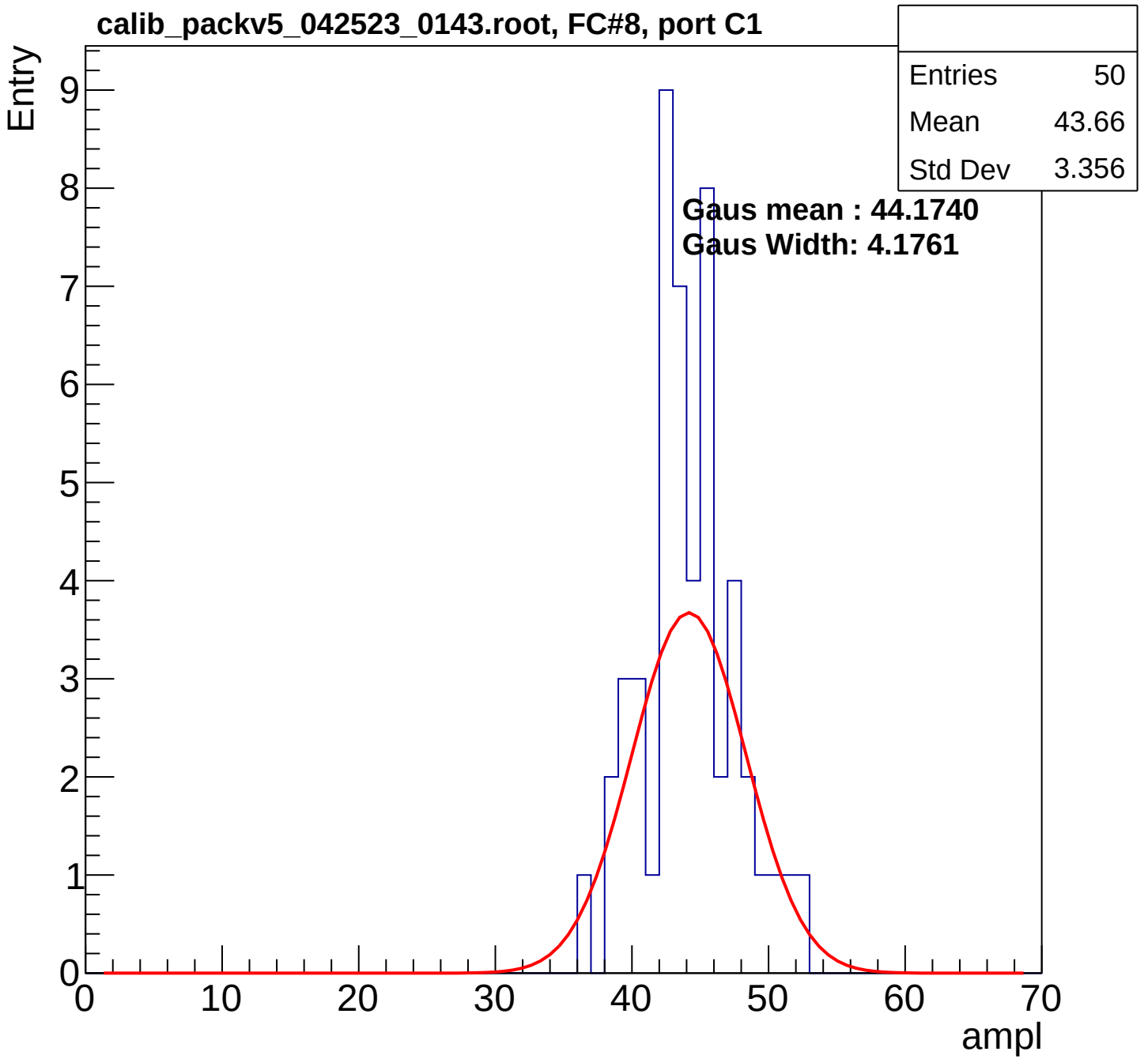

B0L002S, U19-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.92
Std Dev 8.631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

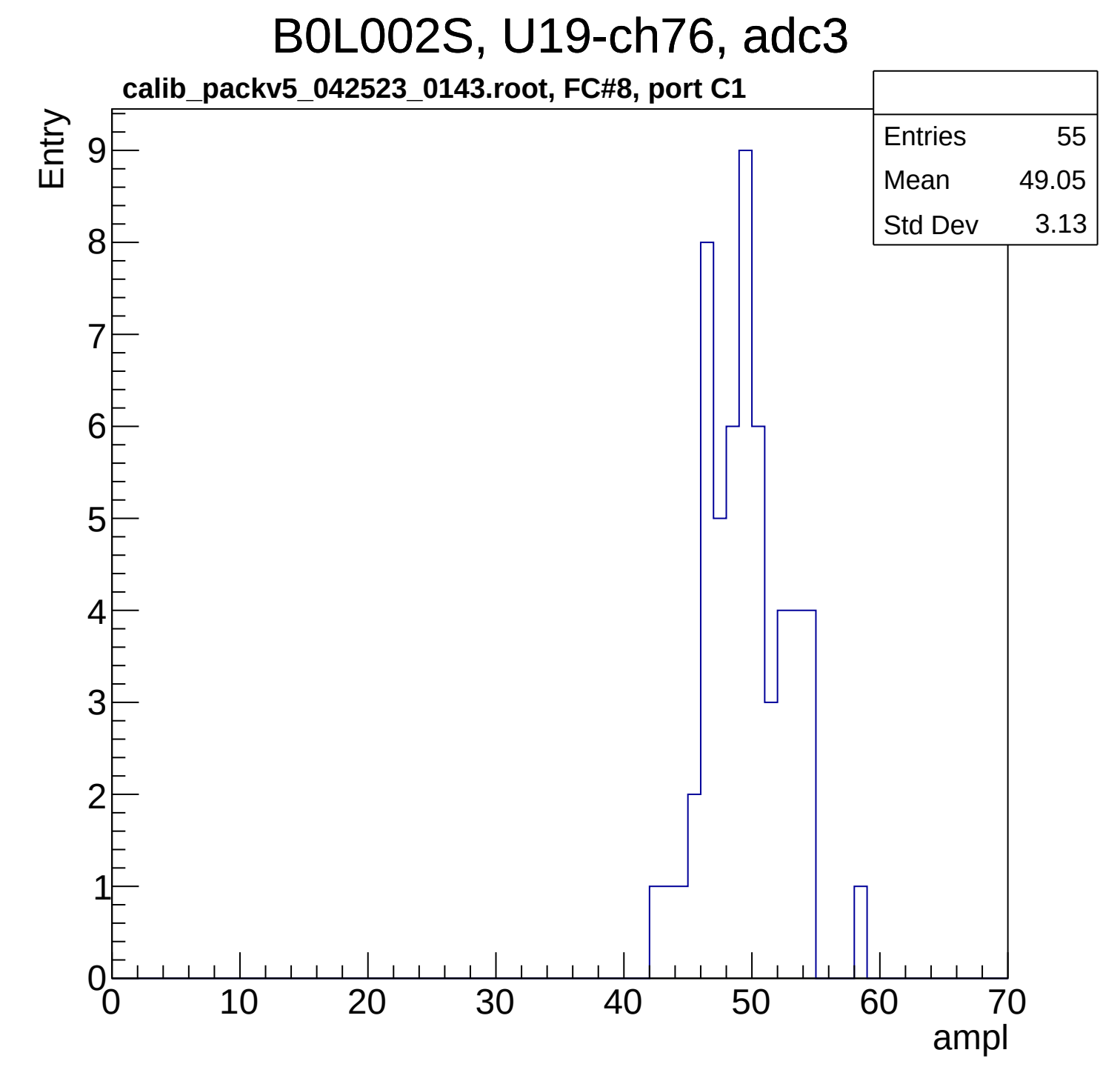

B0L002S, U19-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

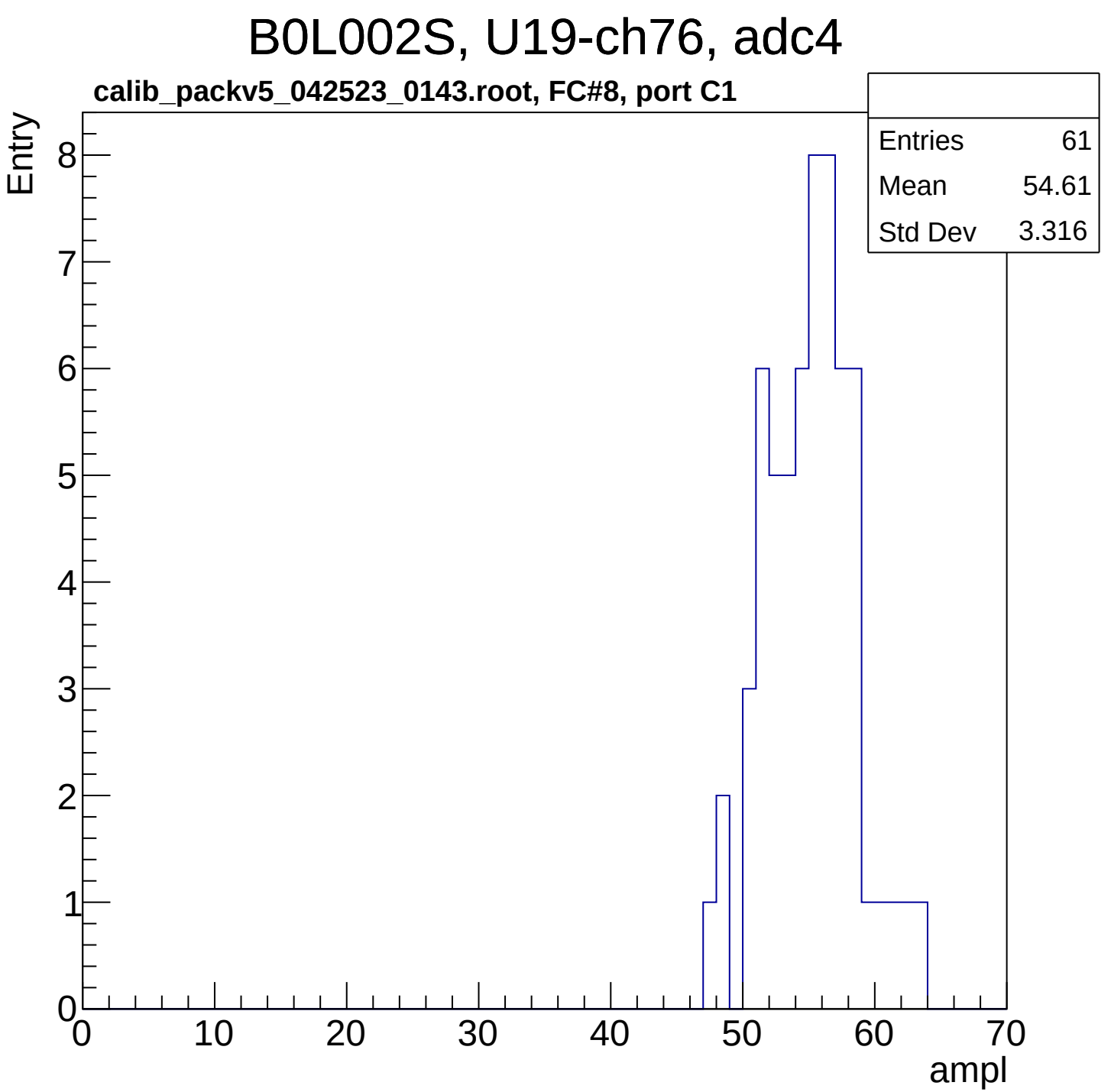

B0L002S, U19-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

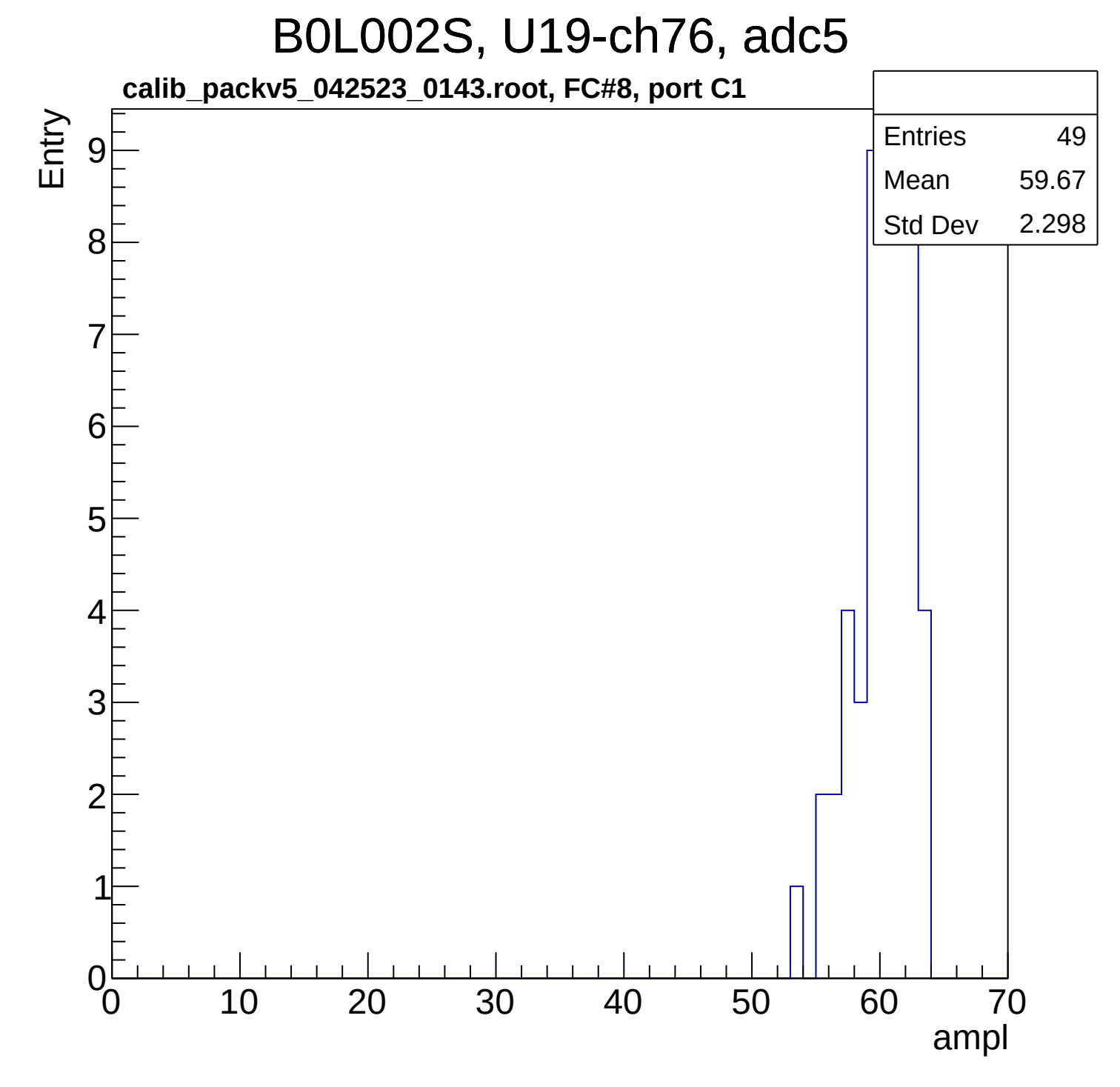

B0L002S, U19-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.47
Std Dev 3.367
Gaus mean : 29.8717
Gaus Width: 3.2201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

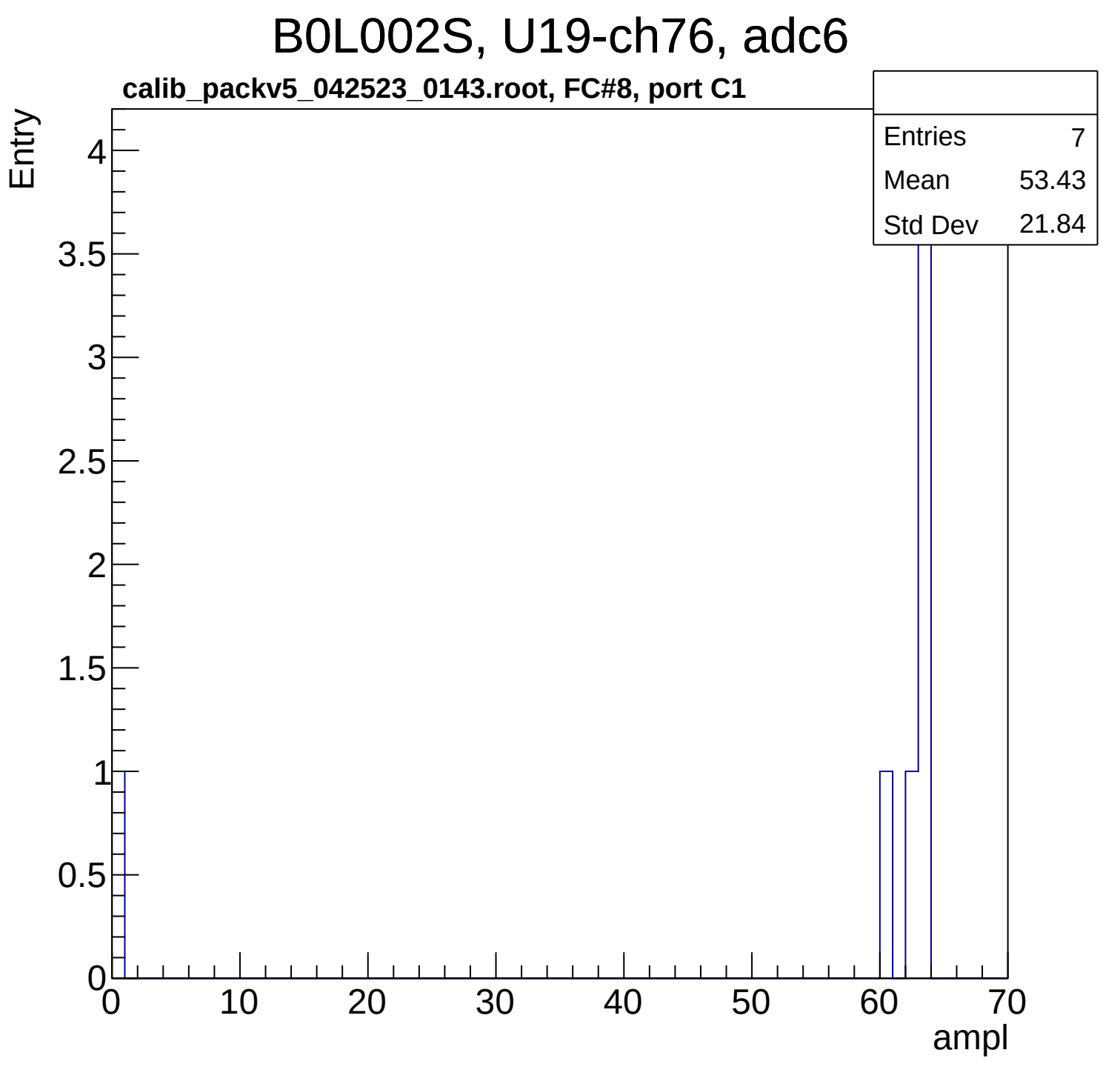

B0L002S, U19-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 36.94
Std Dev 3.686
Gaus mean : 37.6812
Gaus Width: 4.4394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

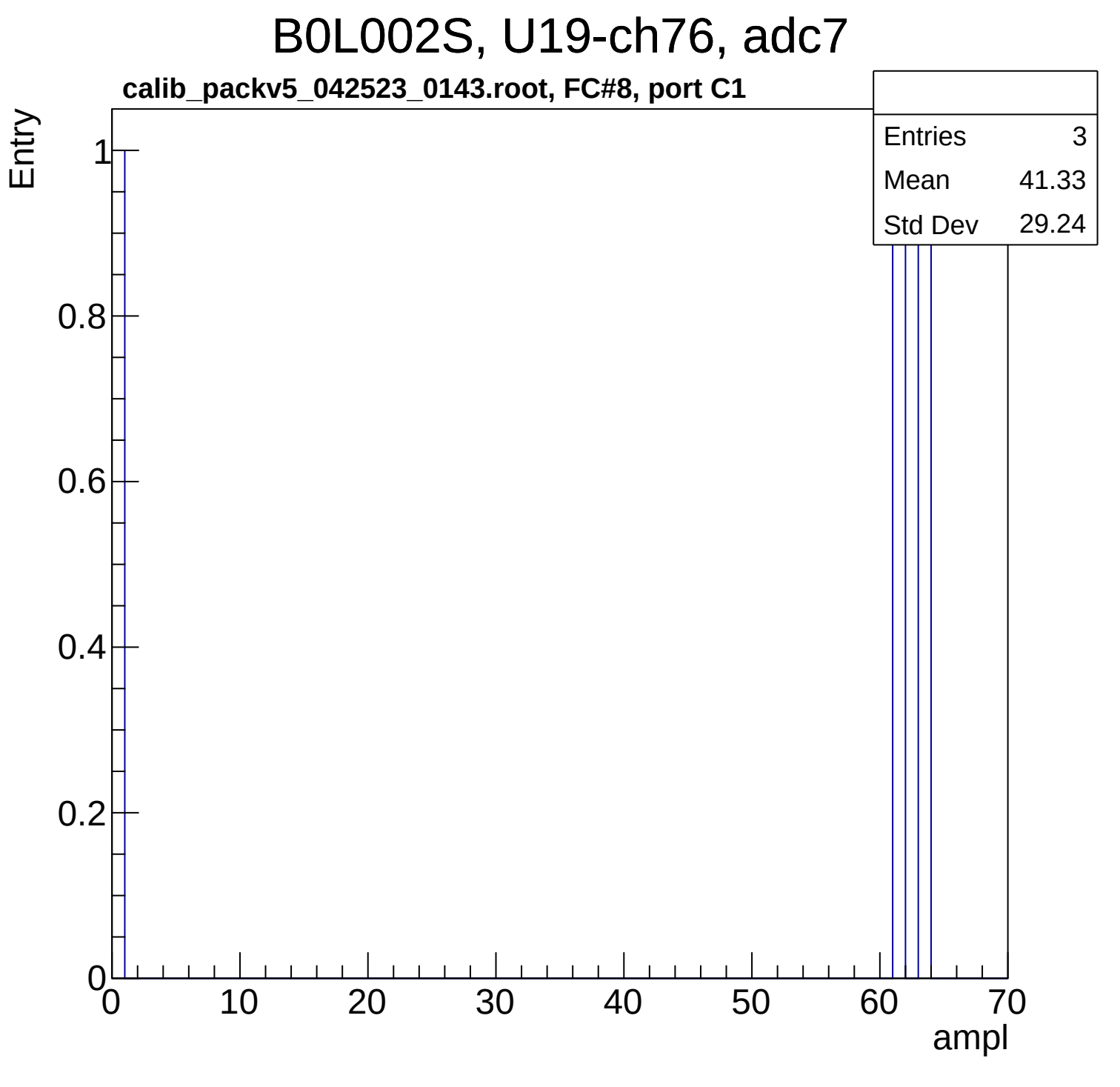

B0L002S, U19-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 43.66
Std Dev 3.356
Gaus mean : 44.1740
Gaus Width: 4.1761
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

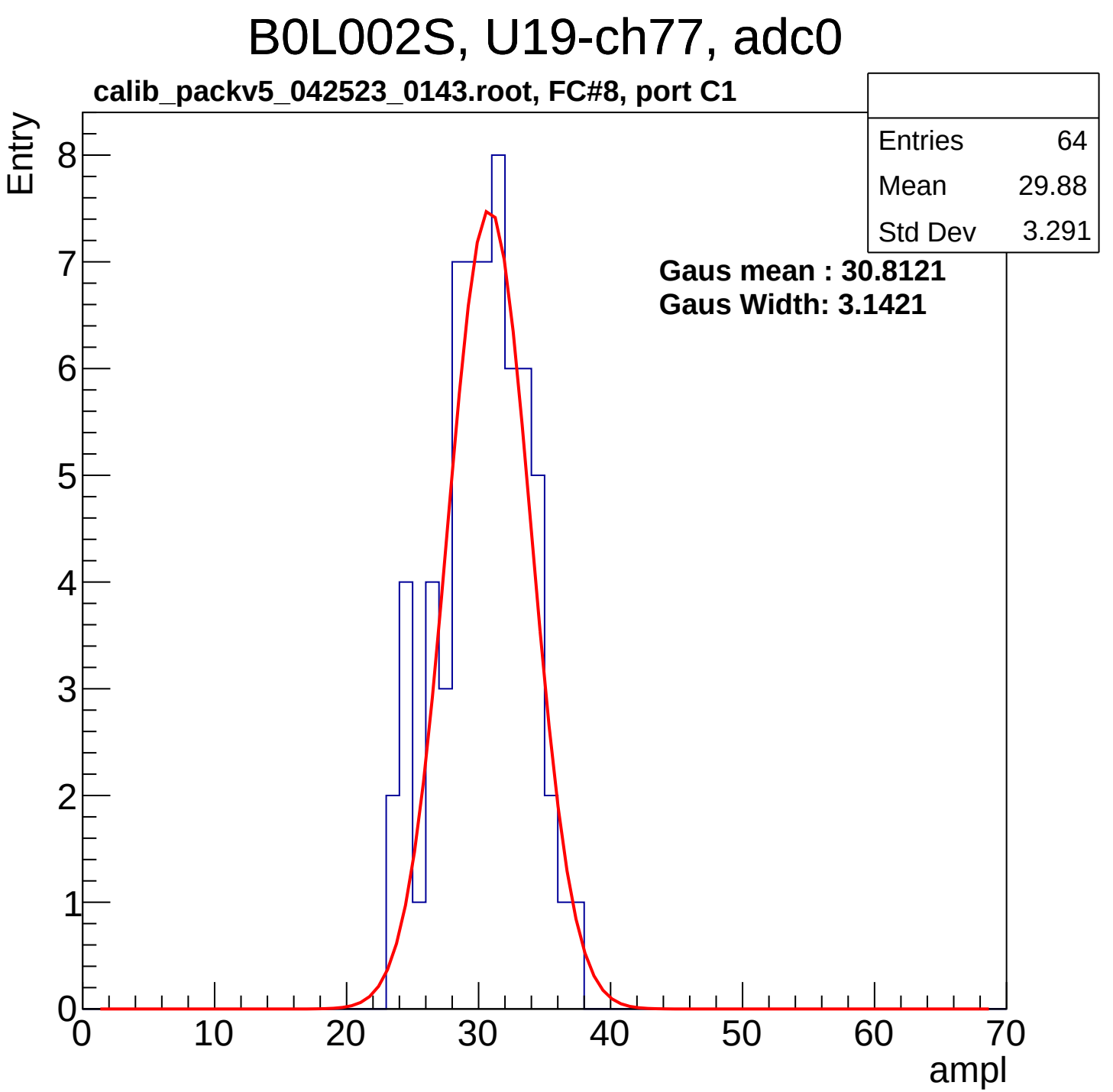

B0L002S, U19-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 49.05
Std Dev 3.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

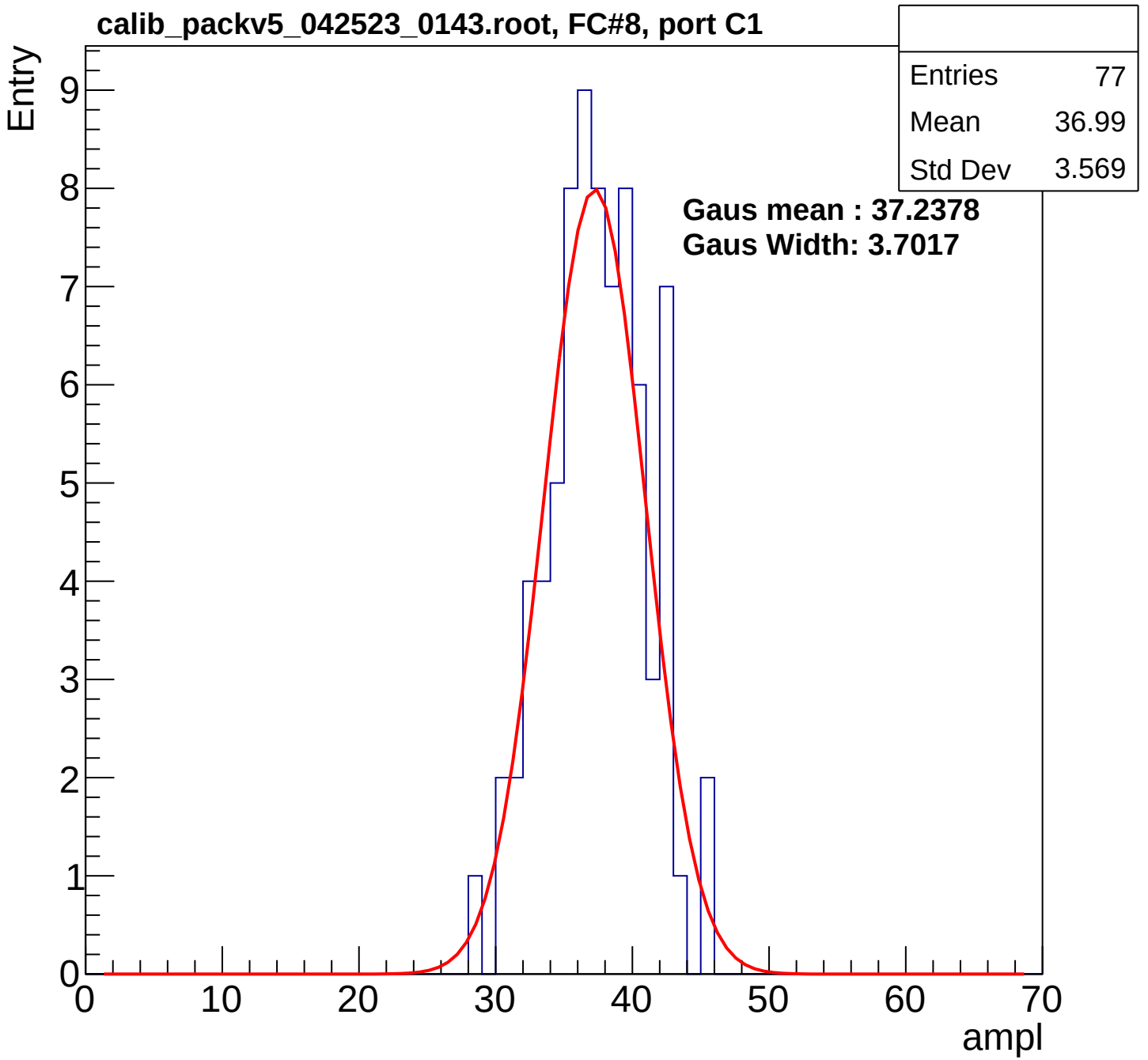

B0L002S, U19-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 54.61
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

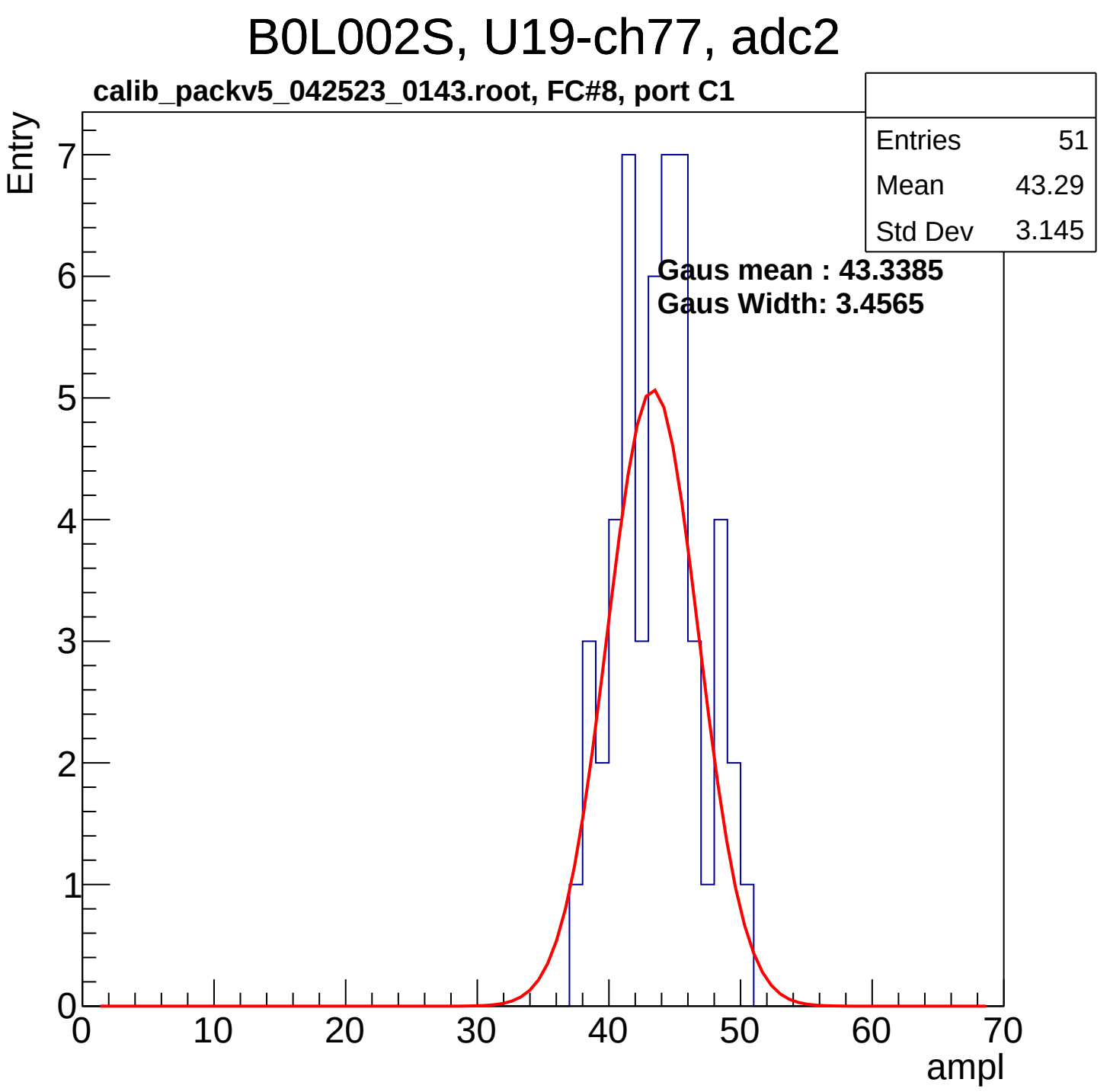

B0L002S, U19-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.67
Std Dev 2.298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

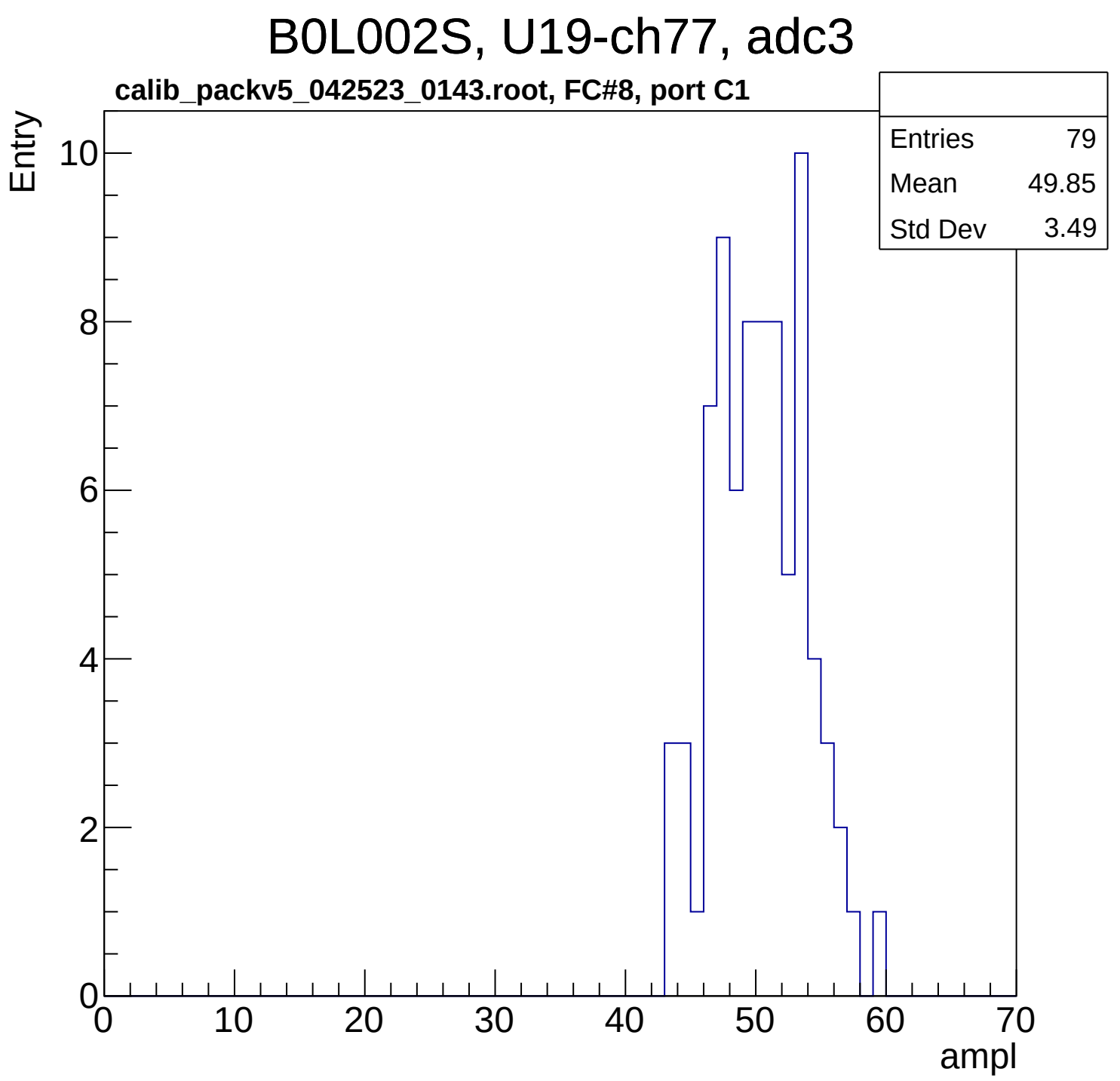

B0L002S, U19-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.43
Std Dev 21.84
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

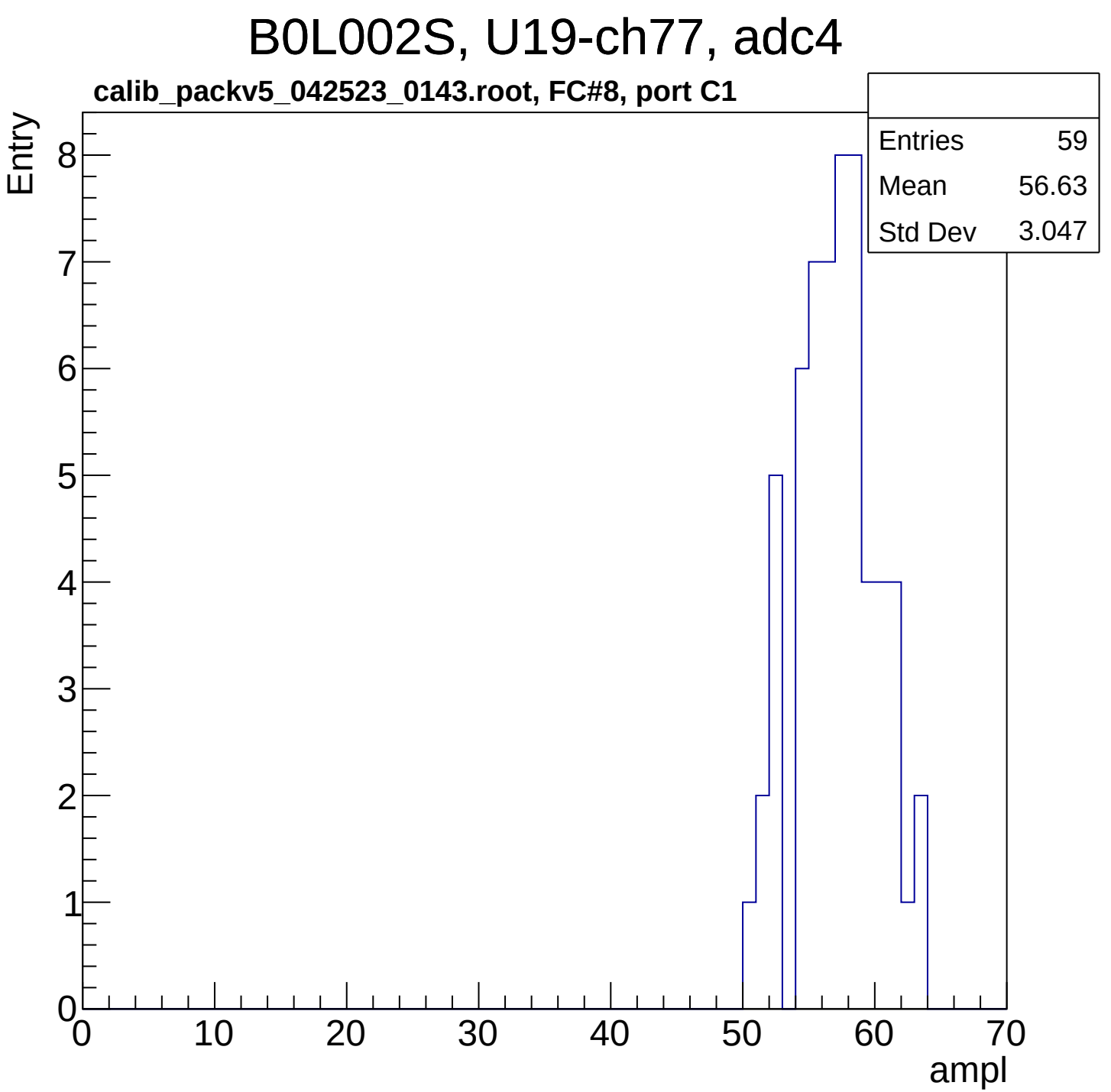

B0L002S, U19-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

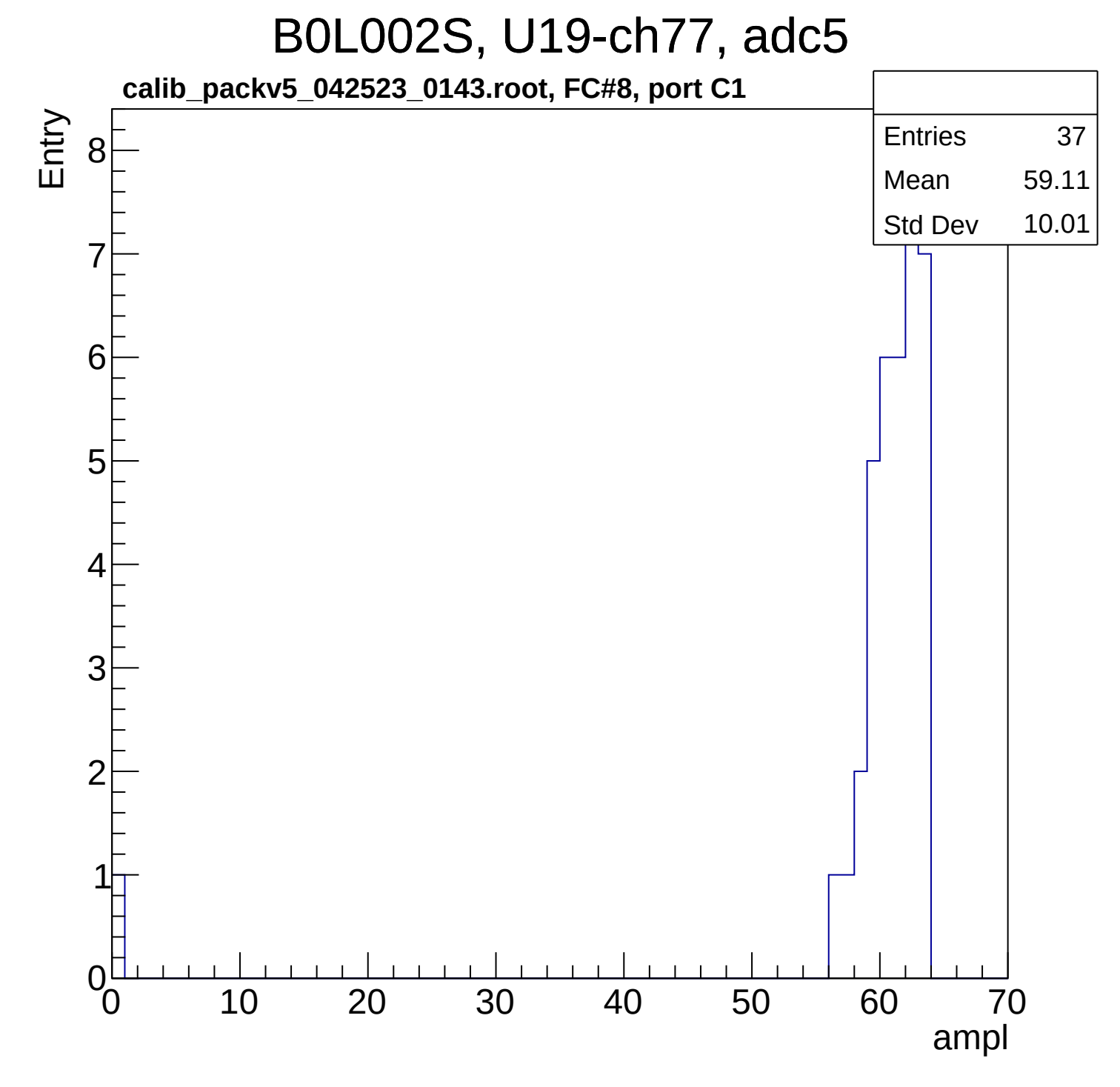

B0L002S, U19-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.88
Std Dev 3.291
Gaus mean : 30.8121
Gaus Width: 3.1421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

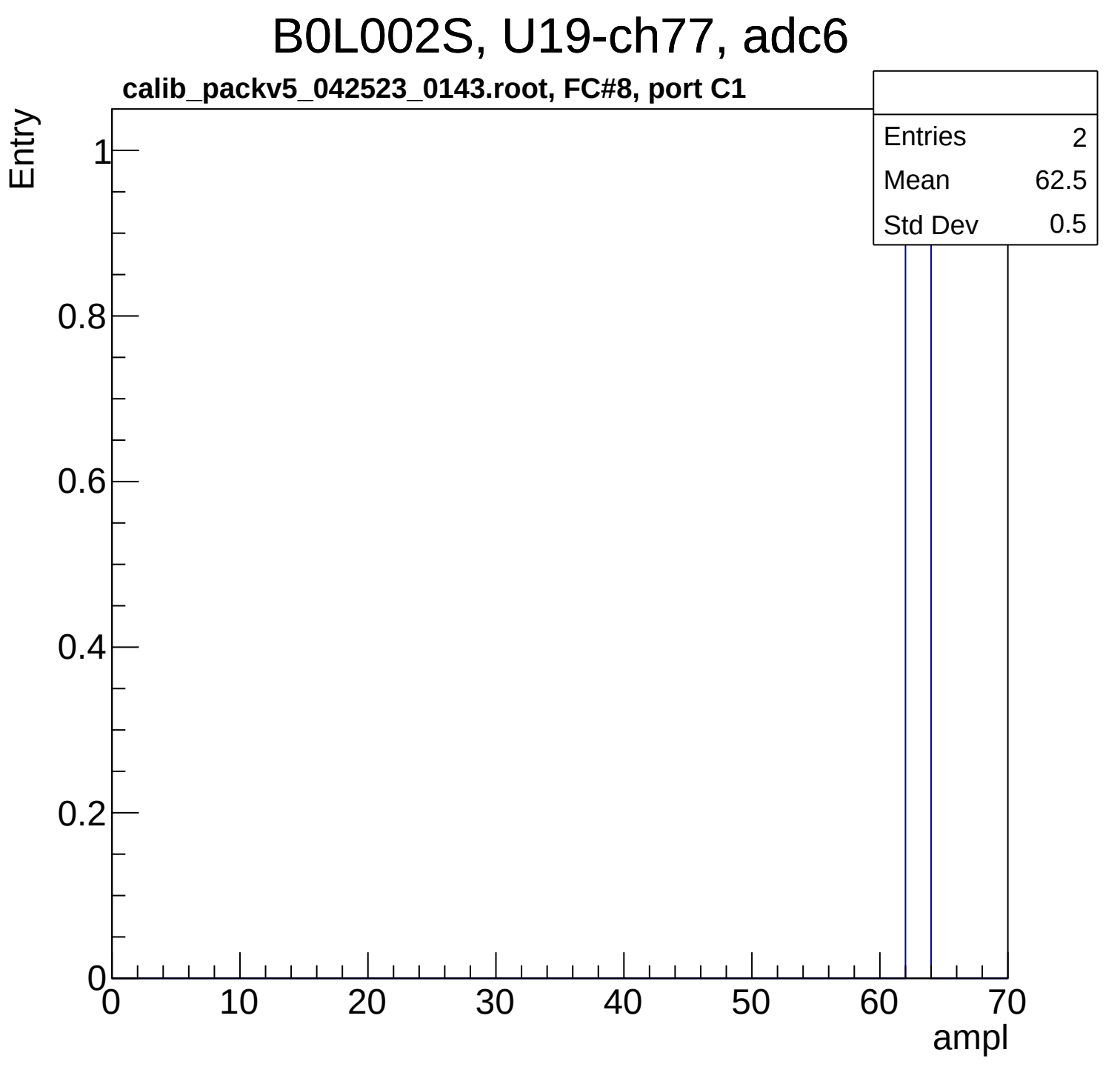

B0L002S, U19-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 36.99
Std Dev 3.569
Gaus mean : 37.2378
Gaus Width: 3.7017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

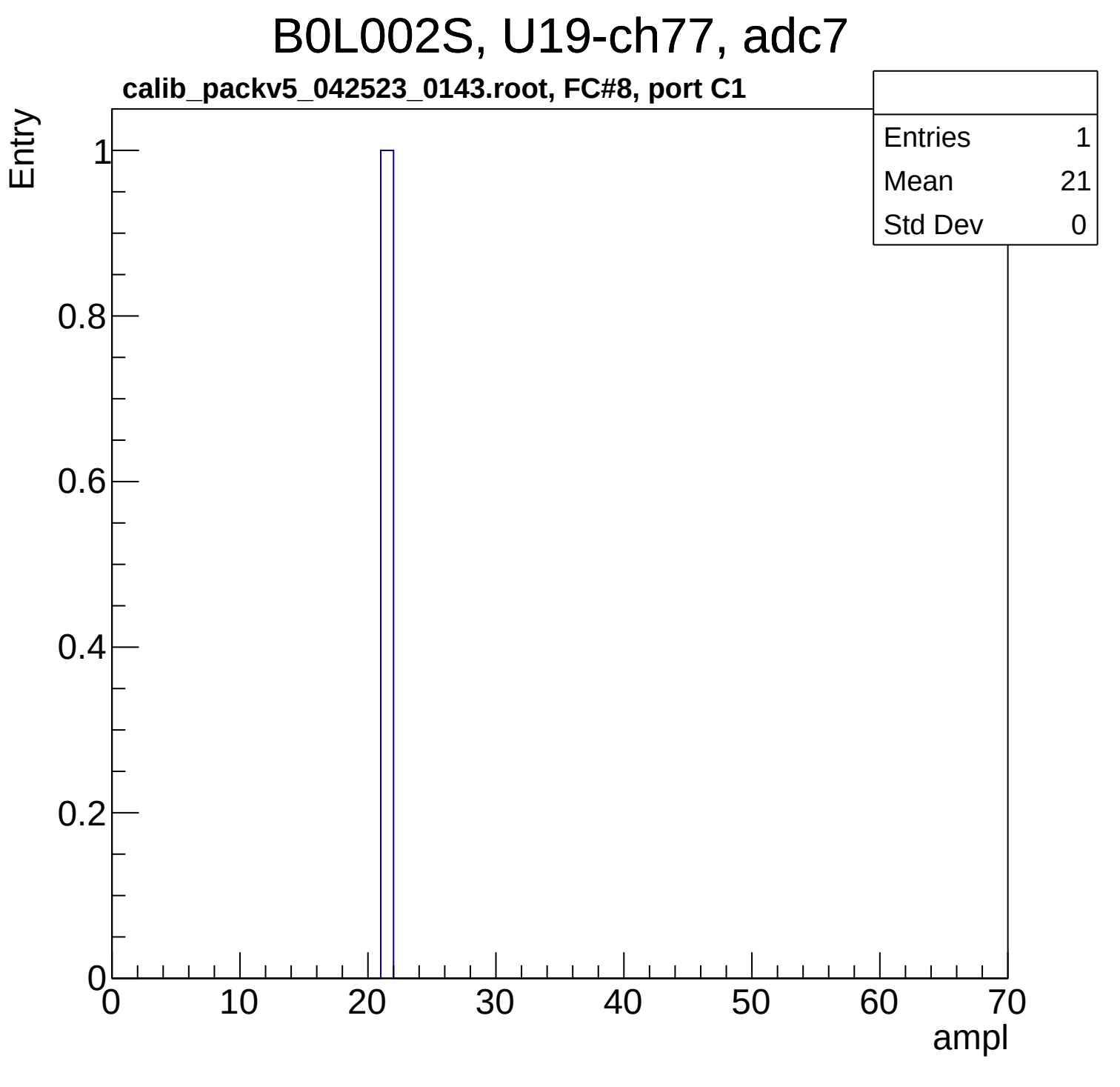

B0L002S, U19-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 43.29
Std Dev 3.145
Gaus mean : 43.3385
Gaus Width: 3.4565
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

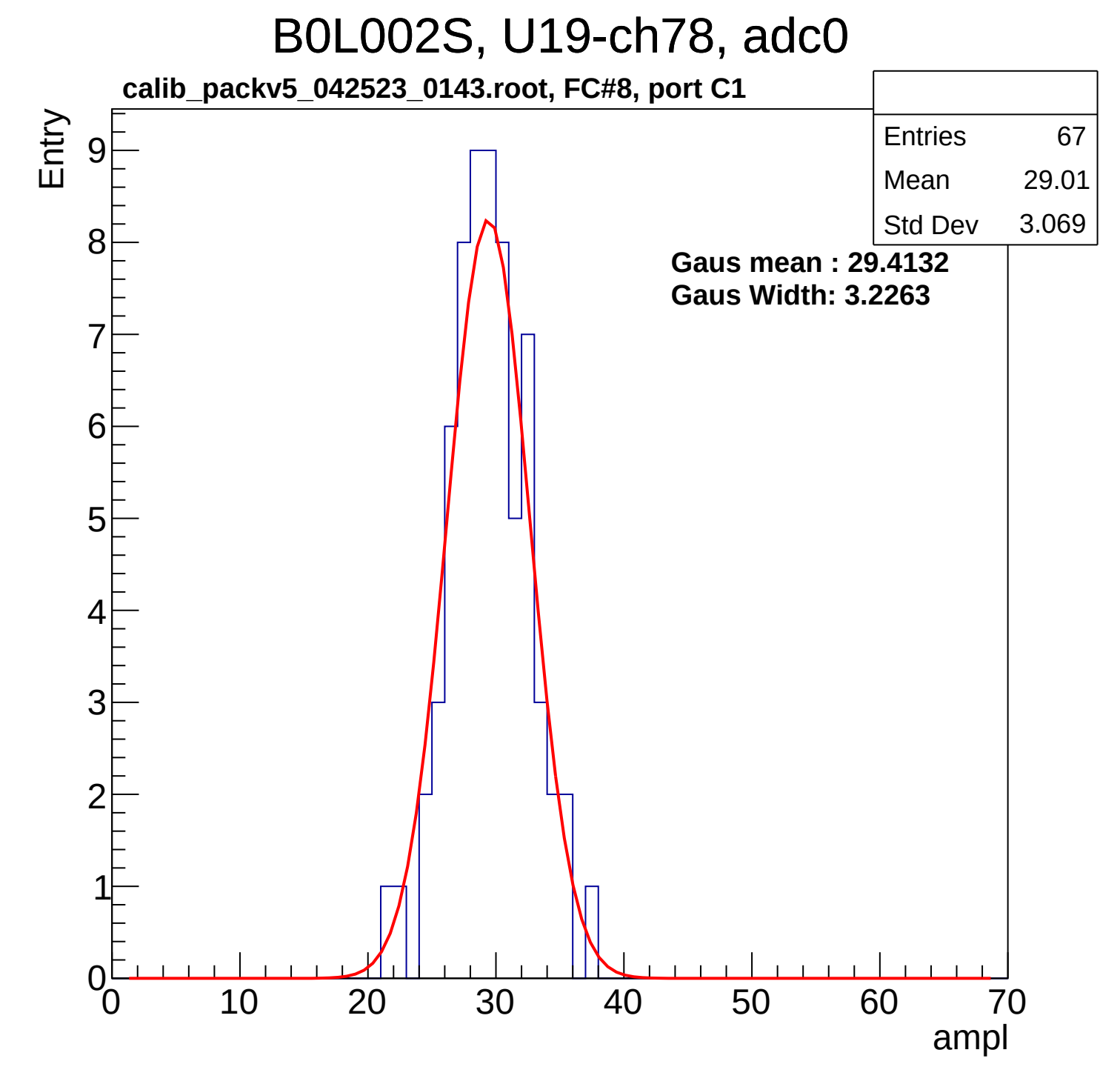

B0L002S, U19-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 49.85
Std Dev 3.49
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

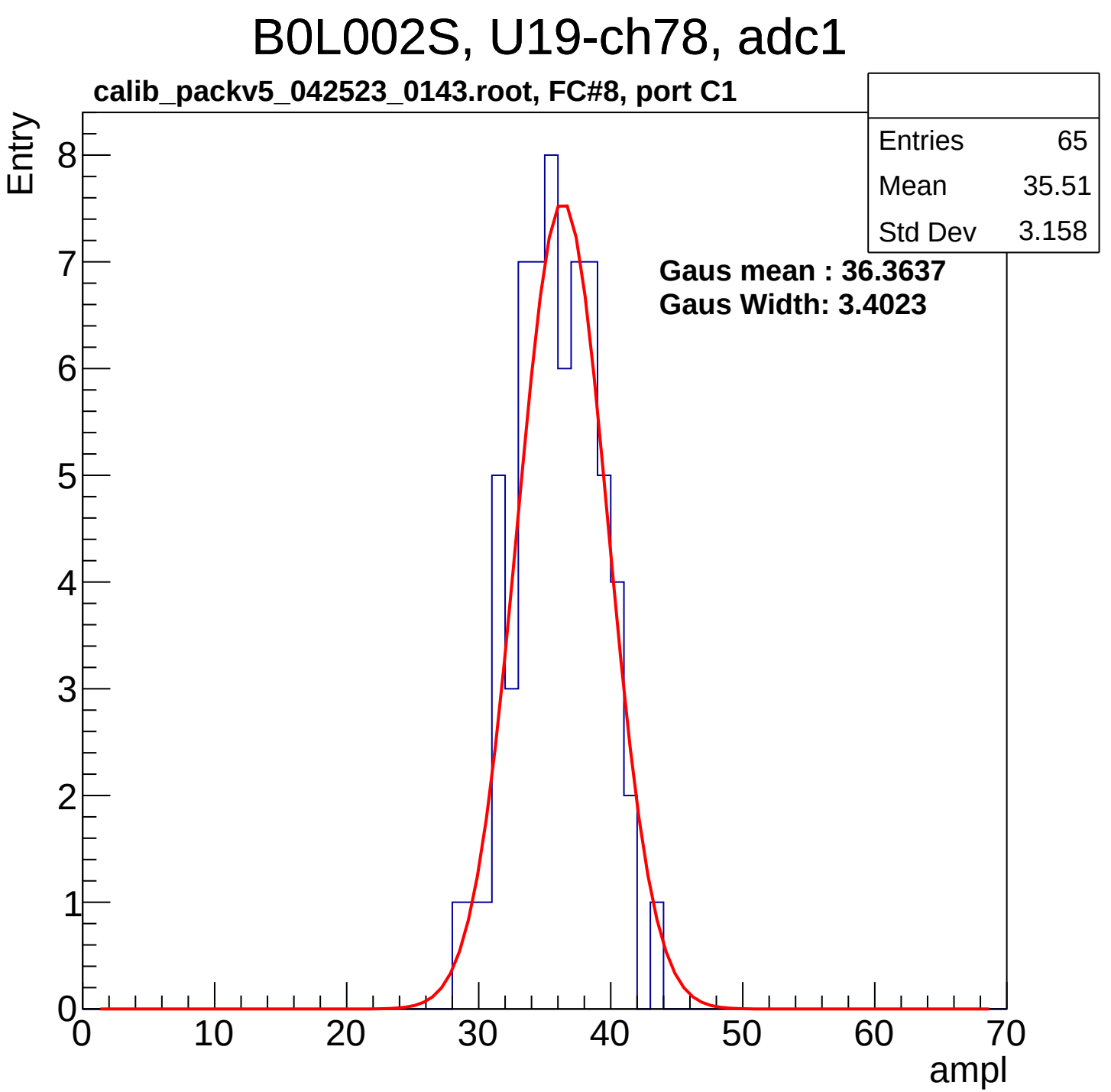

B0L002S, U19-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.63
Std Dev 3.047
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

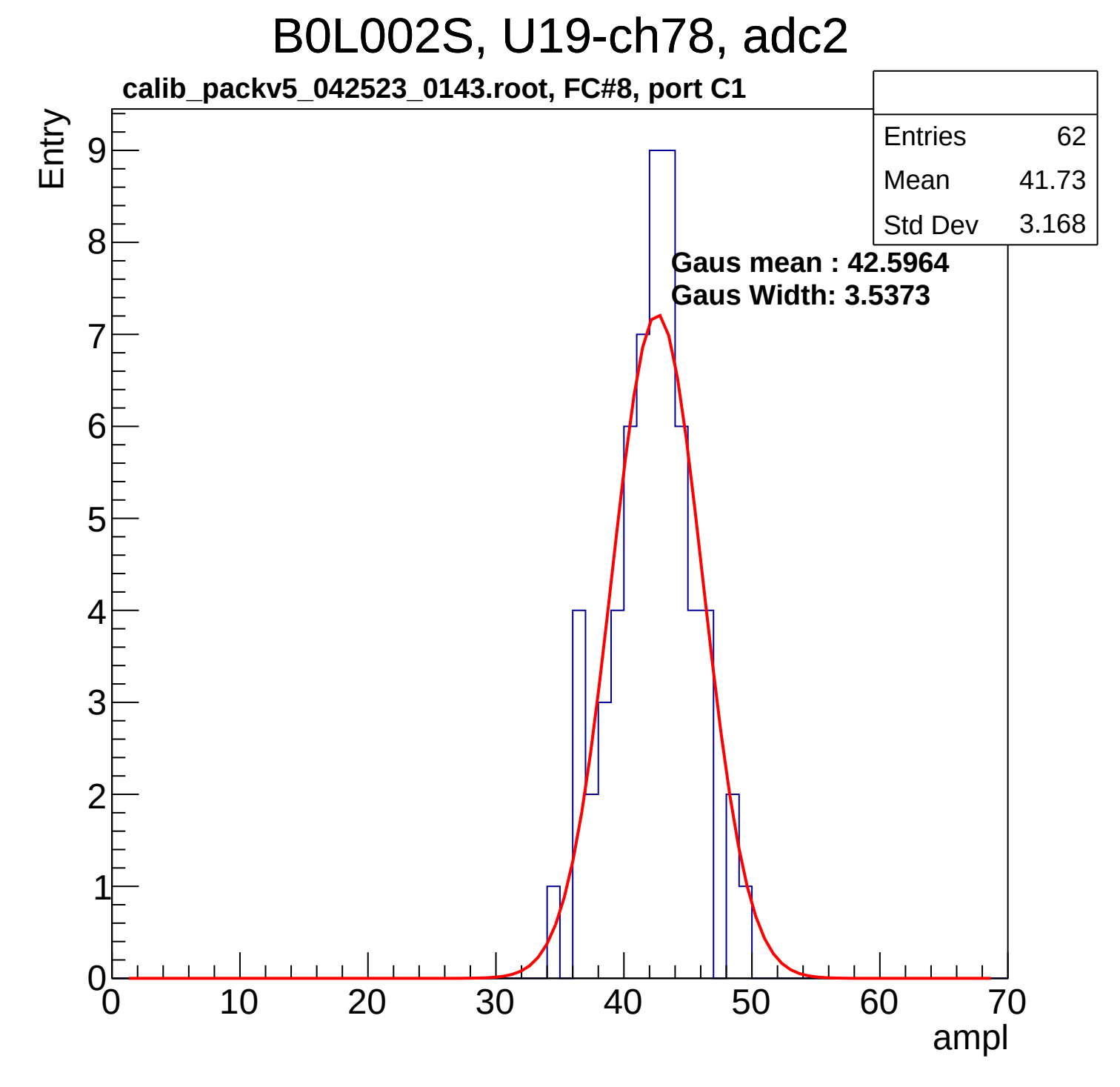

B0L002S, U19-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.11
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

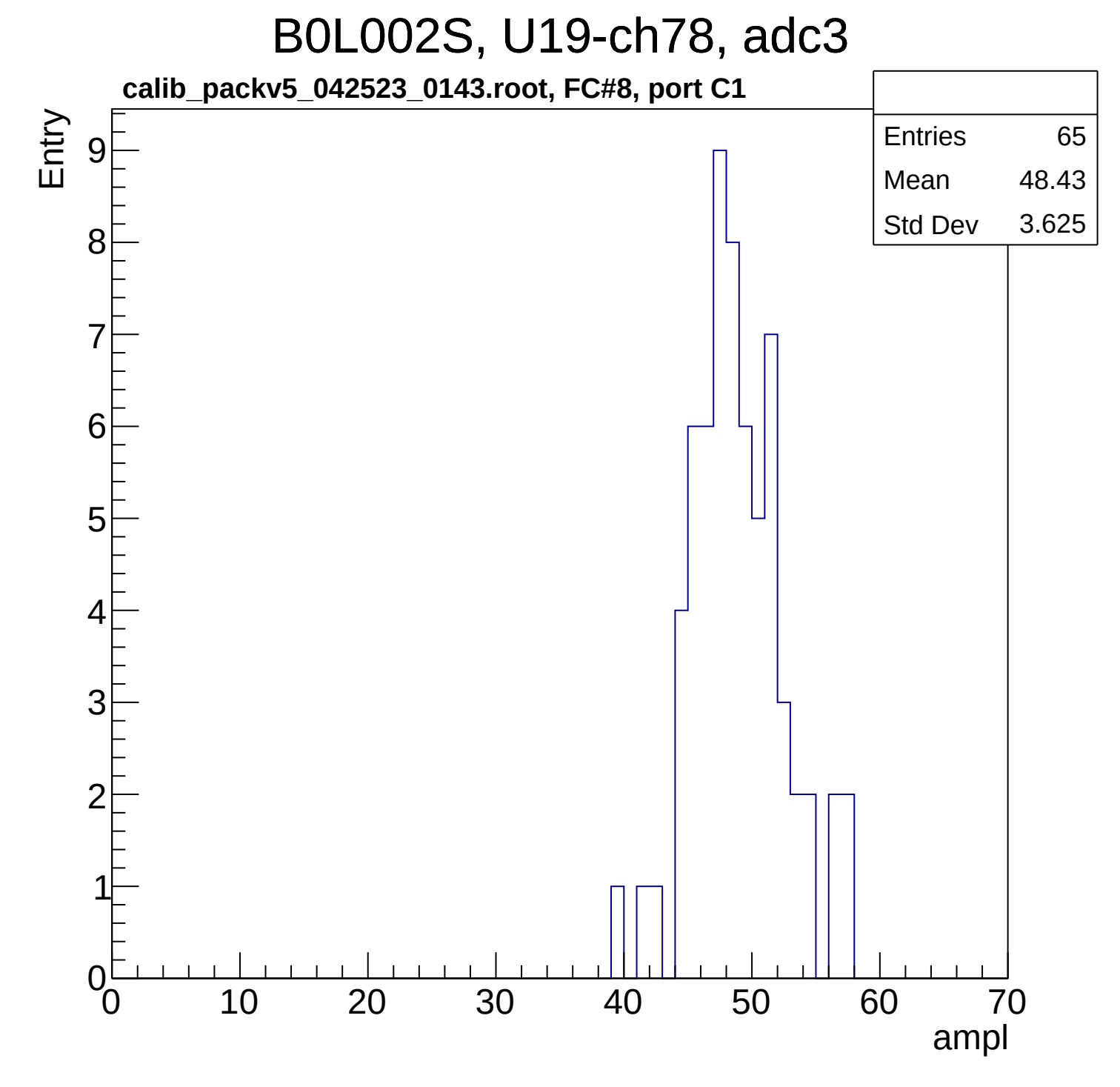

B0L002S, U19-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

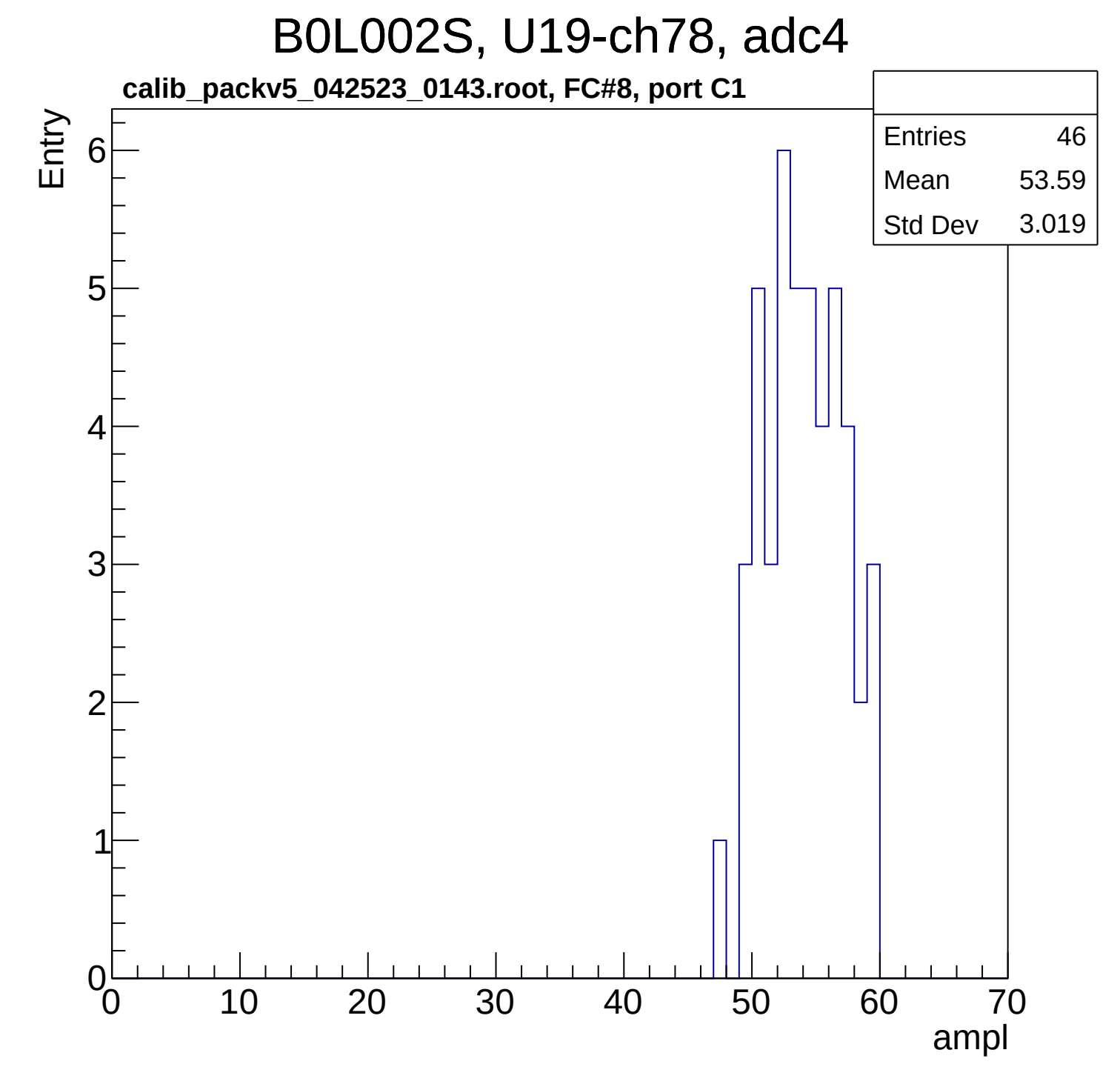

B0L002S, U19-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

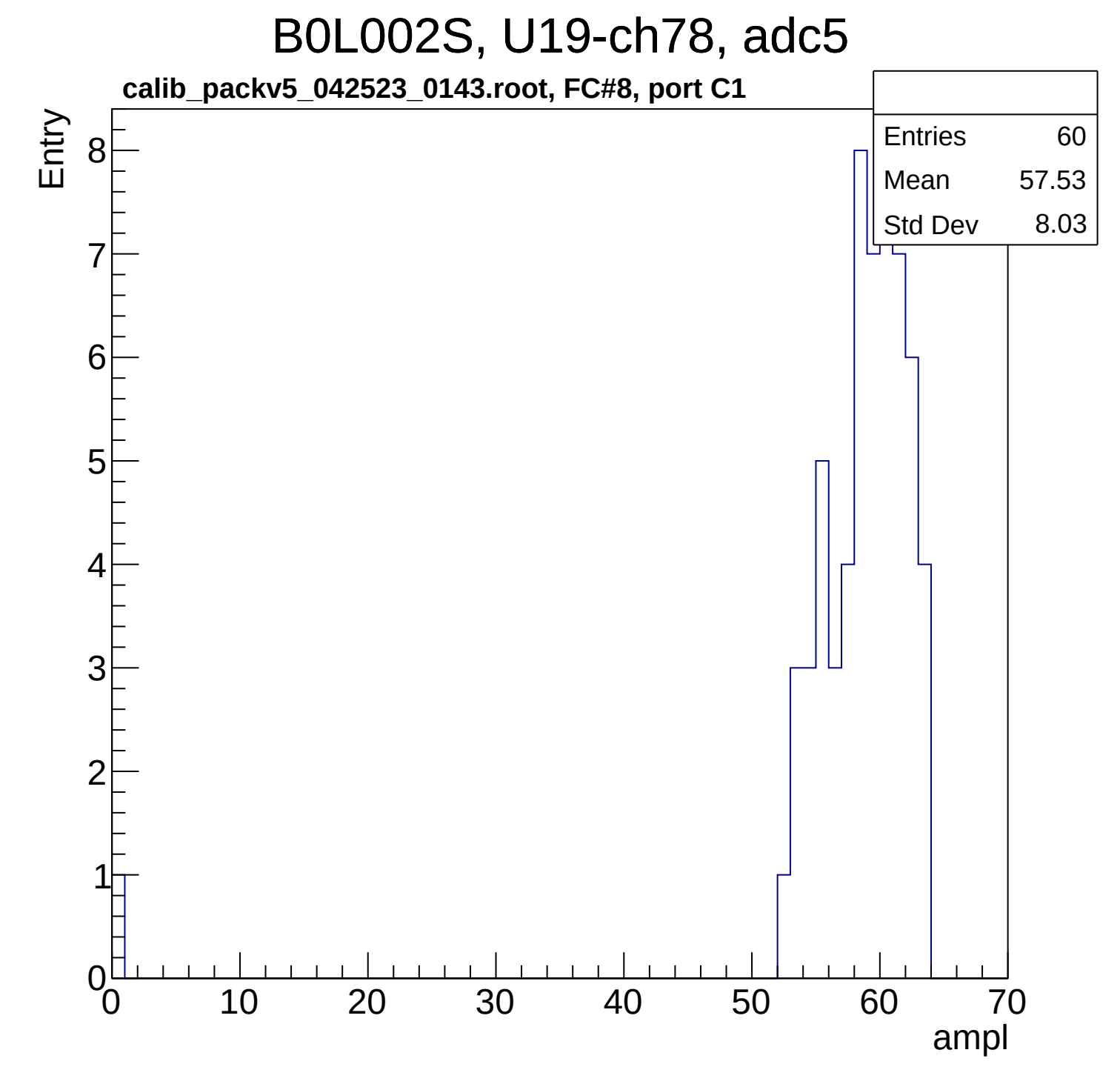

B0L002S, U19-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.01
Std Dev 3.069
Gaus mean : 29.4132
Gaus Width: 3.2263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

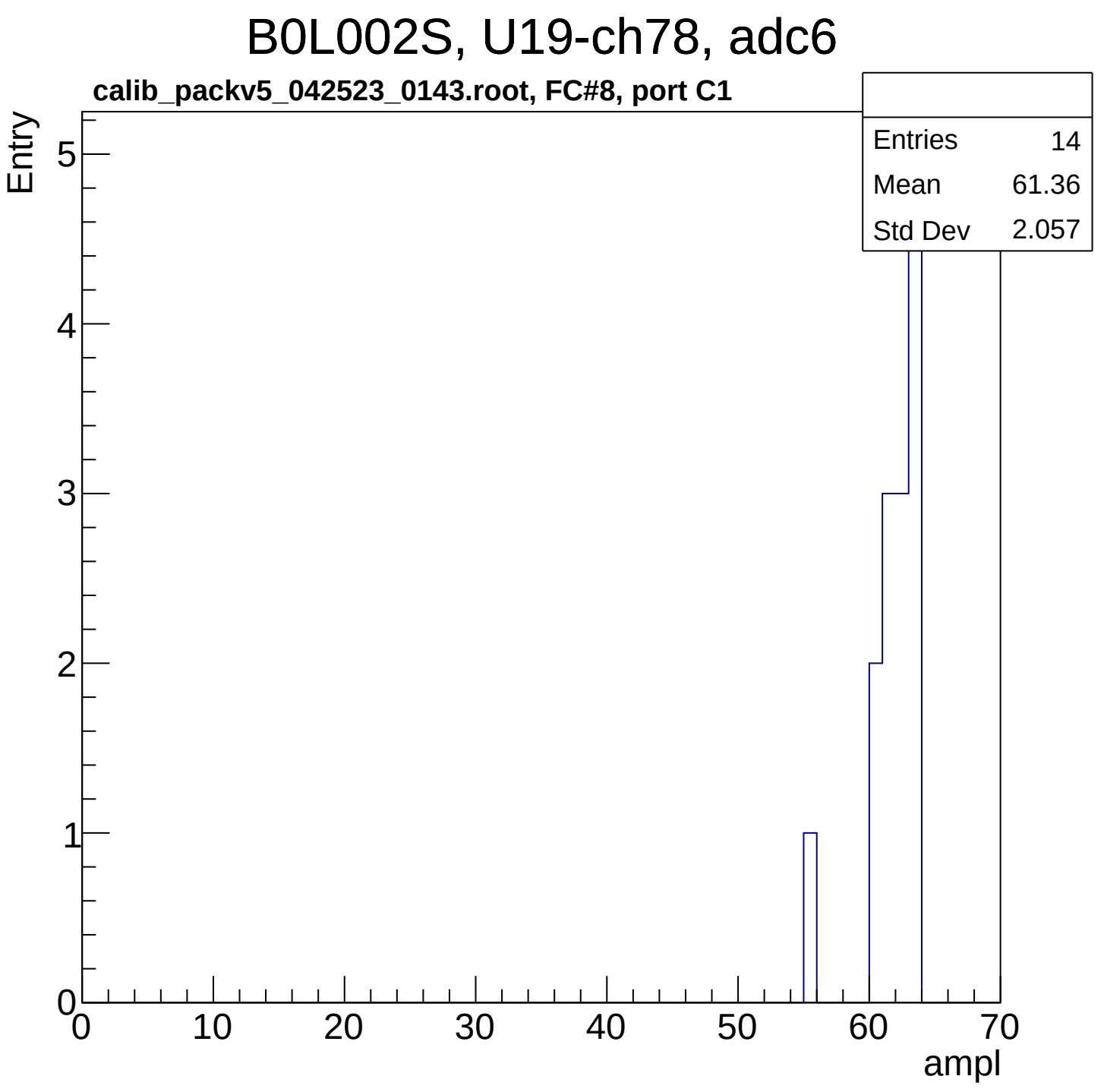

B0L002S, U19-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 35.51
Std Dev 3.158
Gaus mean : 36.3637
Gaus Width: 3.4023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

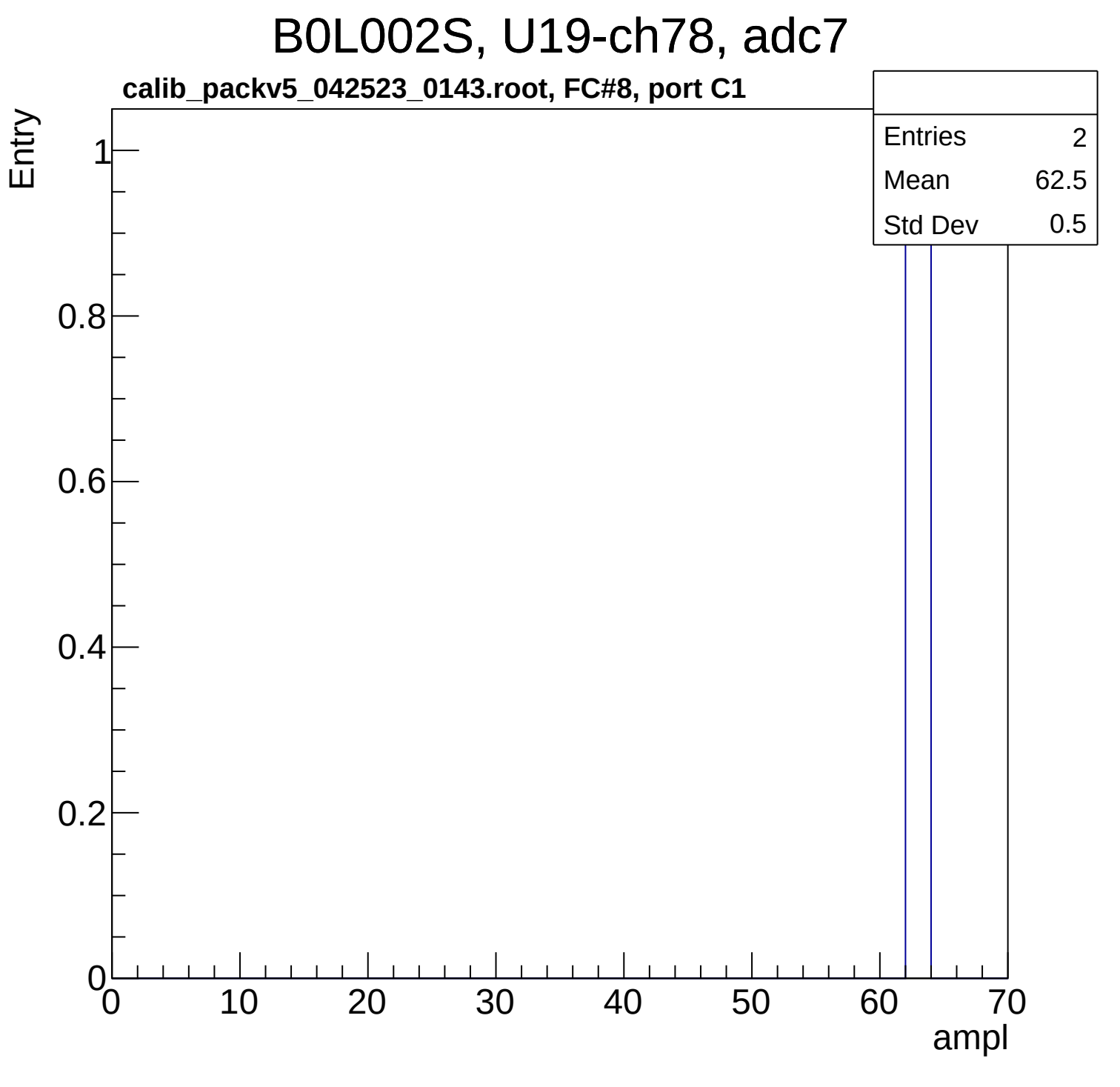

B0L002S, U19-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 41.73
Std Dev 3.168
Gaus mean : 42.5964
Gaus Width: 3.5373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

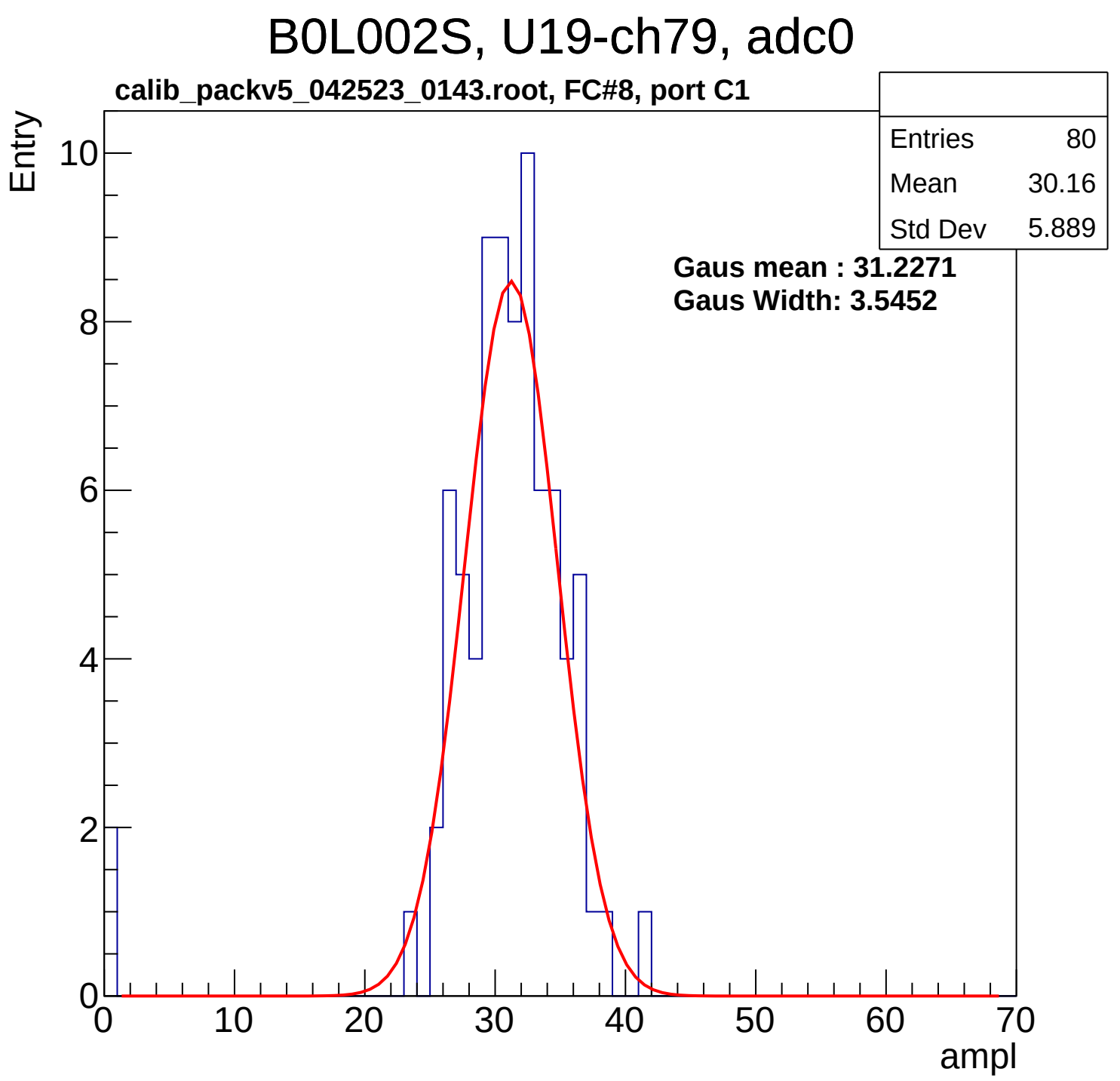

B0L002S, U19-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 48.43
Std Dev 3.625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

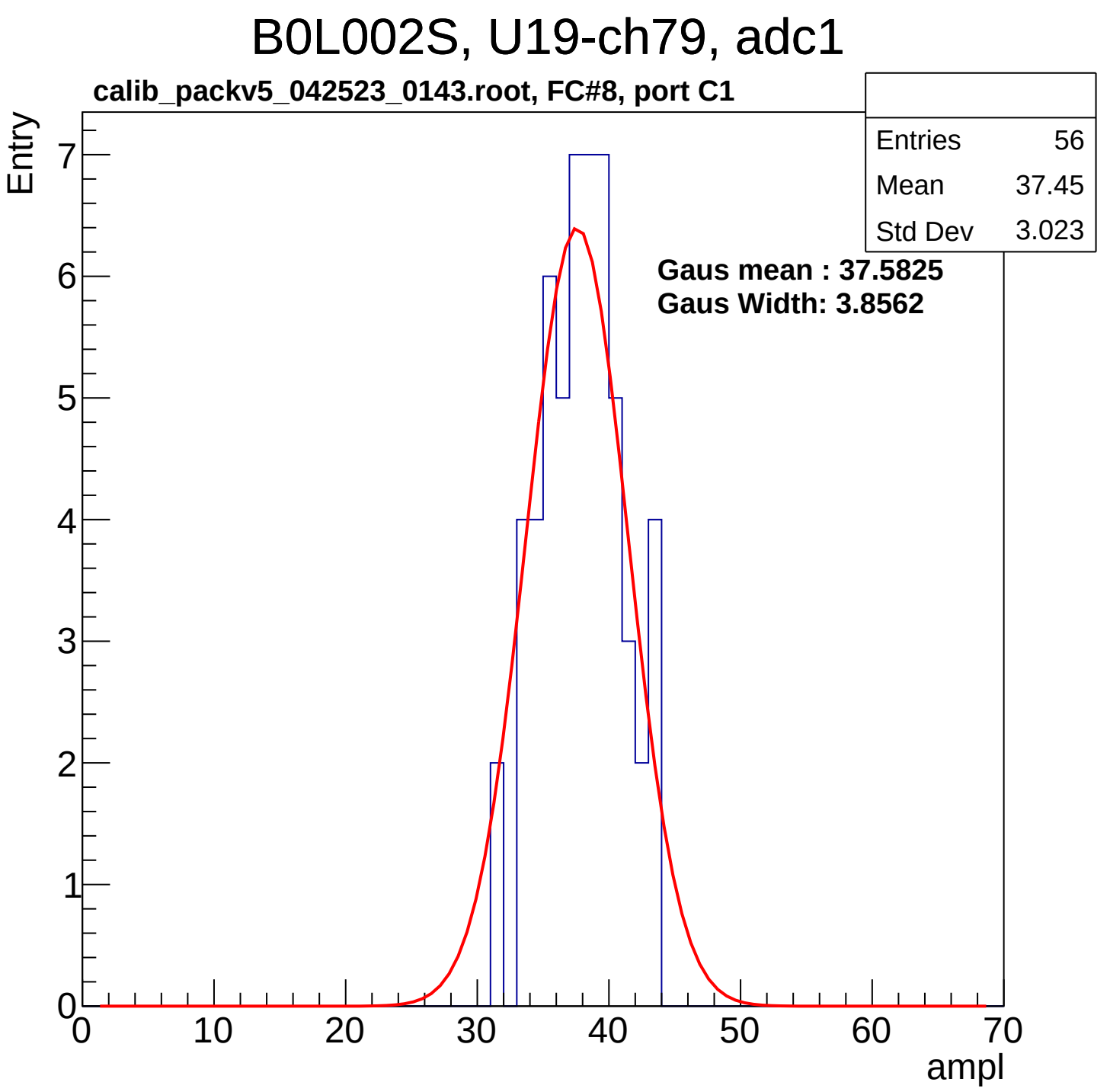

B0L002S, U19-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 53.59
Std Dev 3.019
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

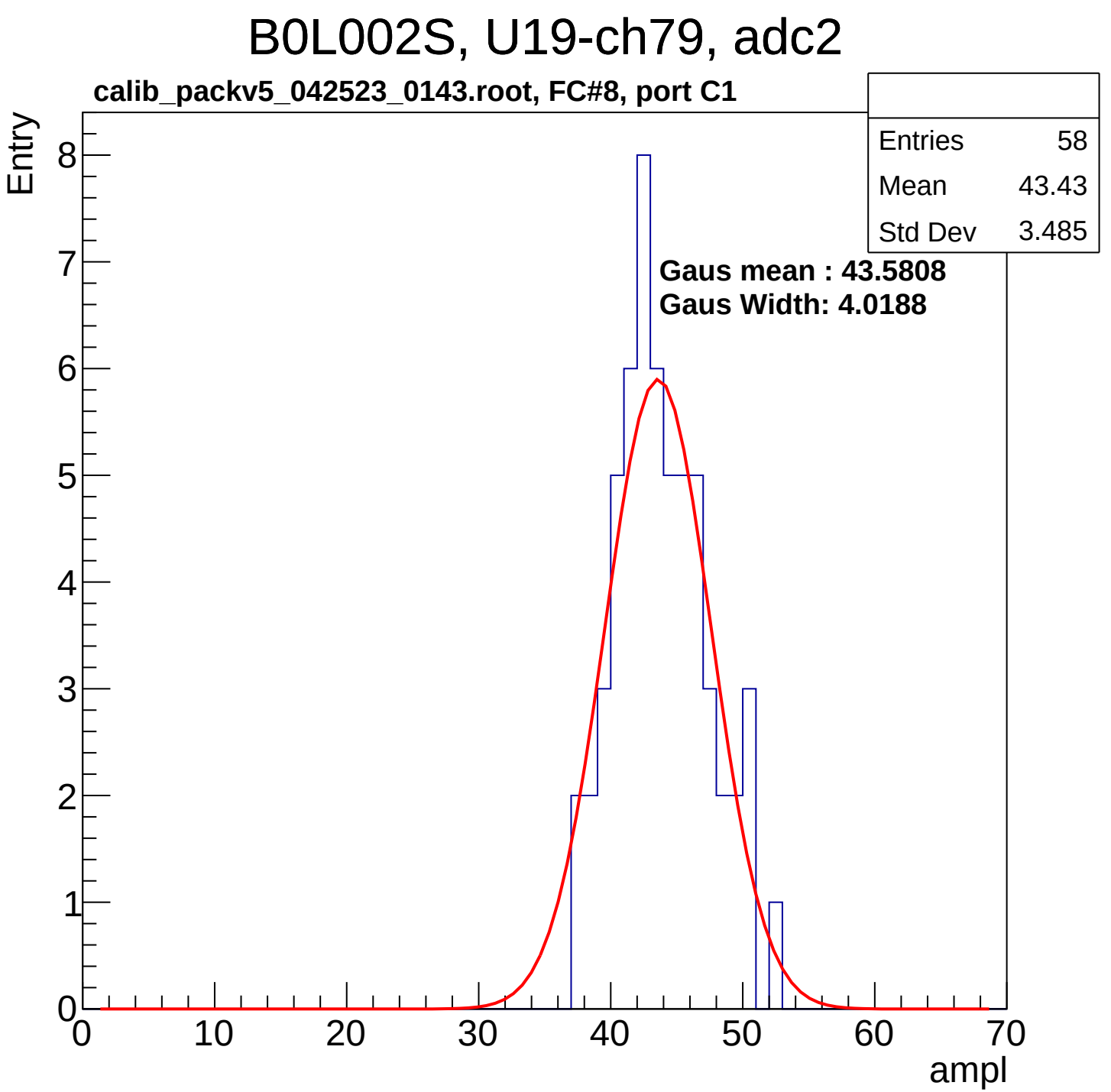

B0L002S, U19-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.53
Std Dev 8.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

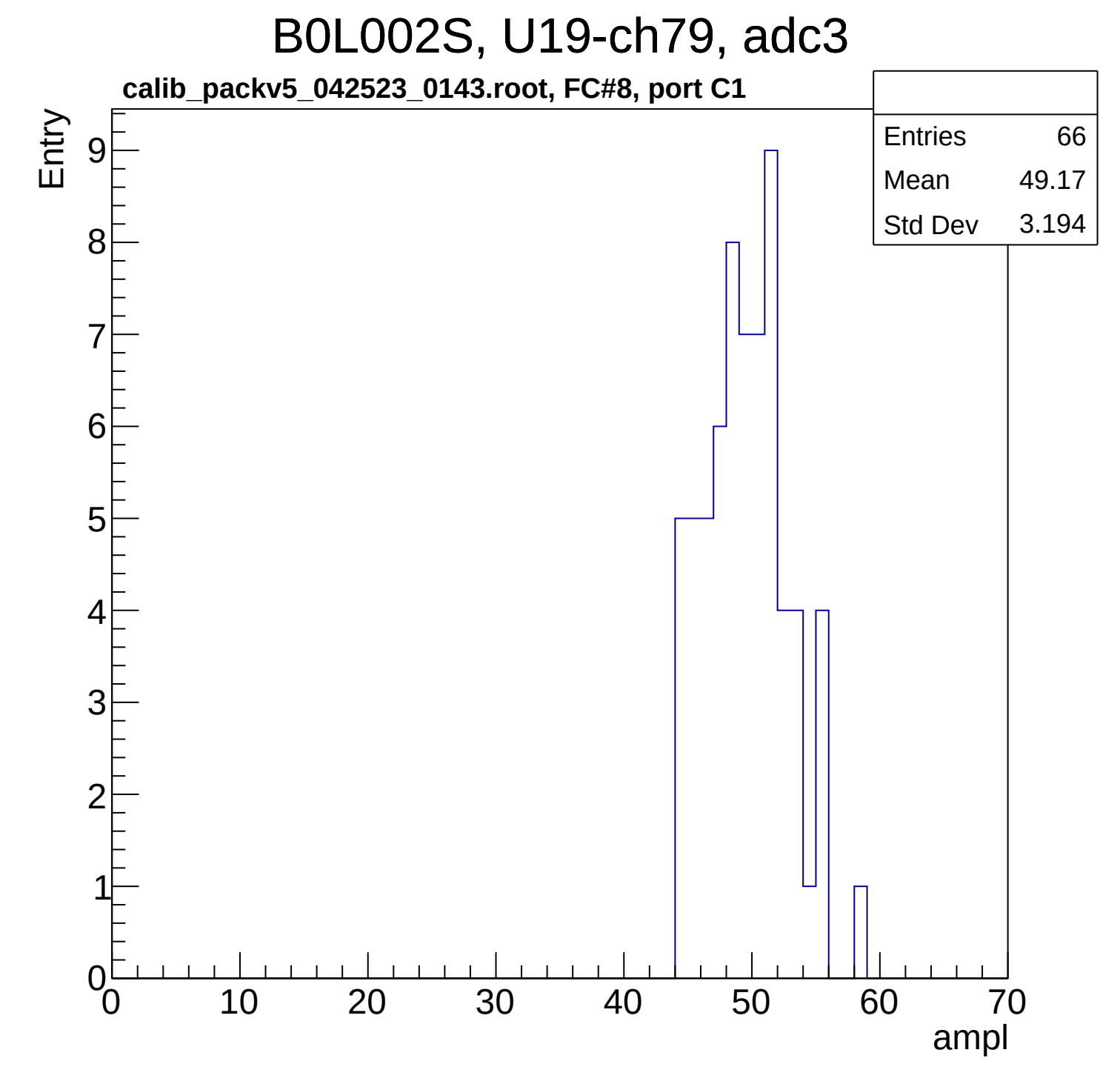

B0L002S, U19-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 61.36
Std Dev 2.057
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

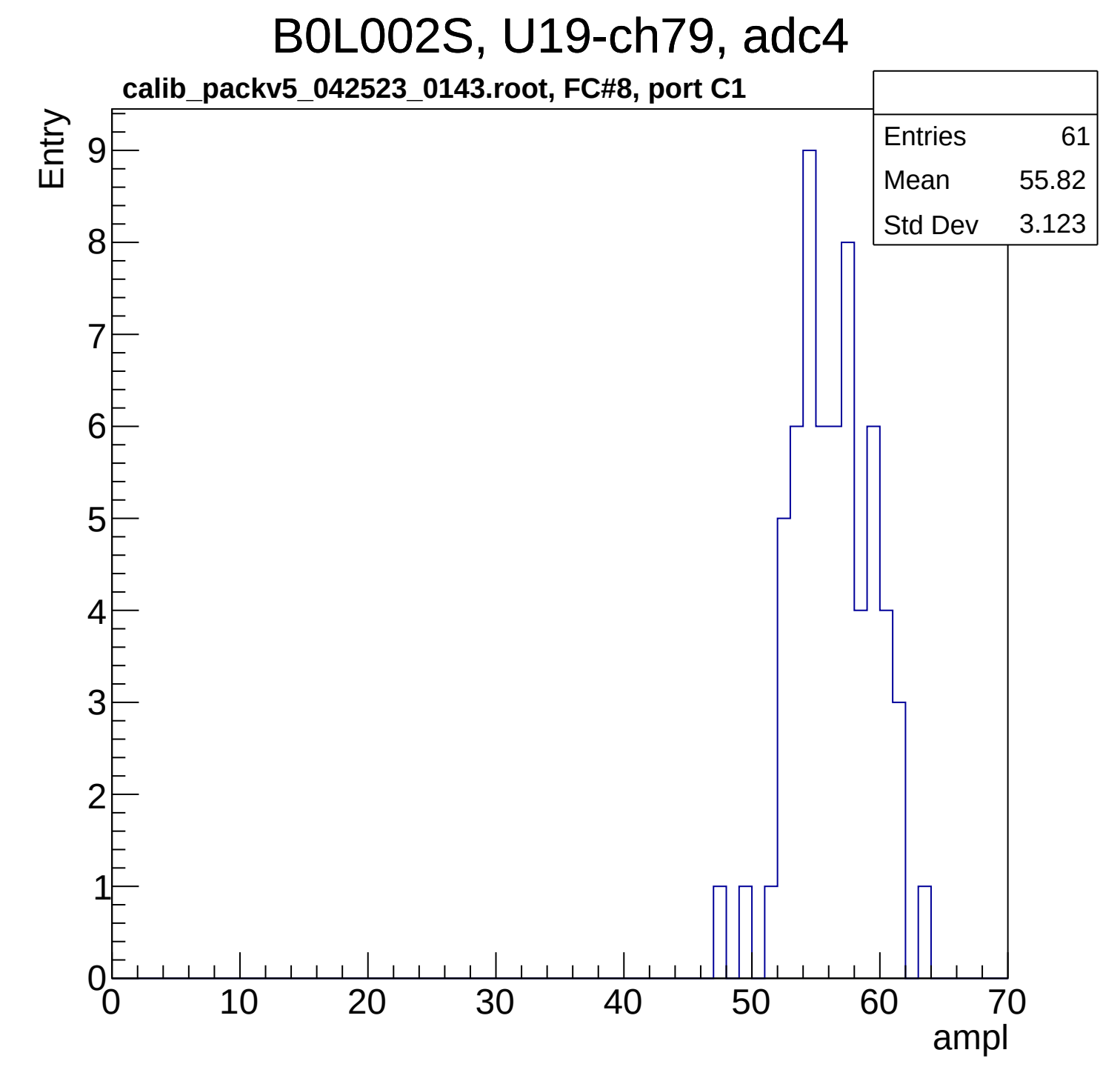

B0L002S, U19-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

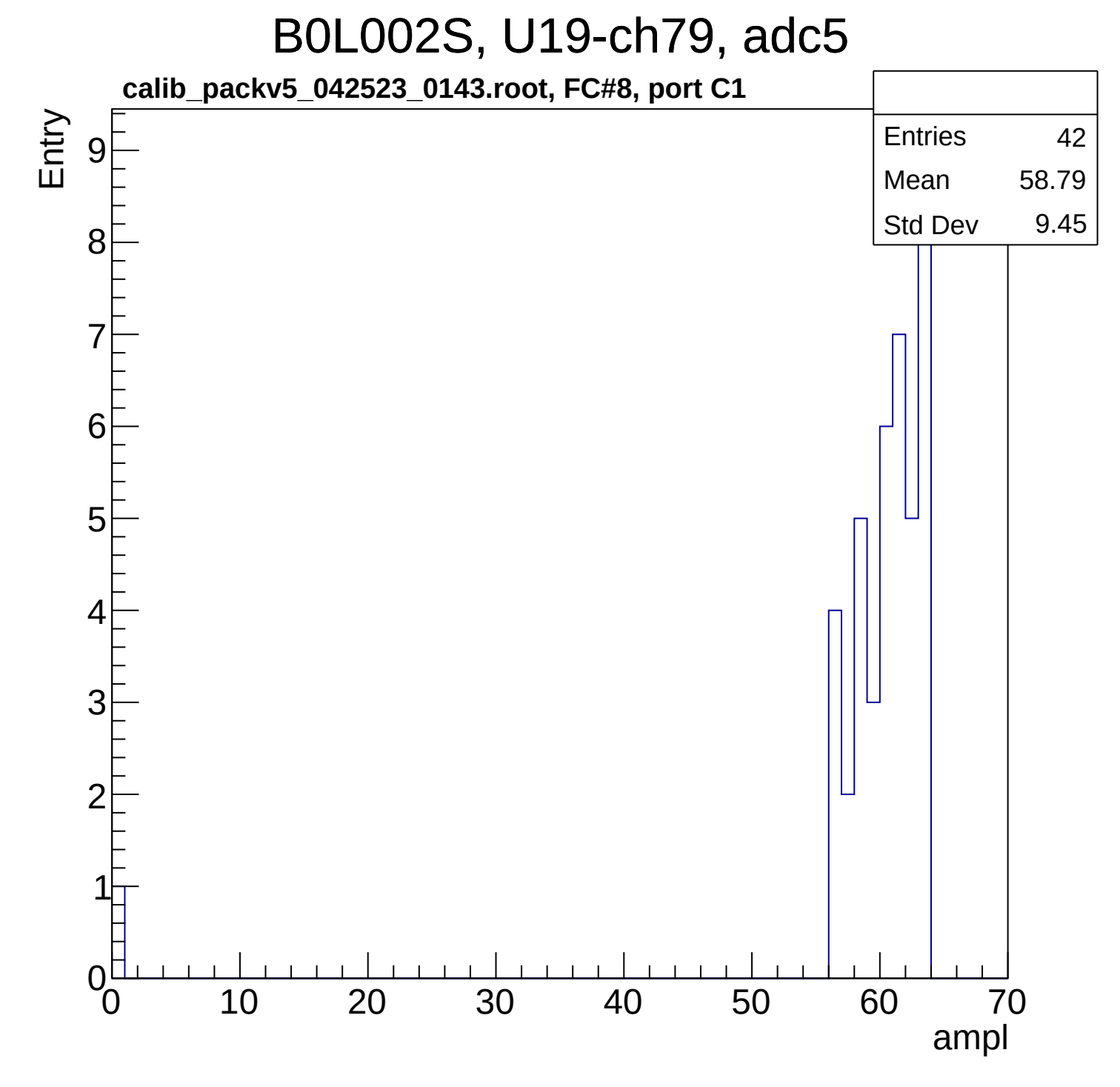

B0L002S, U19-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.16
Std Dev 5.889
Gaus mean : 31.2271
Gaus Width: 3.5452
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

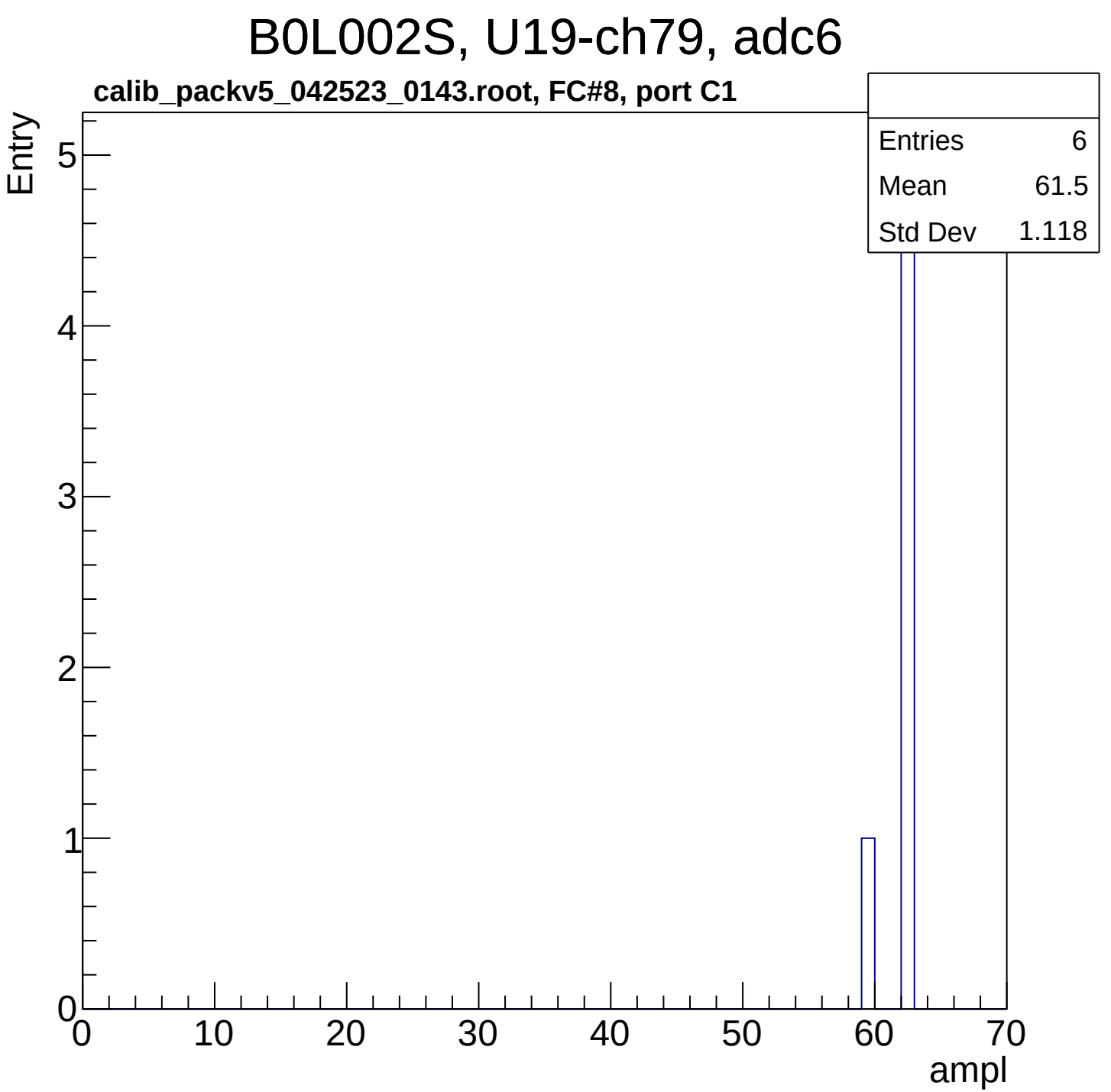

B0L002S, U19-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 37.45
Std Dev 3.023
Gaus mean : 37.5825
Gaus Width: 3.8562
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

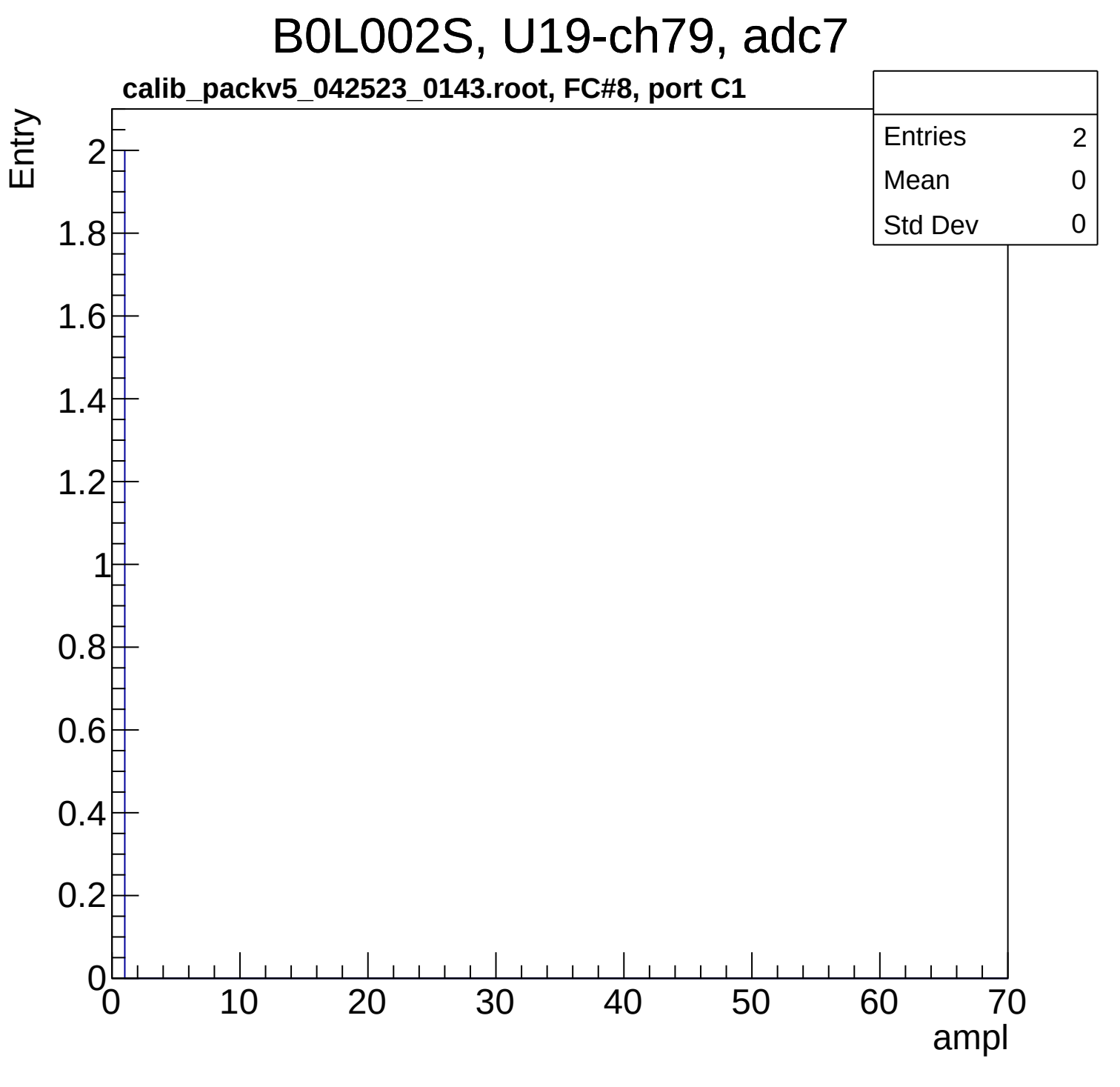

B0L002S, U19-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.43
Std Dev 3.485
Gaus mean : 43.5808
Gaus Width: 4.0188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

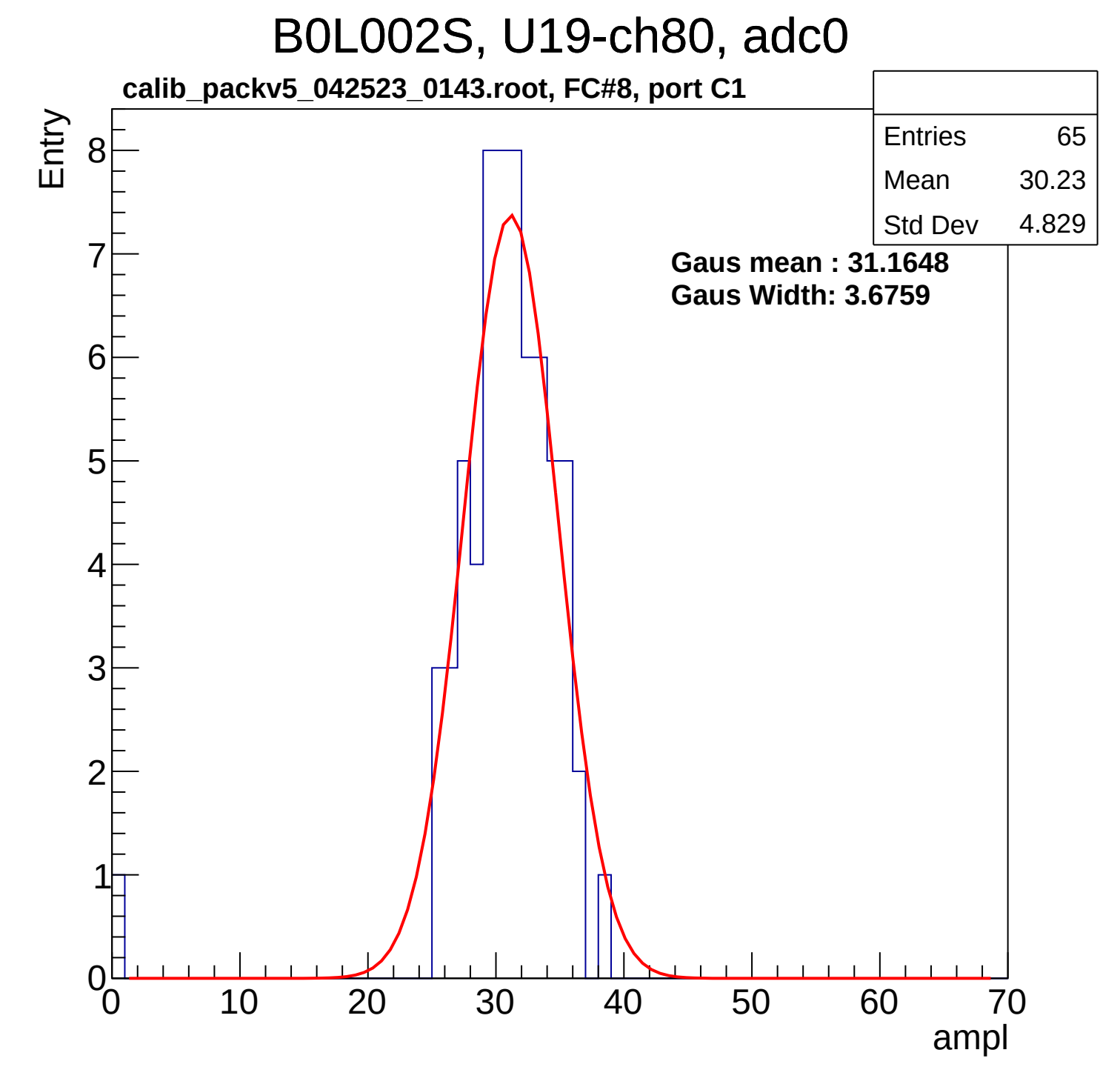

B0L002S, U19-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.17
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

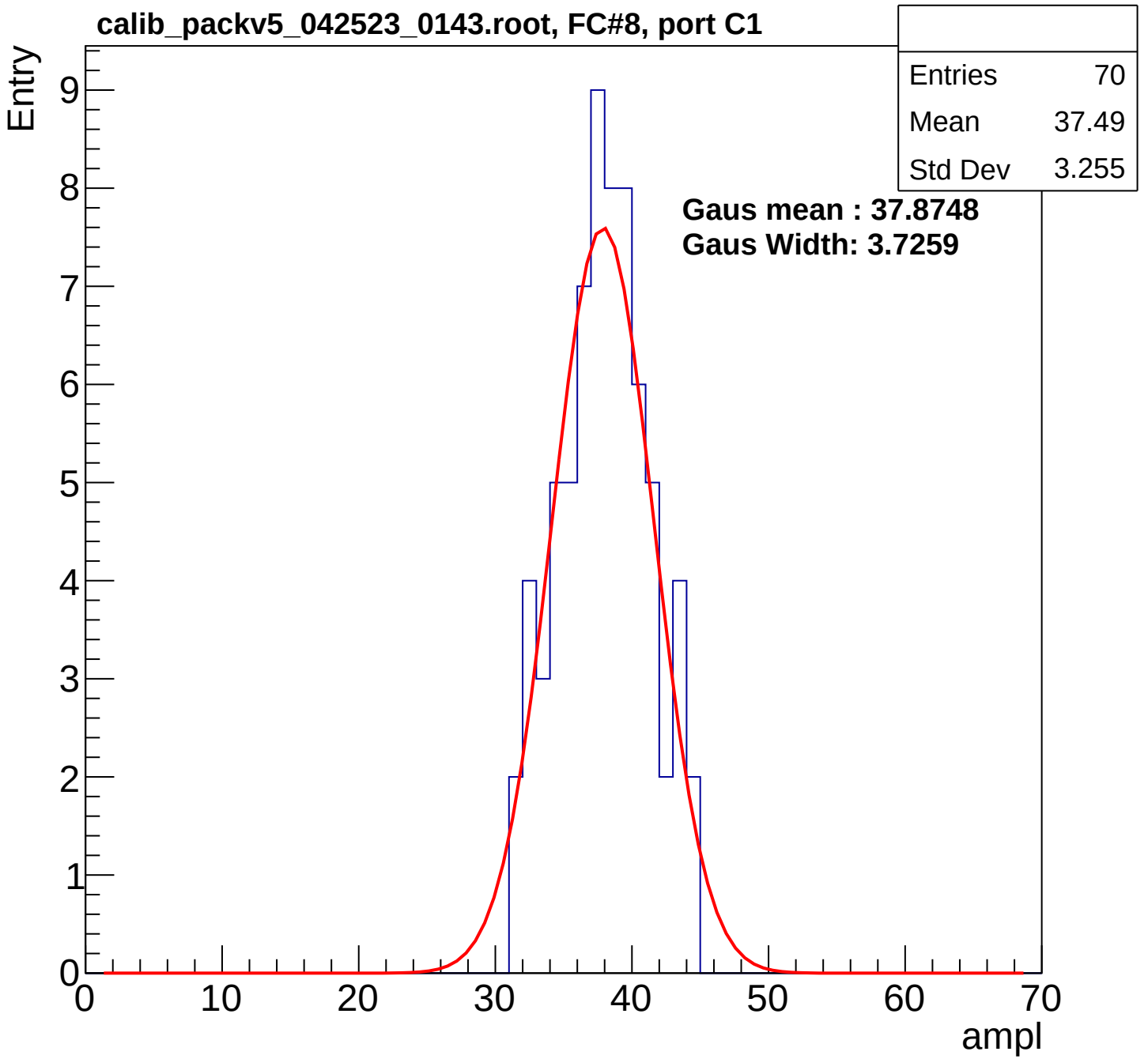

B0L002S, U19-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.82
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

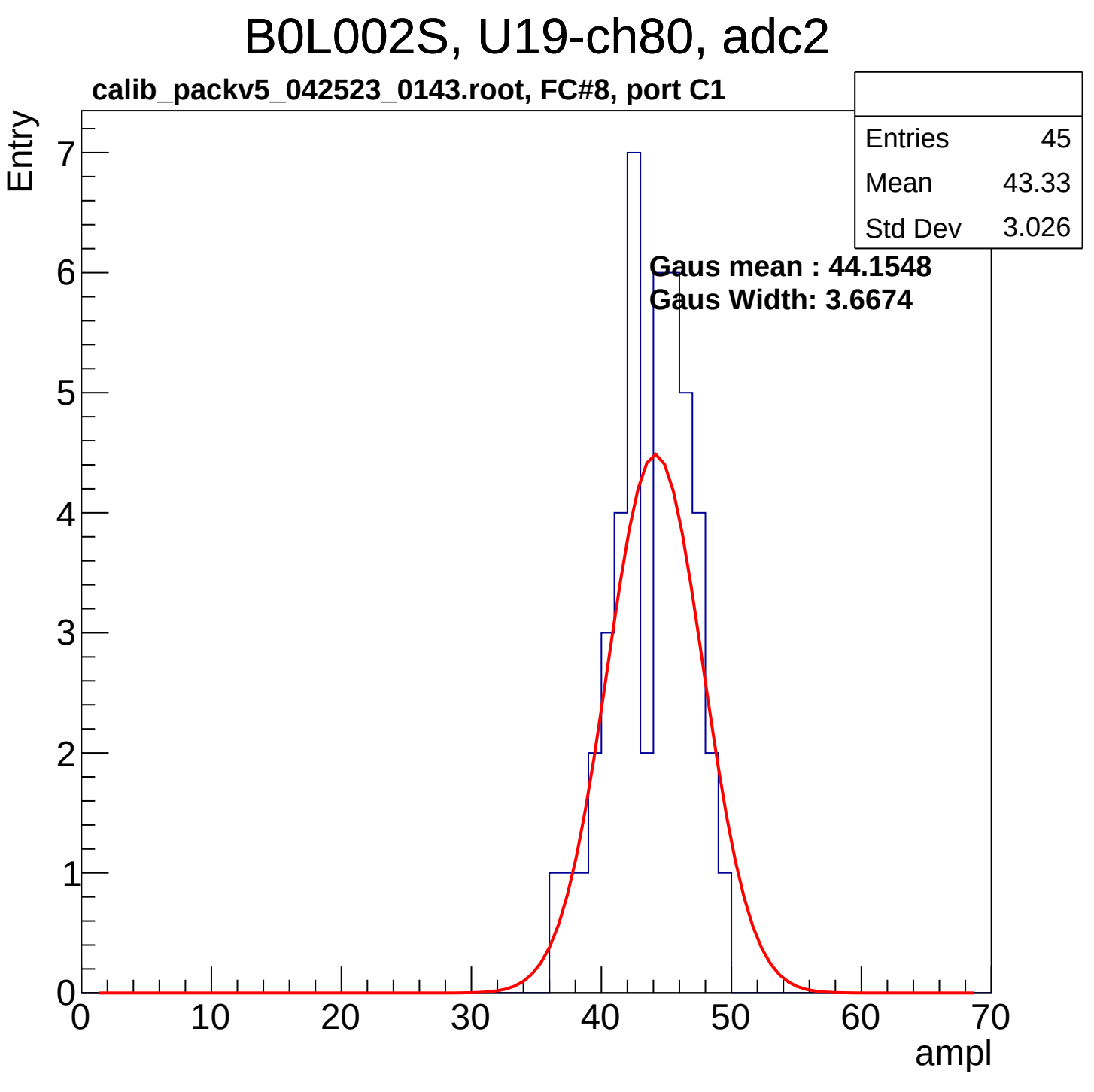

B0L002S, U19-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.79
Std Dev 9.45
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

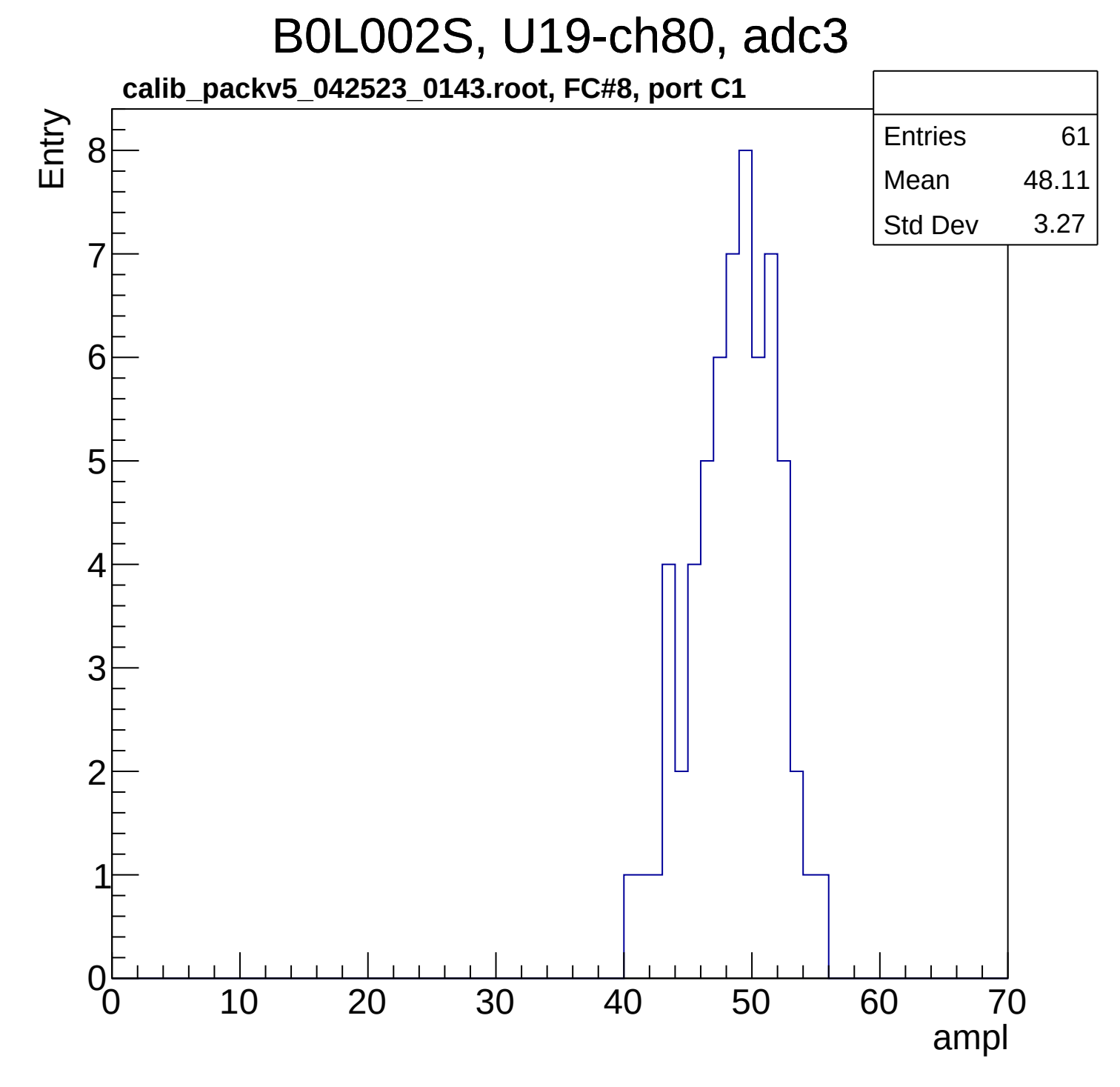

B0L002S, U19-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.5
Std Dev 1.118
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

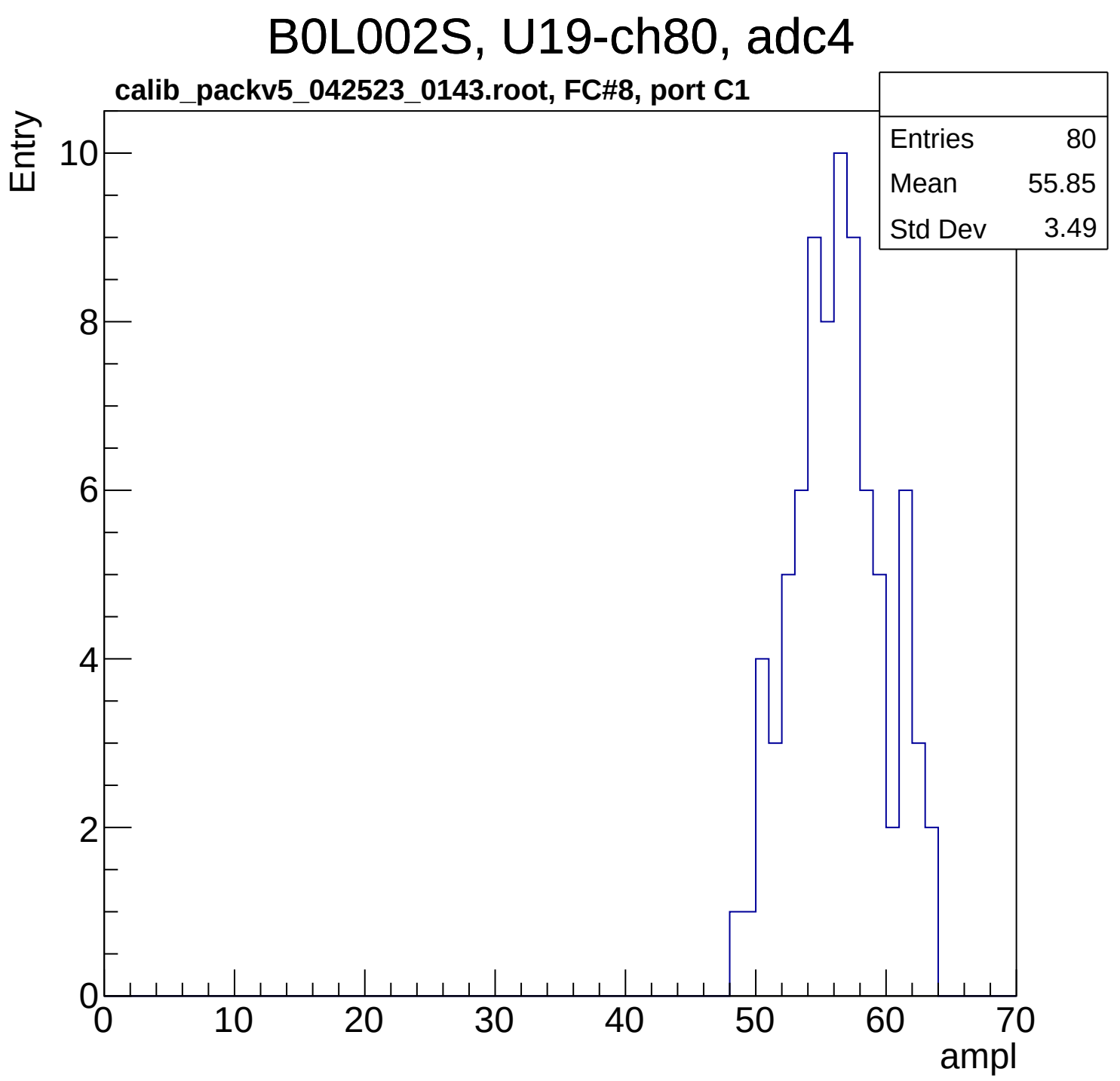

B0L002S, U19-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

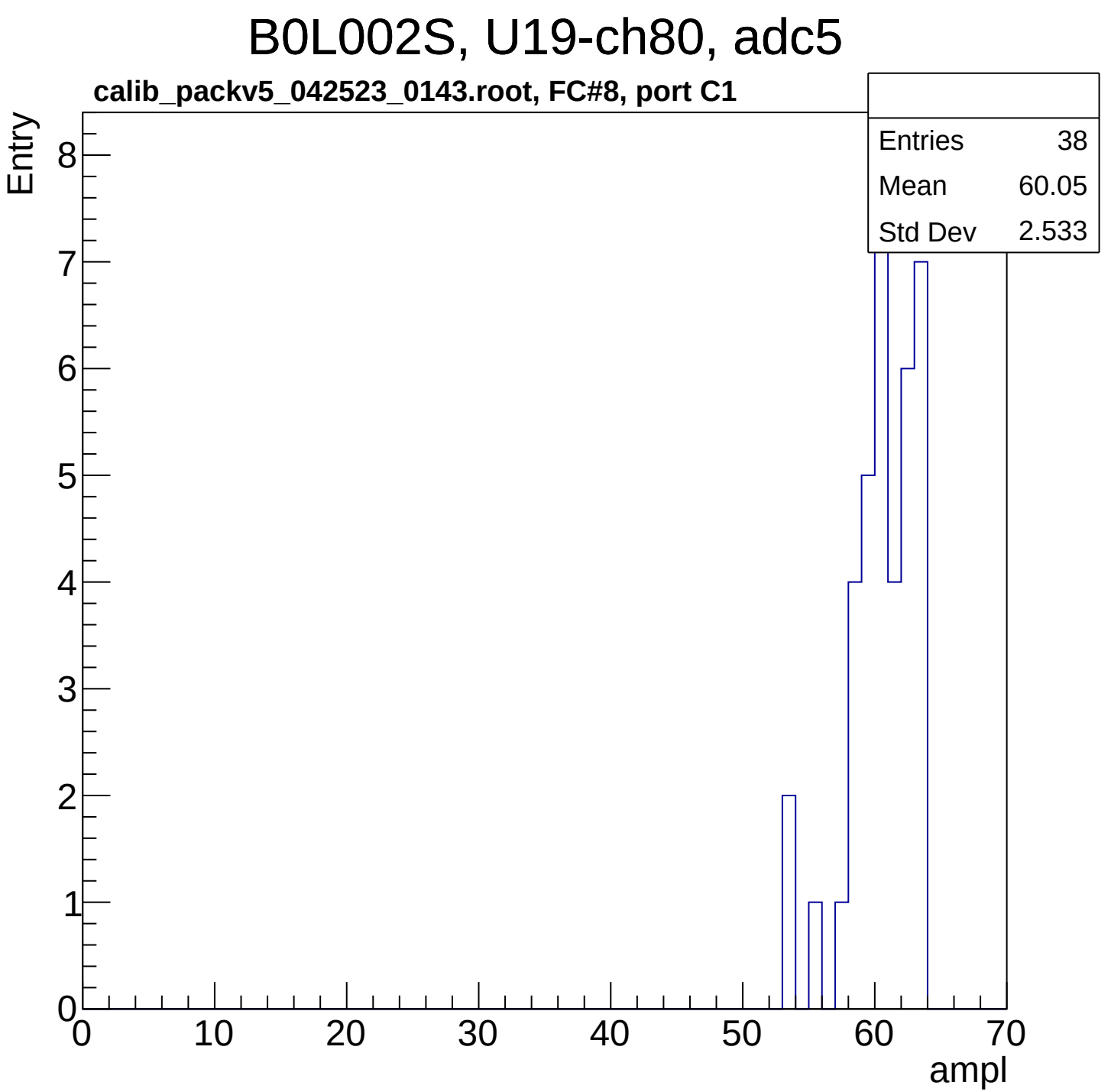

B0L002S, U19-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.23
Std Dev 4.829
Gaus mean : 31.1648
Gaus Width: 3.6759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

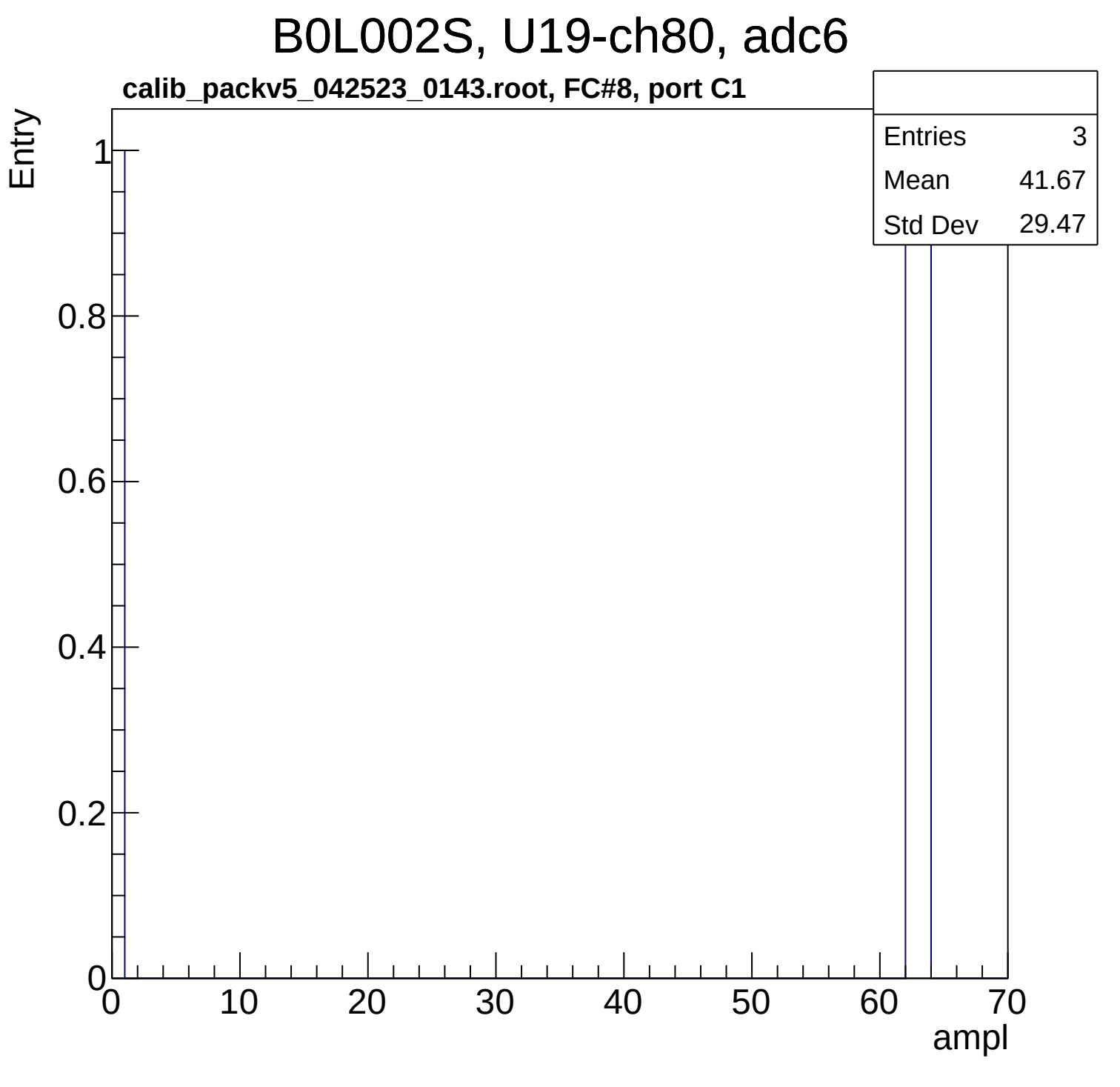

B0L002S, U19-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.49
Std Dev 3.255
Gaus mean : 37.8748
Gaus Width: 3.7259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

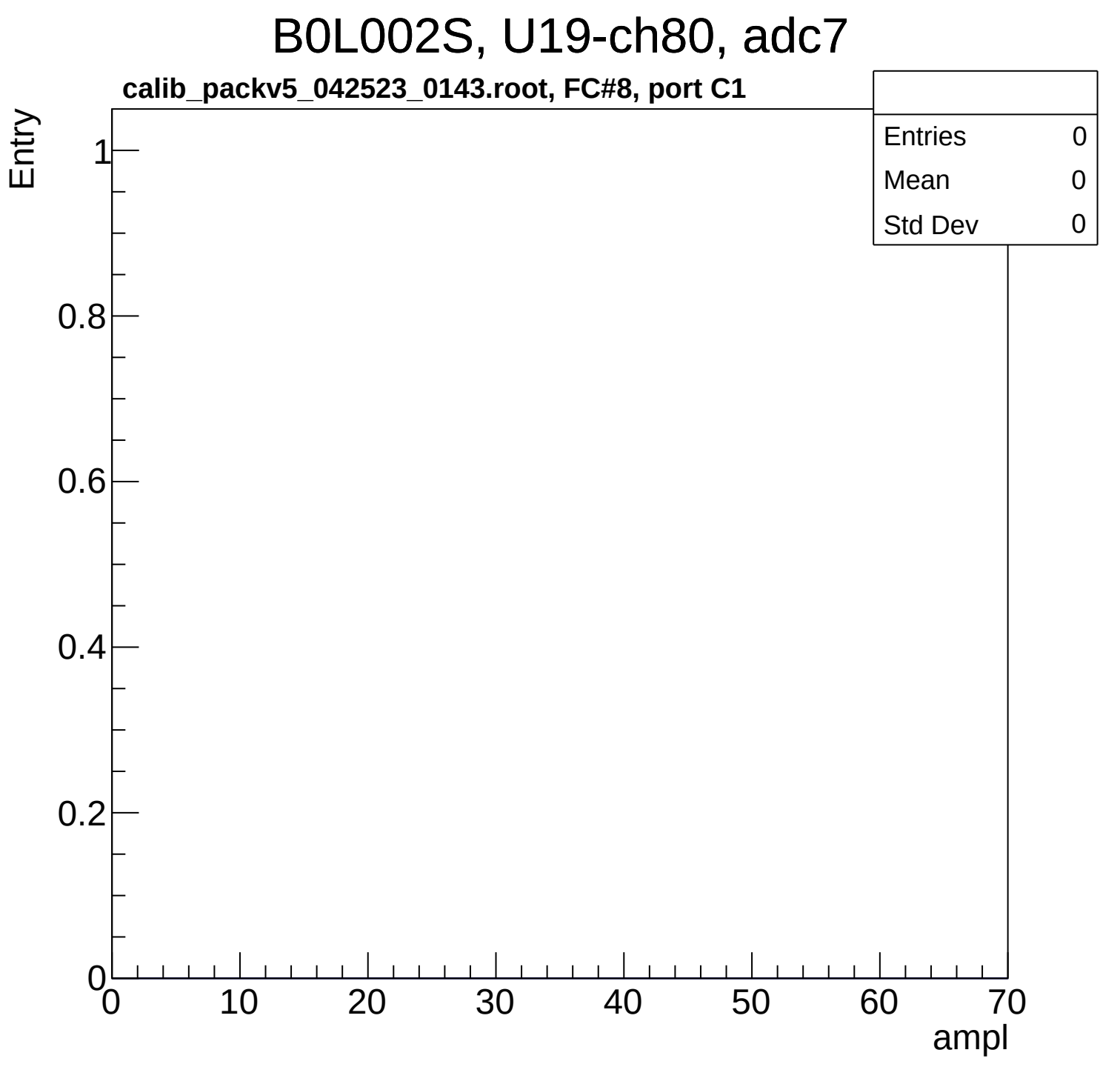

B0L002S, U19-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 43.33
Std Dev 3.026
Gaus mean : 44.1548
Gaus Width: 3.6674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

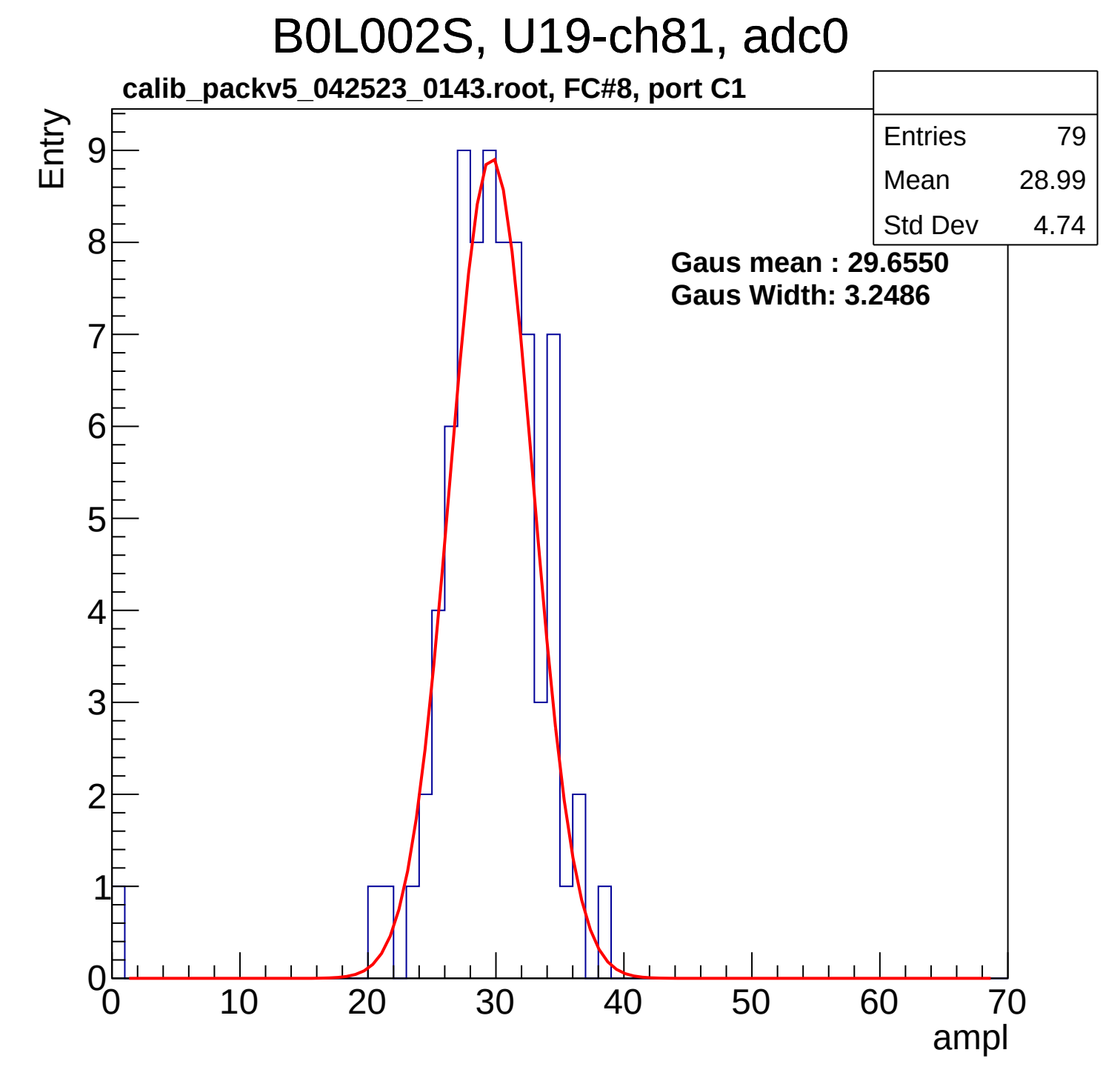

B0L002S, U19-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 48.11
Std Dev 3.27
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

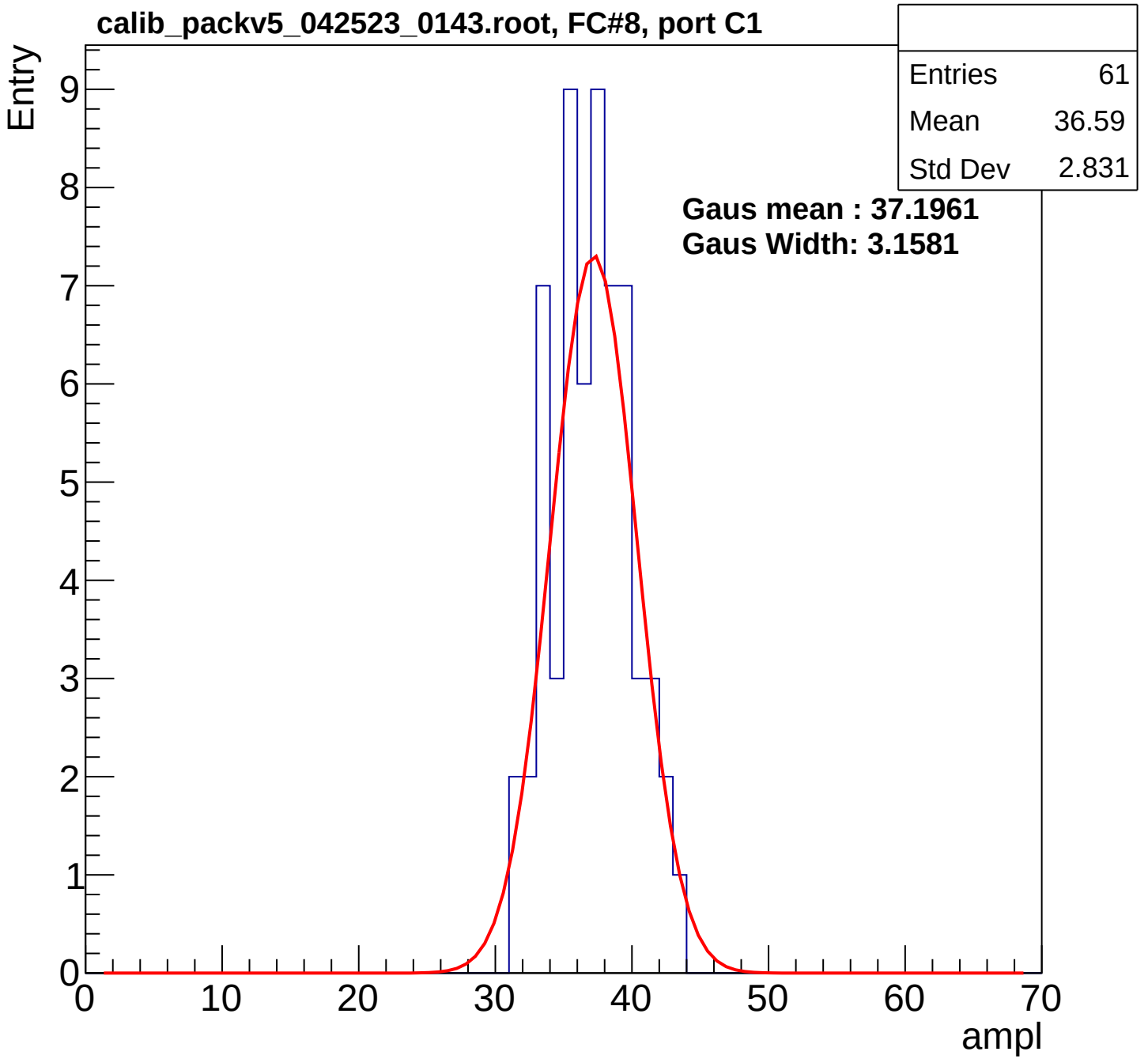

B0L002S, U19-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 55.85
Std Dev 3.49
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

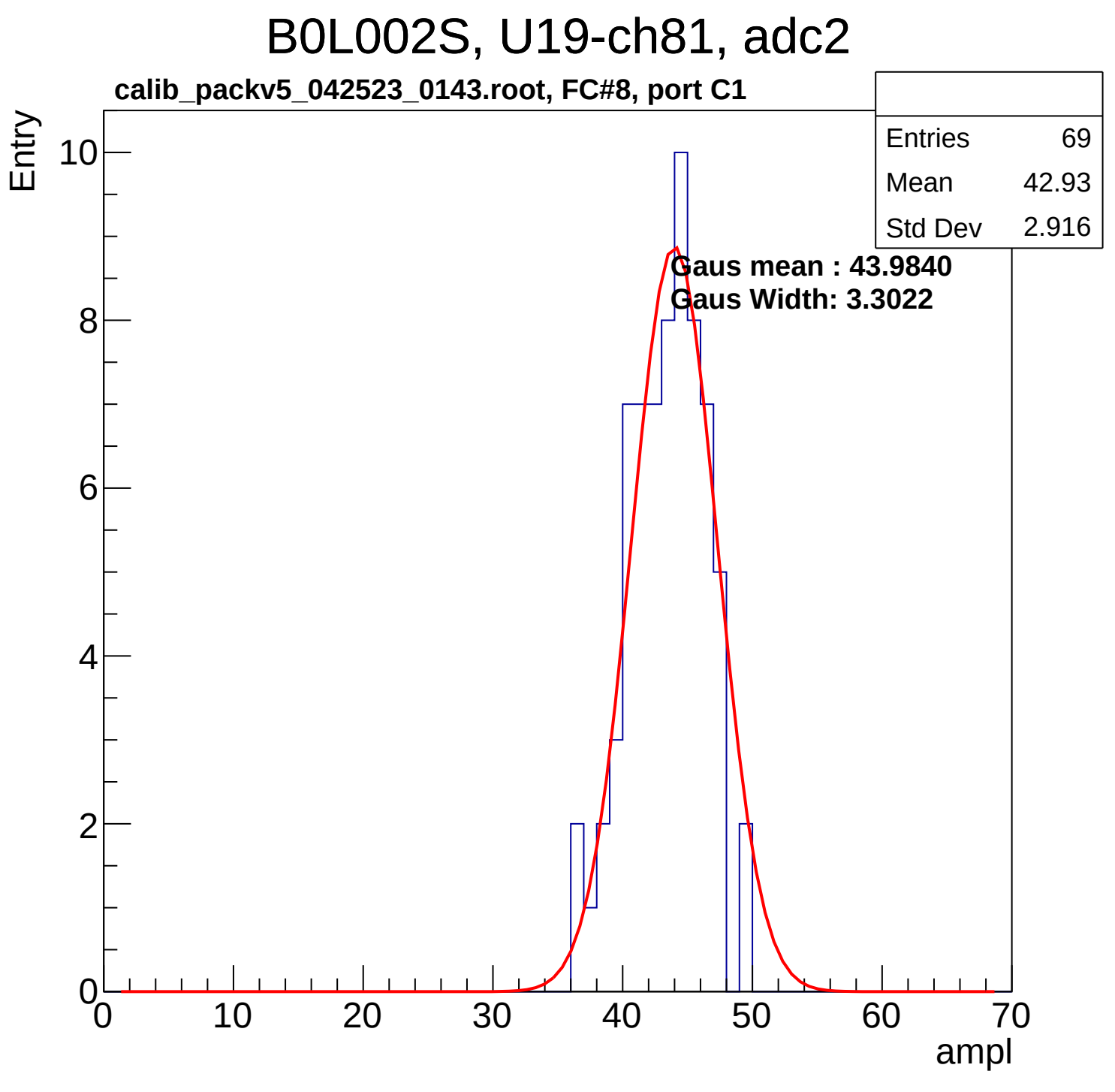

B0L002S, U19-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.05
Std Dev 2.533
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

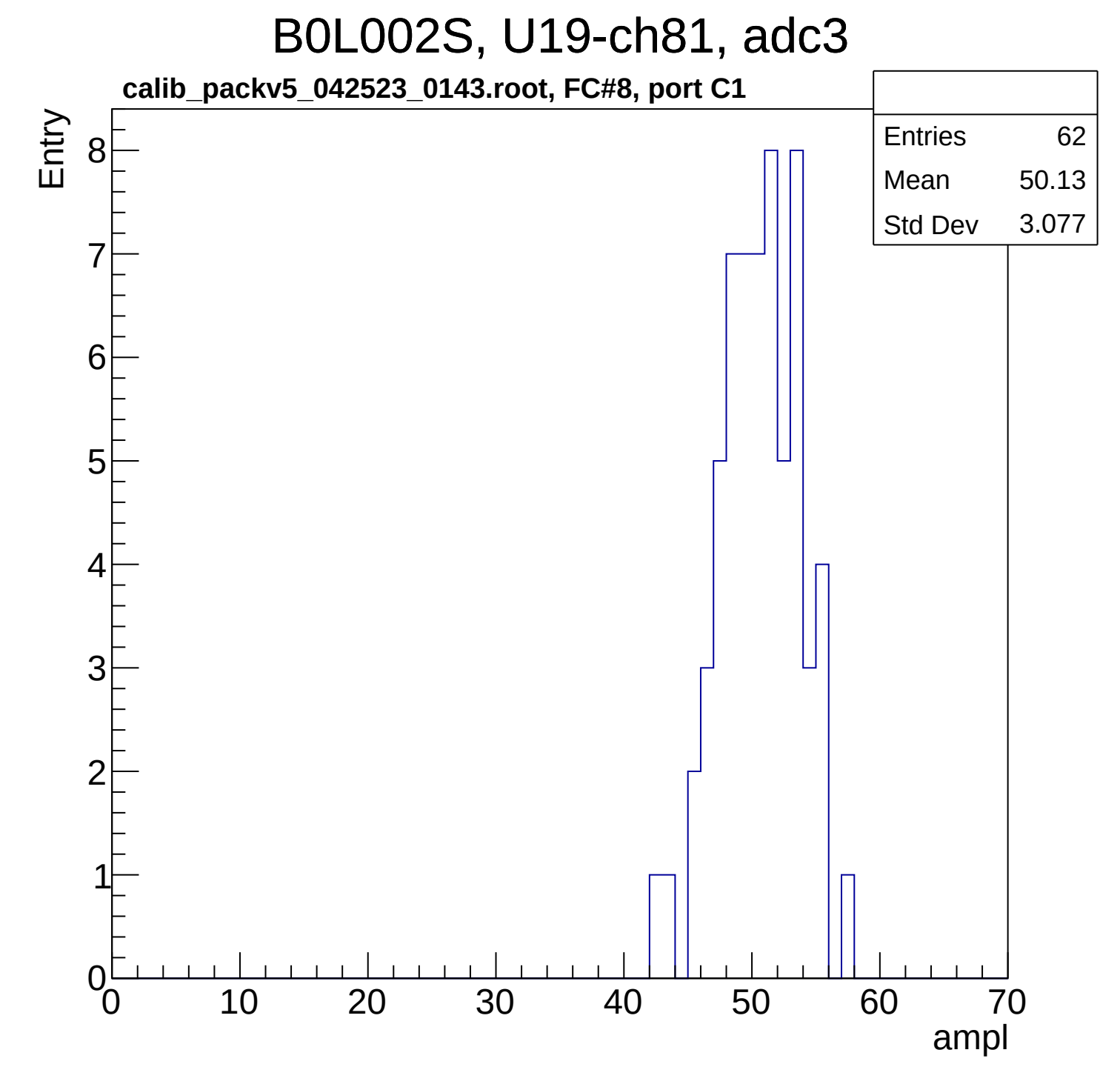

B0L002S, U19-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

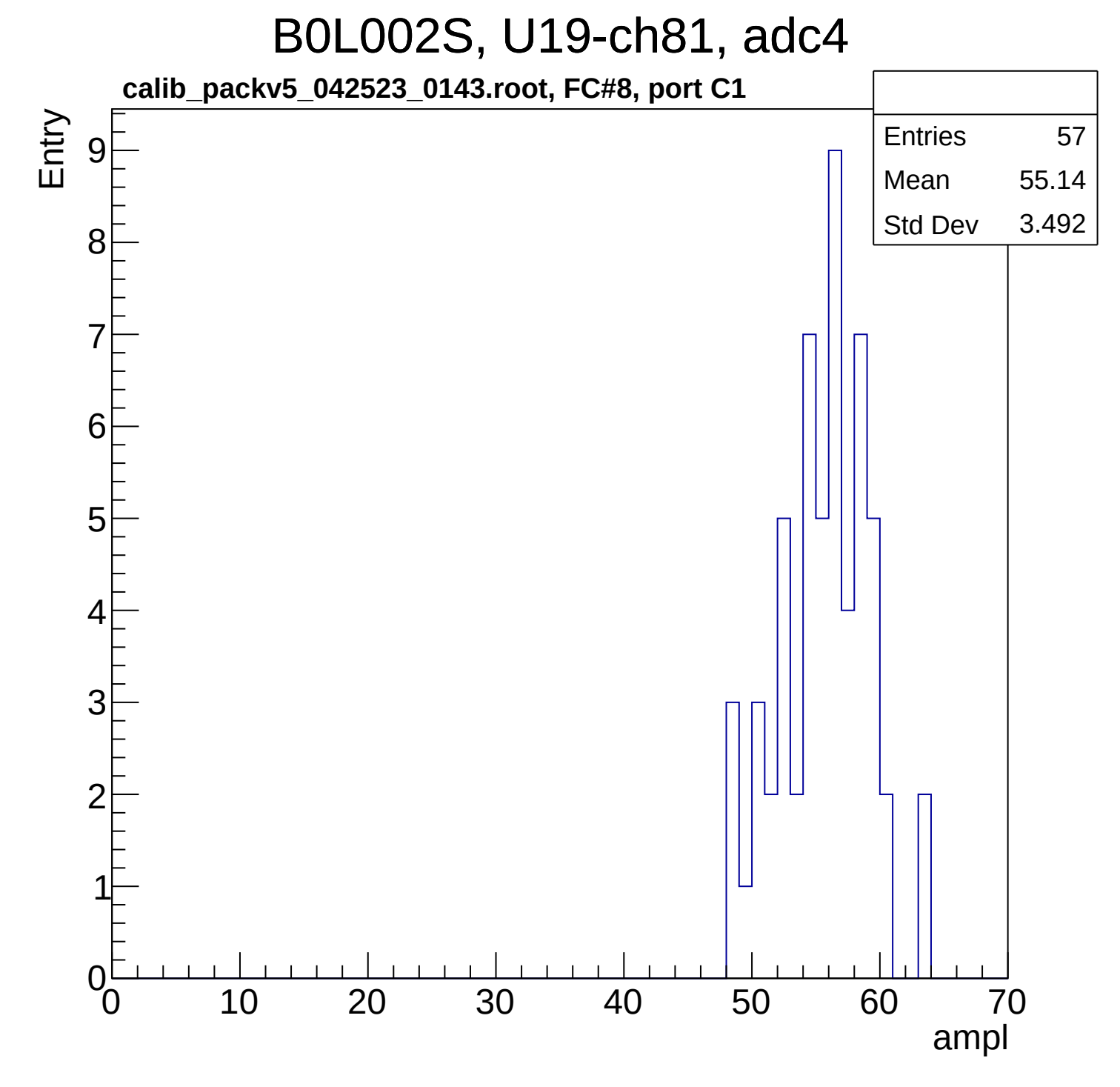

B0L002S, U19-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

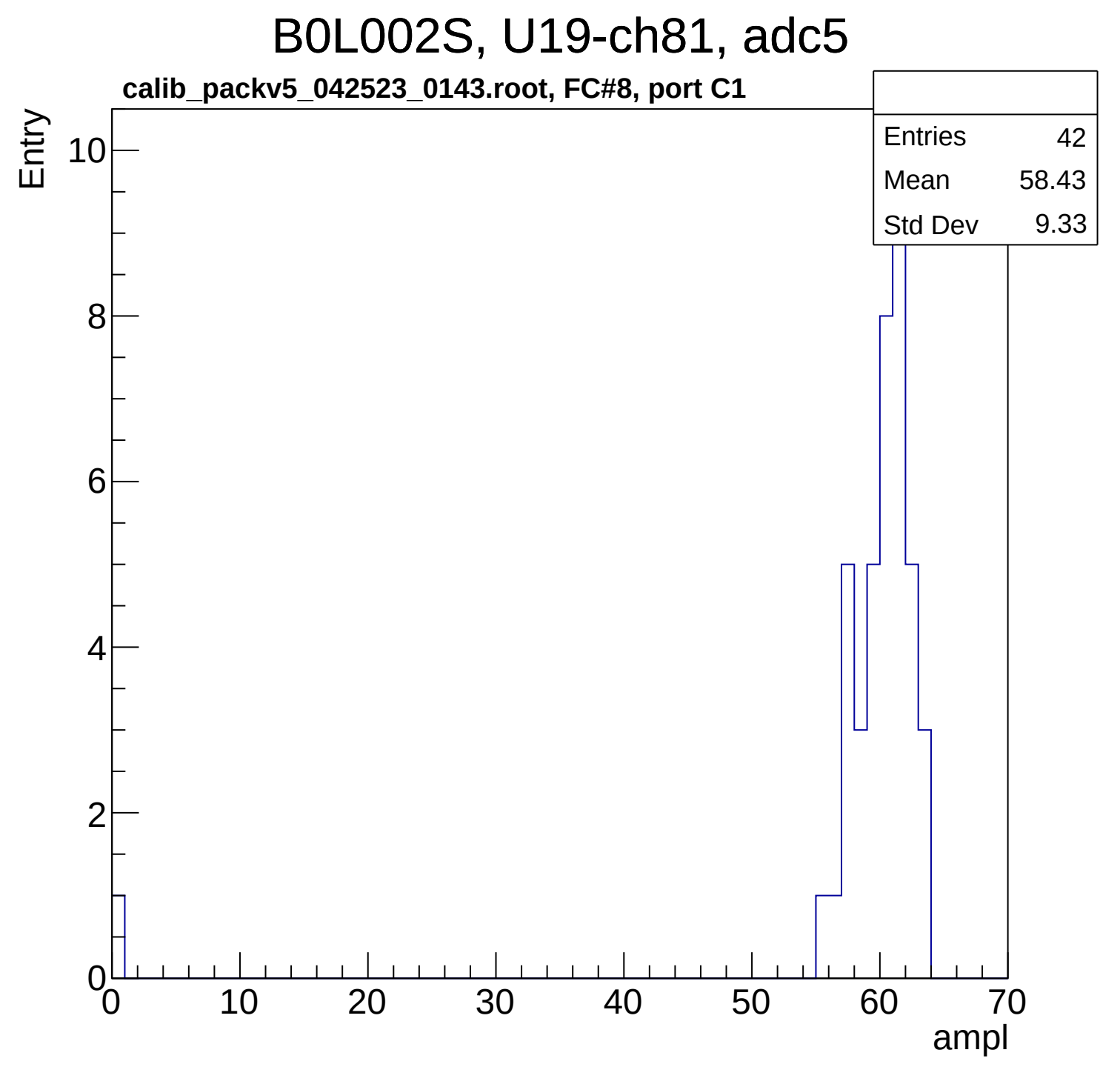

B0L002S, U19-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 28.99
Std Dev 4.74
Gaus mean : 29.6550
Gaus Width: 3.2486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

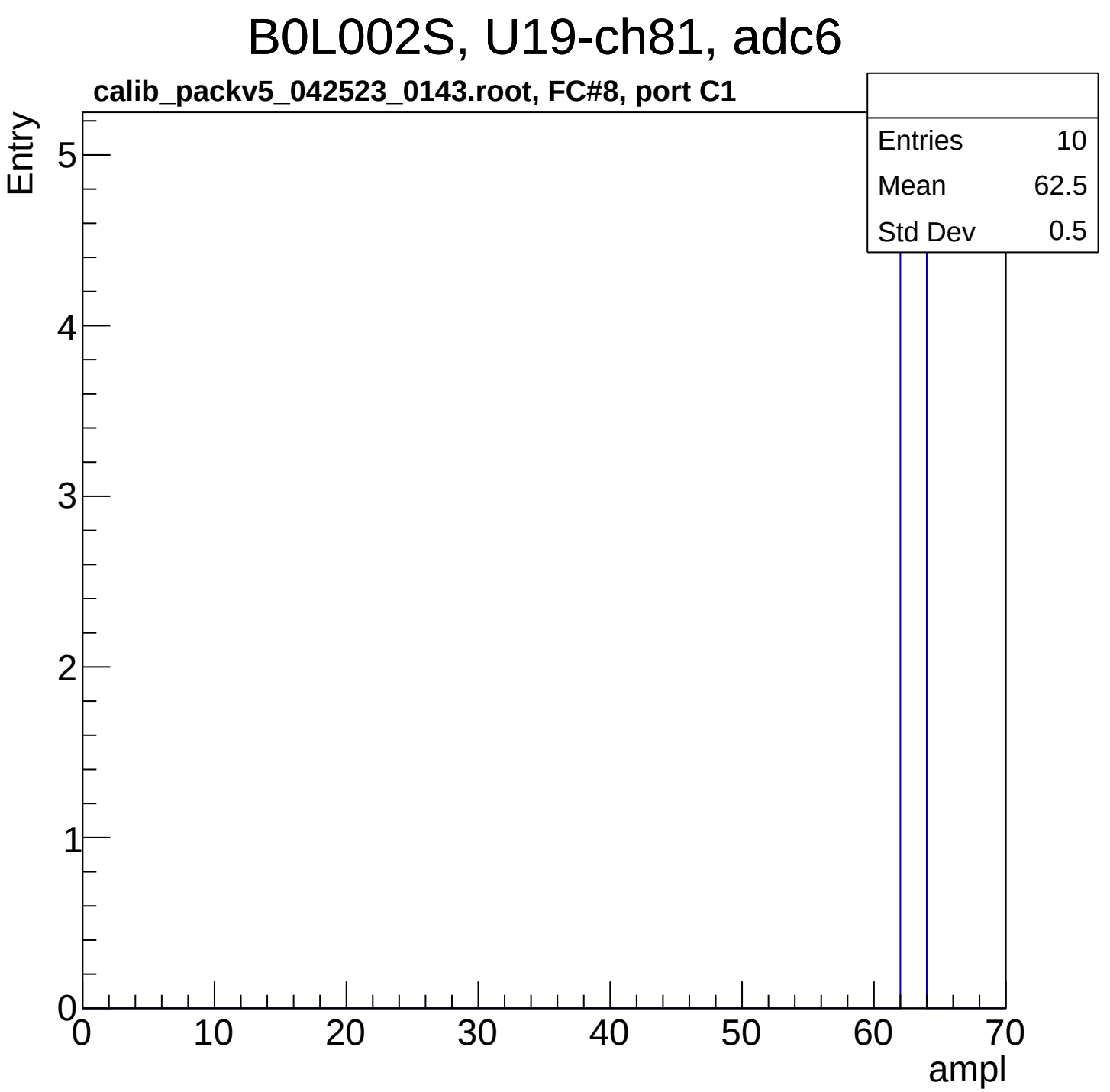

B0L002S, U19-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.59
Std Dev 2.831
Gaus mean : 37.1961
Gaus Width: 3.1581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

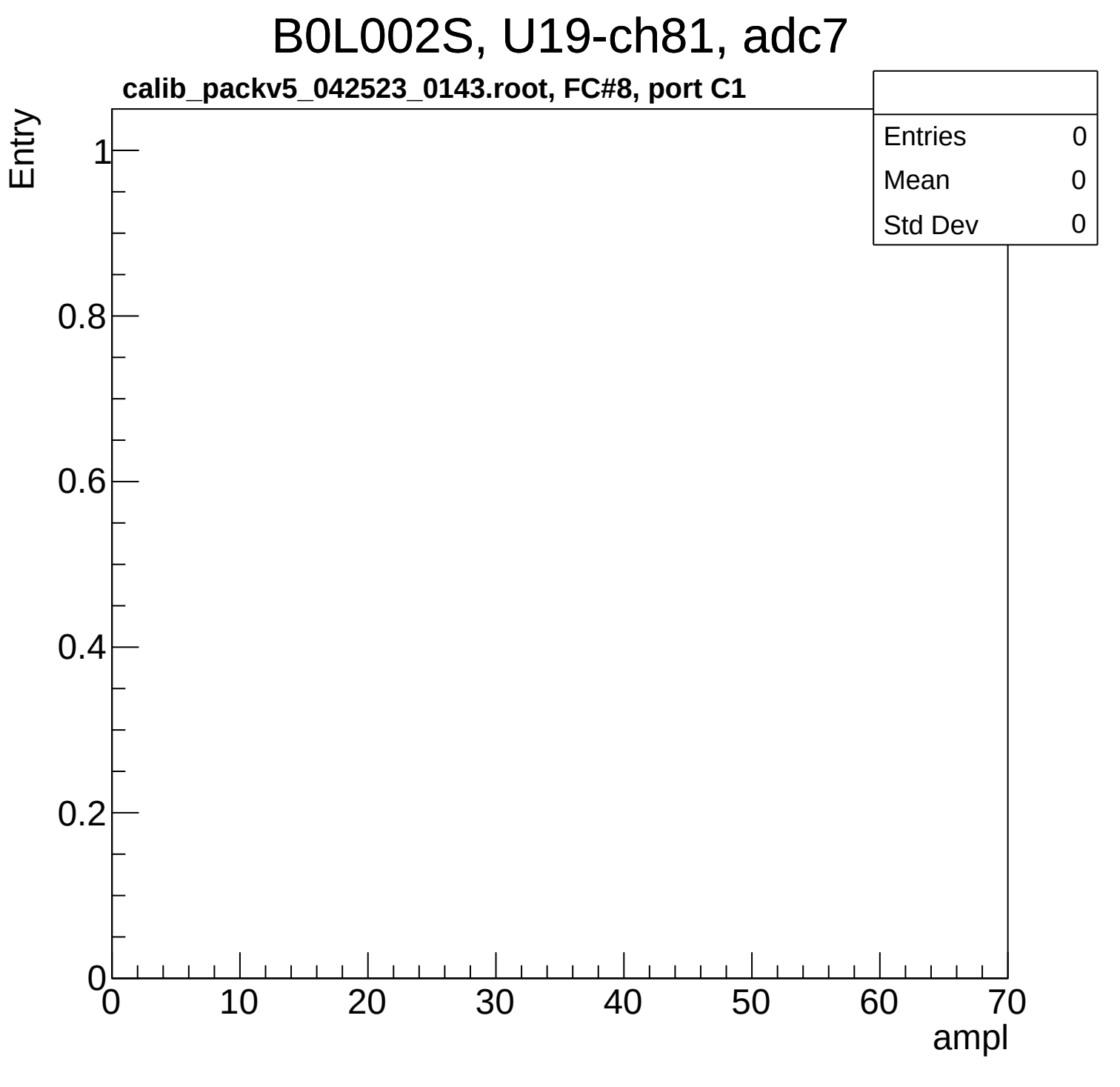

B0L002S, U19-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.93
Std Dev 2.916
Gaus mean : 43.9840
Gaus Width: 3.3022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

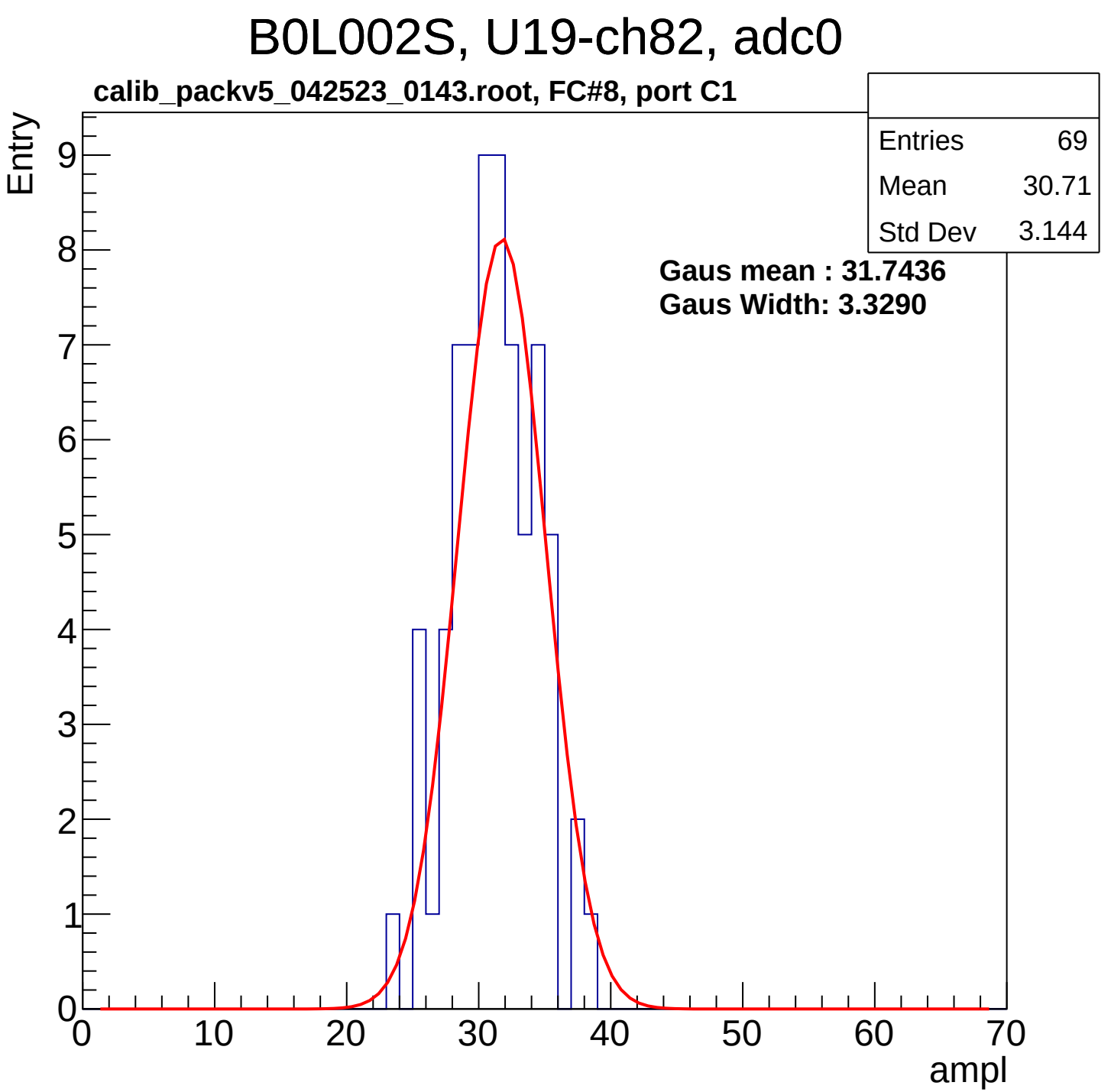

B0L002S, U19-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.13
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

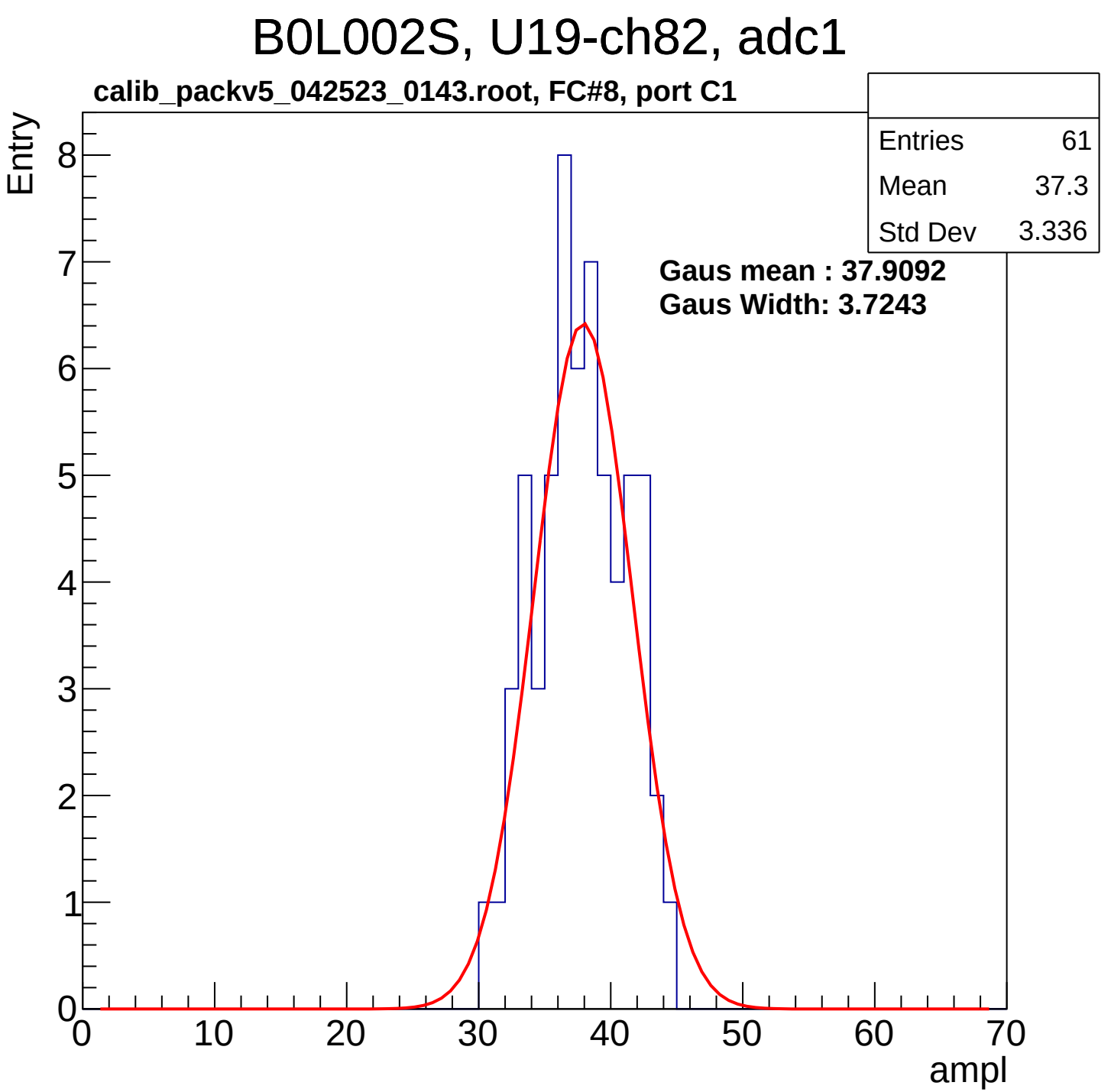

B0L002S, U19-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.14
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

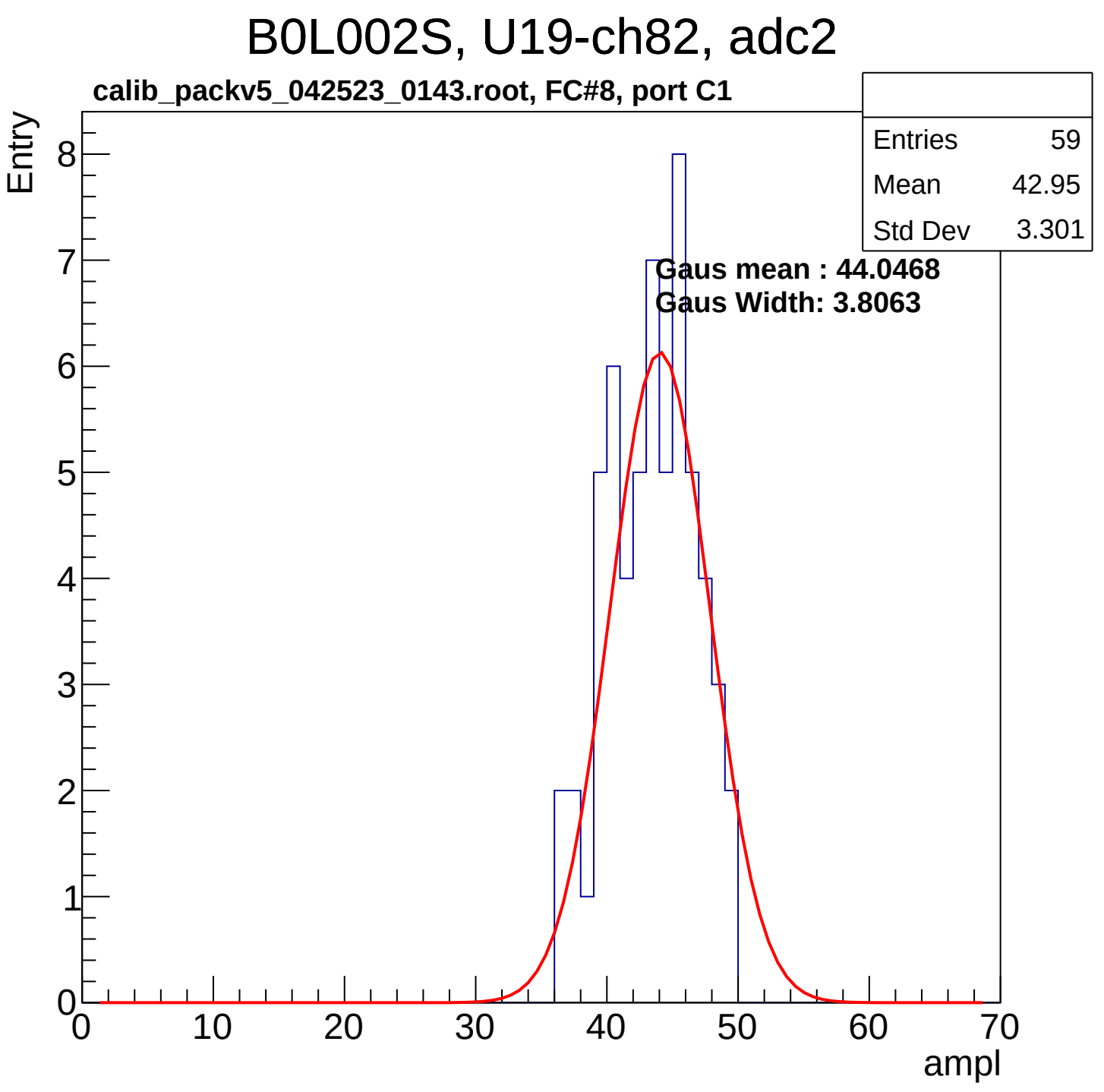

B0L002S, U19-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.43
Std Dev 9.33
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

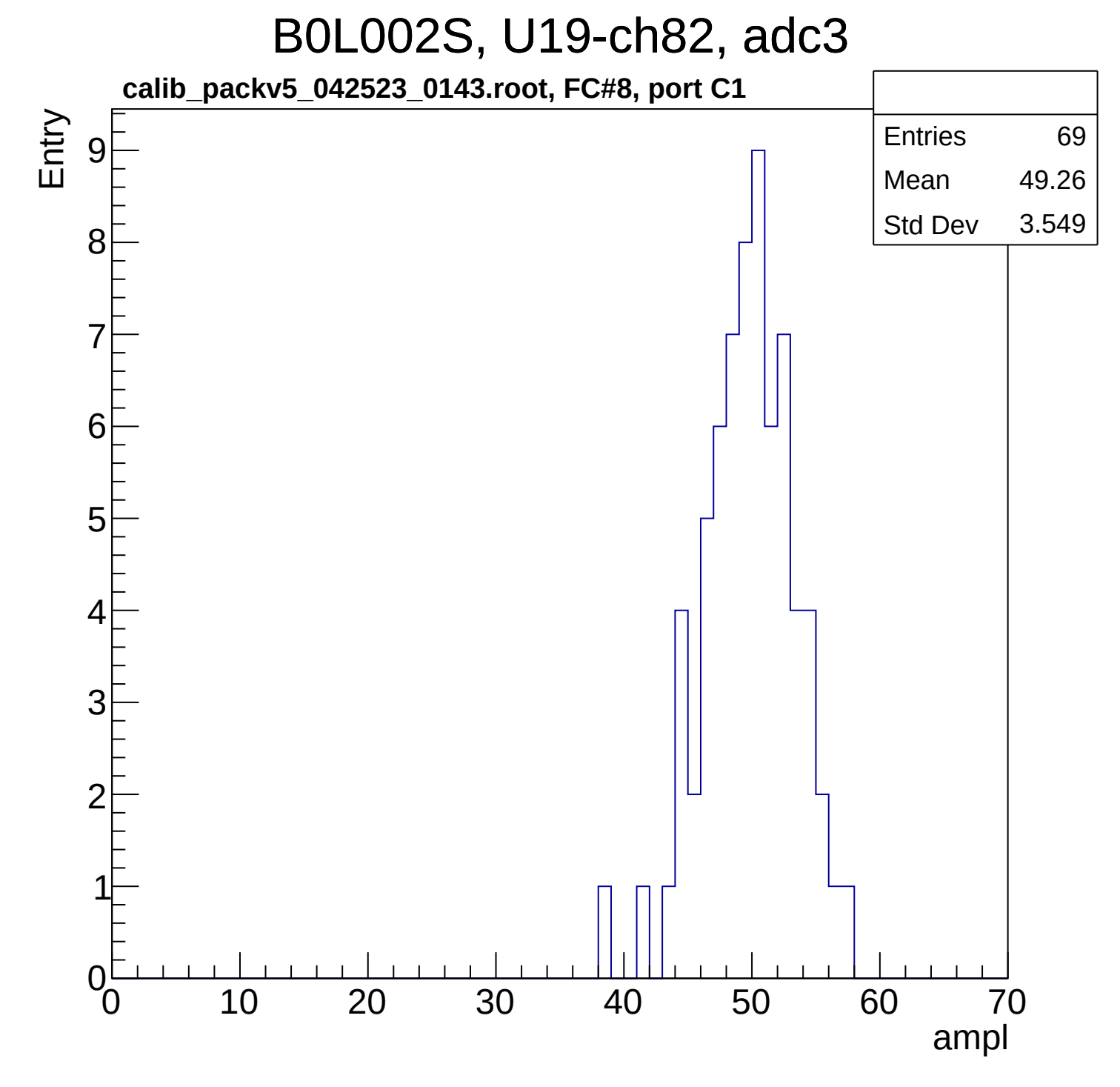

B0L002S, U19-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 62.5
Std Dev 0.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

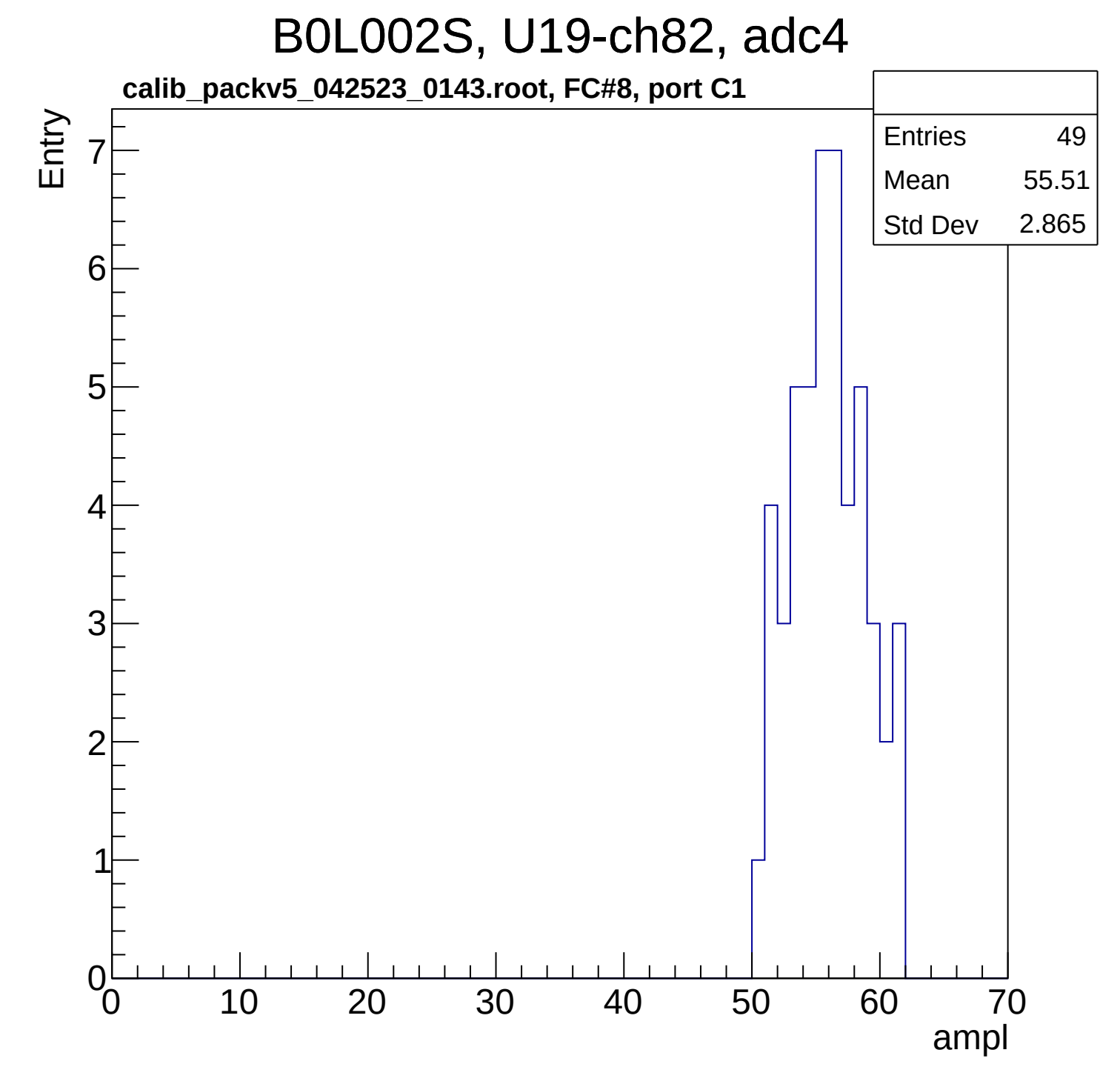

B0L002S, U19-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

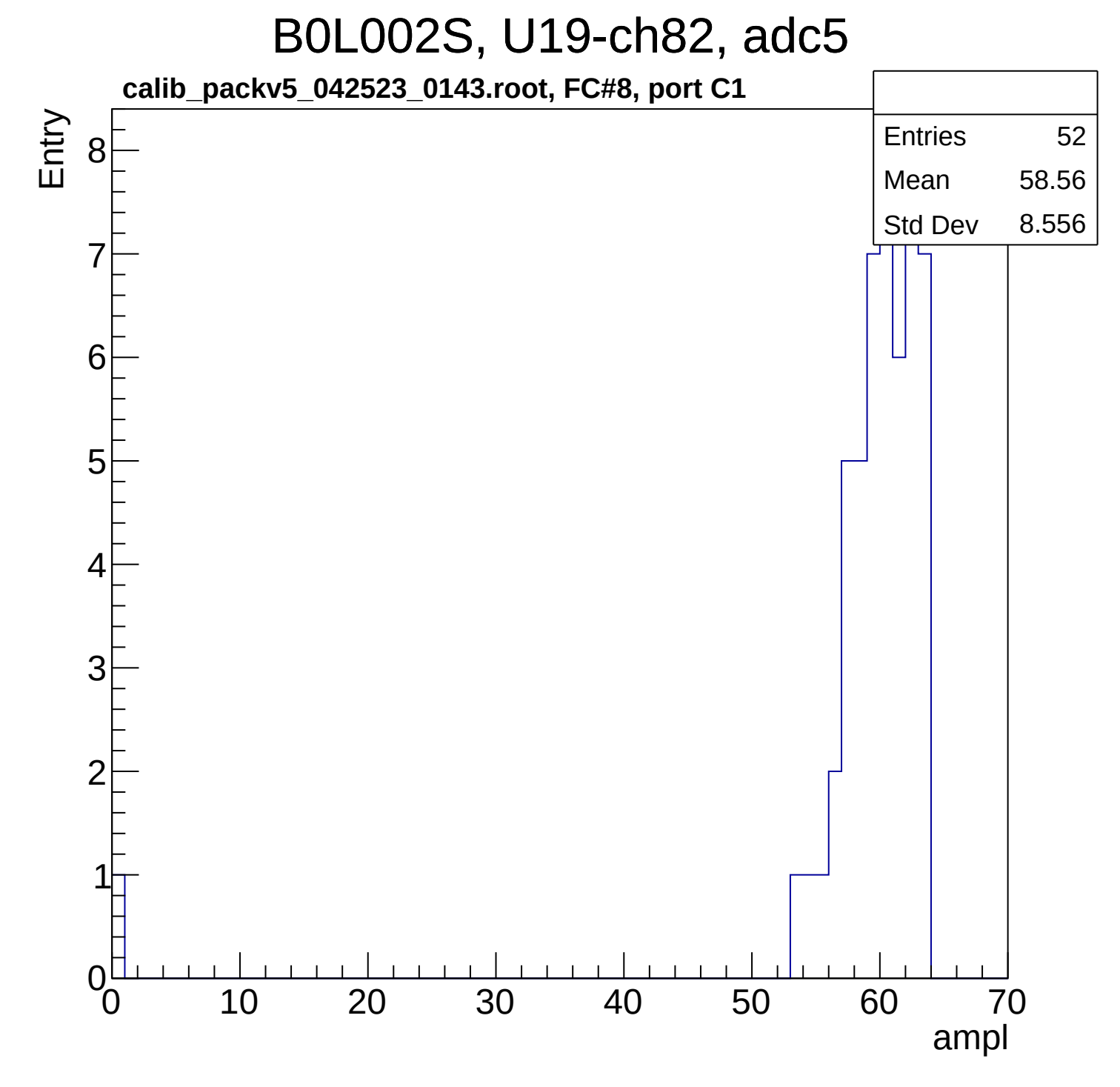

B0L002S, U19-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.71
Std Dev 3.144
Gaus mean : 31.7436
Gaus Width: 3.3290
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

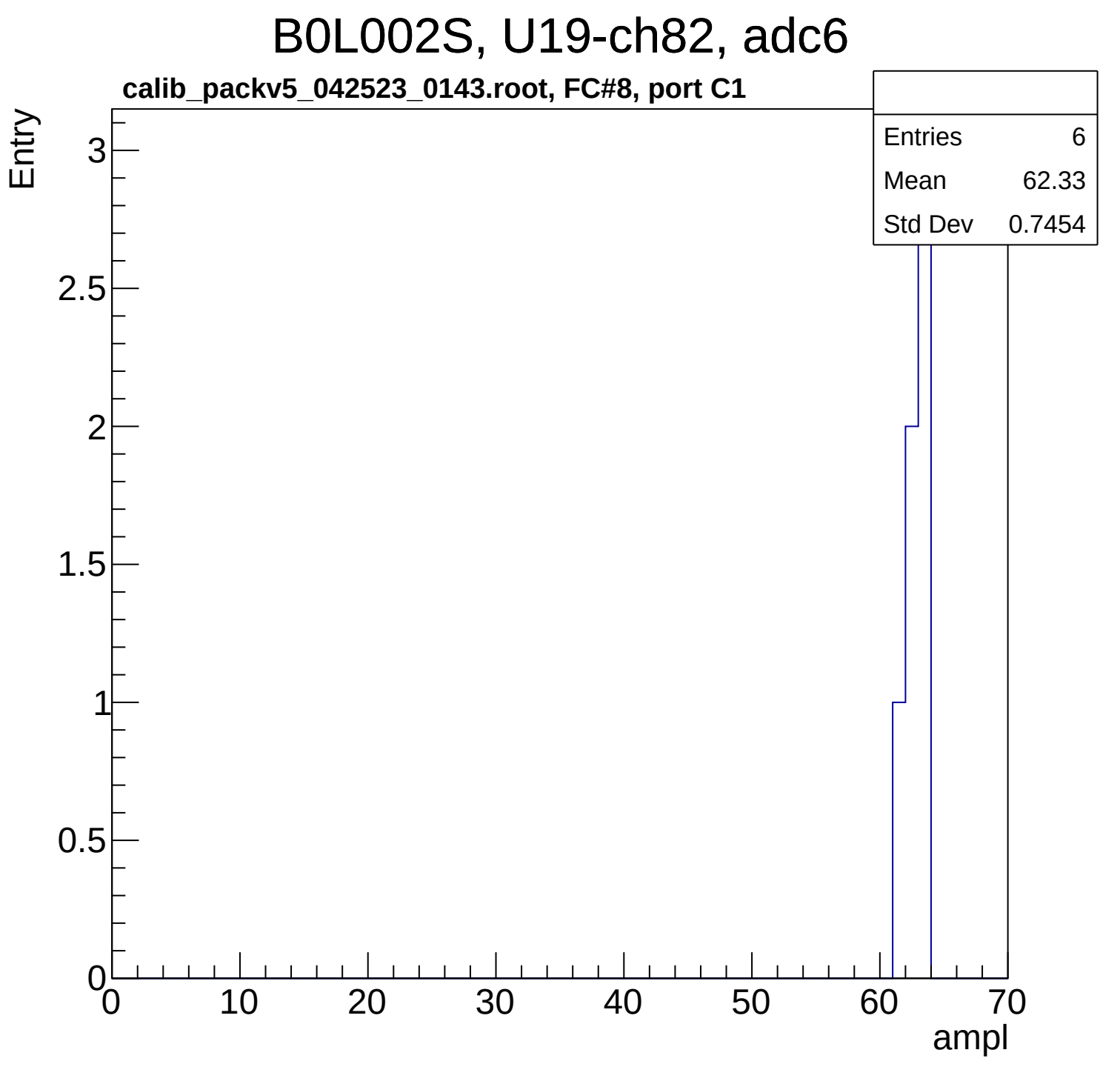

B0L002S, U19-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.3
Std Dev 3.336
Gaus mean : 37.9092
Gaus Width: 3.7243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

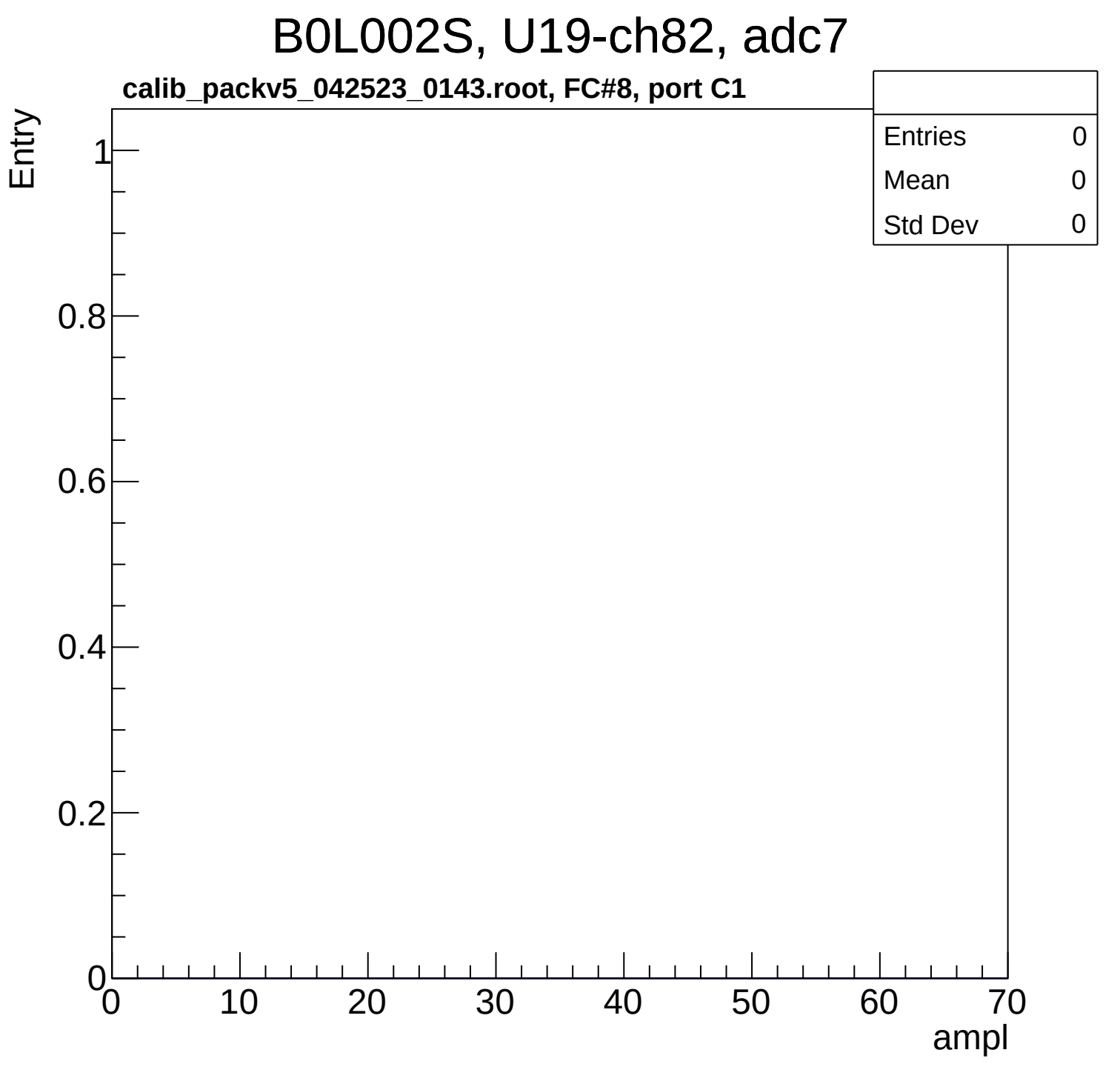

B0L002S, U19-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 42.95
Std Dev 3.301
Gaus mean : 44.0468
Gaus Width: 3.8063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

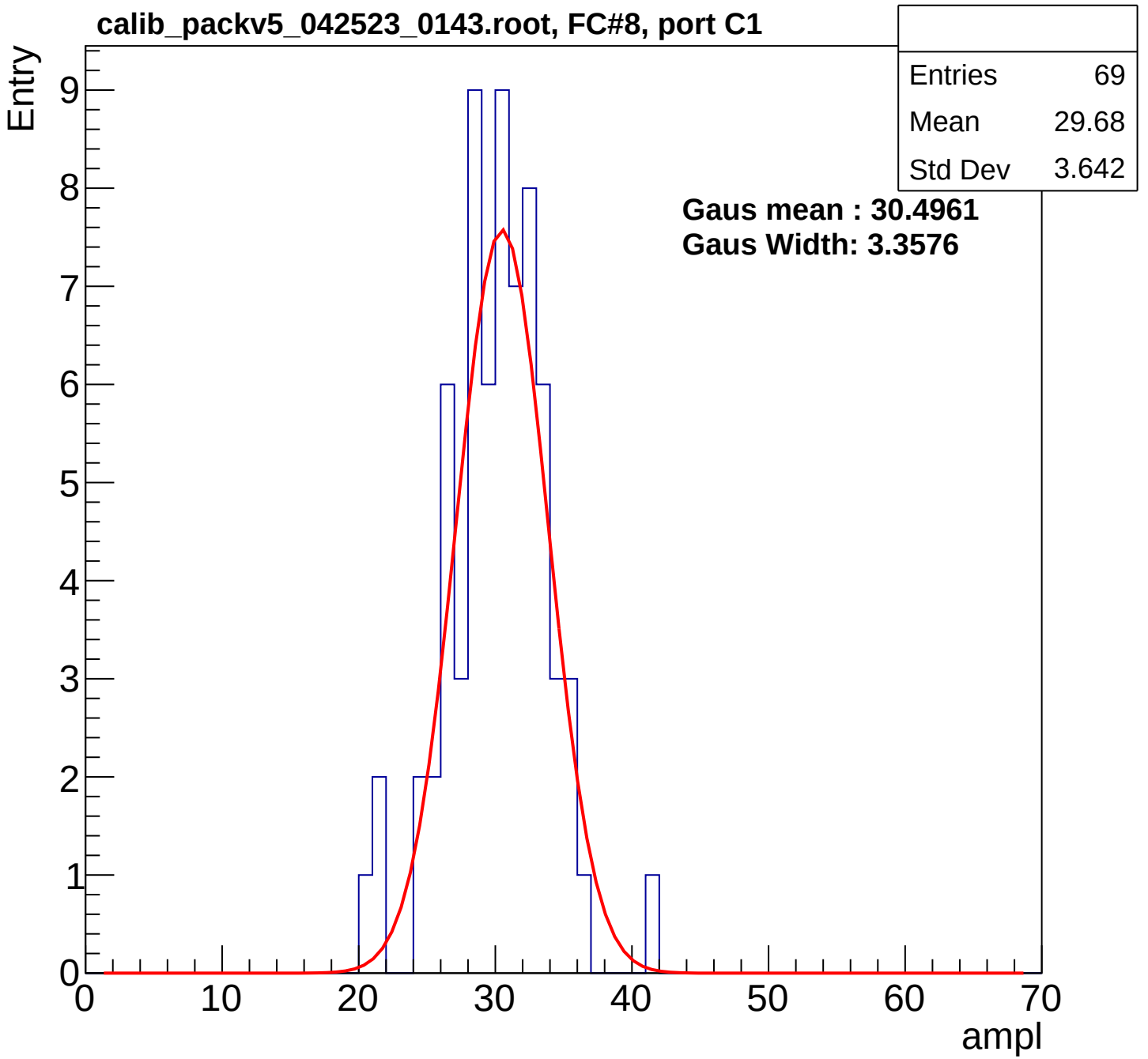

B0L002S, U19-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 49.26
Std Dev 3.549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

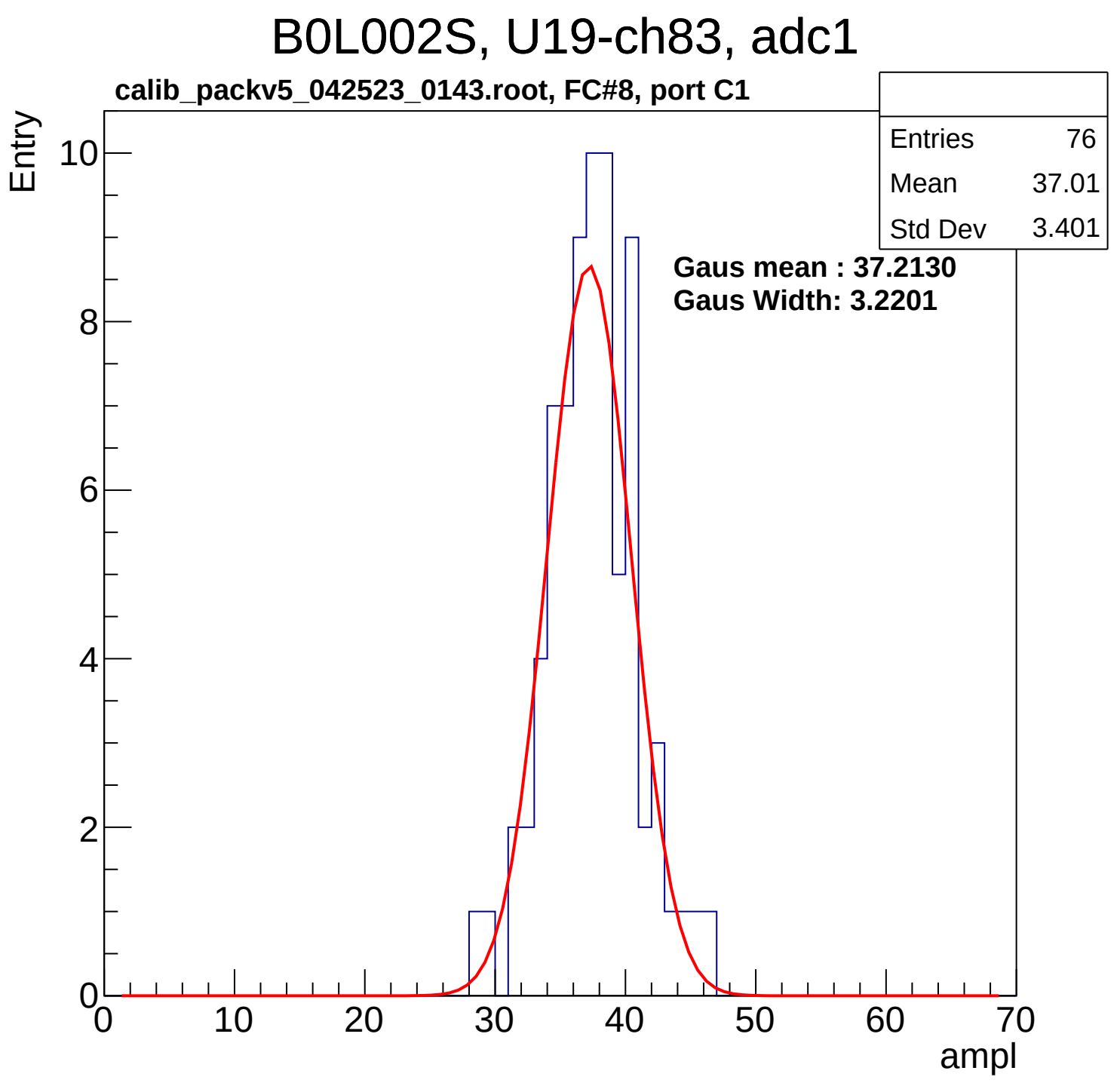

B0L002S, U19-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 55.51
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

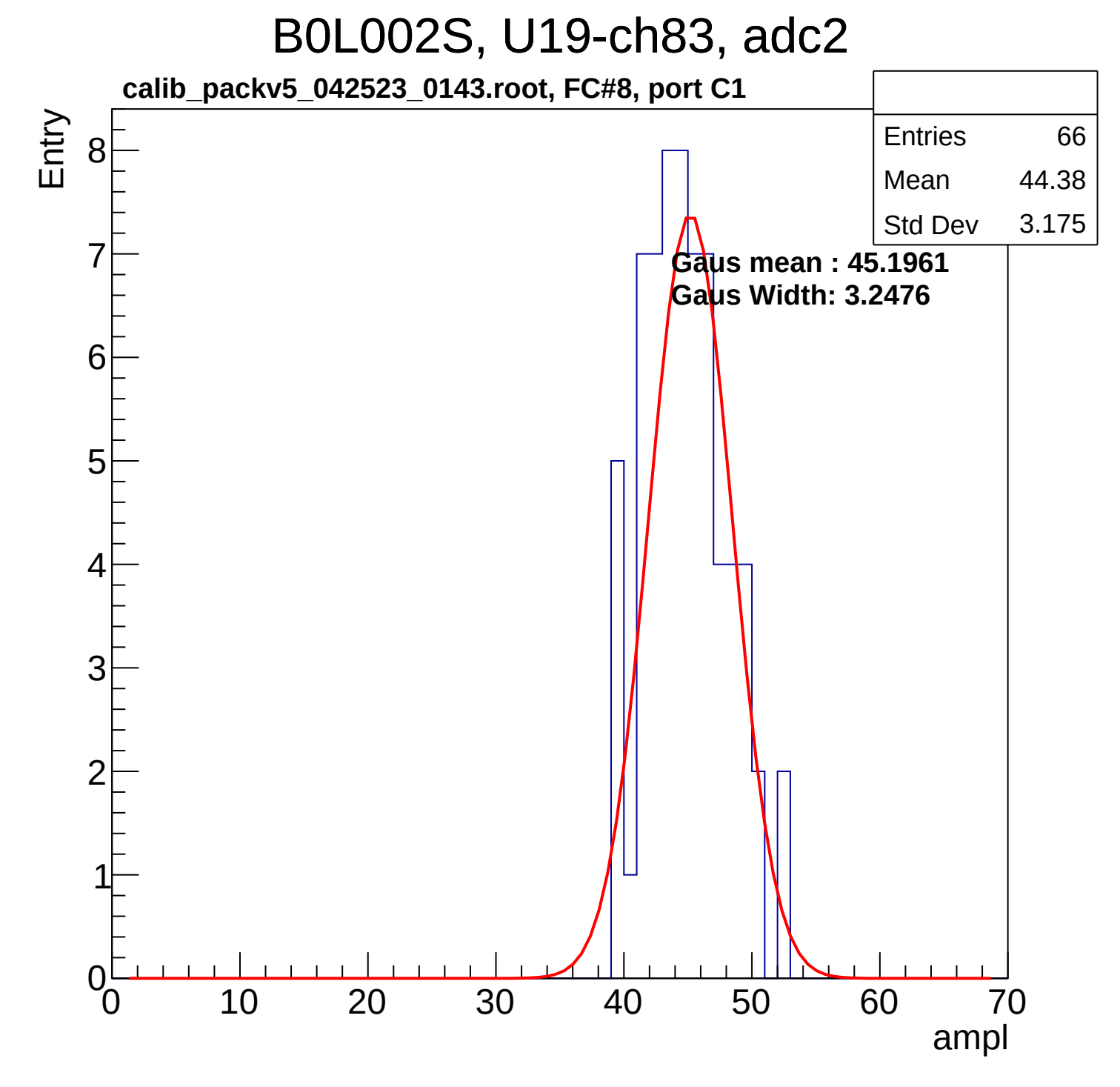

B0L002S, U19-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.56
Std Dev 8.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

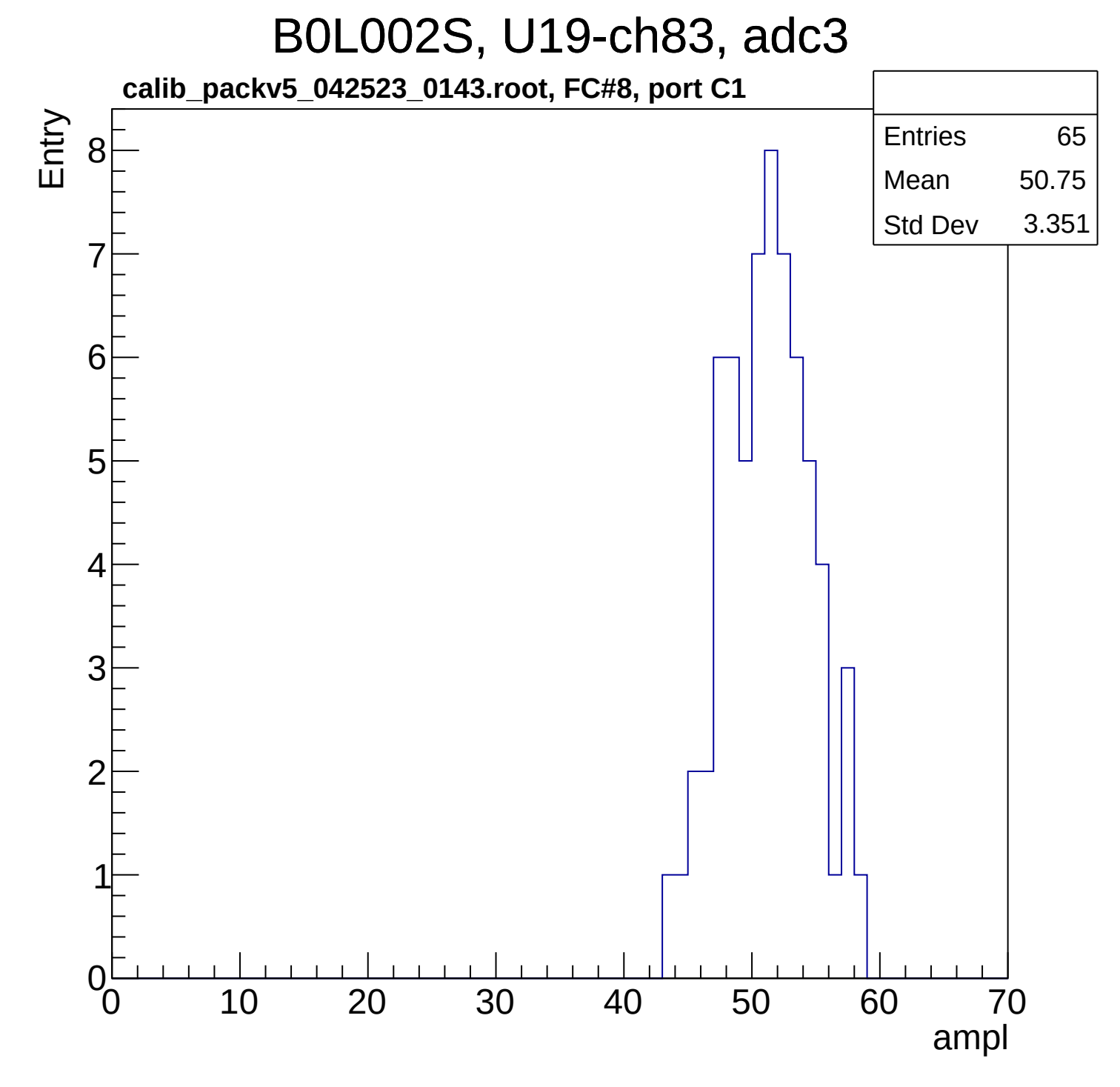

B0L002S, U19-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

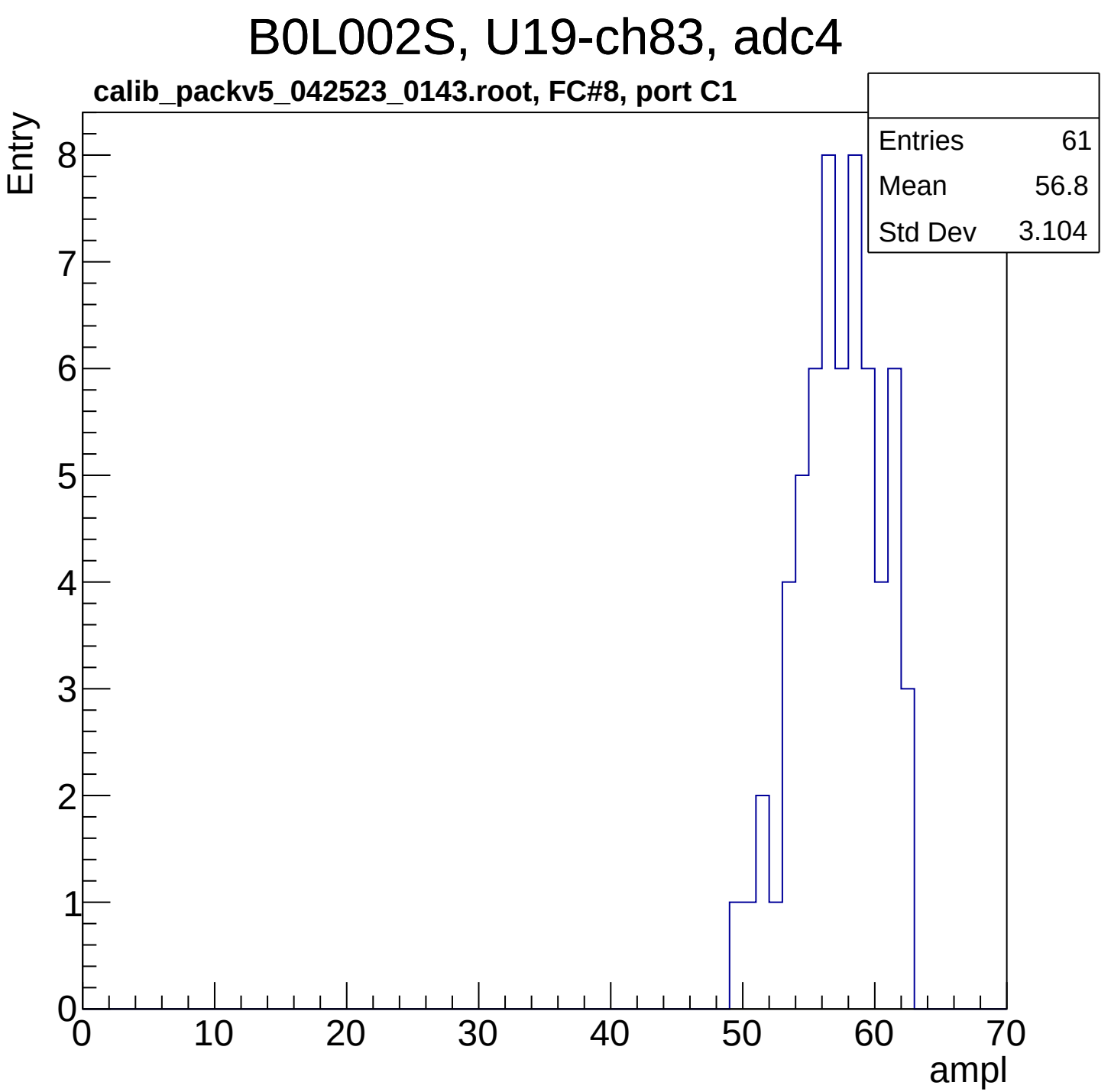

B0L002S, U19-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

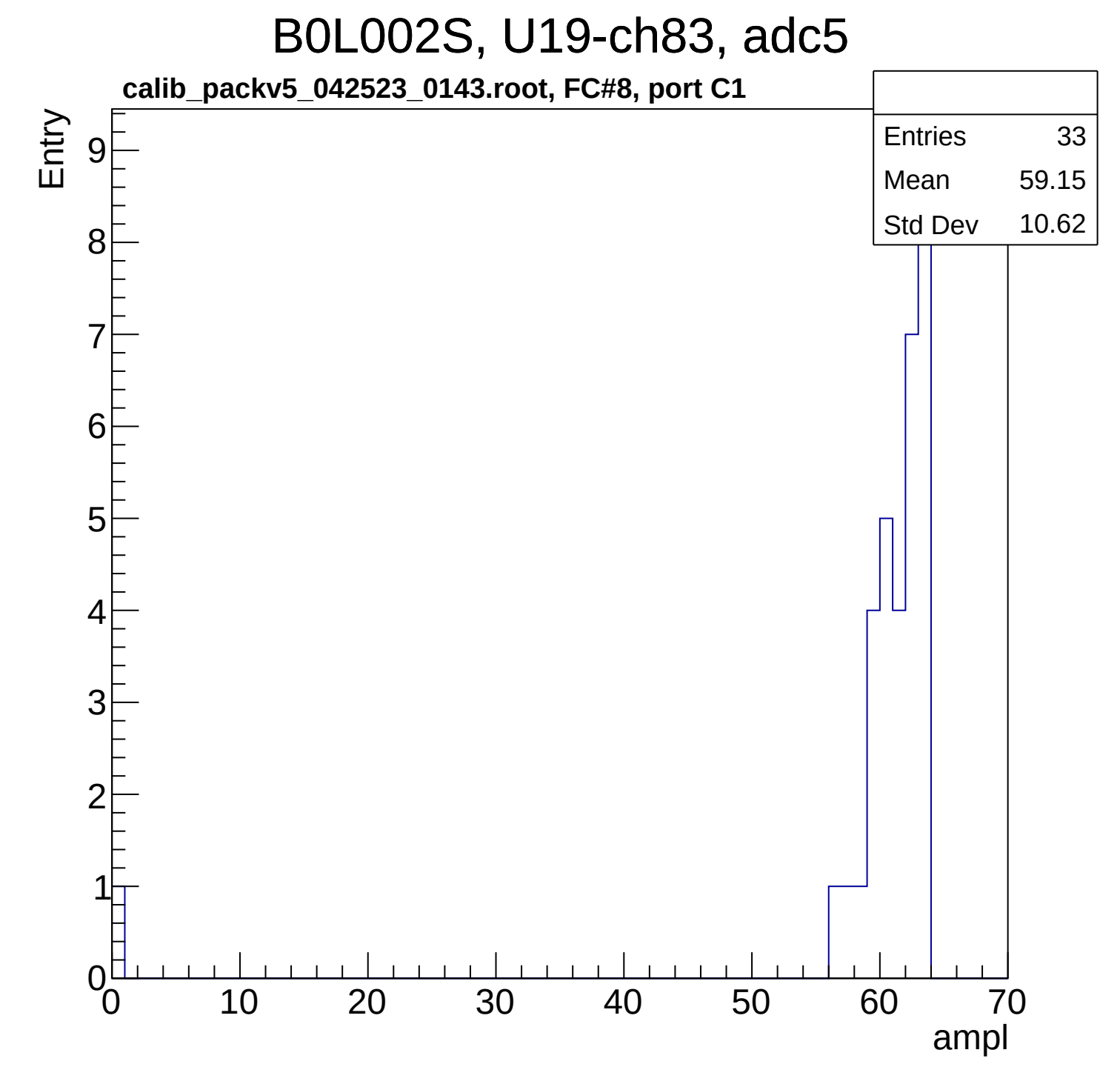

B0L002S, U19-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.68
Std Dev 3.642
Gaus mean : 30.4961
Gaus Width: 3.3576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

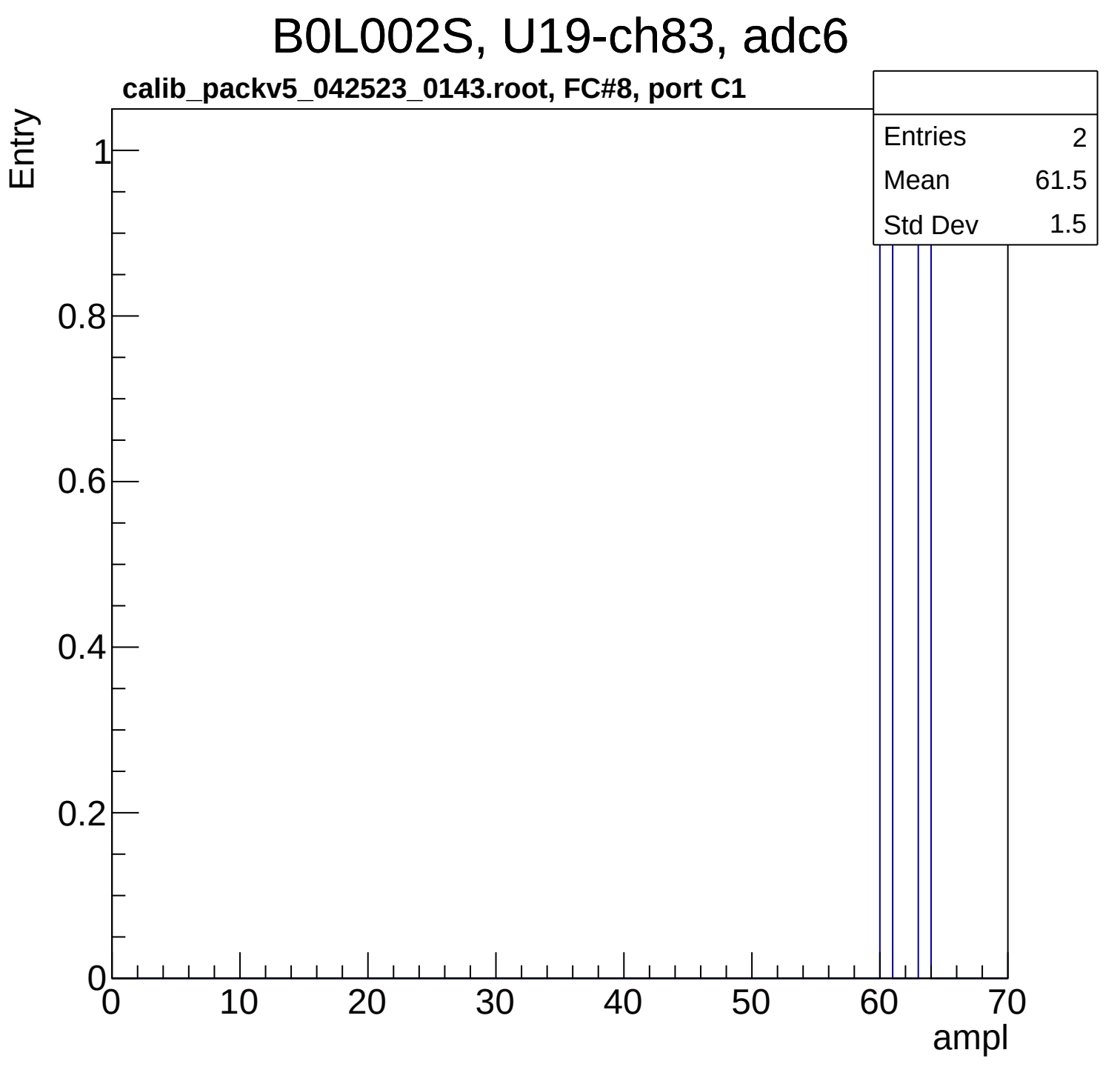

B0L002S, U19-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.01
Std Dev 3.401
Gaus mean : 37.2130
Gaus Width: 3.2201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

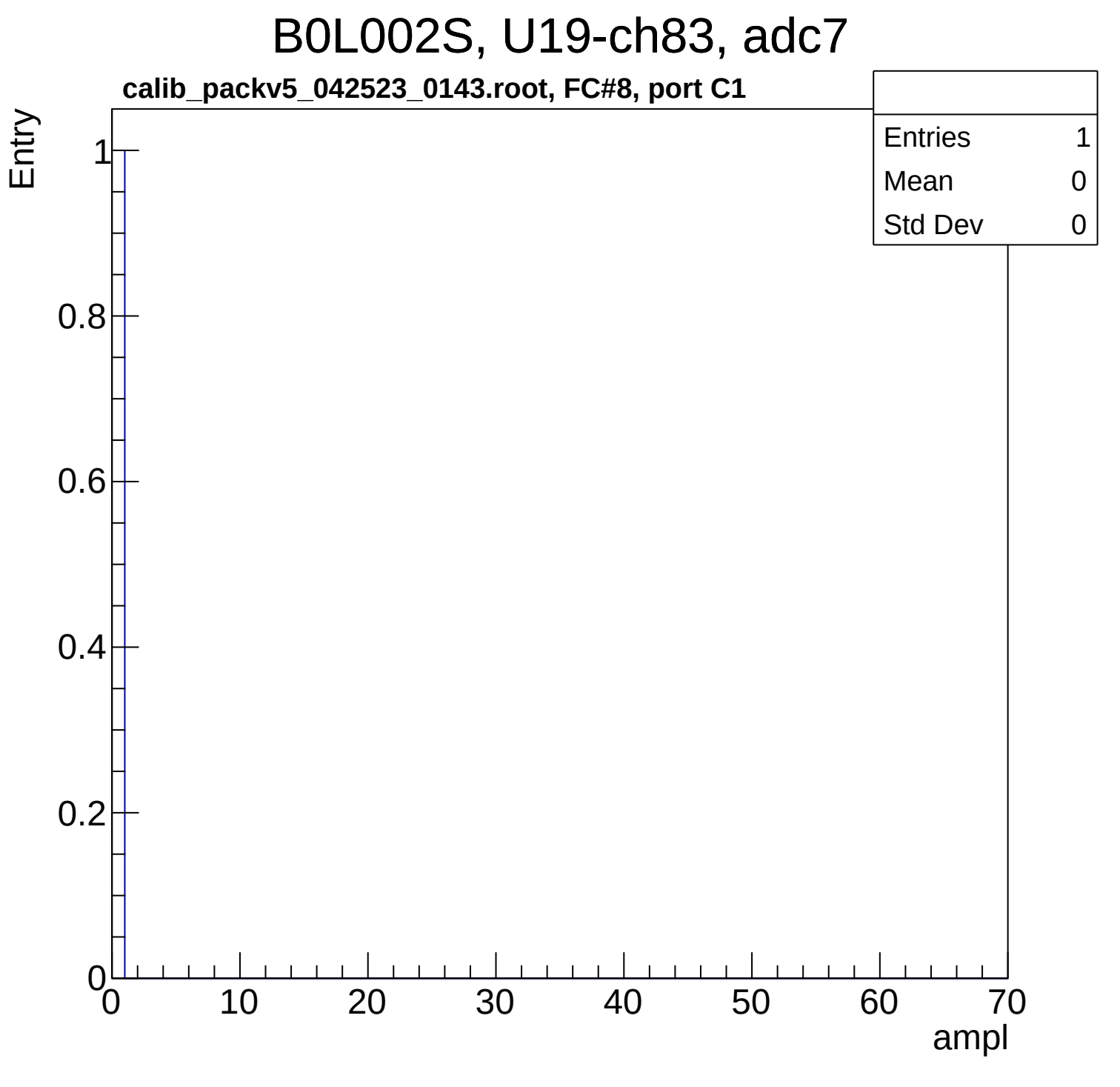

B0L002S, U19-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.38
Std Dev 3.175
Gaus mean : 45.1961
Gaus Width: 3.2476
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

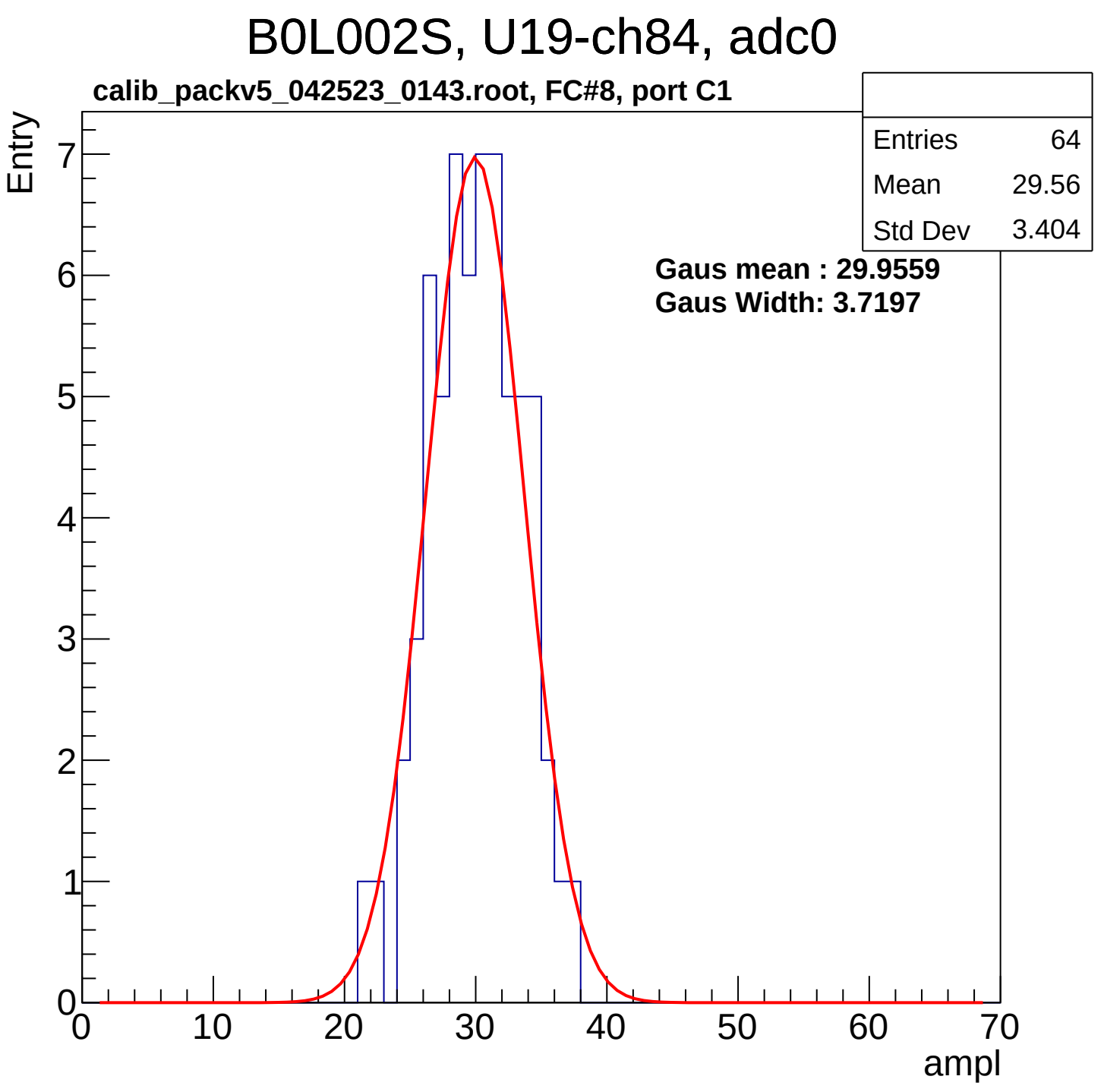

B0L002S, U19-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.75
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

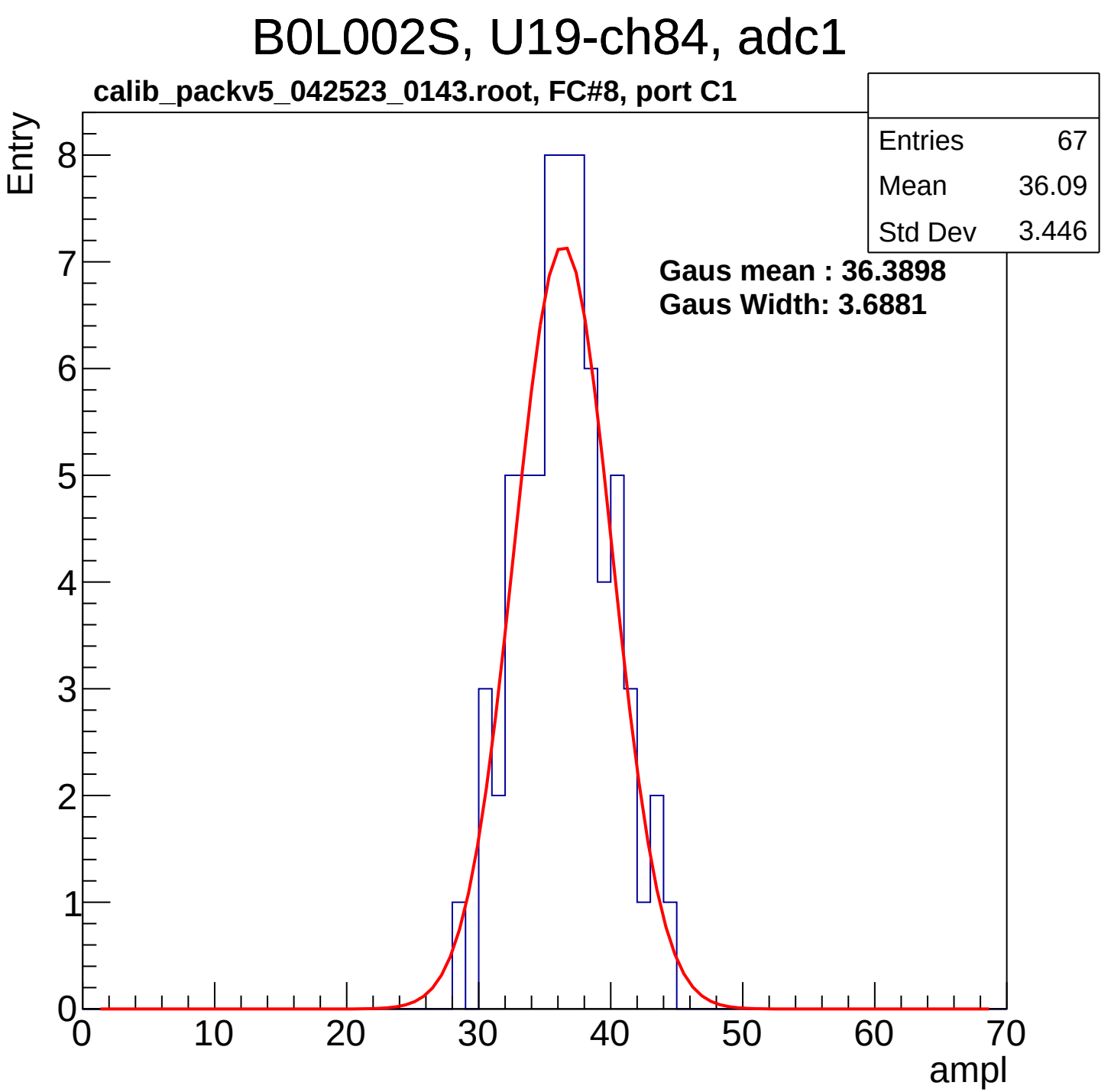

B0L002S, U19-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.8
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

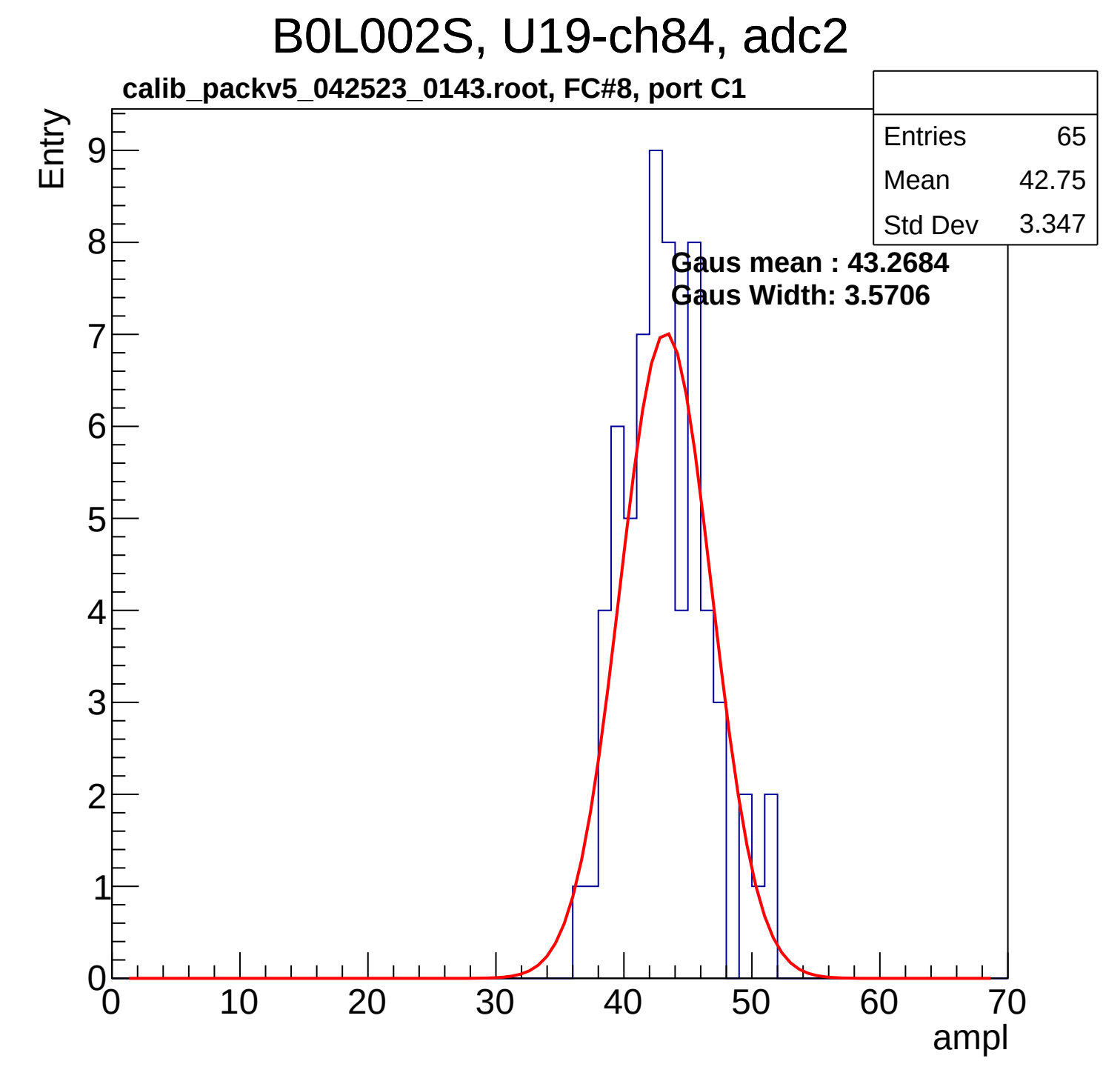

B0L002S, U19-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.15
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

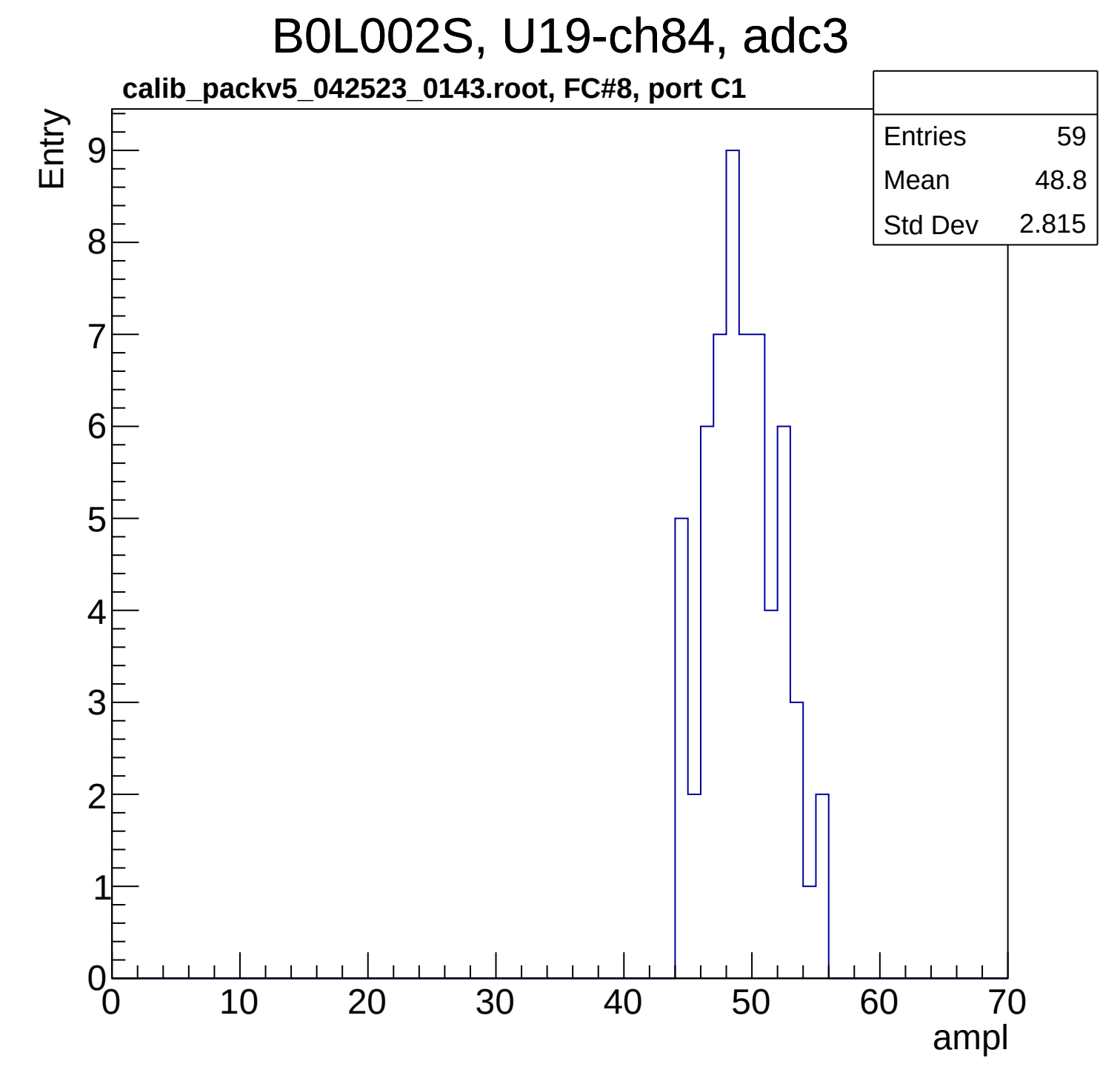

B0L002S, U19-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

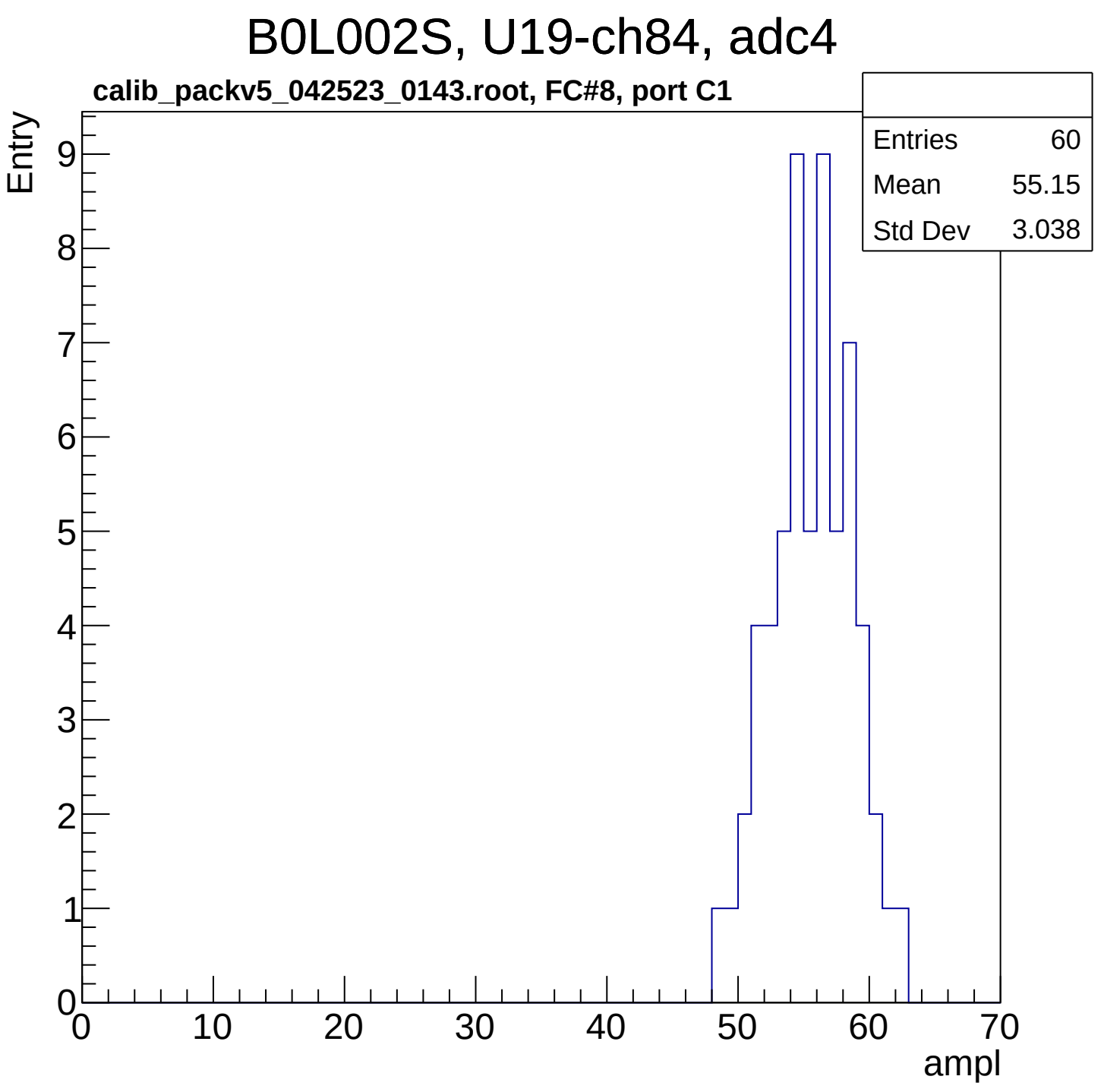

B0L002S, U19-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

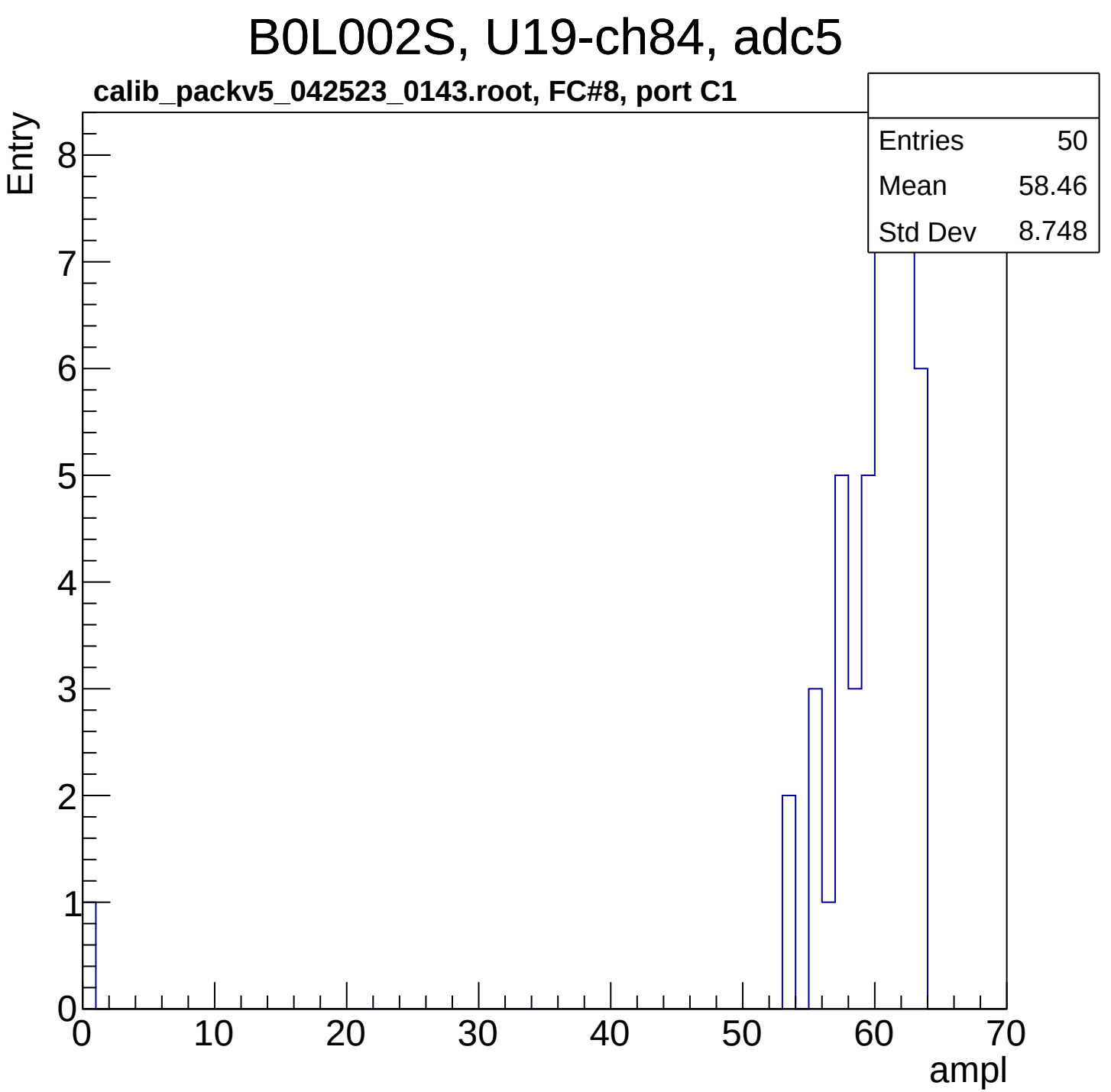

B0L002S, U19-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.56
Std Dev 3.404
Gaus mean : 29.9559
Gaus Width: 3.7197
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

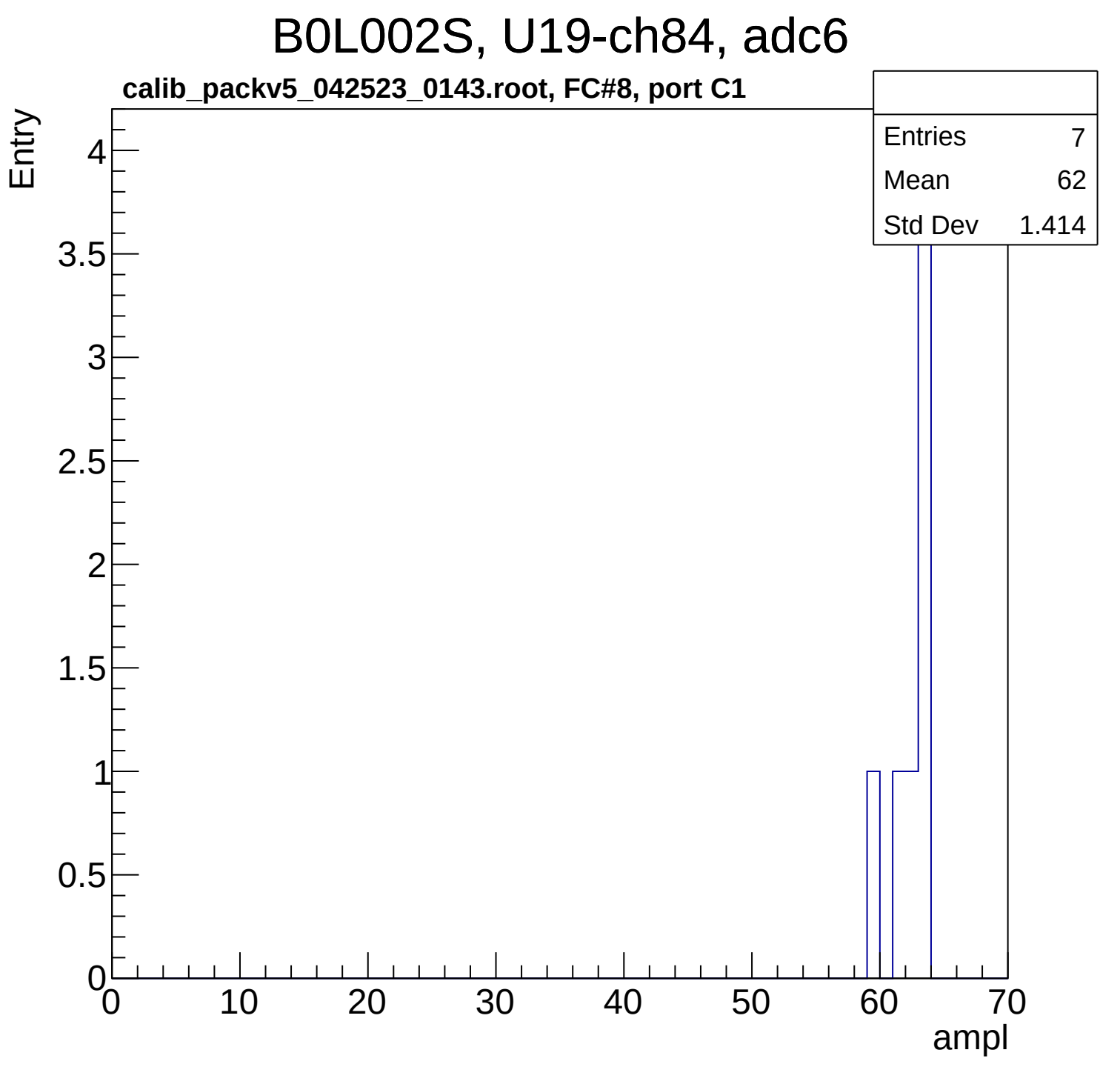

B0L002S, U19-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.09
Std Dev 3.446
Gaus mean : 36.3898
Gaus Width: 3.6881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

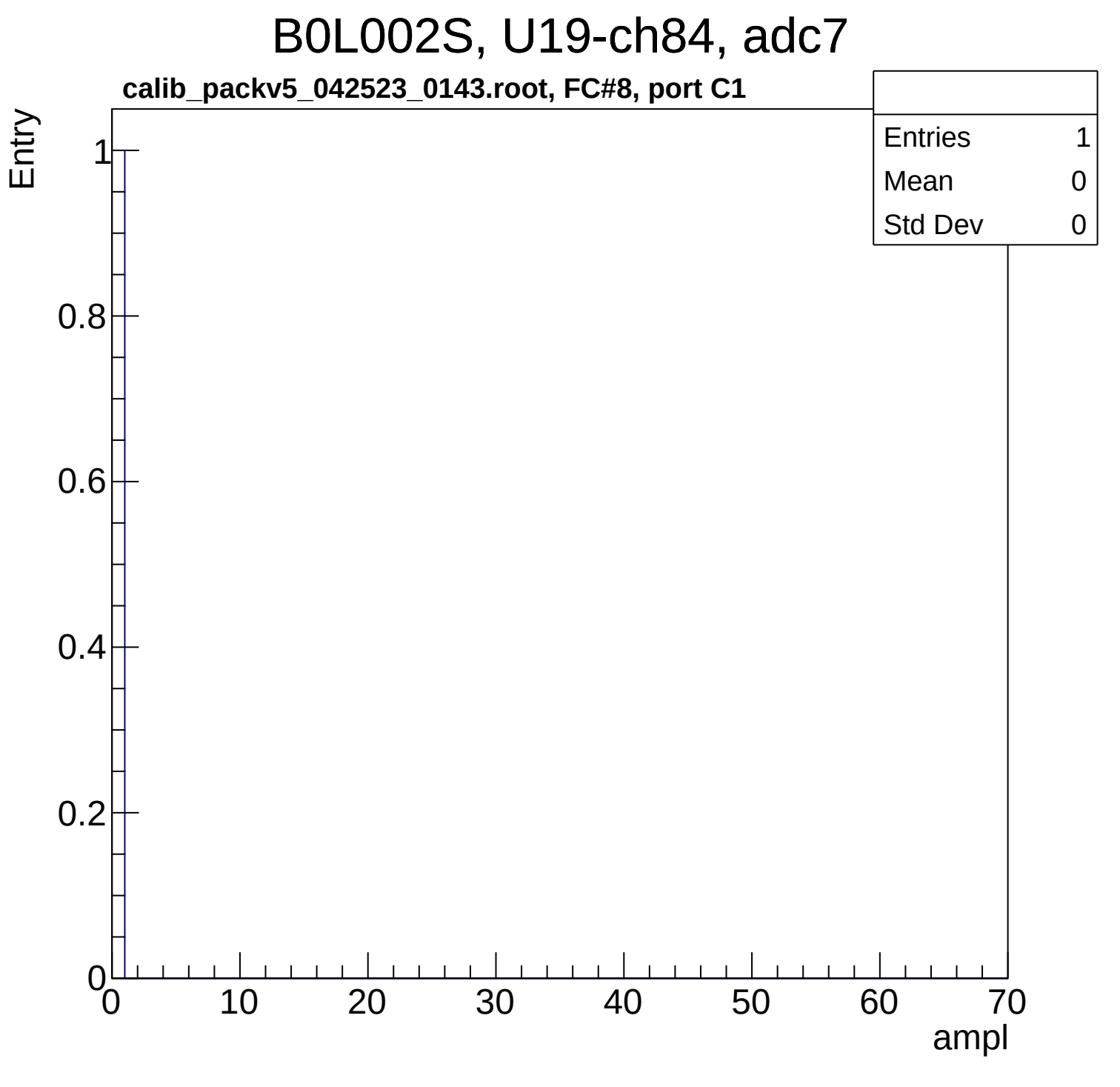

B0L002S, U19-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 42.75
Std Dev 3.347
Gaus mean : 43.2684
Gaus Width: 3.5706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

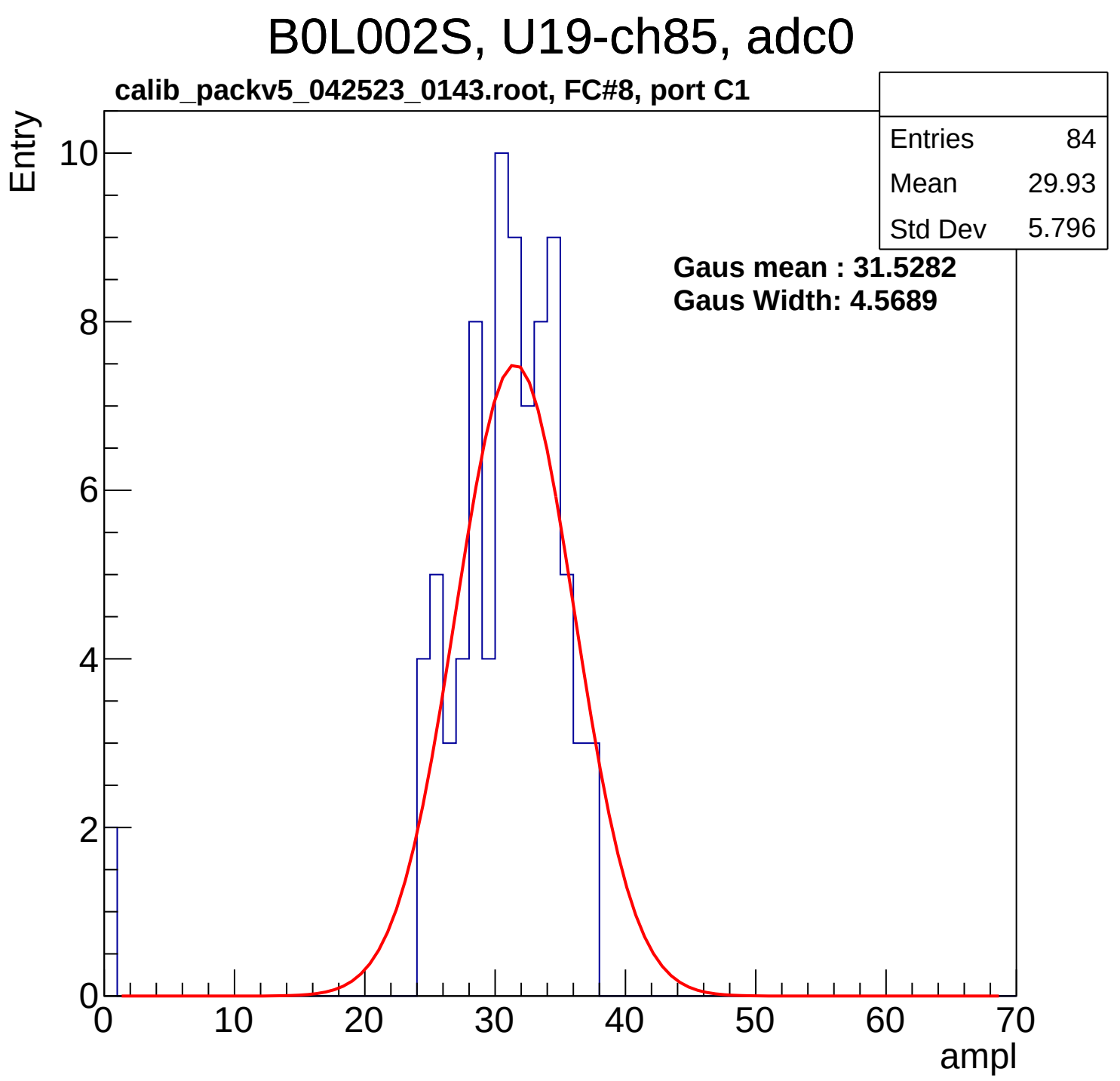

B0L002S, U19-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 48.8
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

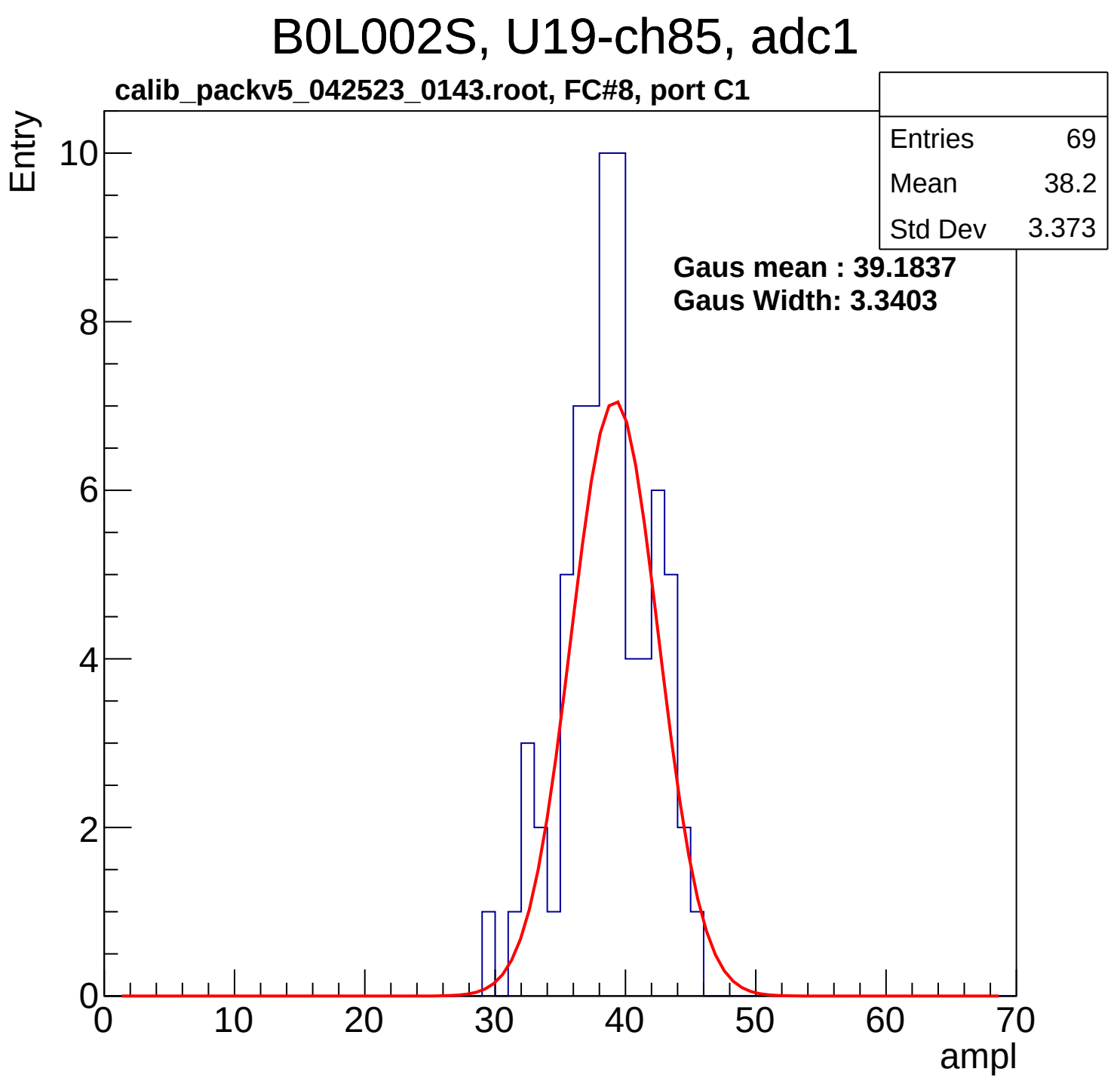

B0L002S, U19-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.15
Std Dev 3.038
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

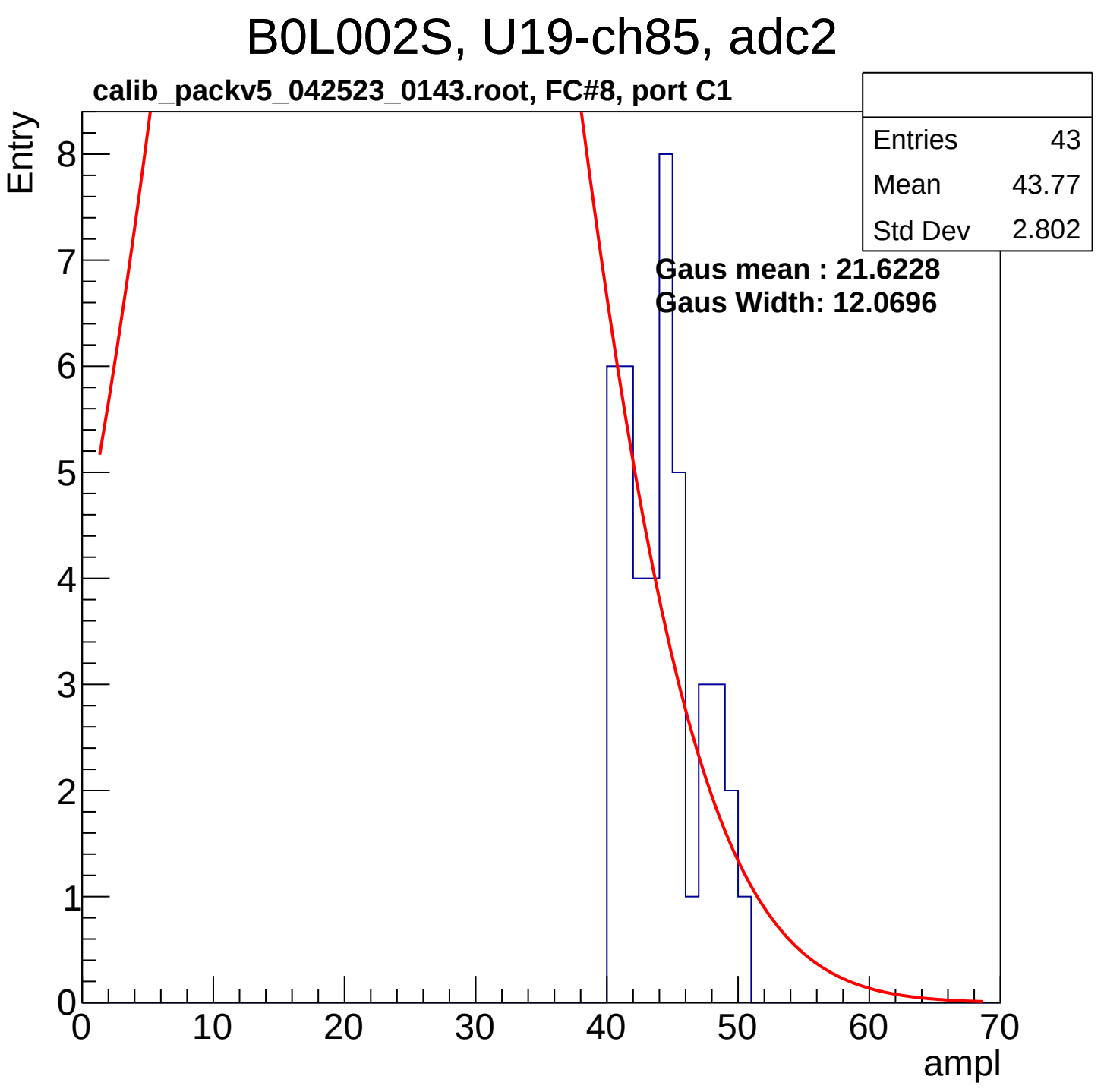

B0L002S, U19-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.46
Std Dev 8.748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

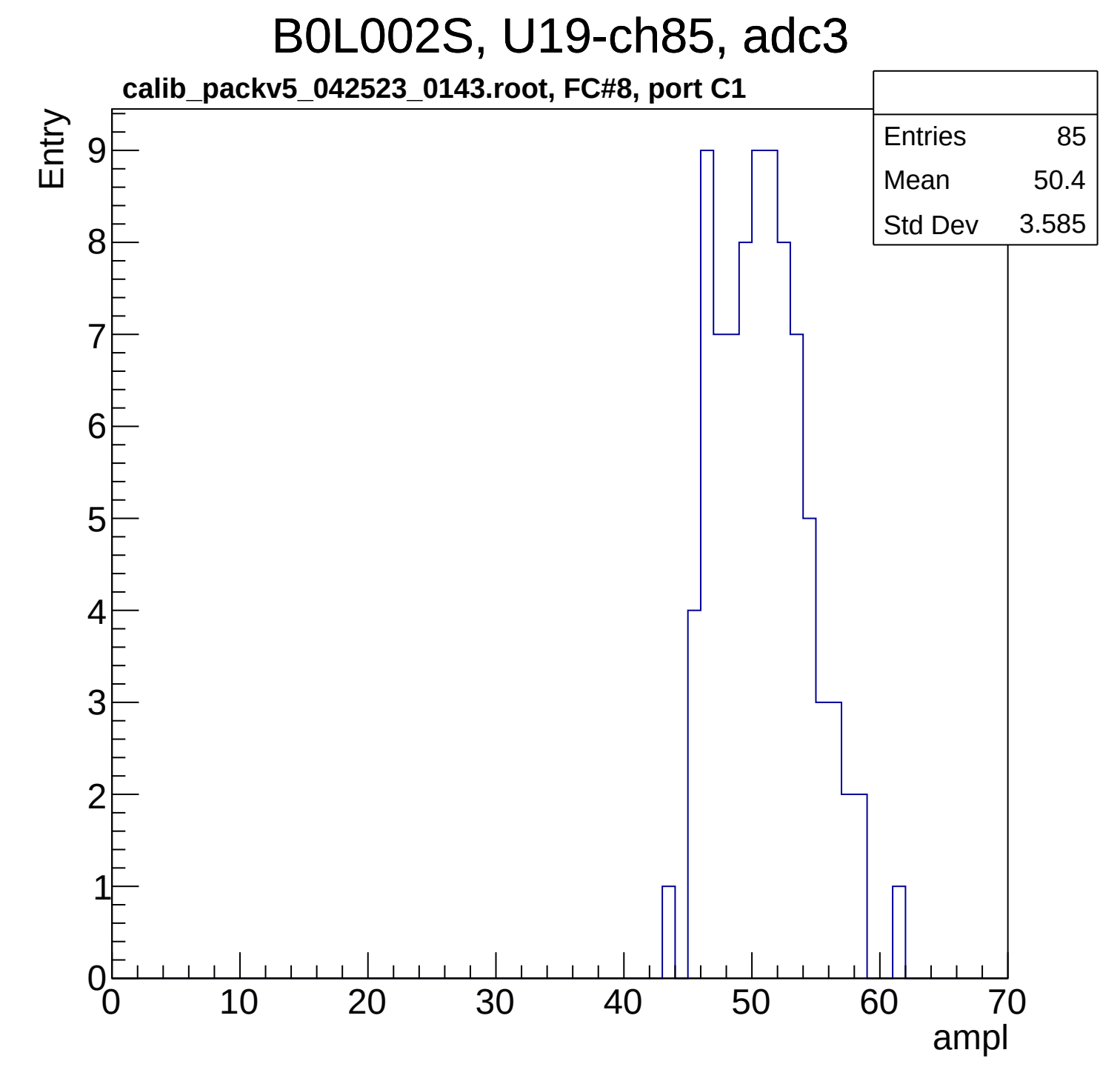

B0L002S, U19-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

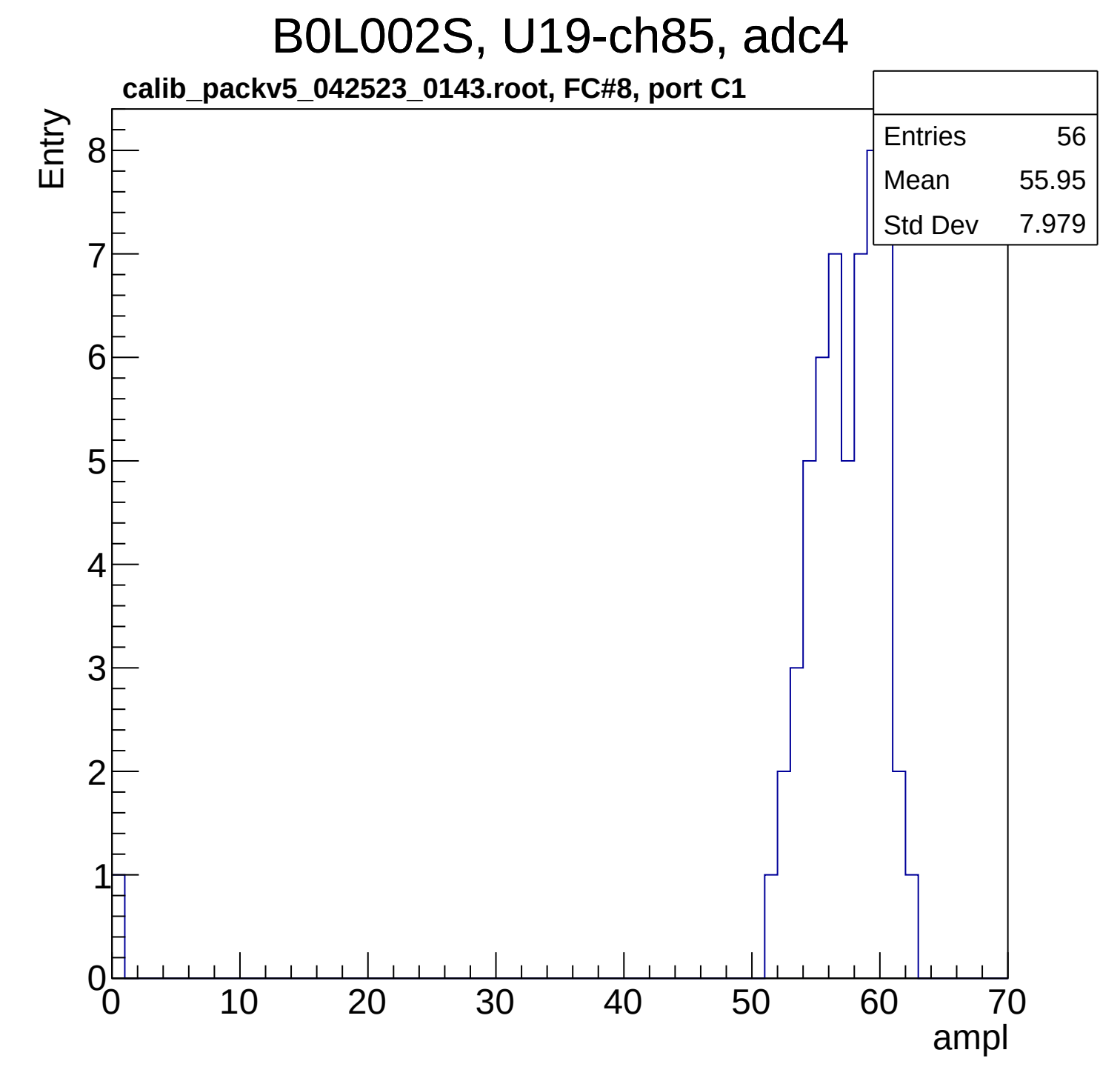

B0L002S, U19-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

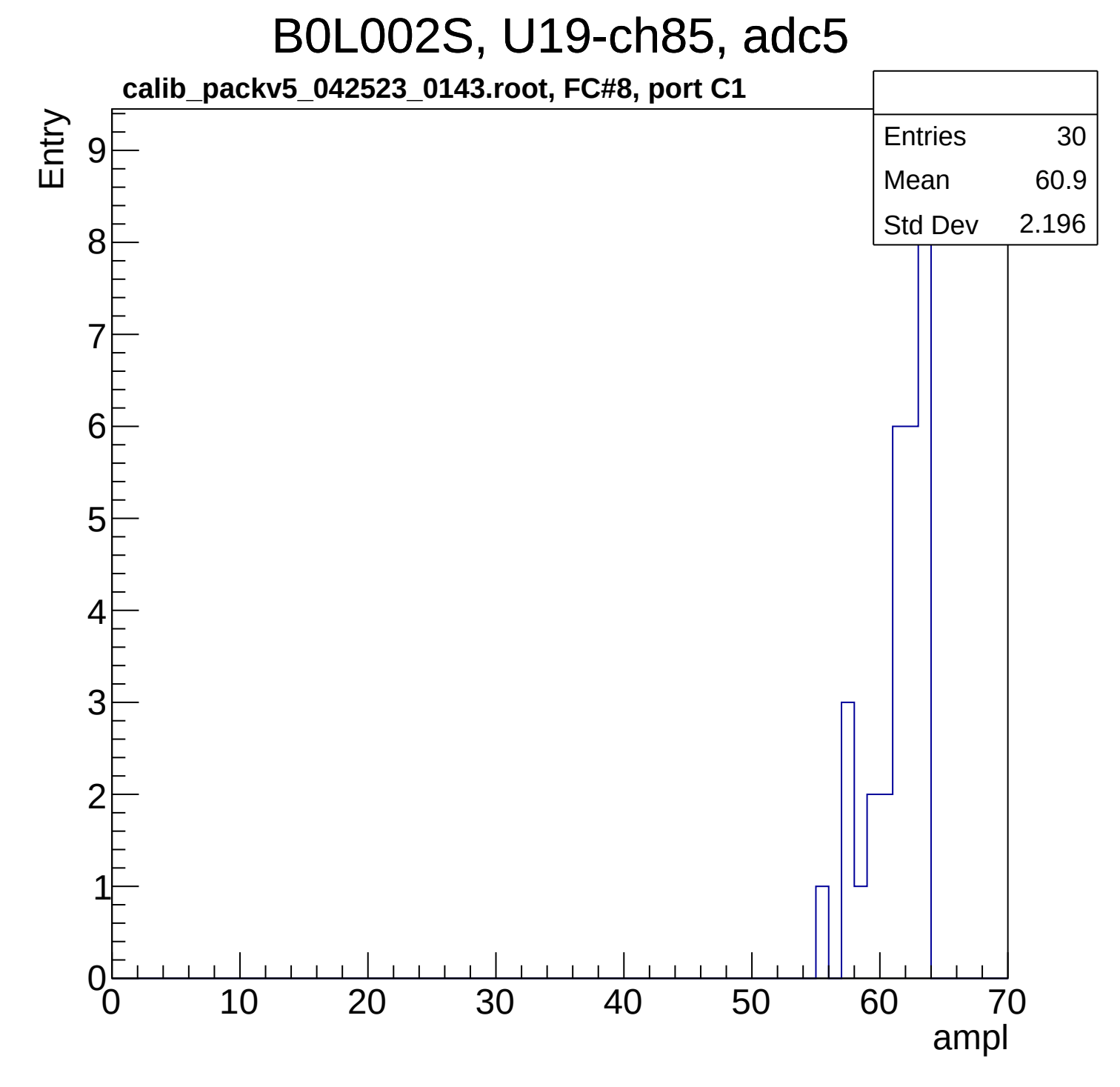

B0L002S, U19-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.93
Std Dev 5.796
Gaus mean : 31.5282
Gaus Width: 4.5689
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

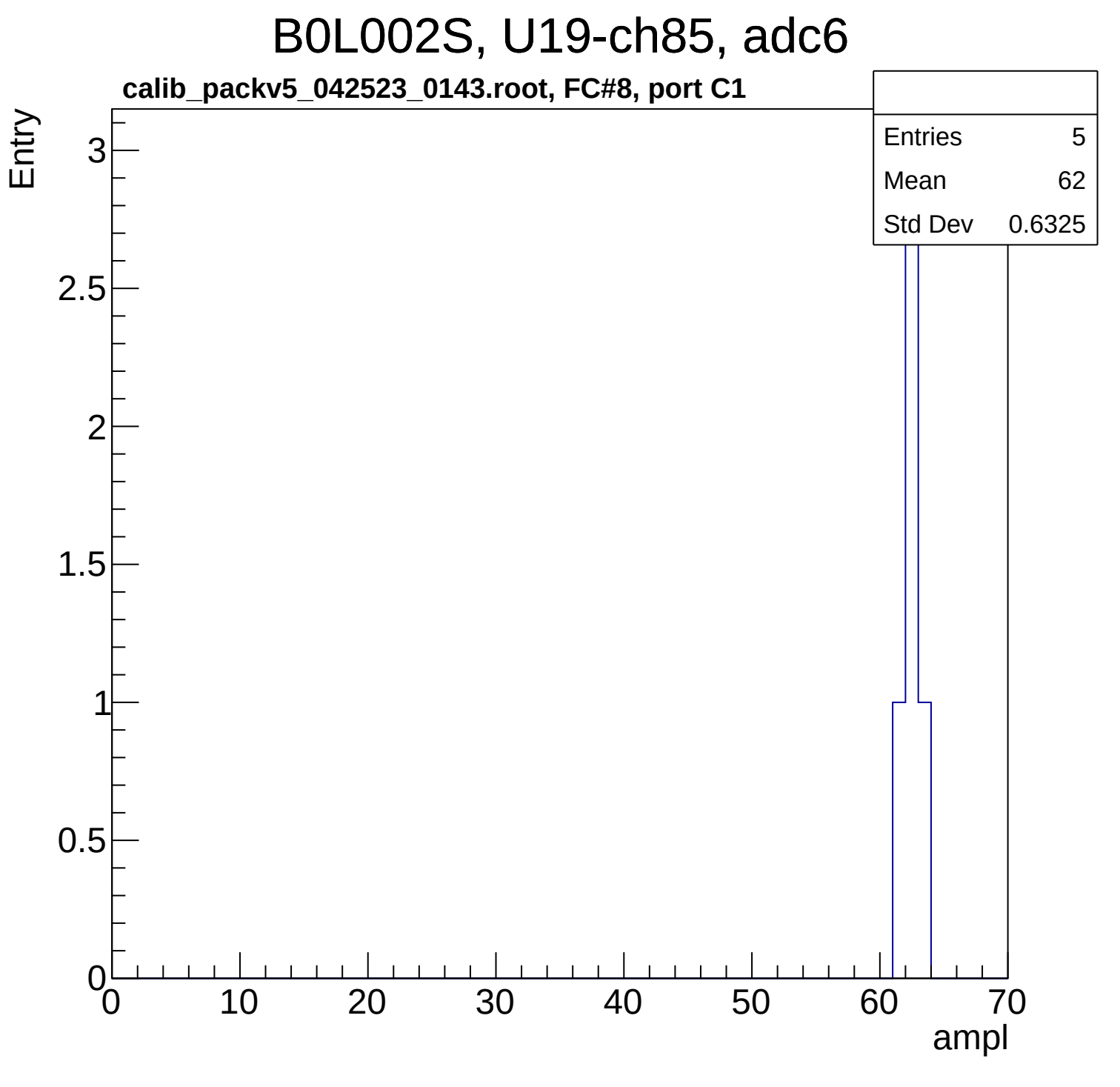

B0L002S, U19-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.2
Std Dev 3.373
Gaus mean : 39.1837
Gaus Width: 3.3403
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

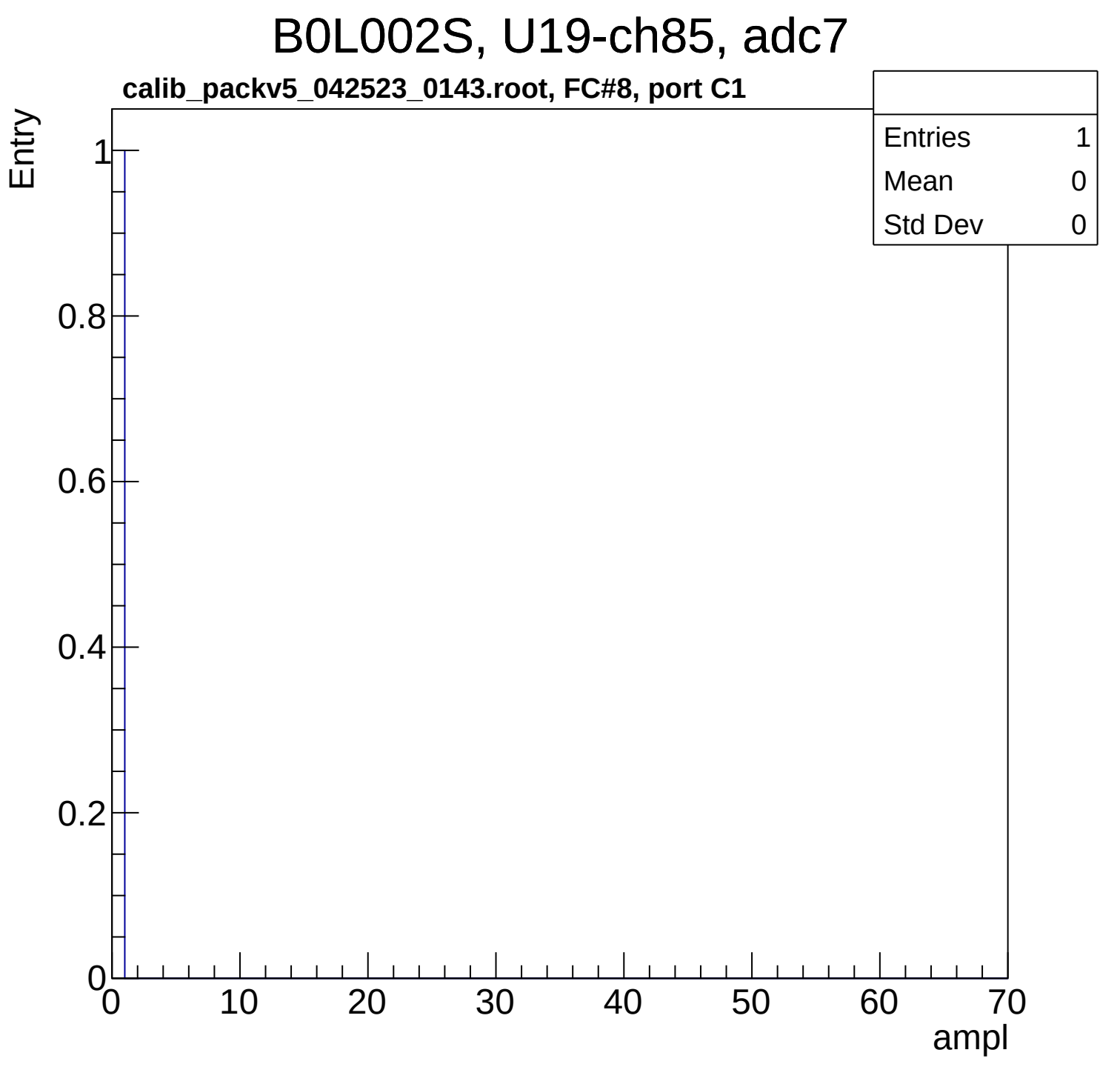

B0L002S, U19-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 43.77
Std Dev 2.802
Gaus mean : 21.6228
Gaus Width: 12.0696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

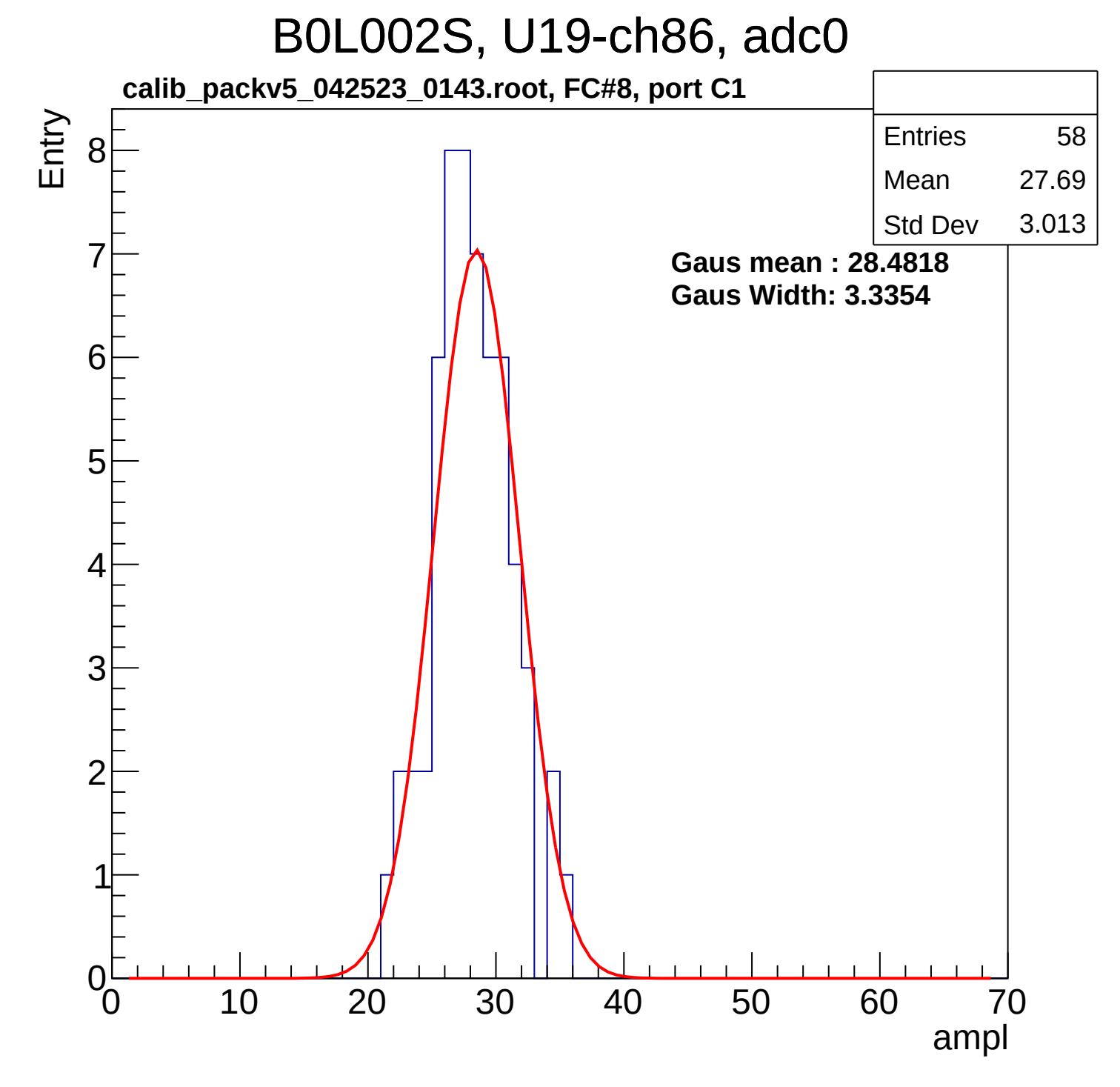

B0L002S, U19-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 50.4
Std Dev 3.585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

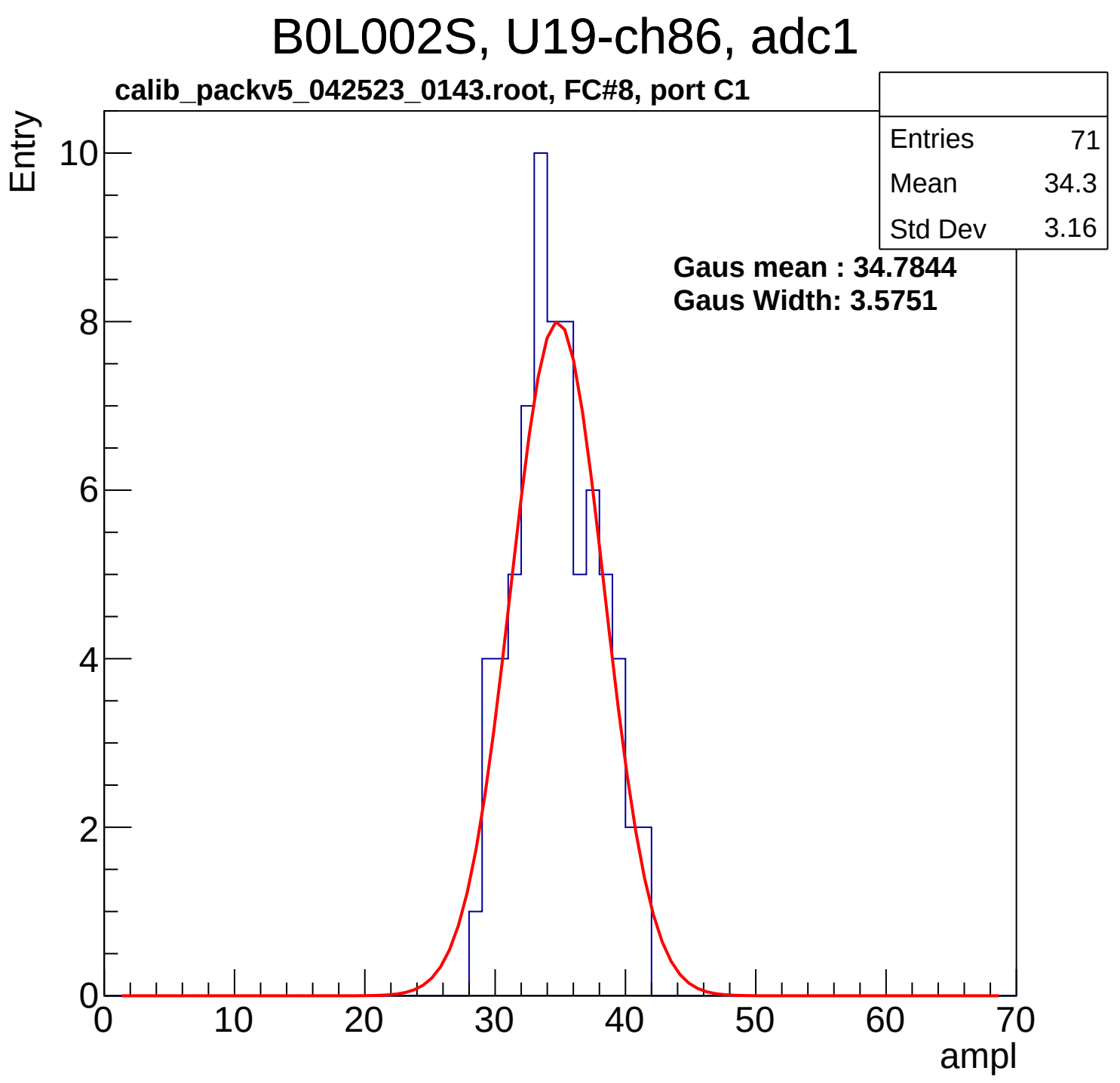

B0L002S, U19-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.95
Std Dev 7.979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

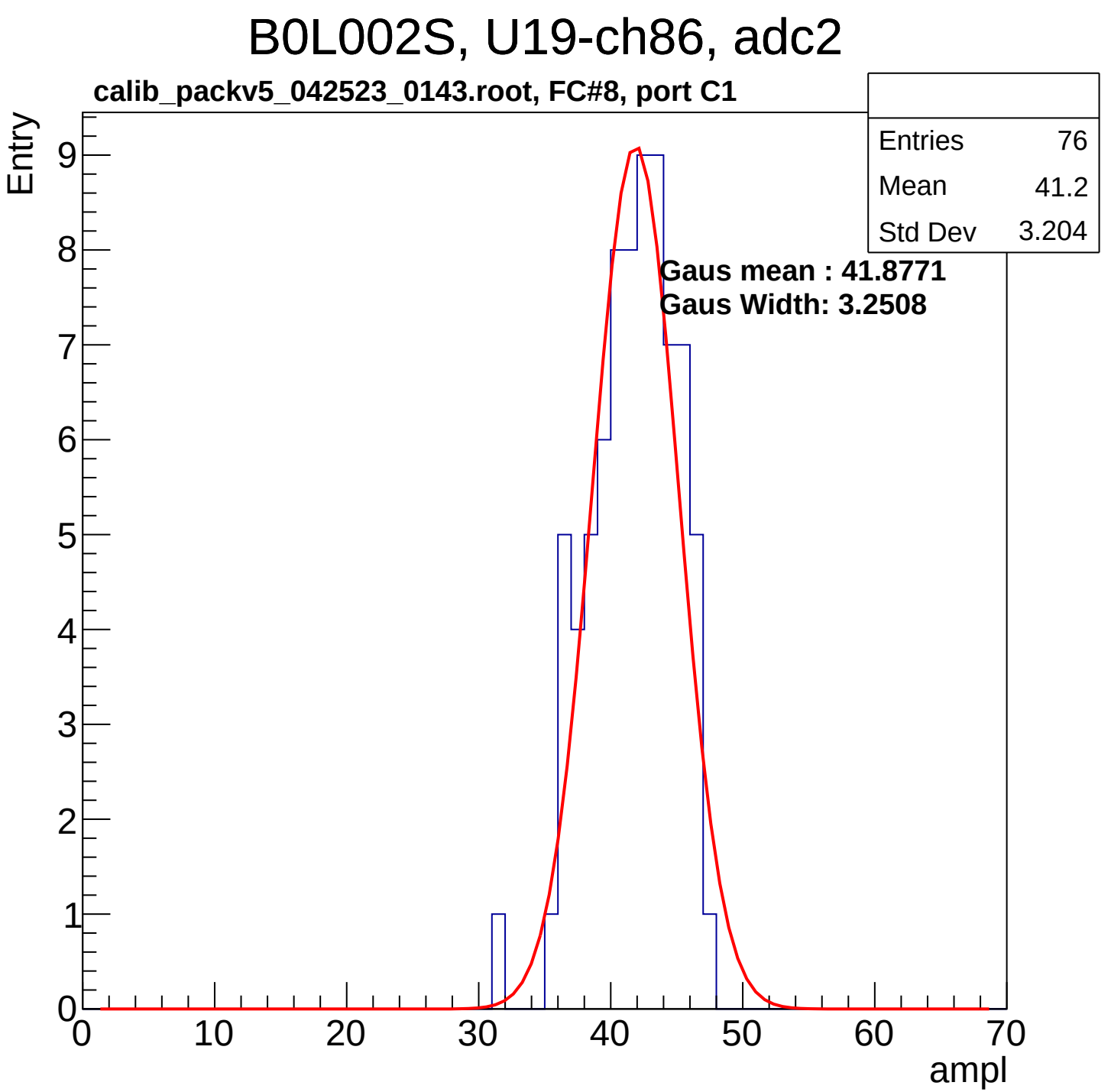

B0L002S, U19-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 60.9
Std Dev 2.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

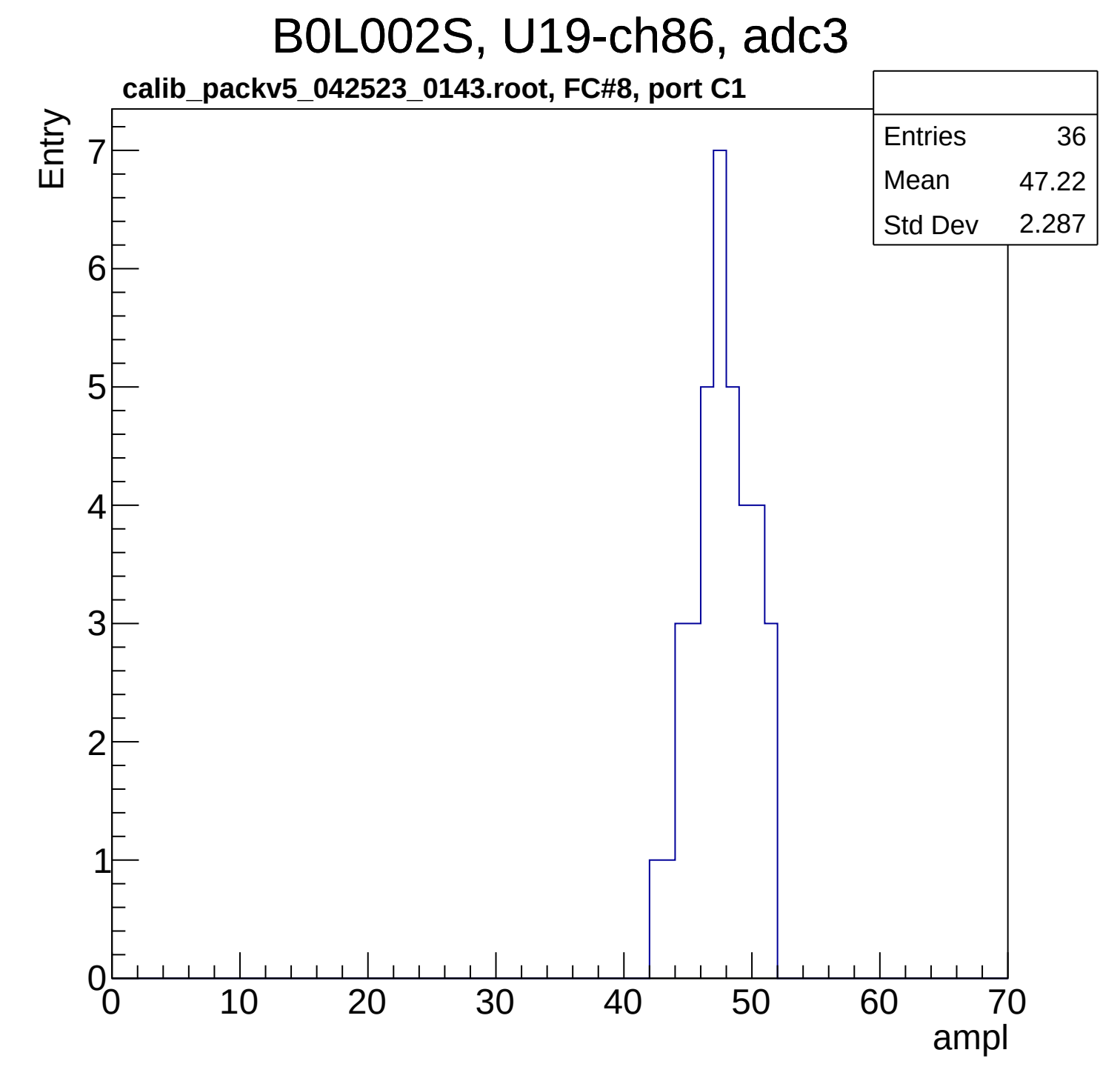

B0L002S, U19-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

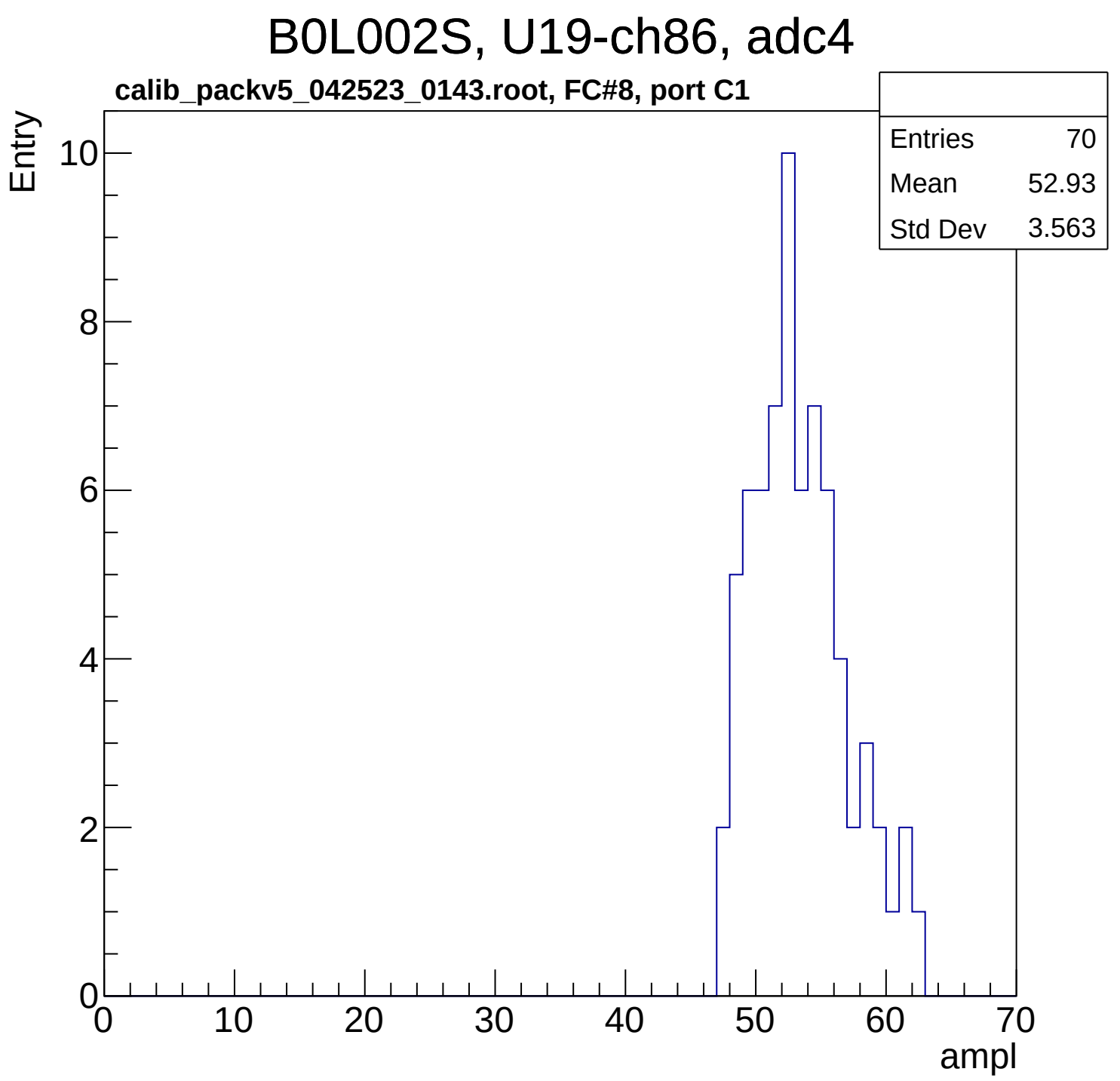

B0L002S, U19-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

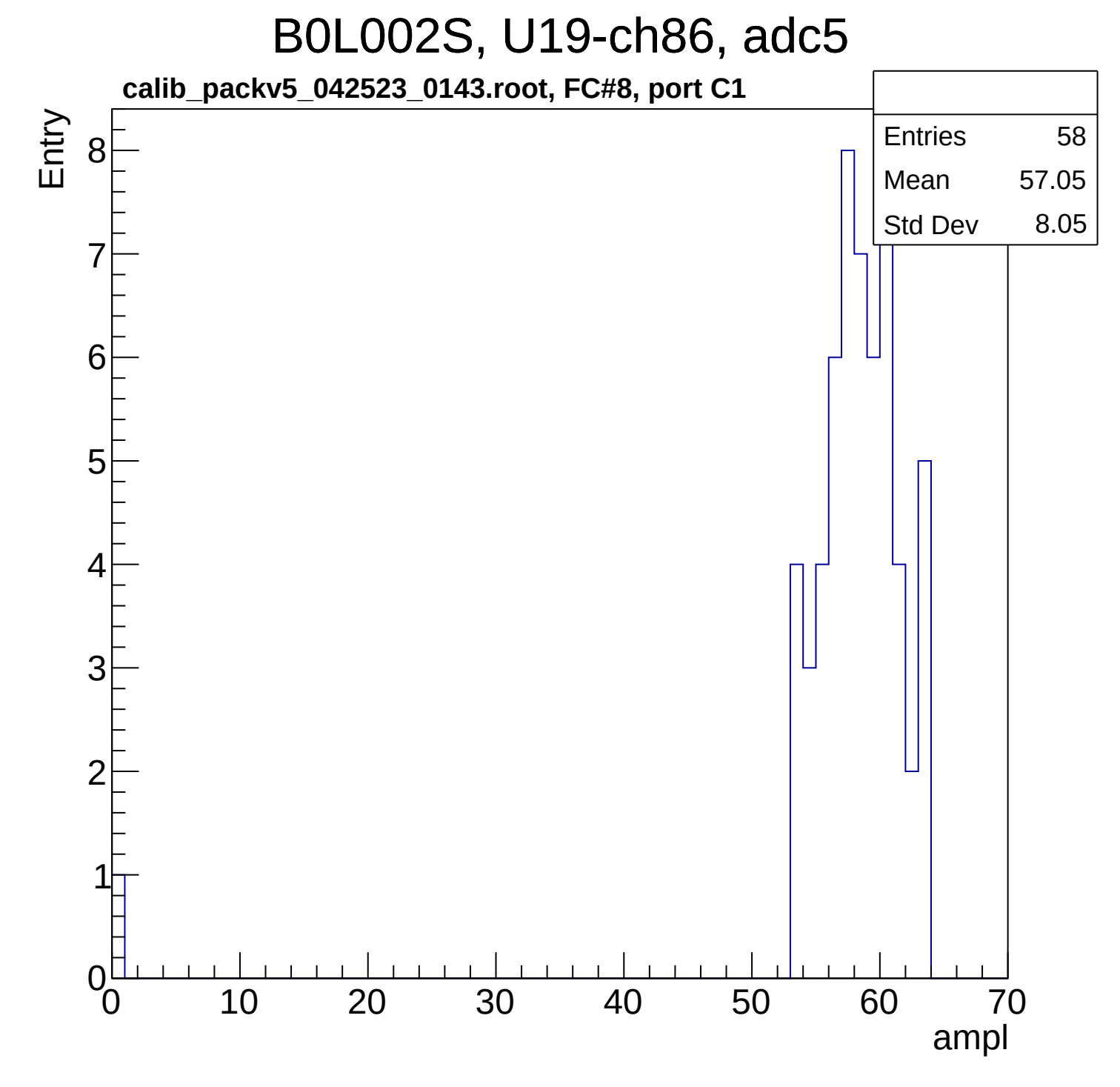

B0L002S, U19-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 27.69
Std Dev 3.013
Gaus mean : 28.4818
Gaus Width: 3.3354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

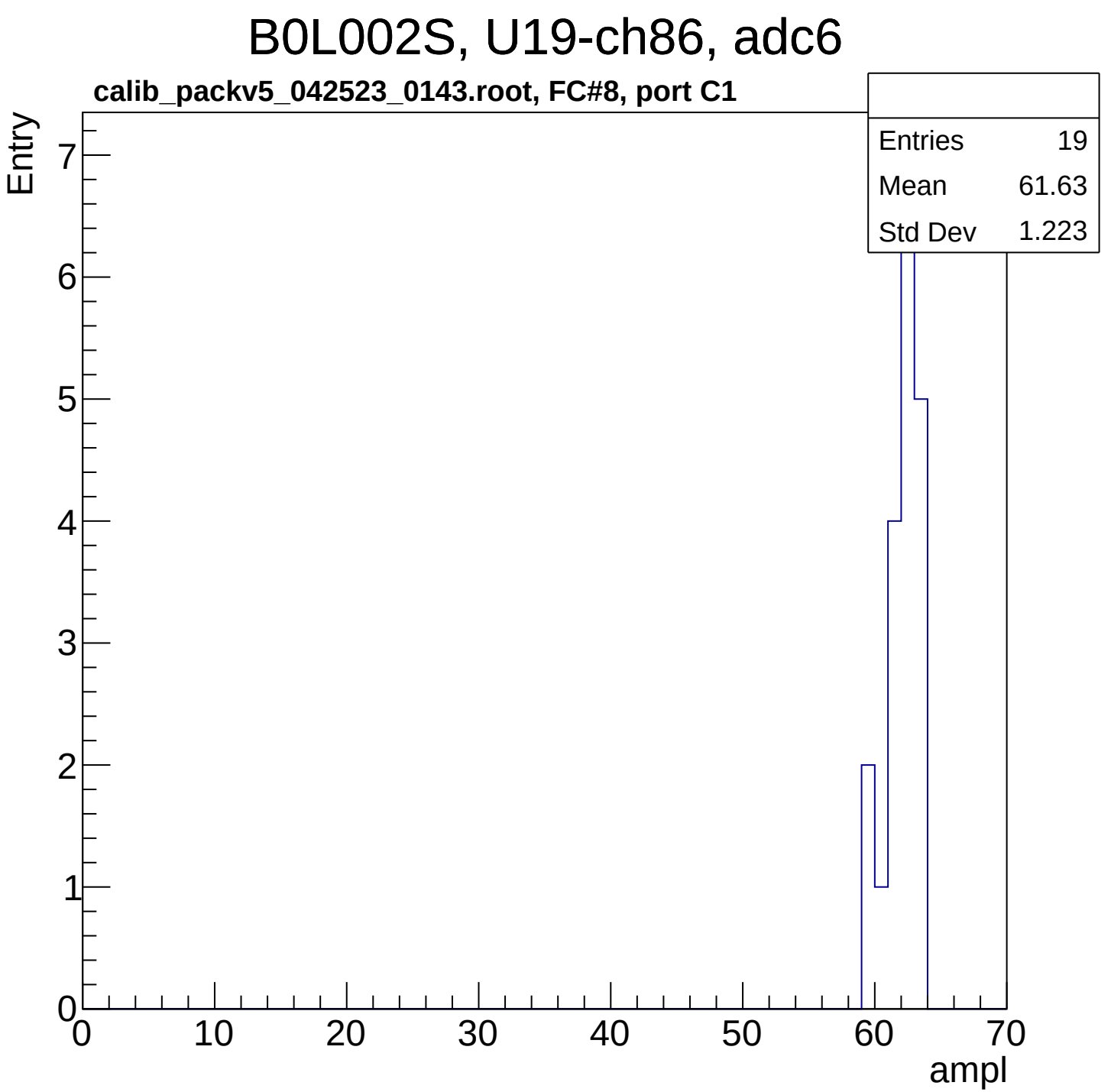

B0L002S, U19-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 34.3
Std Dev 3.16
Gaus mean : 34.7844
Gaus Width: 3.5751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

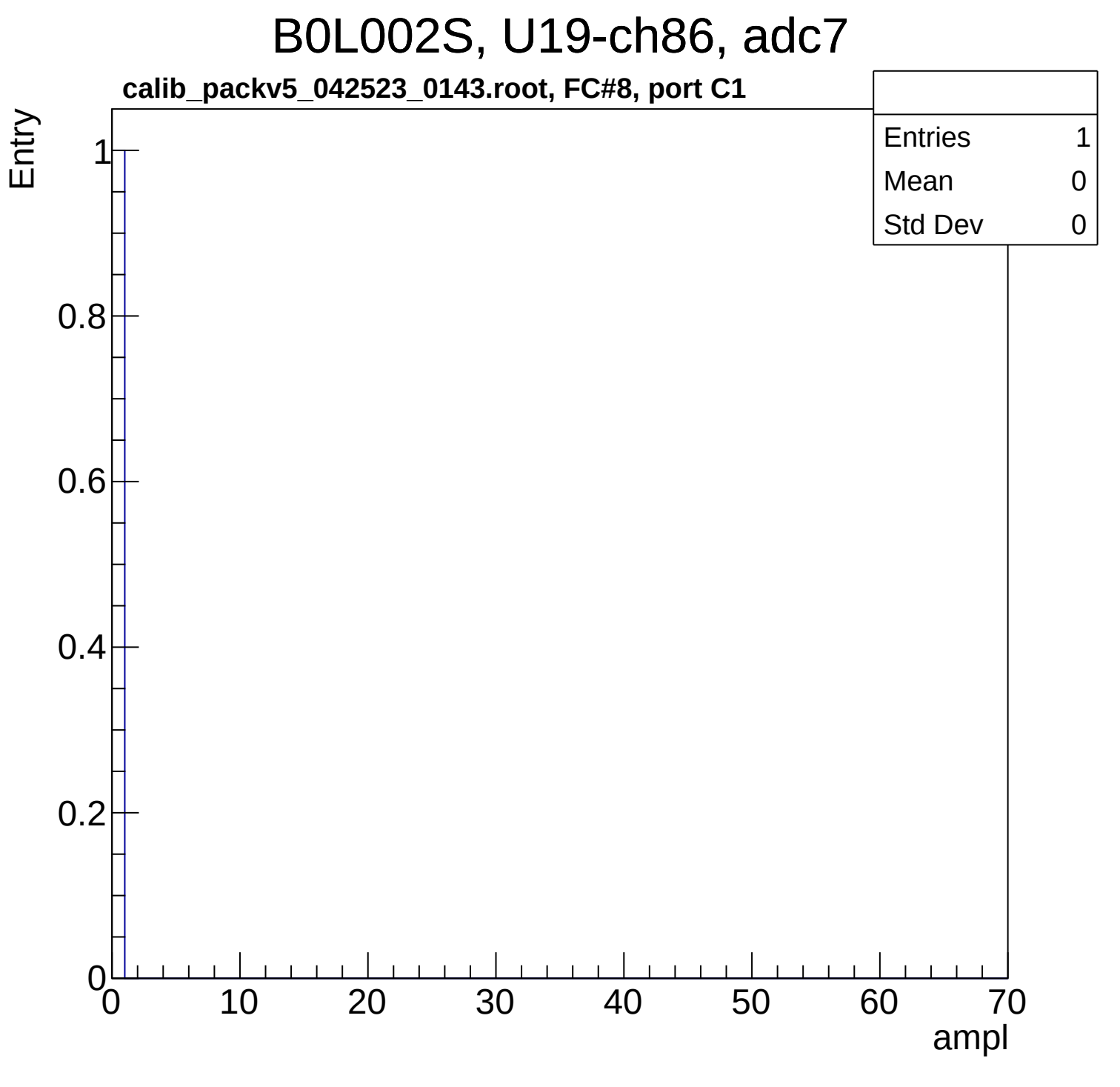

B0L002S, U19-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 41.2
Std Dev 3.204
Gaus mean : 41.8771
Gaus Width: 3.2508
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

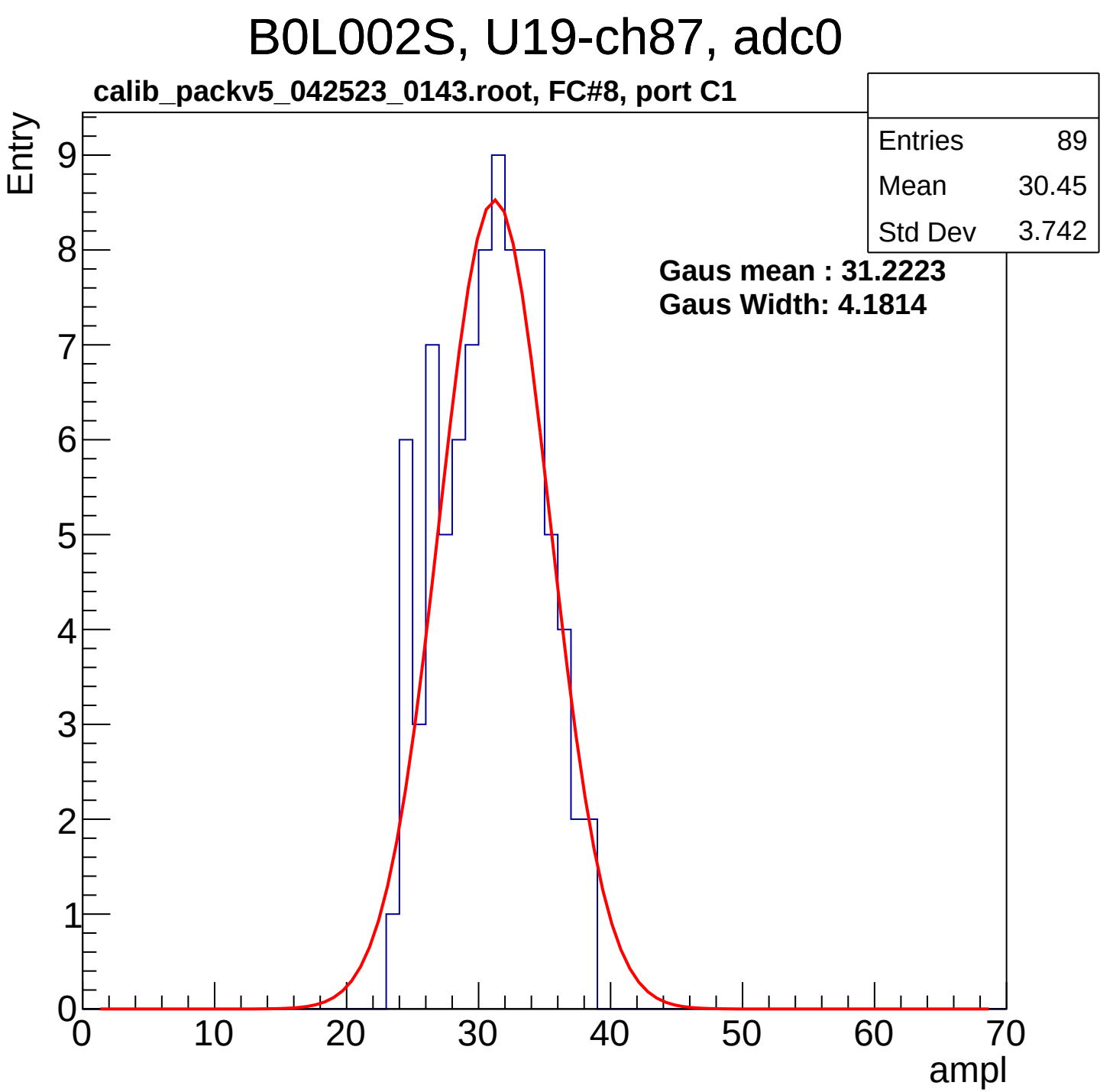

B0L002S, U19-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 47.22
Std Dev 2.287
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

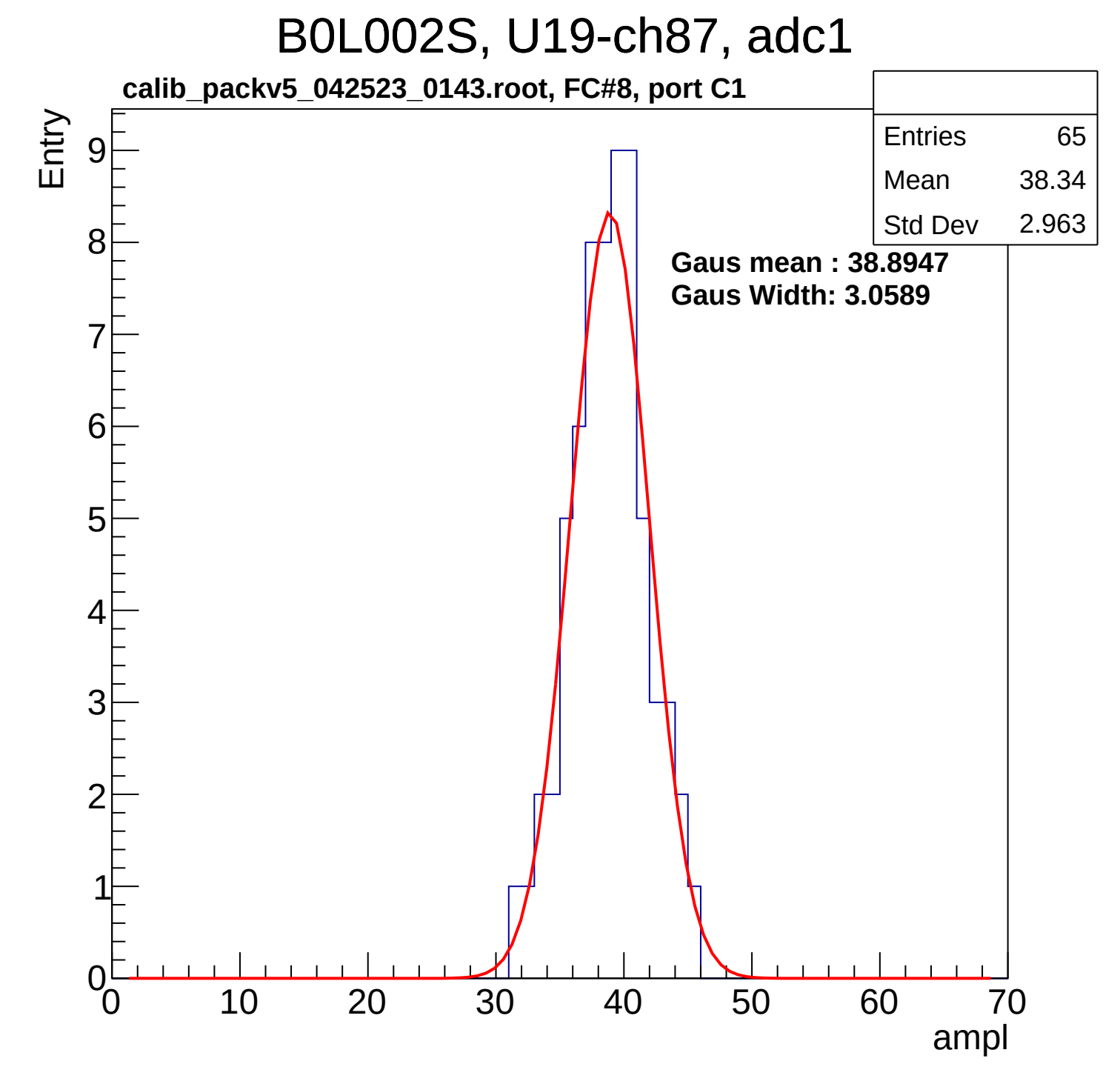

B0L002S, U19-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 52.93
Std Dev 3.563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

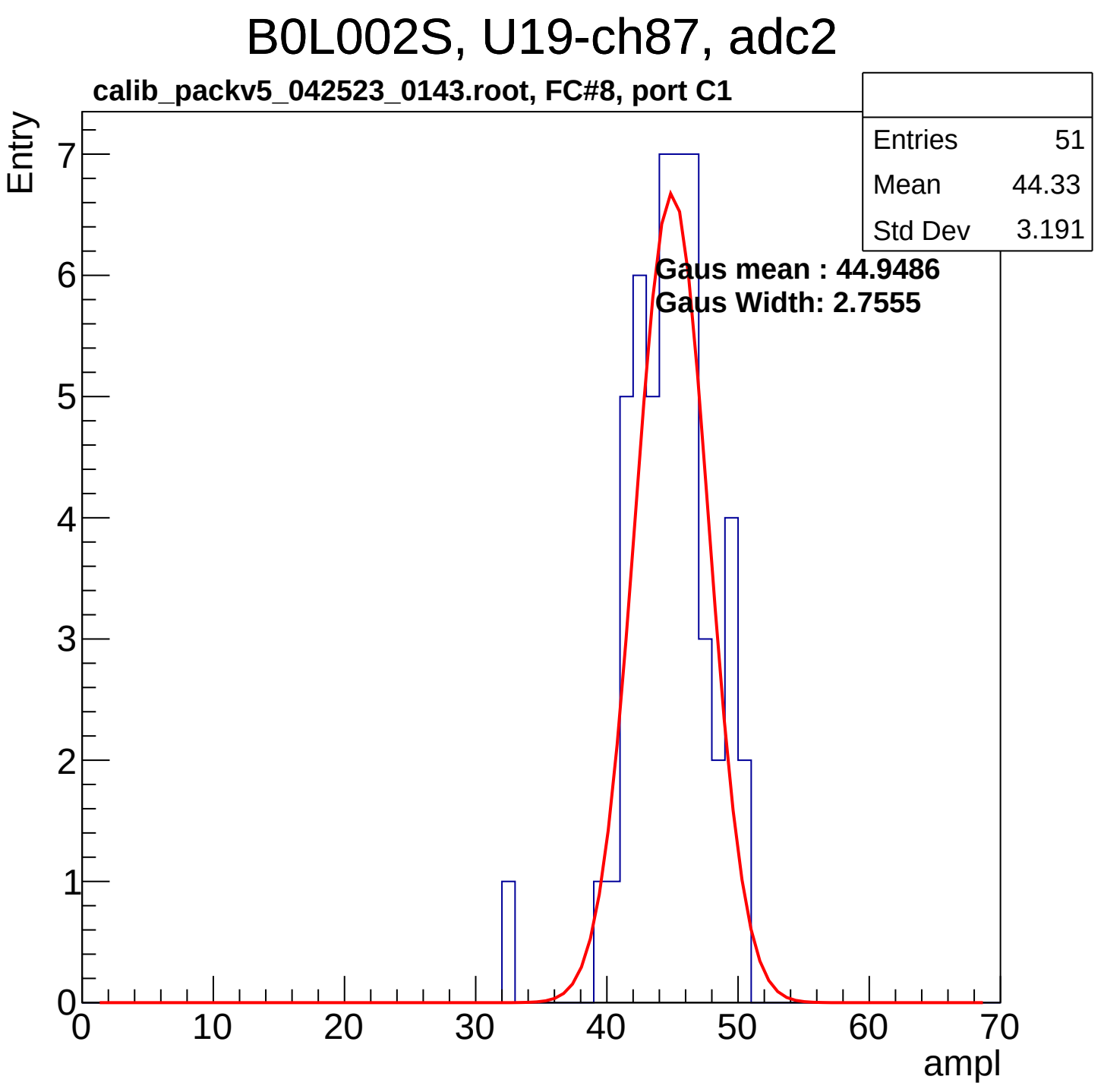

B0L002S, U19-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.05
Std Dev 8.05
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

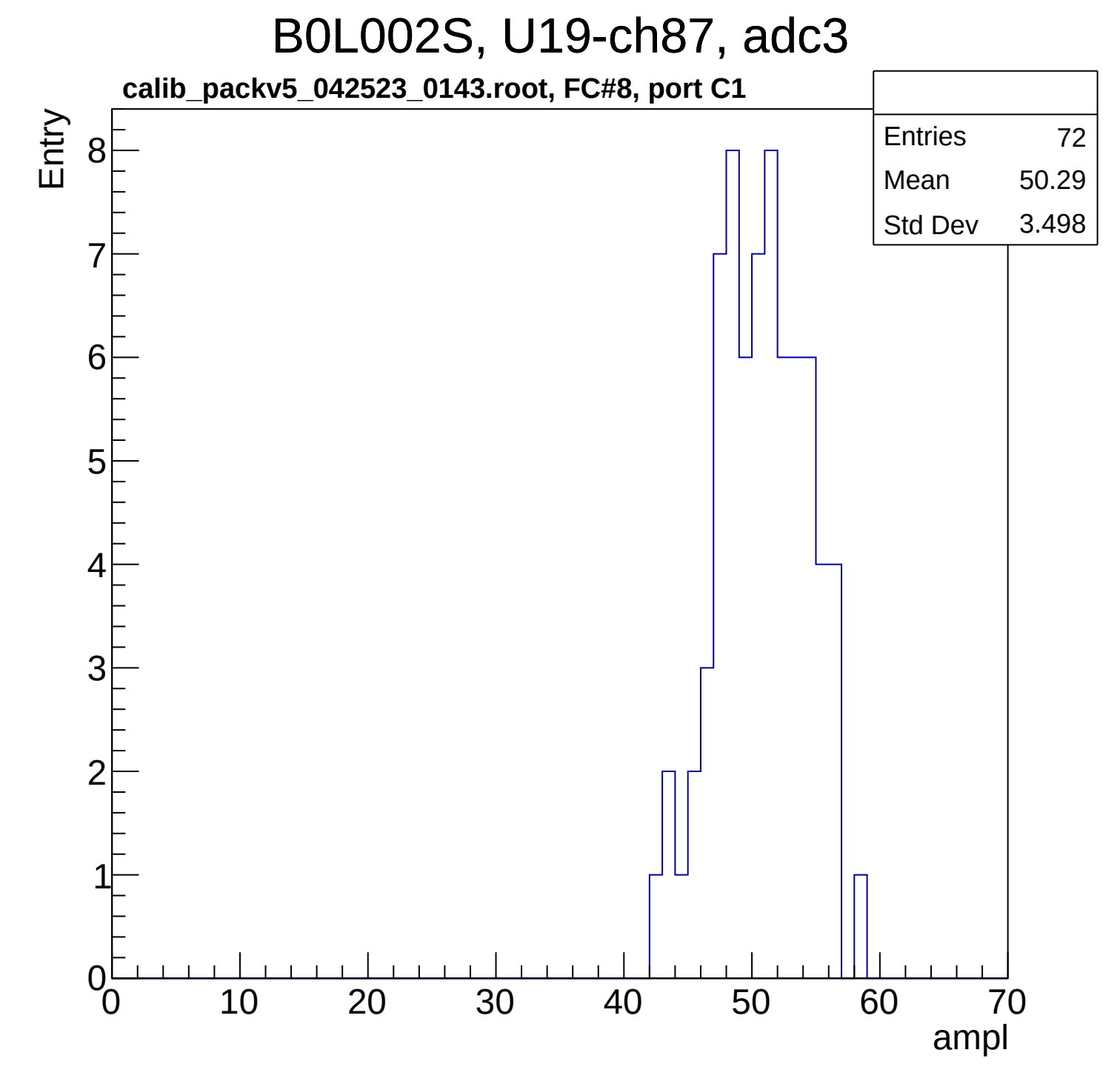

B0L002S, U19-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.63
Std Dev 1.223
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

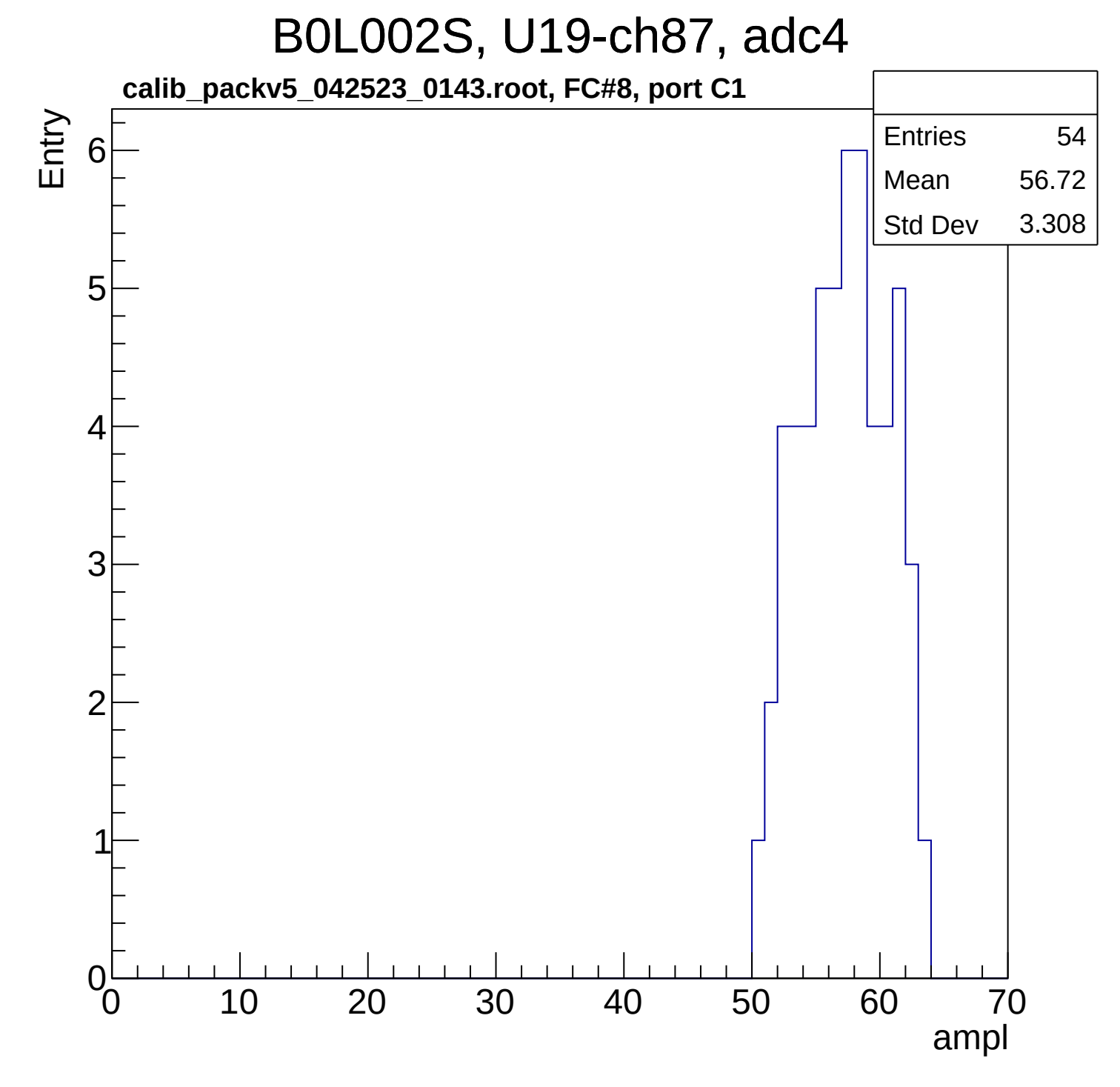

B0L002S, U19-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

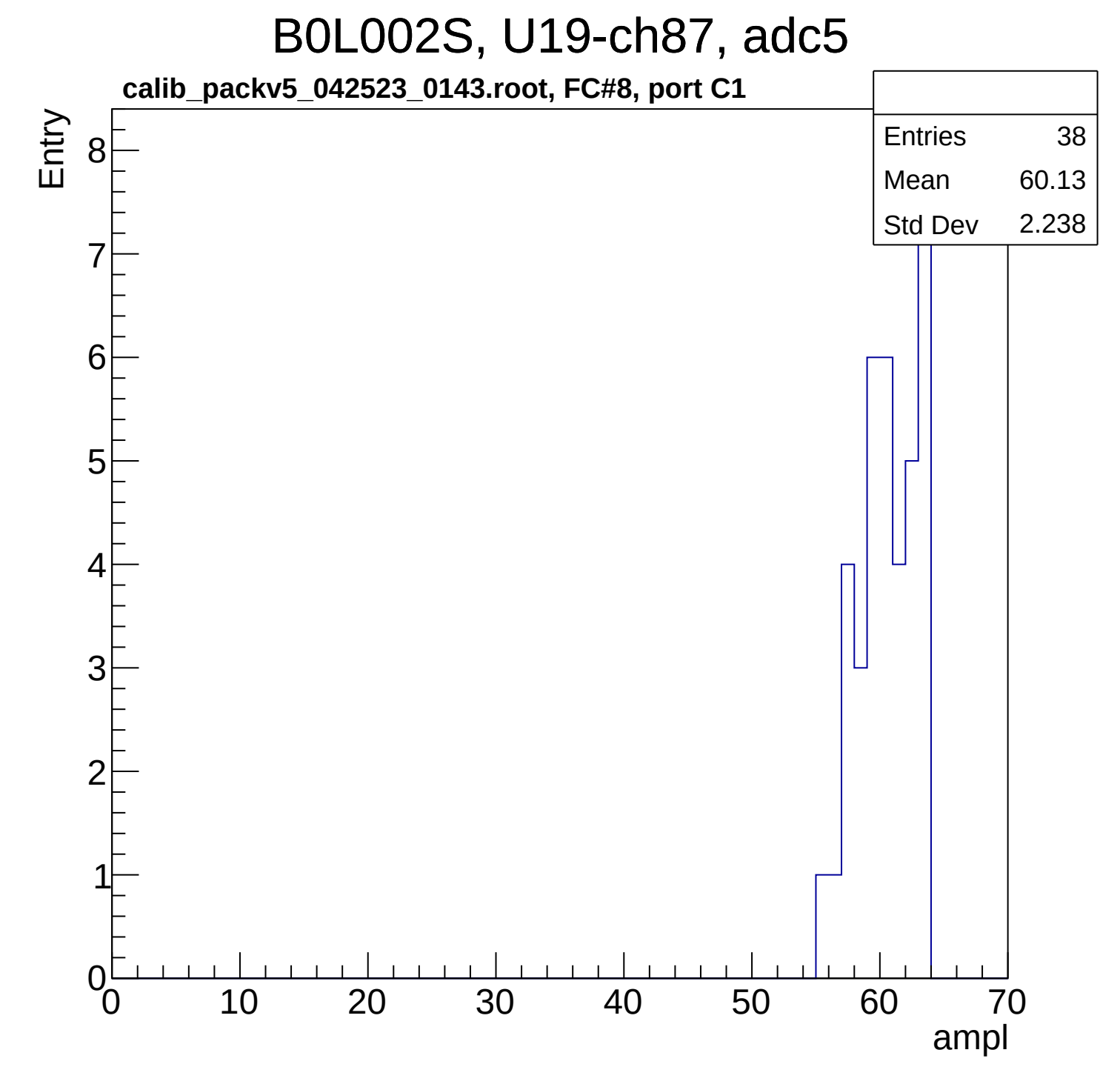

B0L002S, U19-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 30.45
Std Dev 3.742
Gaus mean : 31.2223
Gaus Width: 4.1814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

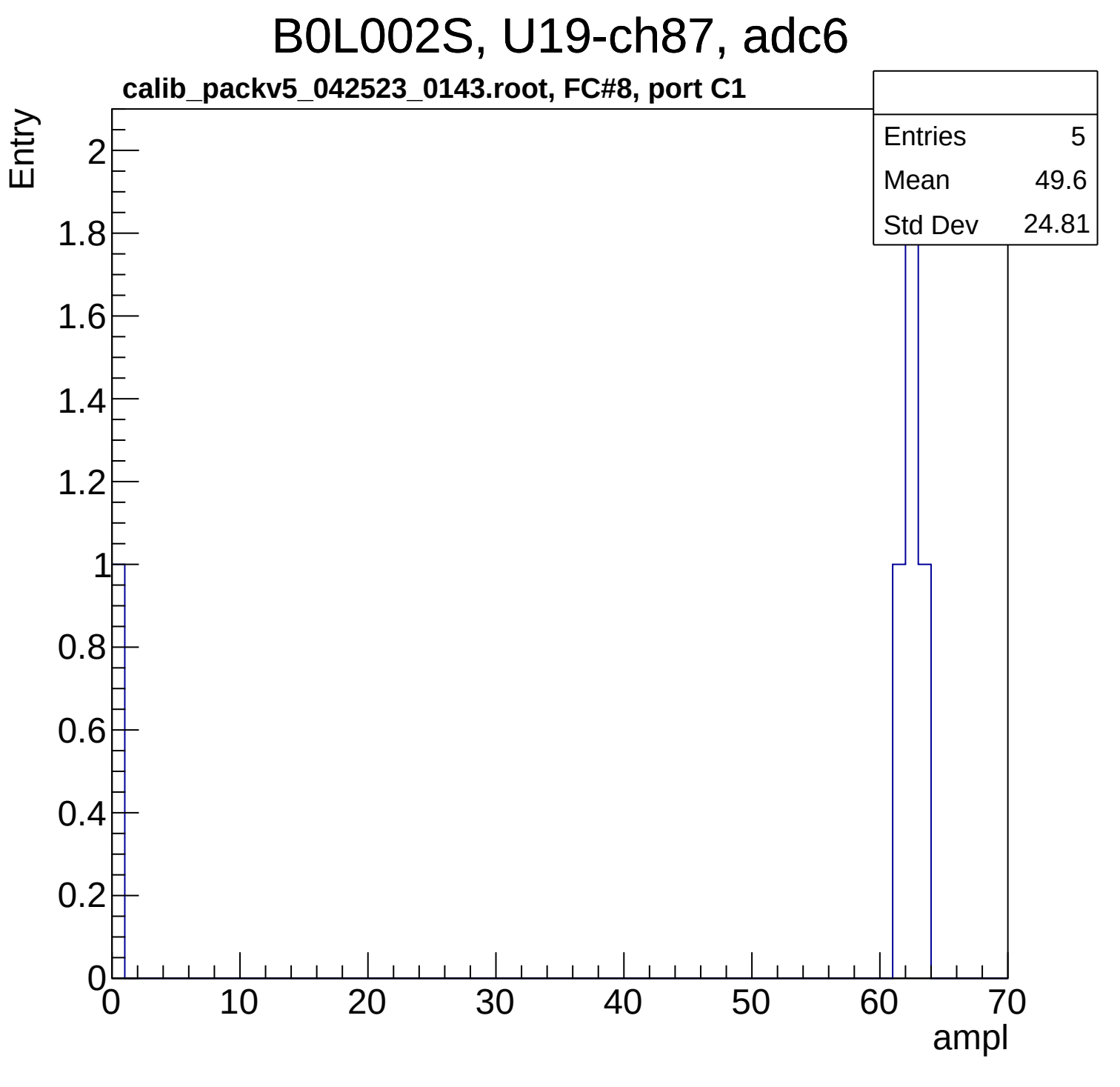

B0L002S, U19-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.34
Std Dev 2.963
Gaus mean : 38.8947
Gaus Width: 3.0589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

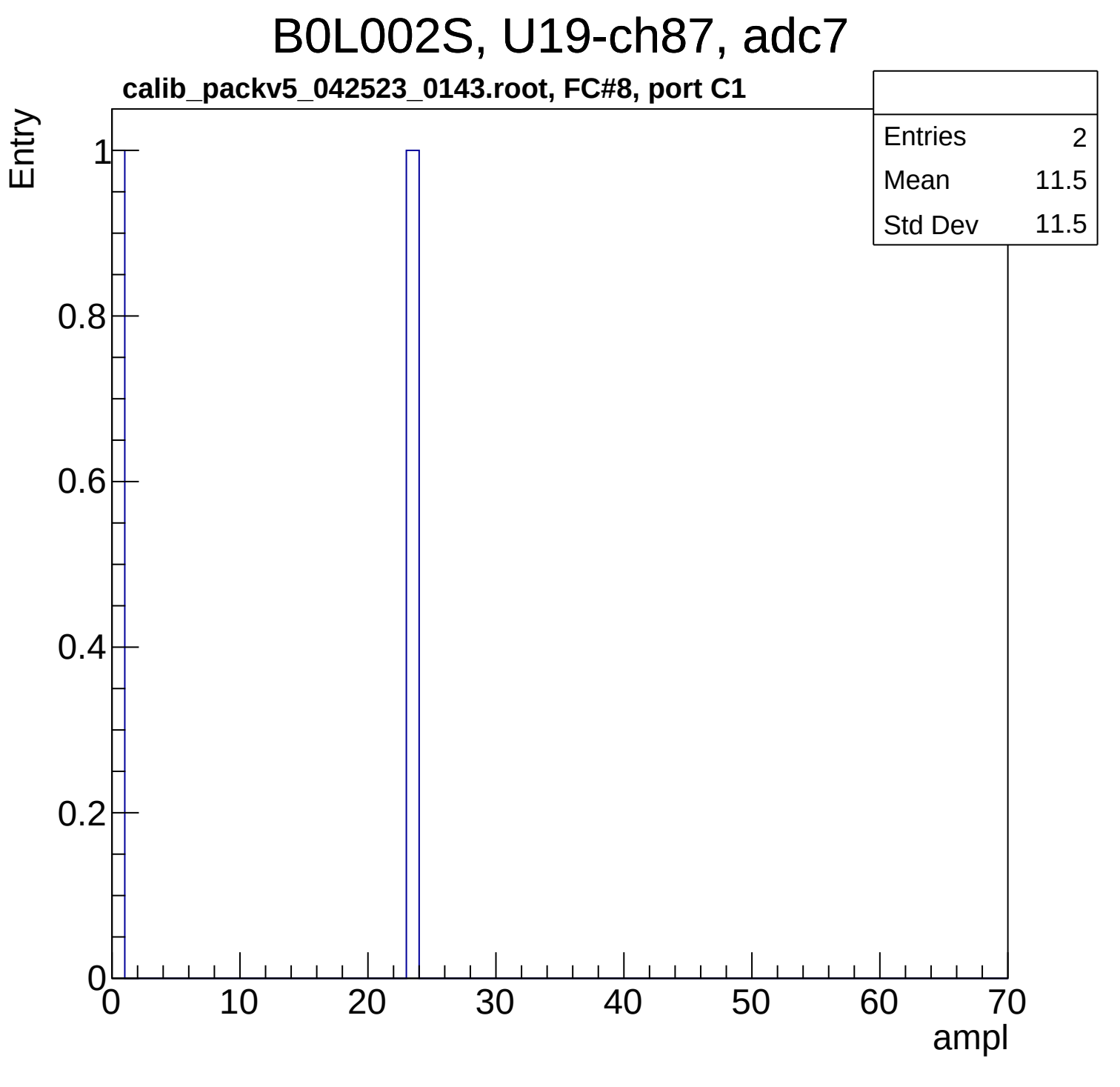

B0L002S, U19-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 44.33
Std Dev 3.191
Gaus mean : 44.9486
Gaus Width: 2.7555
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

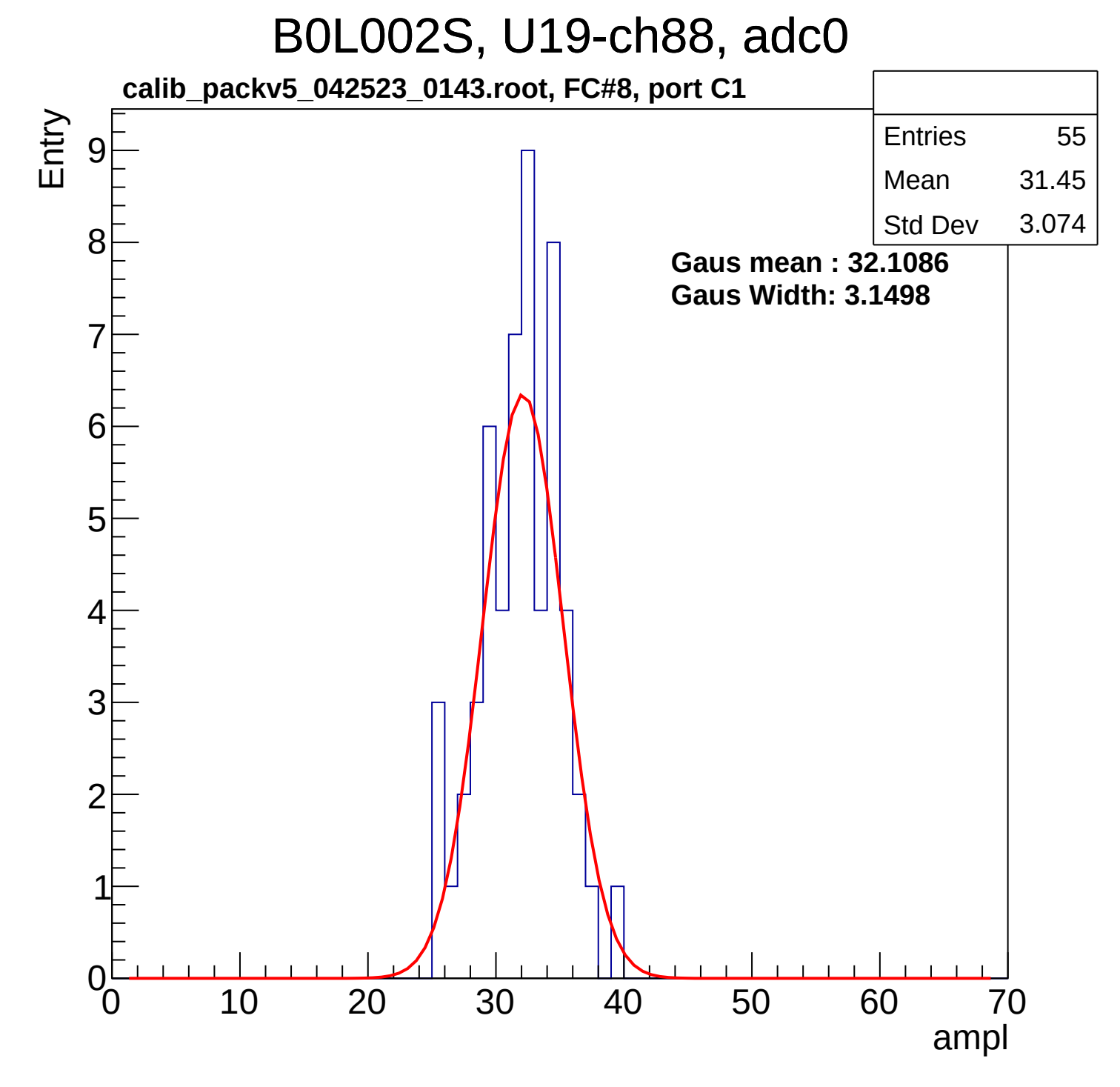

B0L002S, U19-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.29
Std Dev 3.498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

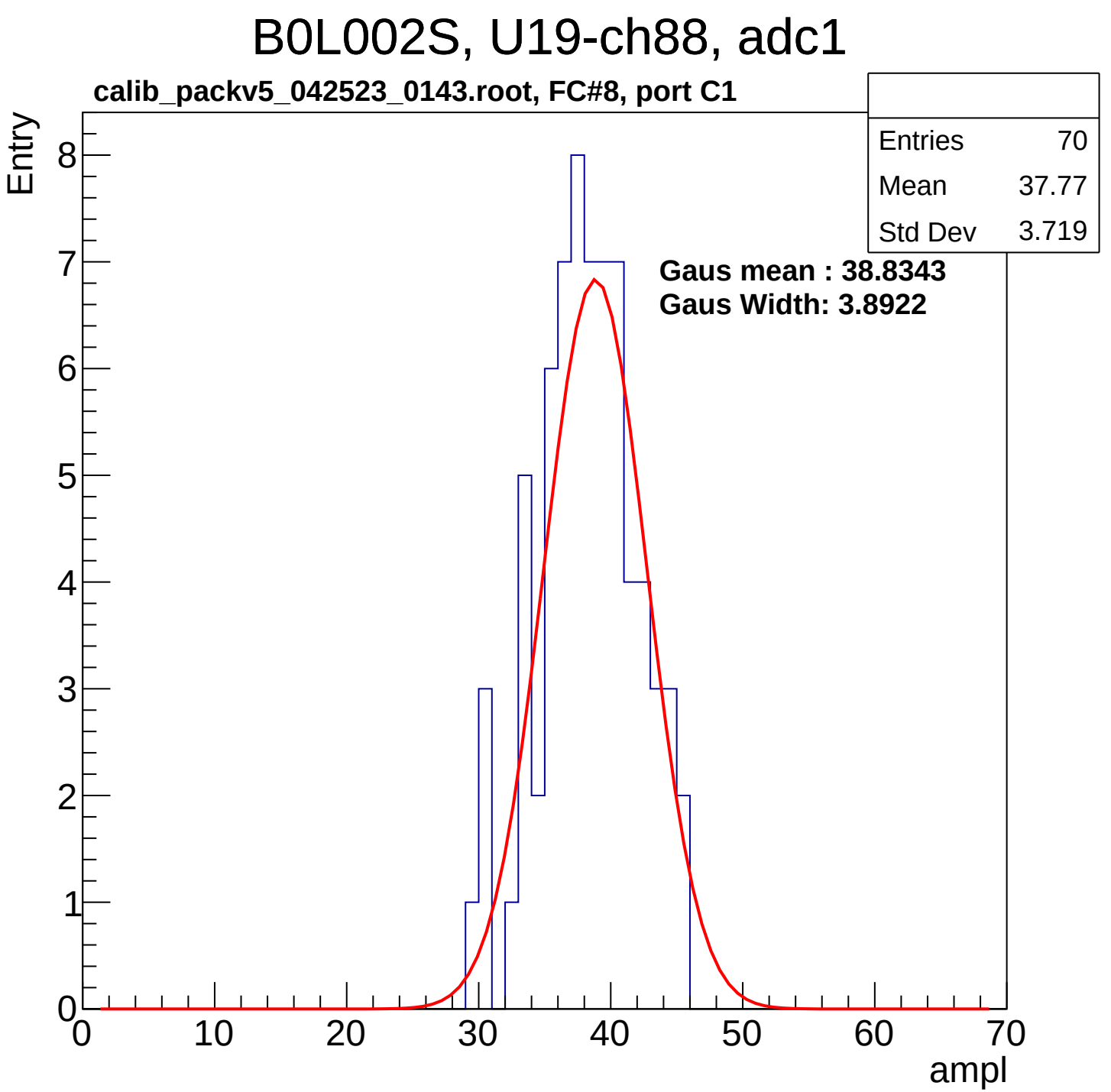

B0L002S, U19-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.72
Std Dev 3.308
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

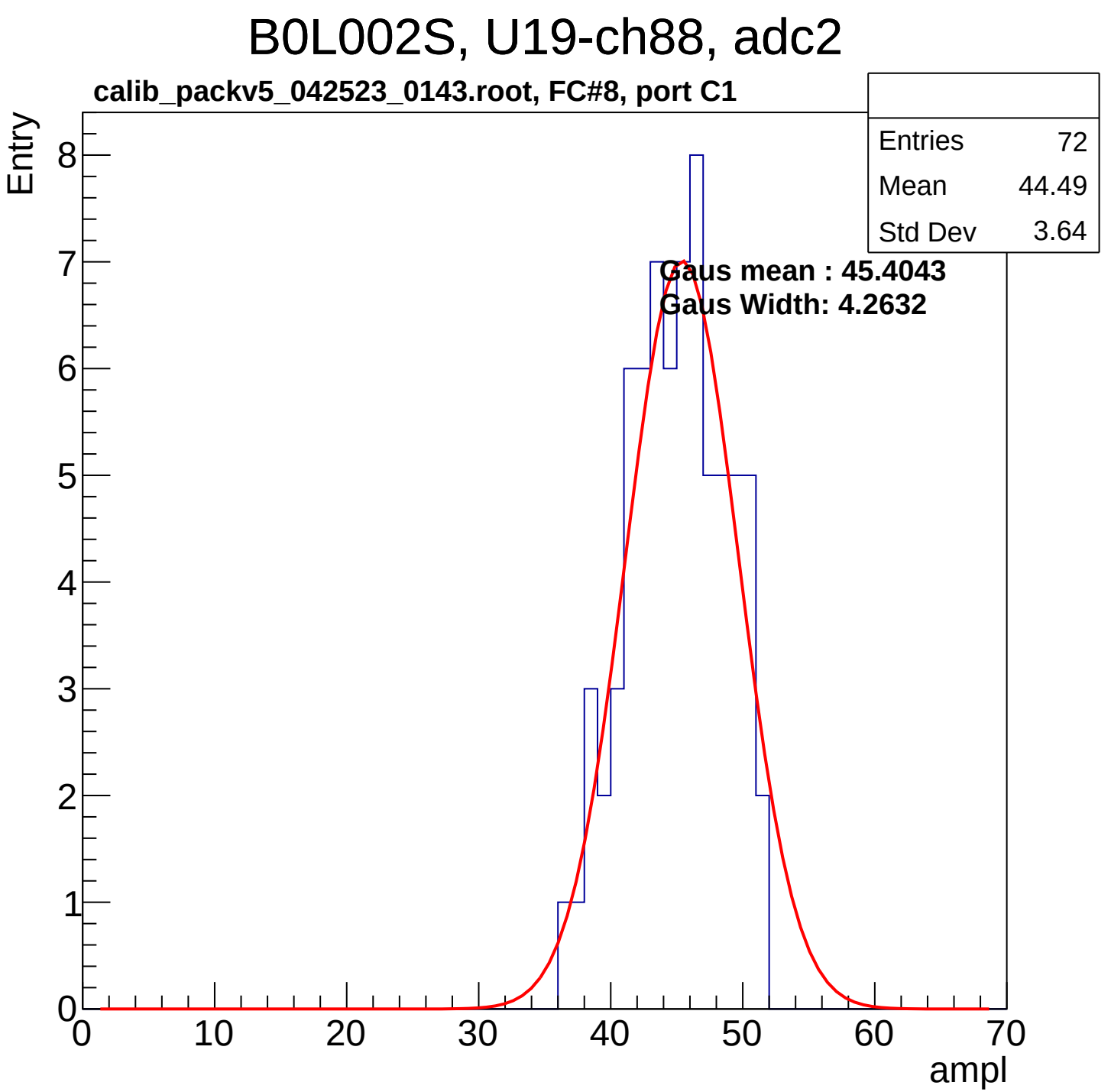

B0L002S, U19-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.13
Std Dev 2.238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

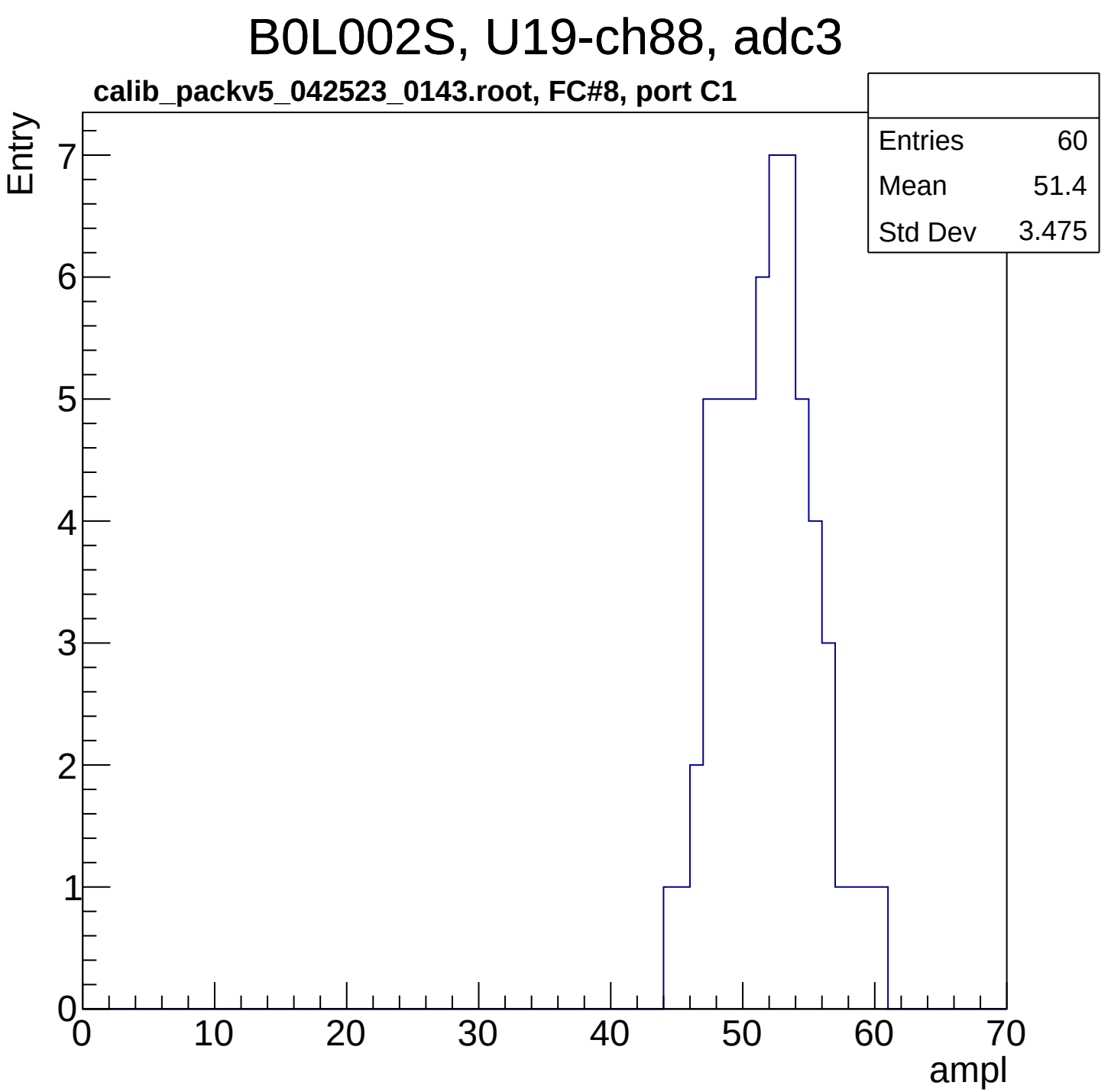

B0L002S, U19-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

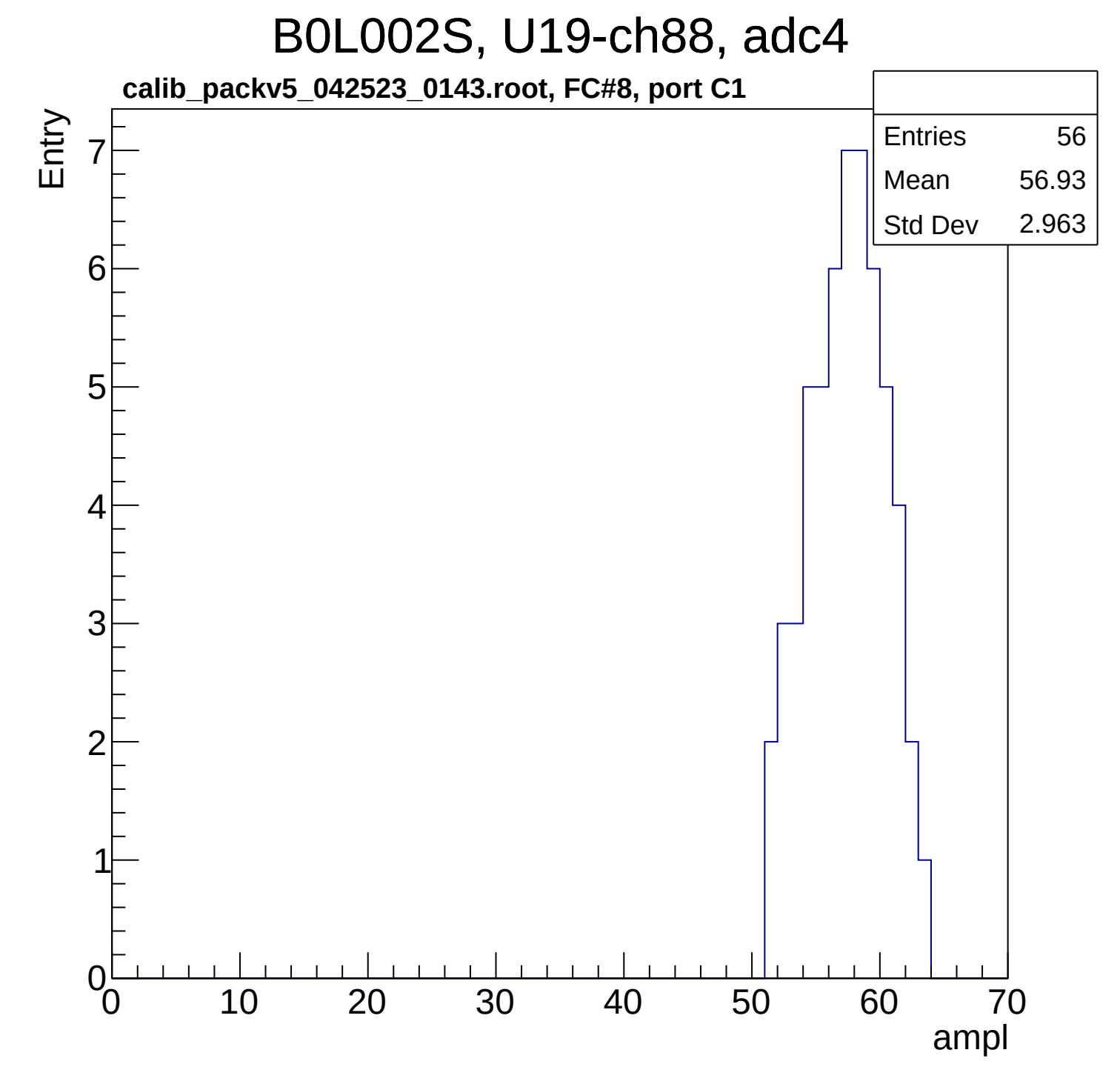

B0L002S, U19-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

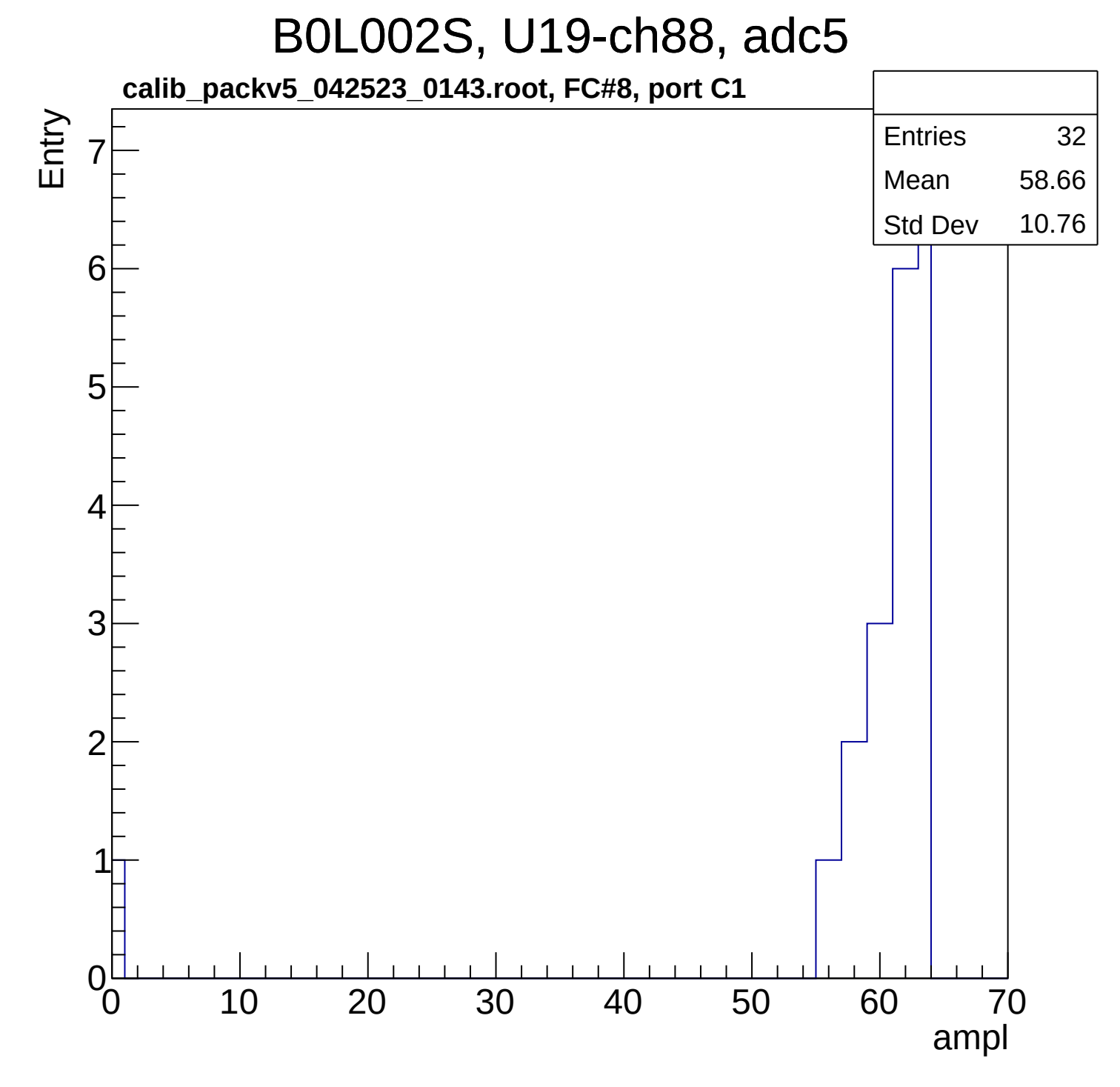

B0L002S, U19-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 31.45
Std Dev 3.074
Gaus mean : 32.1086
Gaus Width: 3.1498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

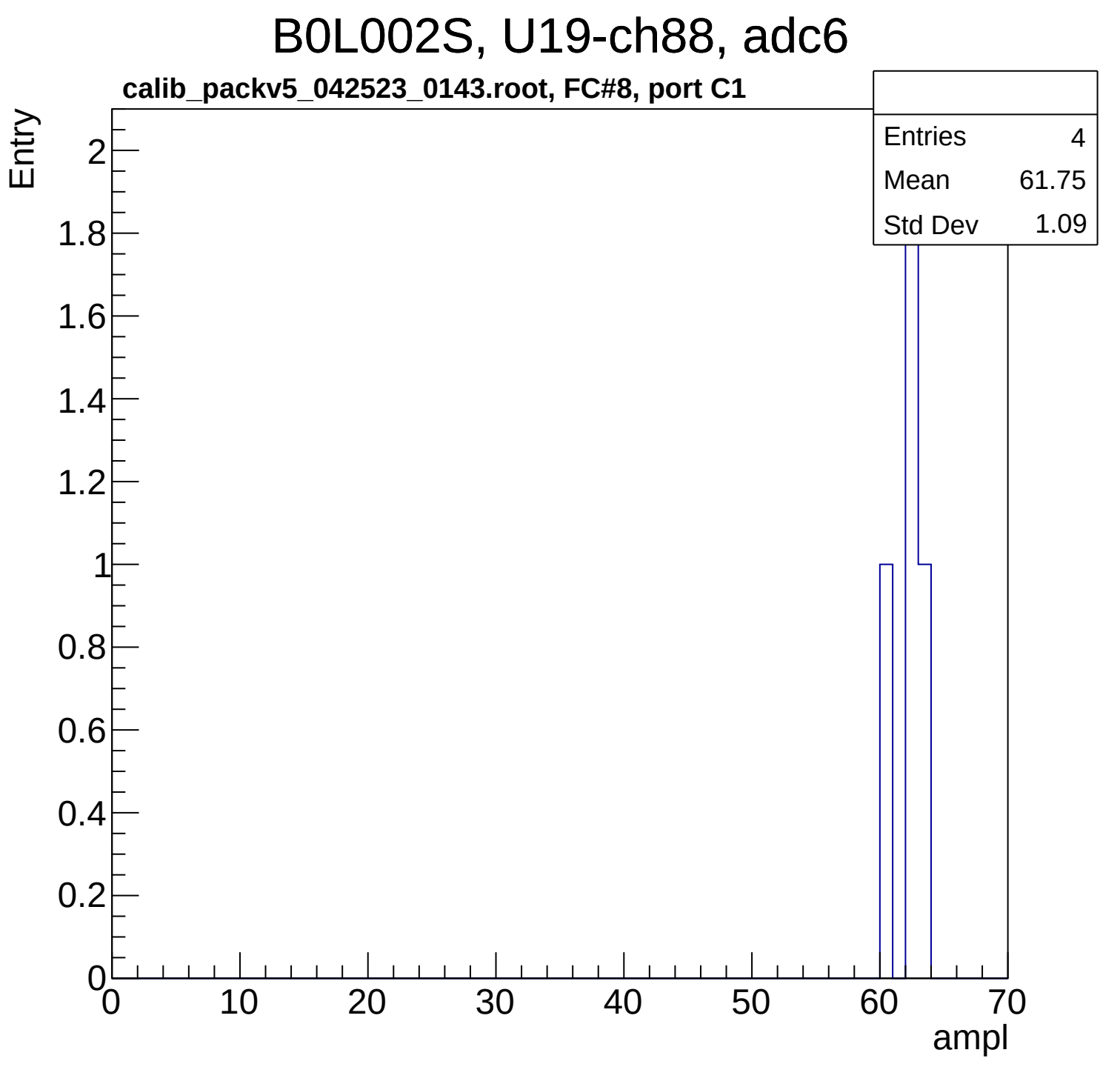

B0L002S, U19-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.77
Std Dev 3.719
Gaus mean : 38.8343
Gaus Width: 3.8922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

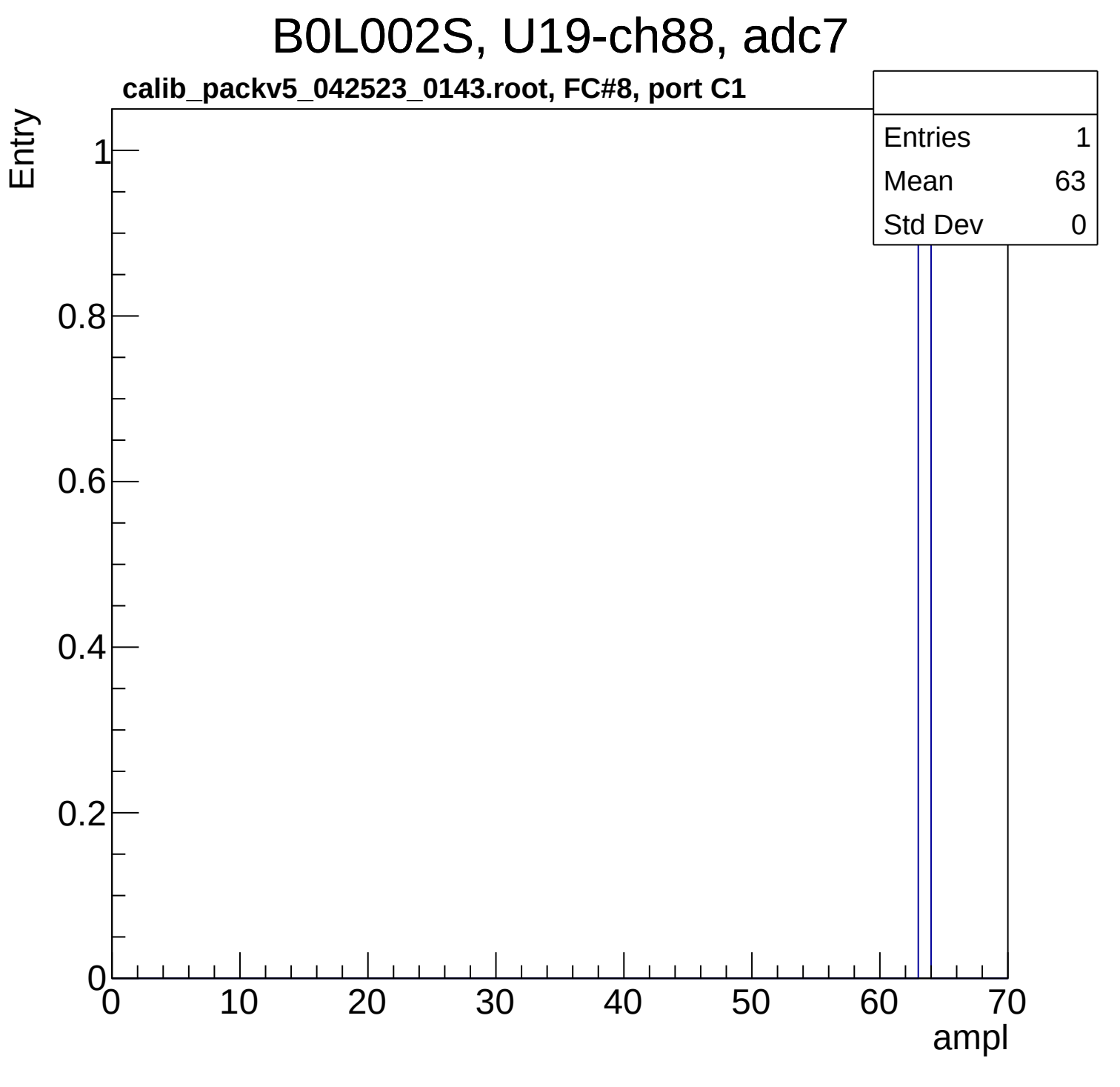

B0L002S, U19-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.49
Std Dev 3.64
Gaus mean : 45.4043
Gaus Width: 4.2632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

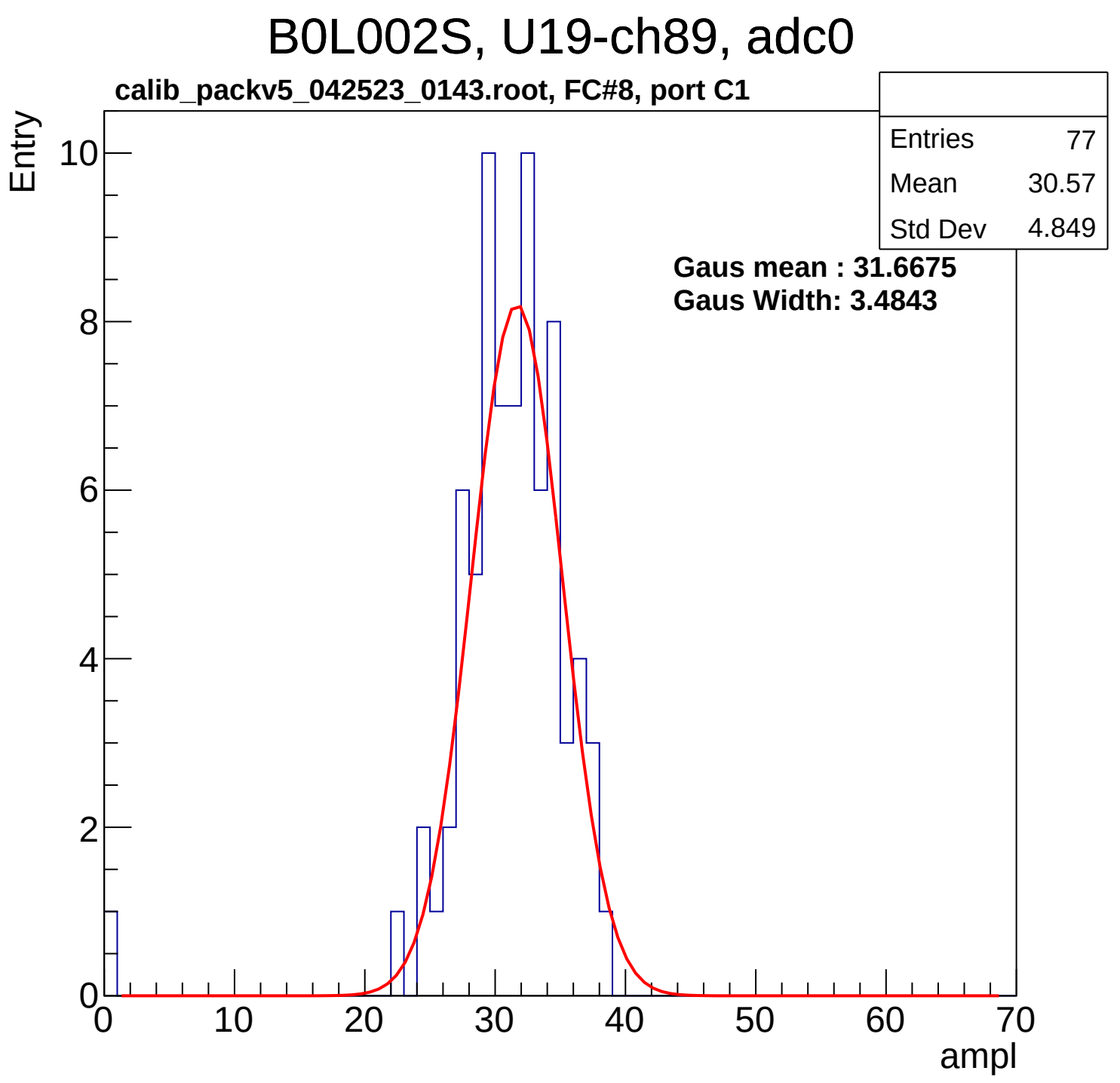

B0L002S, U19-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.4
Std Dev 3.475
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

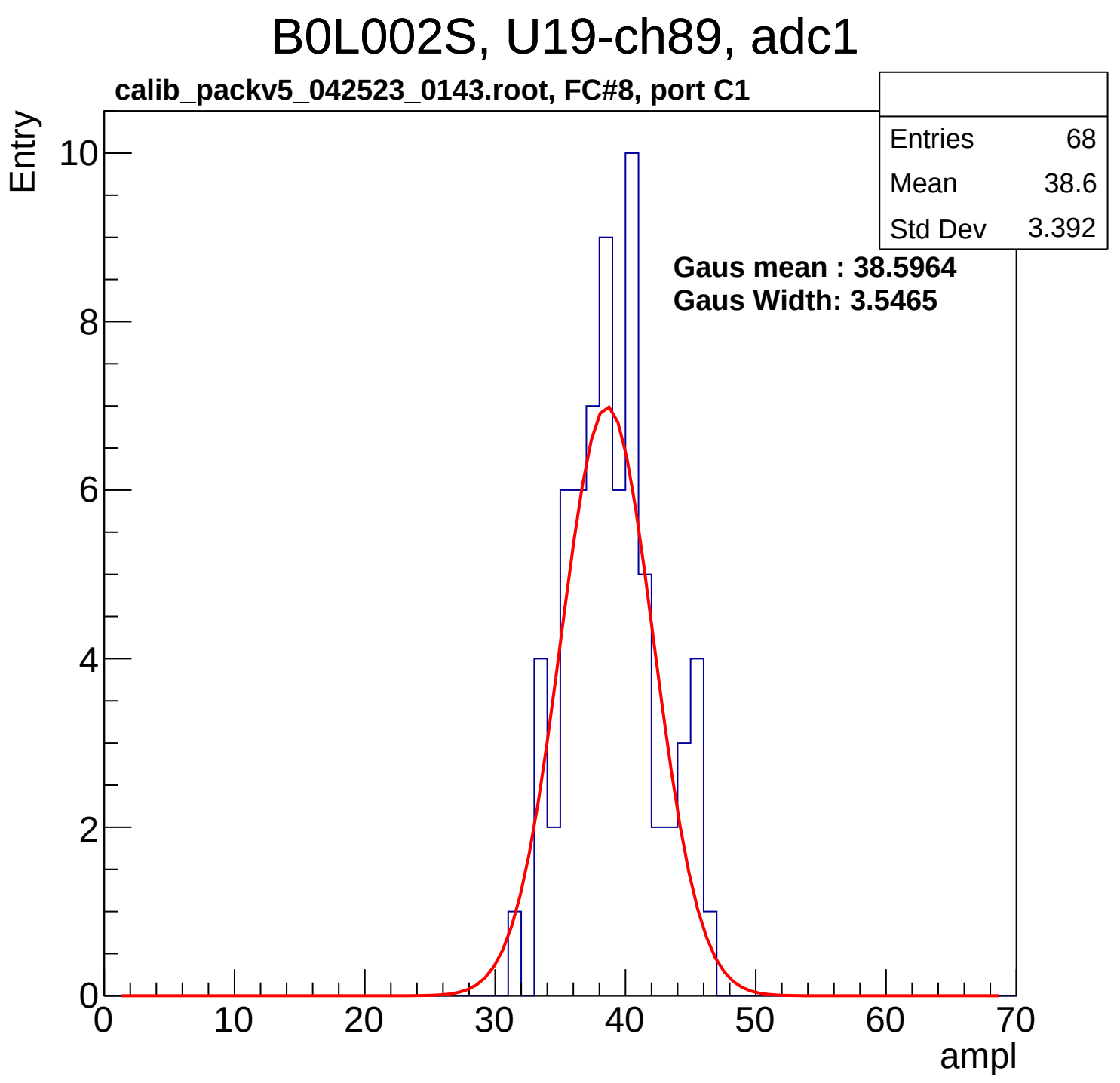

B0L002S, U19-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.93
Std Dev 2.963
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

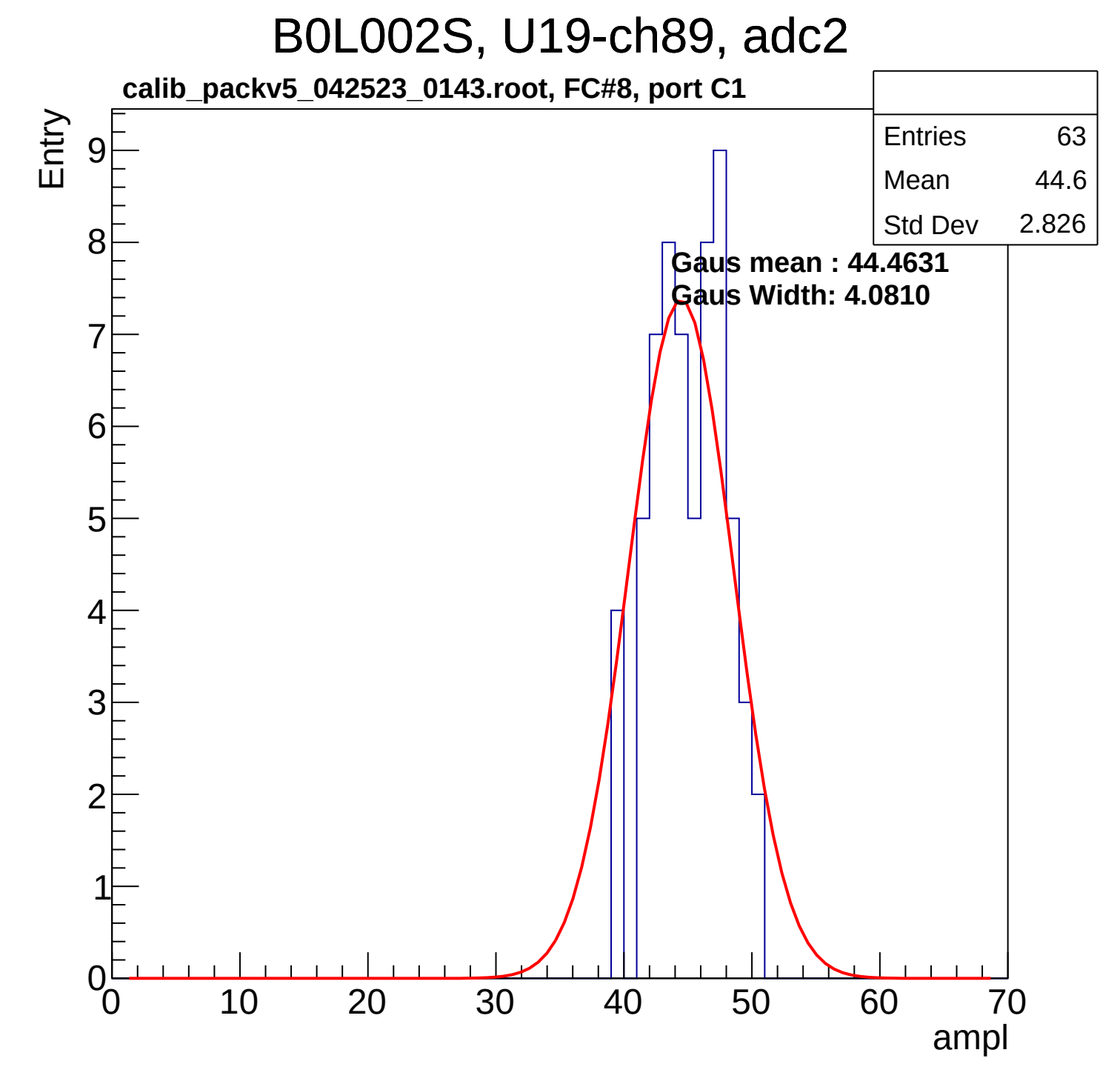

B0L002S, U19-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.66
Std Dev 10.76
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

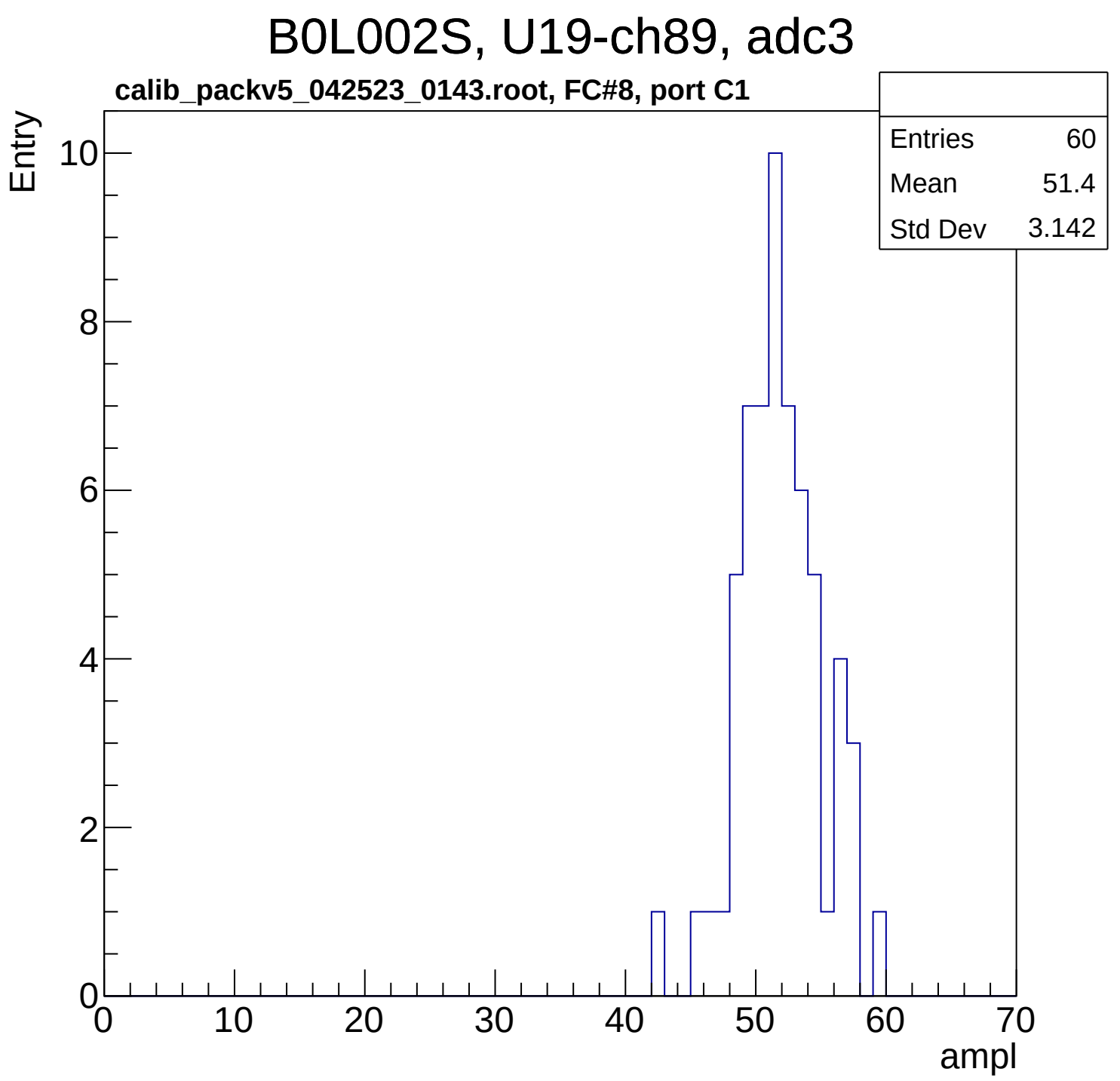

B0L002S, U19-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

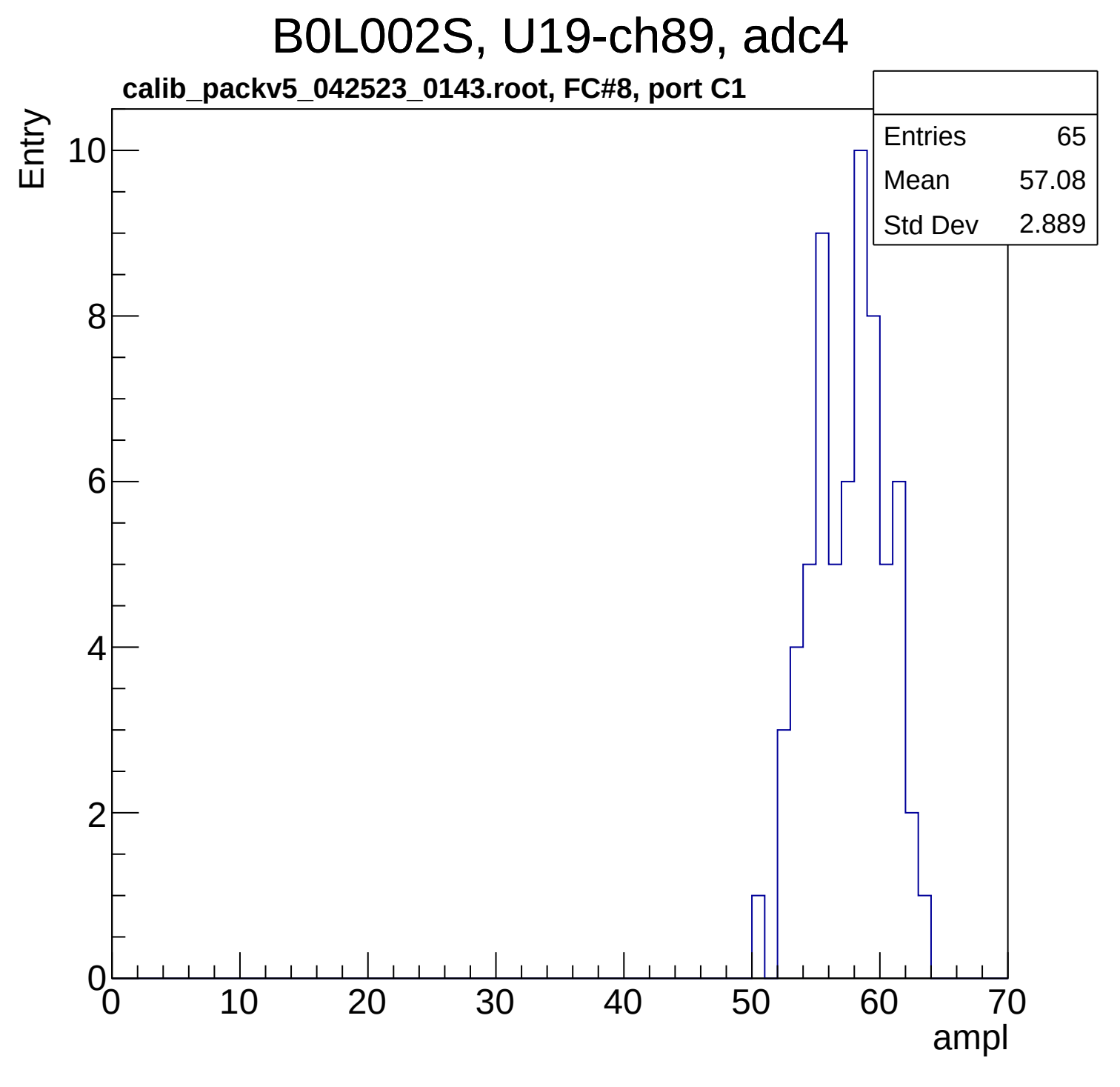

B0L002S, U19-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

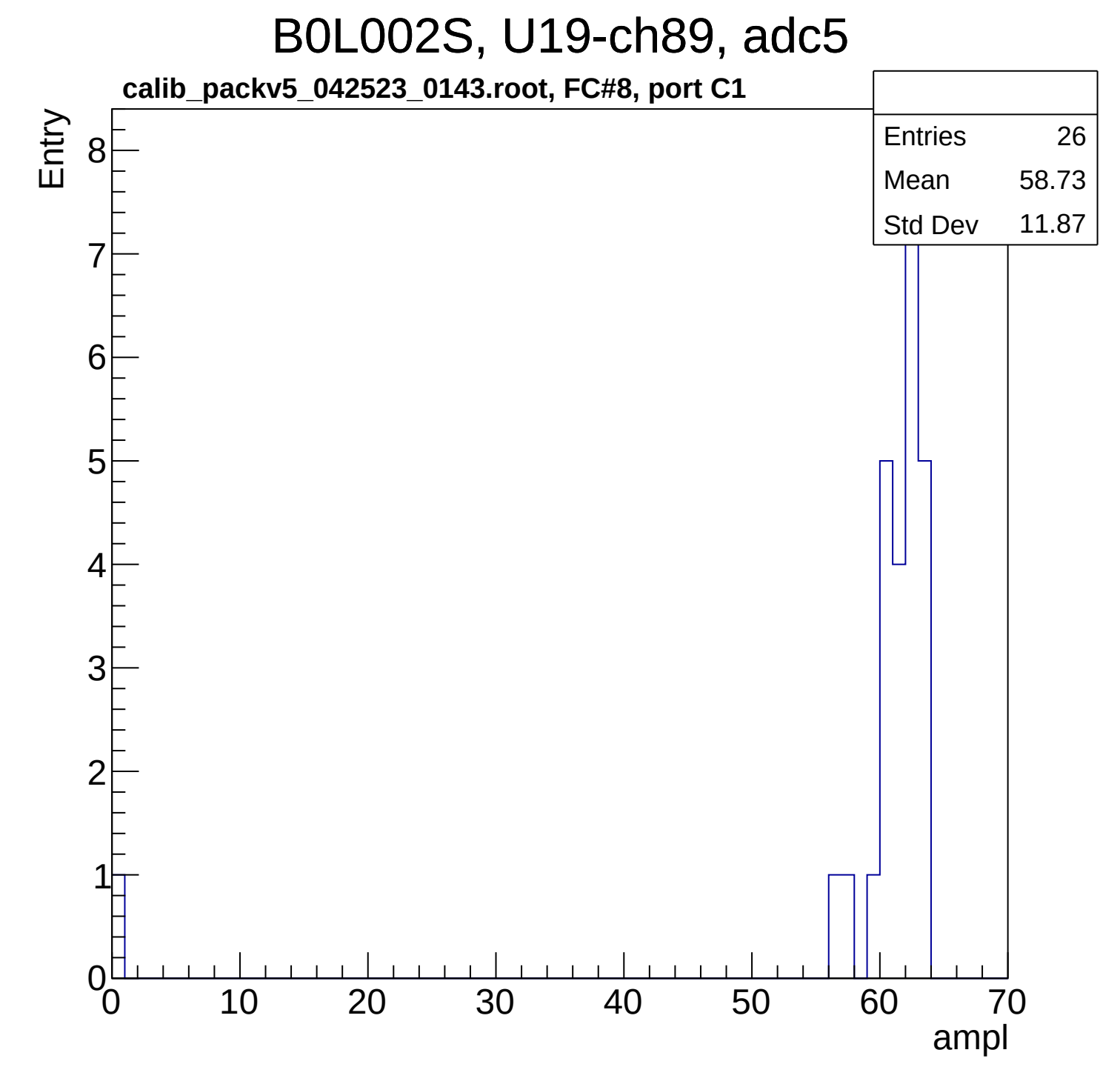

B0L002S, U19-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.57
Std Dev 4.849
Gaus mean : 31.6675
Gaus Width: 3.4843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

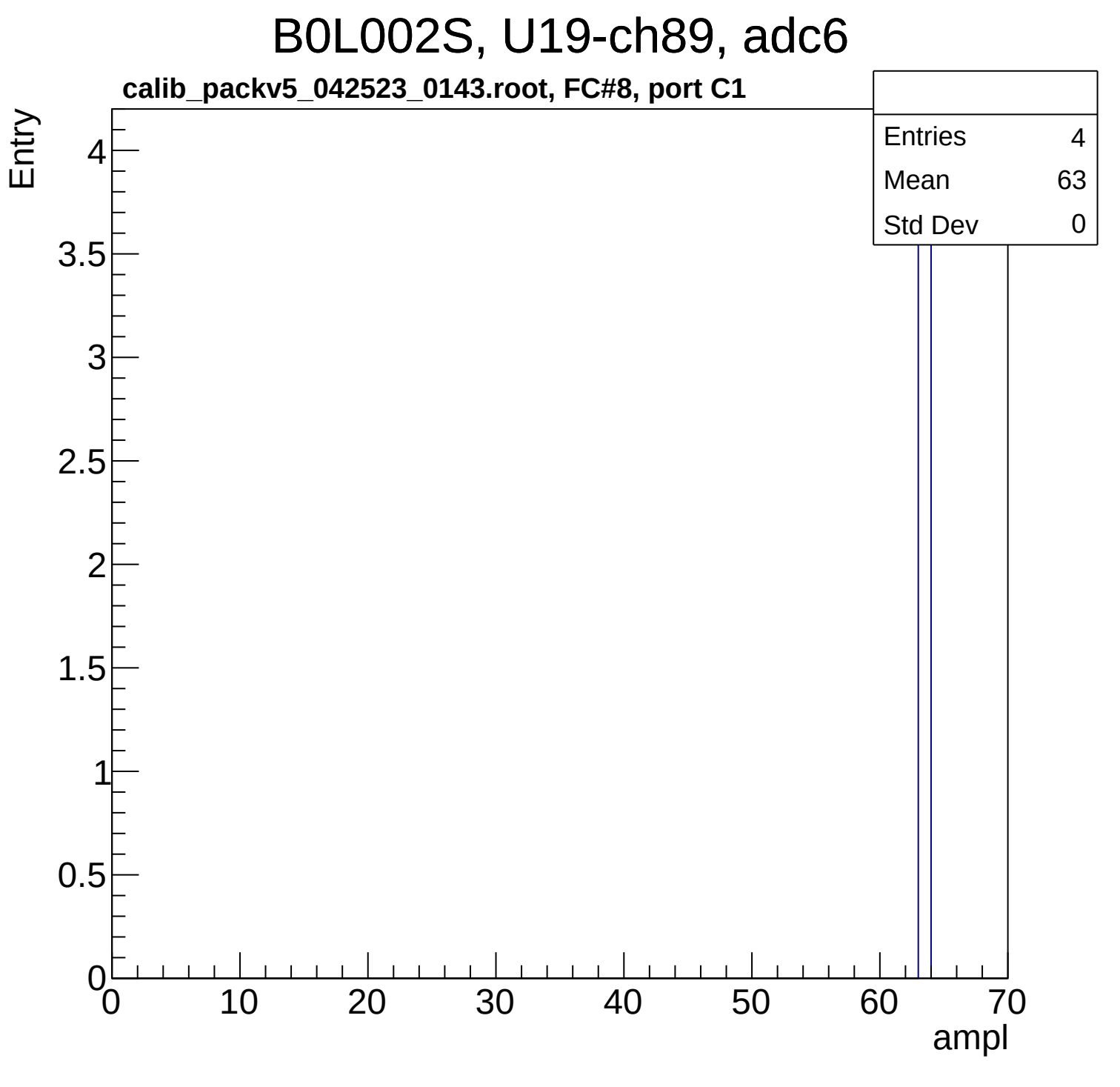

B0L002S, U19-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.6
Std Dev 3.392
Gaus mean : 38.5964
Gaus Width: 3.5465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

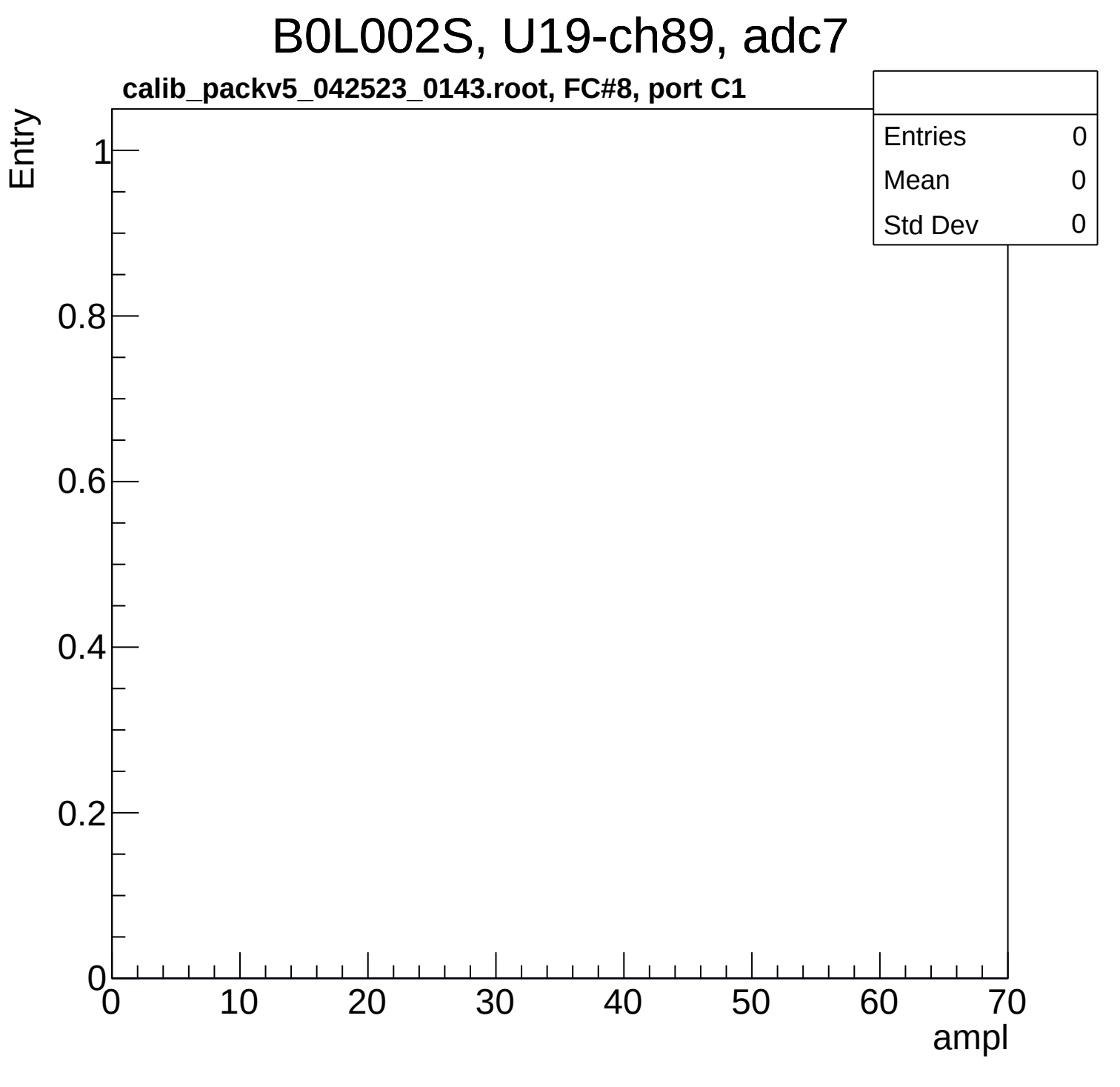

B0L002S, U19-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.6
Std Dev 2.826
Gaus mean : 44.4631
Gaus Width: 4.0810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

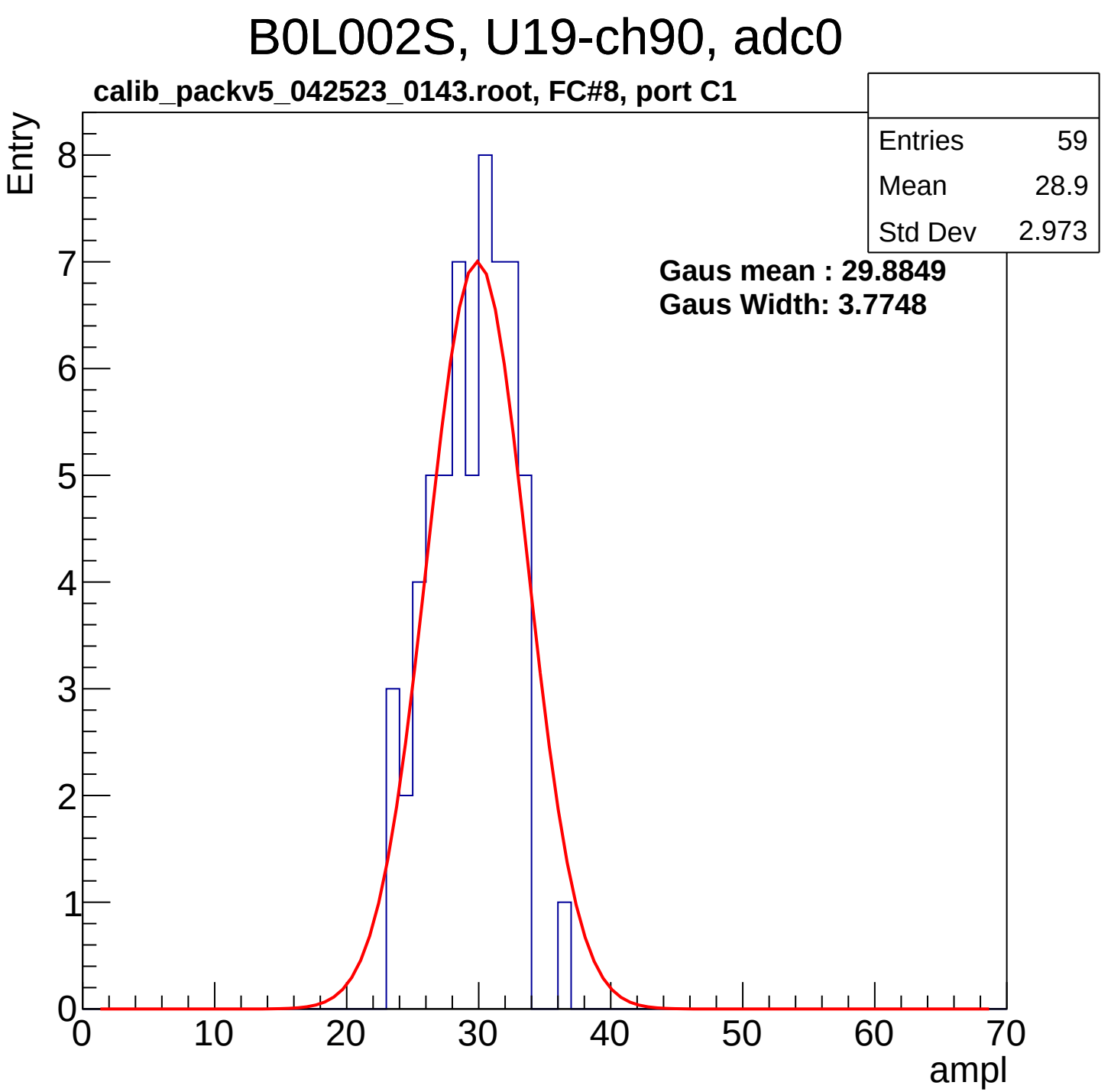

B0L002S, U19-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.4
Std Dev 3.142
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

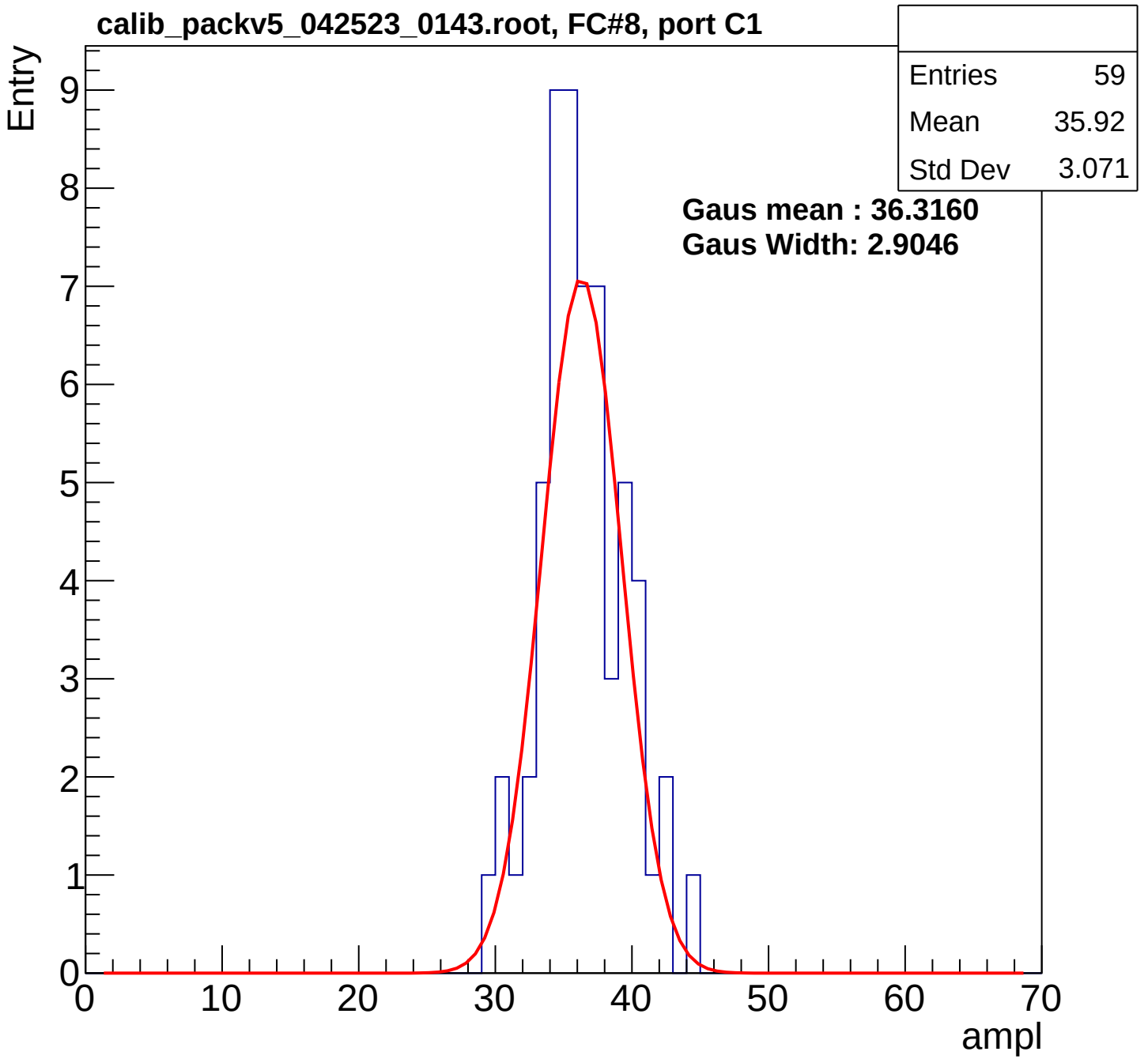

B0L002S, U19-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.08
Std Dev 2.889
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

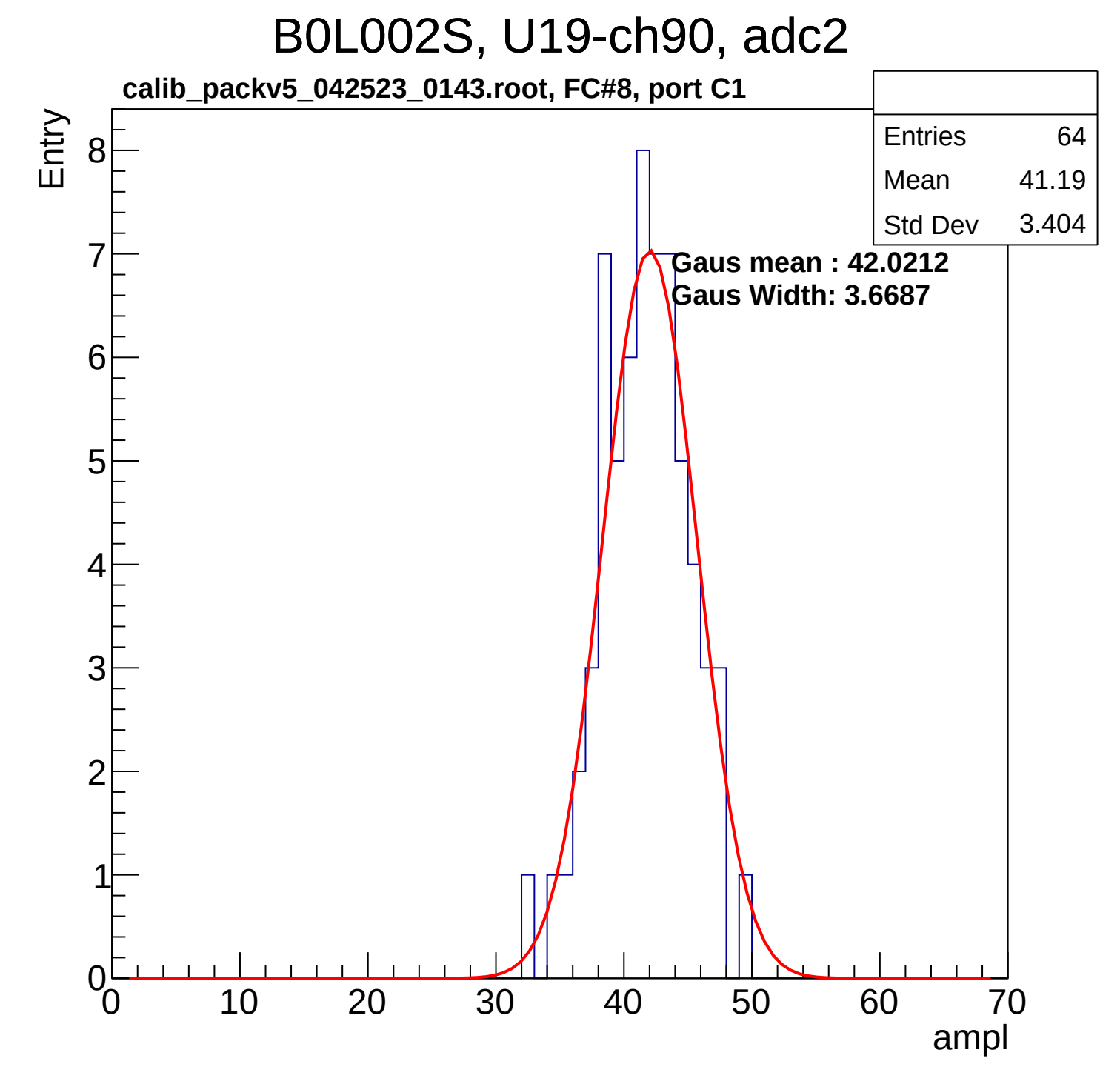

B0L002S, U19-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.73
Std Dev 11.87
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

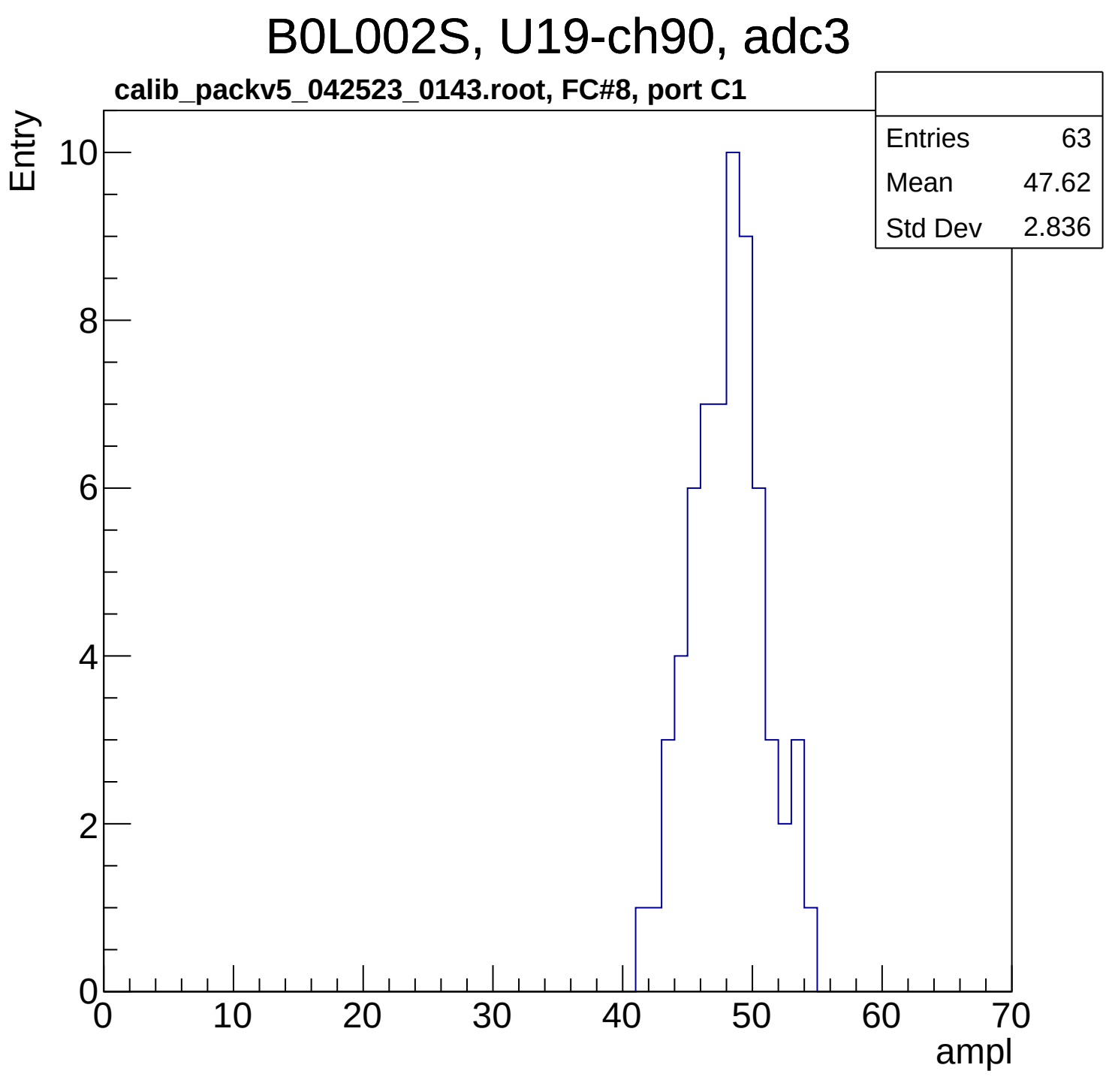

B0L002S, U19-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

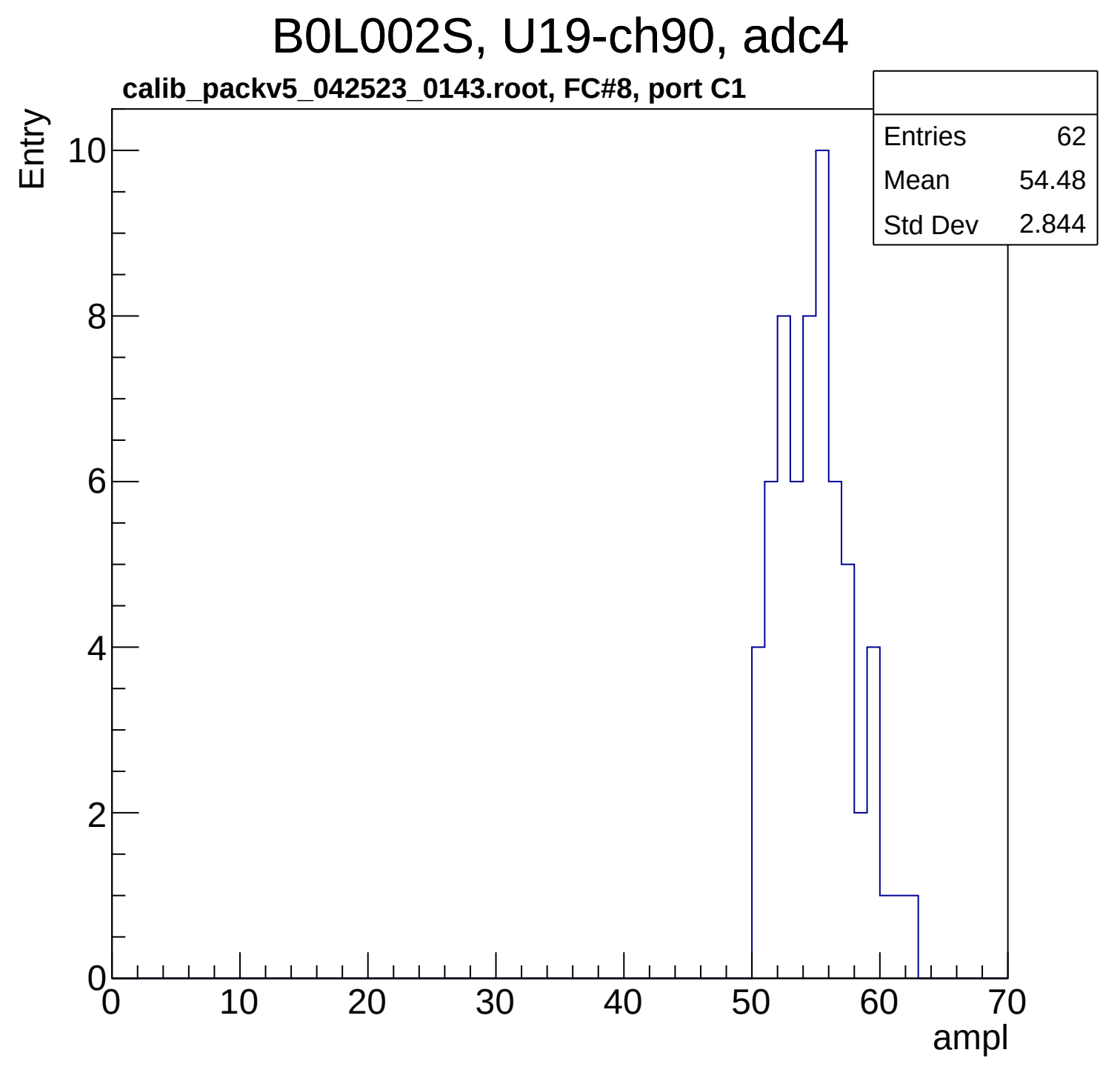

B0L002S, U19-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

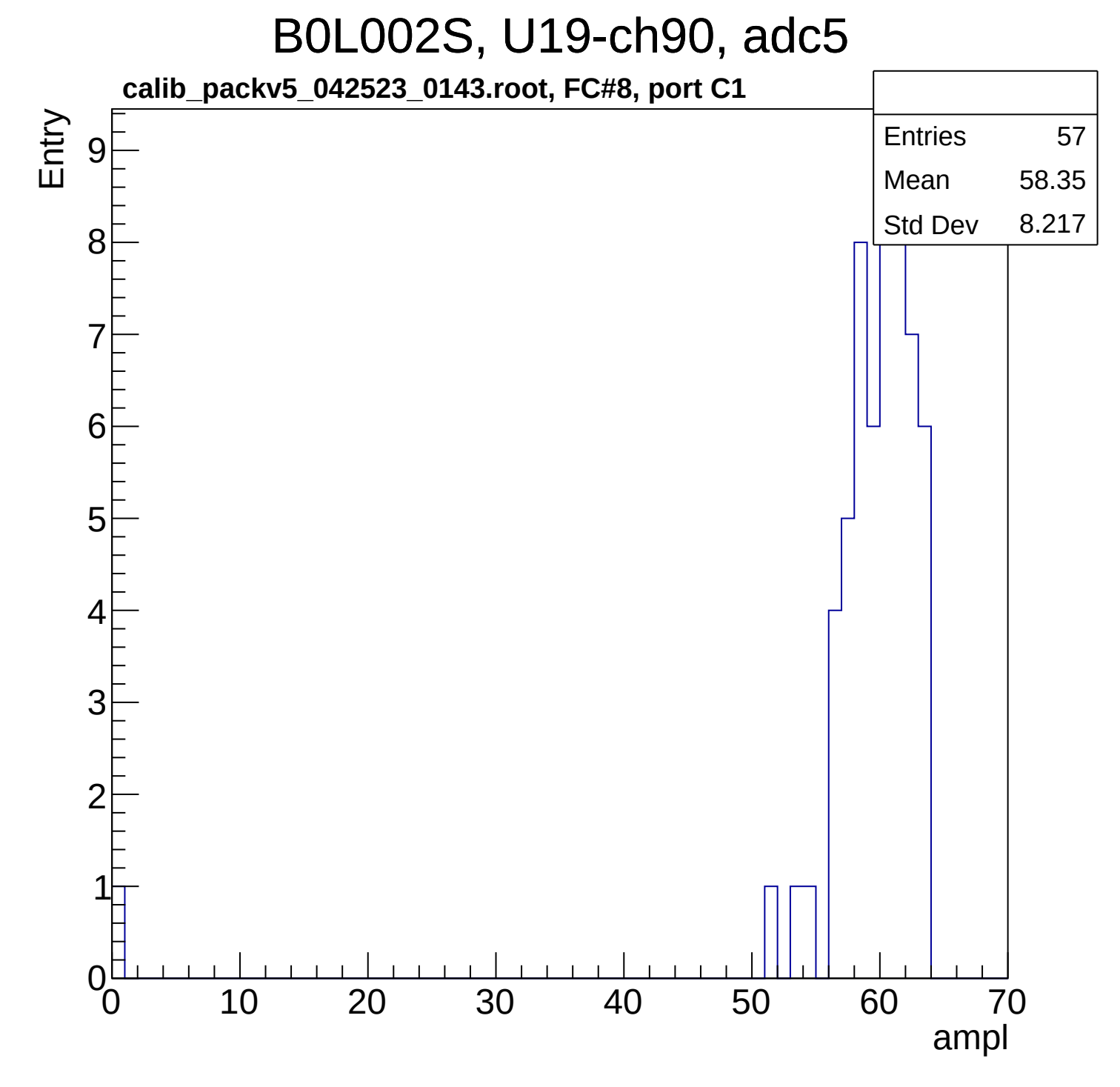

B0L002S, U19-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 28.9
Std Dev 2.973
Gaus mean : 29.8849
Gaus Width: 3.7748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

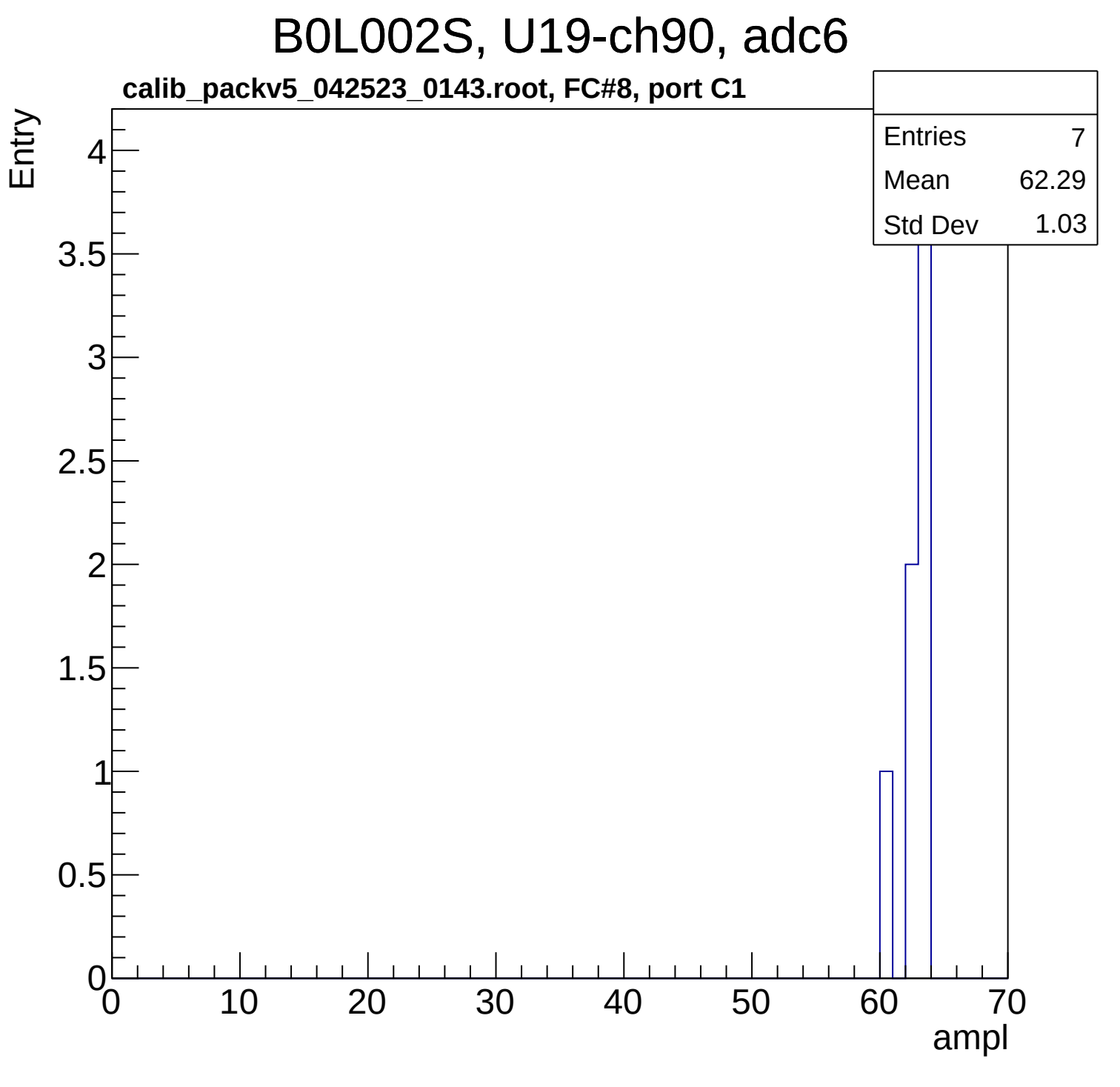

B0L002S, U19-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 35.92
Std Dev 3.071
Gaus mean : 36.3160
Gaus Width: 2.9046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

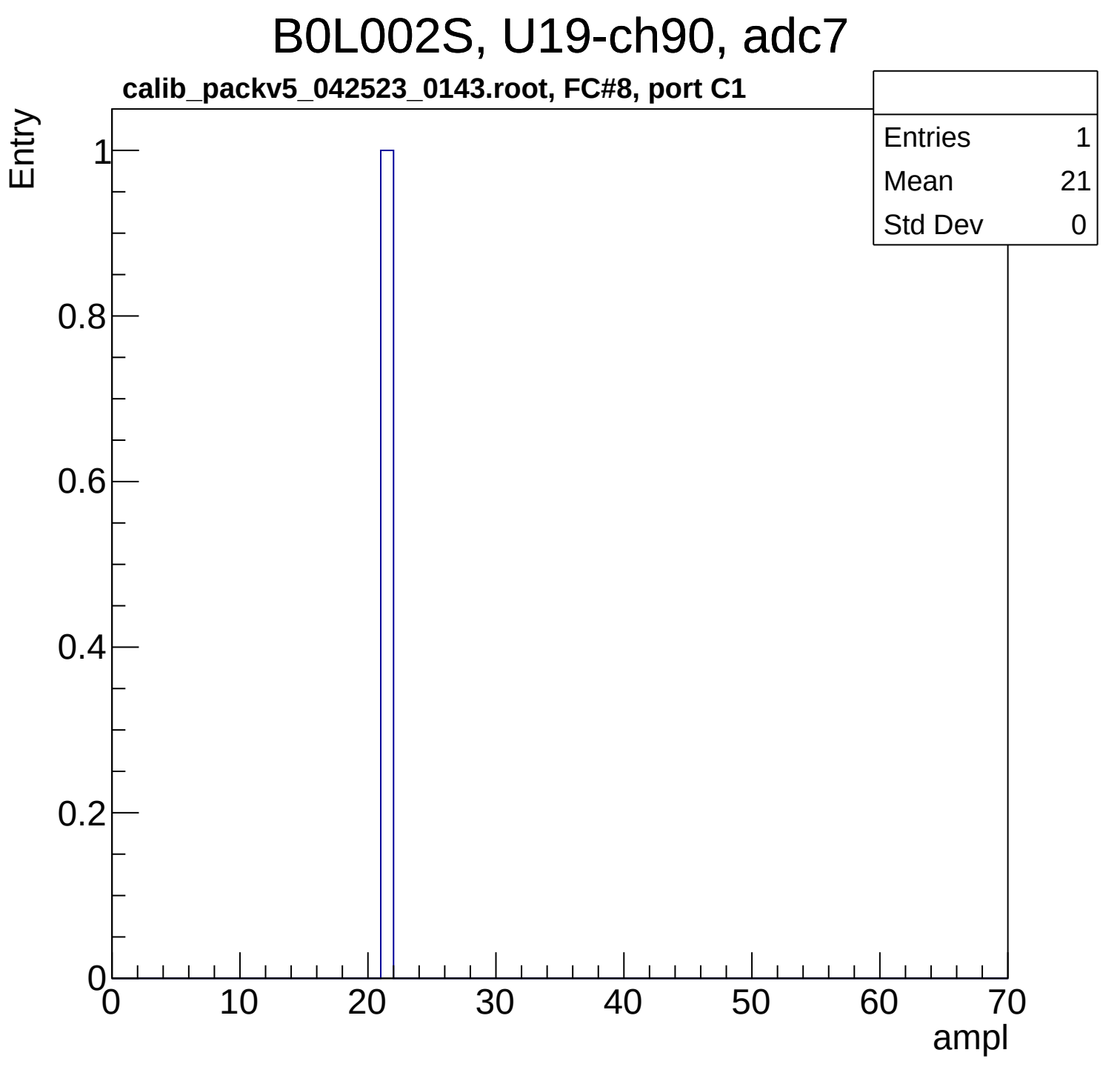

B0L002S, U19-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 41.19
Std Dev 3.404
Gaus mean : 42.0212
Gaus Width: 3.6687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

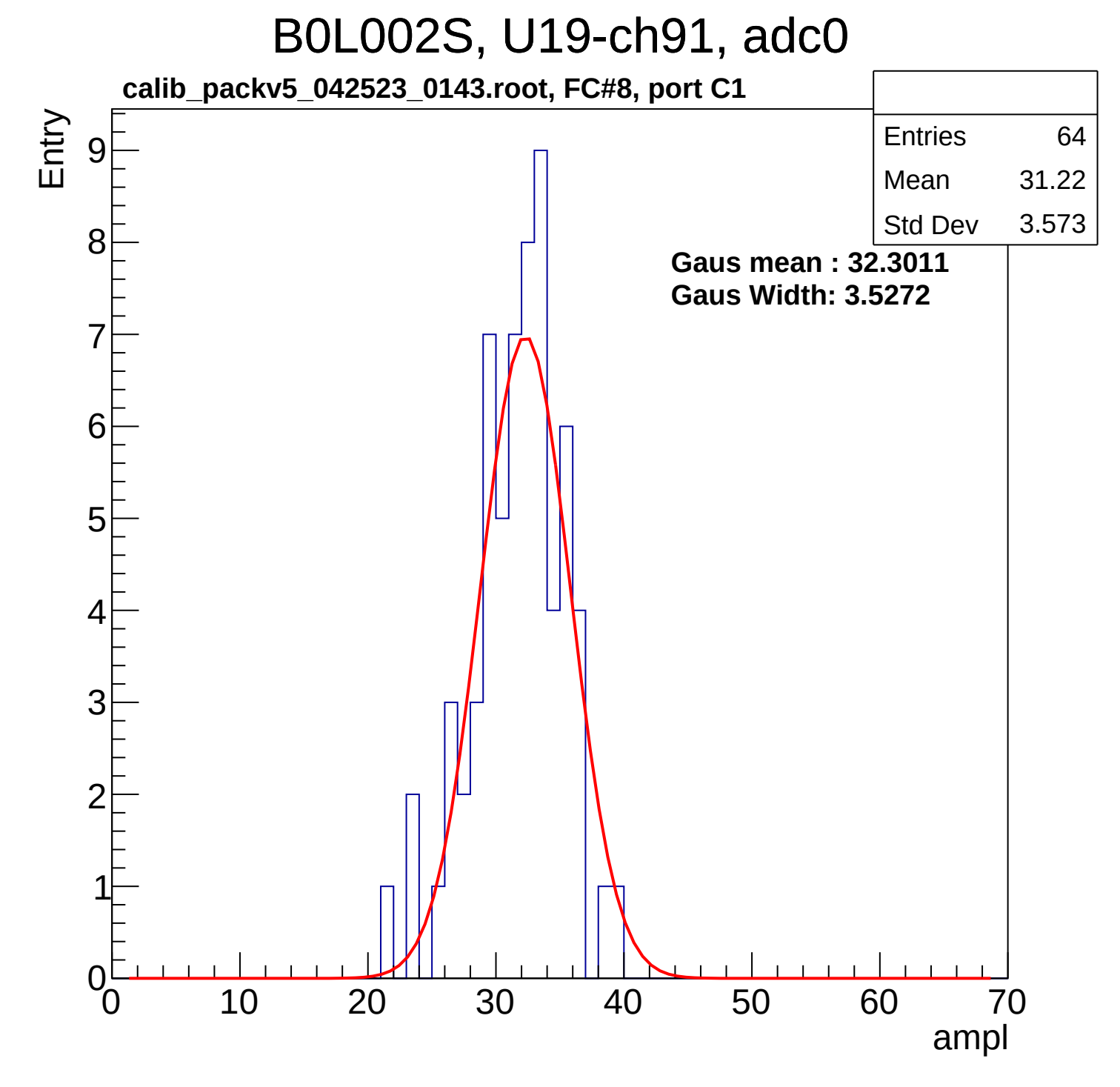

B0L002S, U19-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 47.62
Std Dev 2.836
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

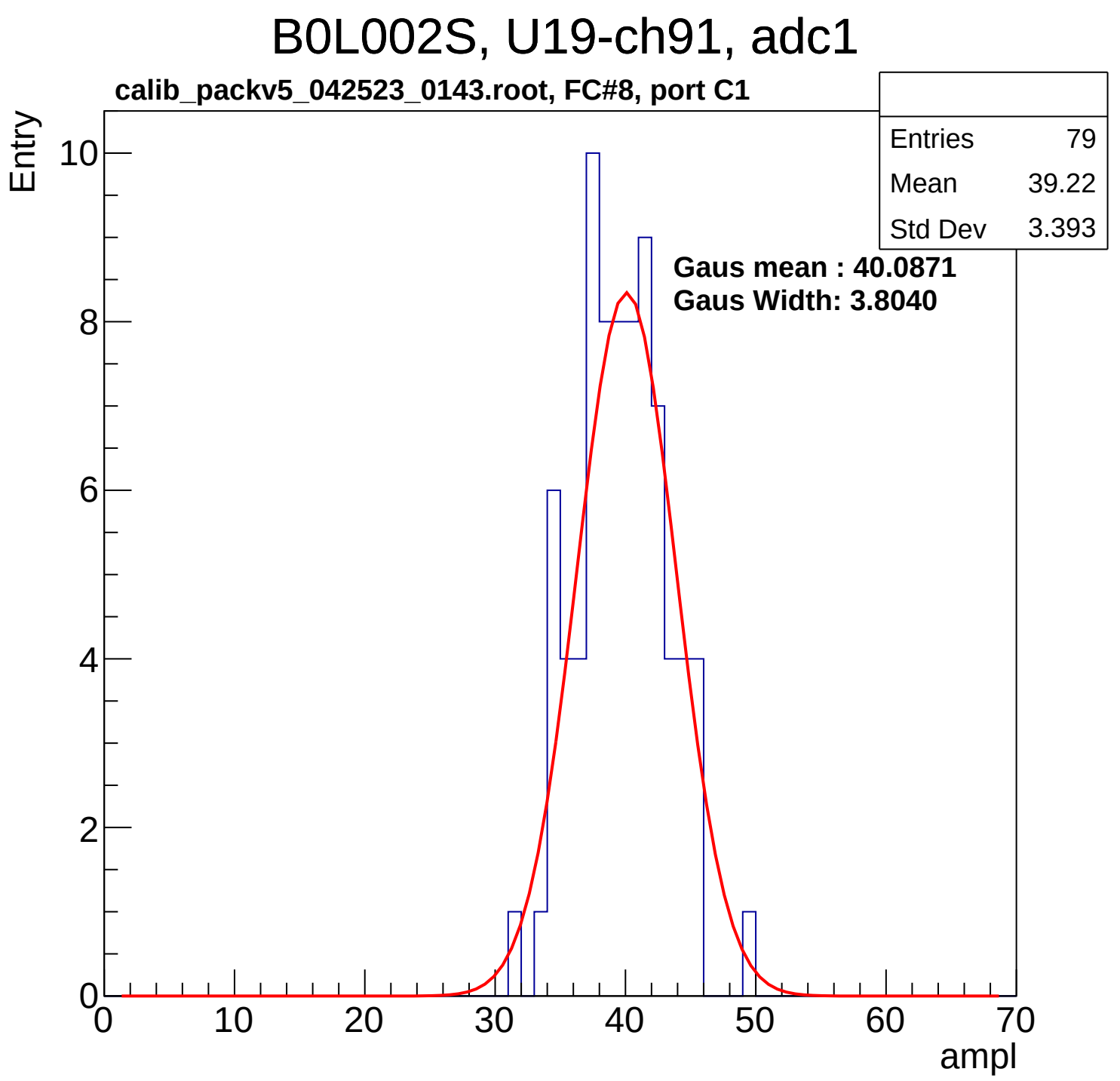

B0L002S, U19-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 54.48
Std Dev 2.844
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

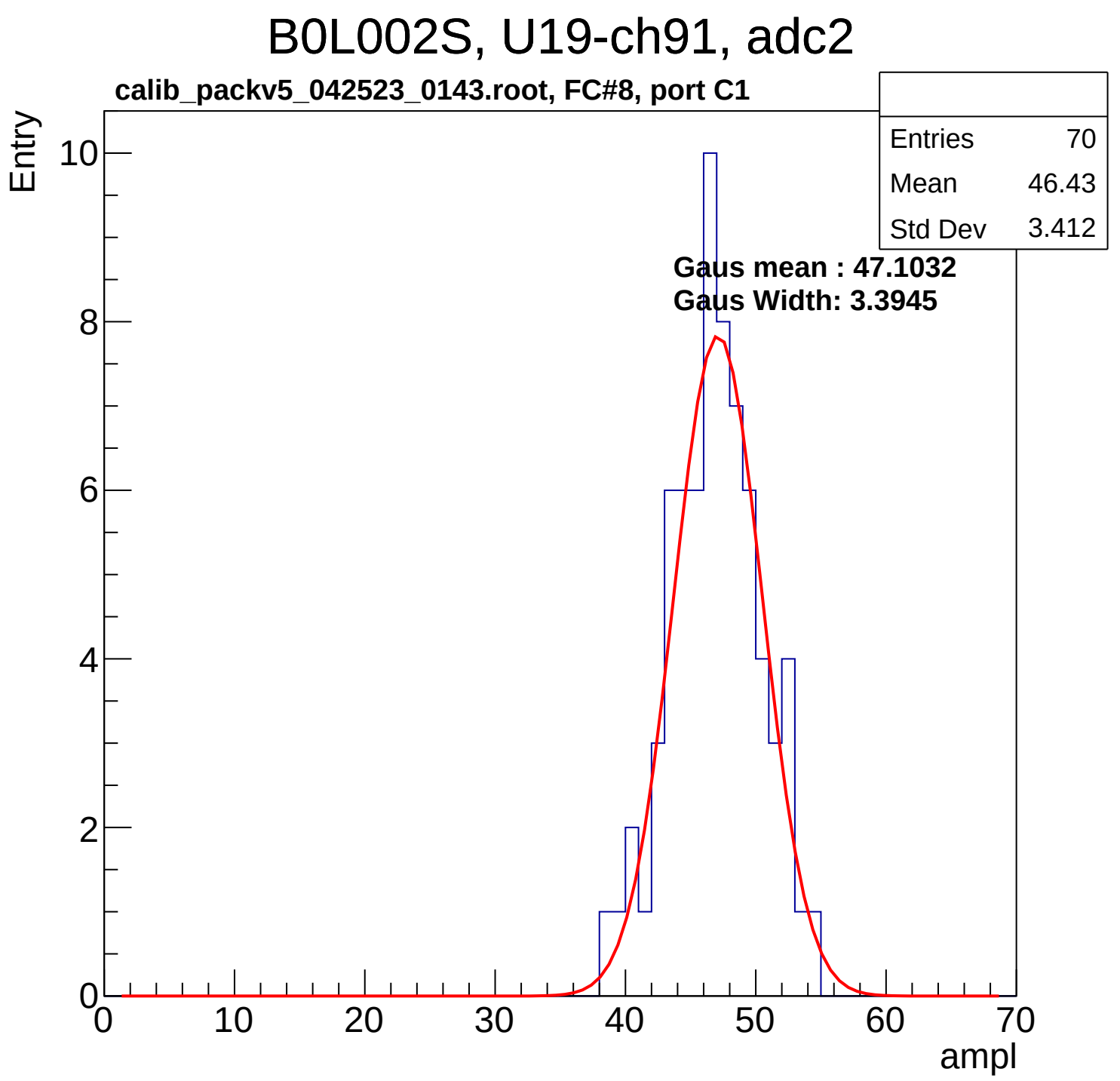

B0L002S, U19-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 58.35
Std Dev 8.217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

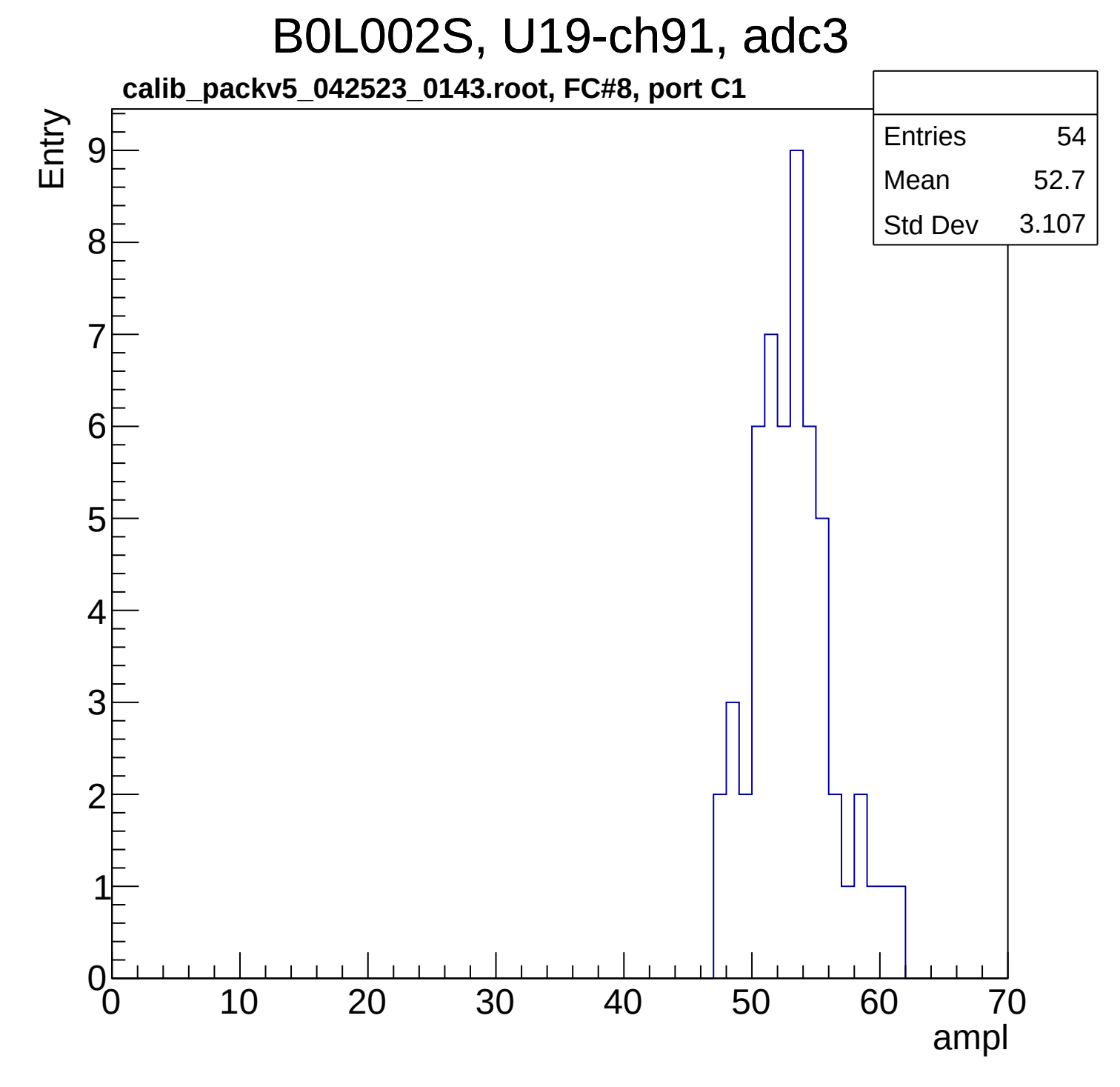

B0L002S, U19-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.29
Std Dev 1.03
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

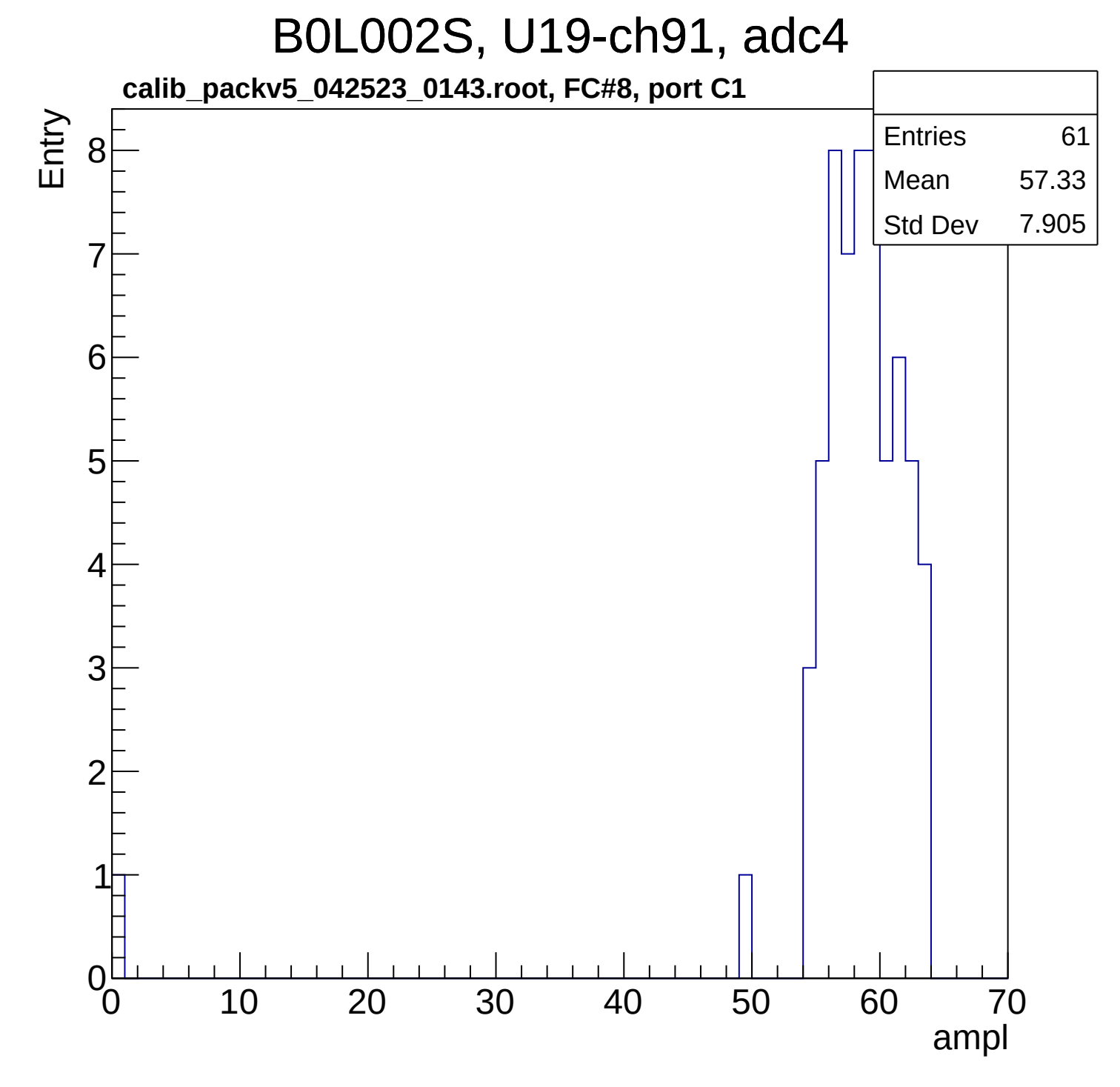

B0L002S, U19-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

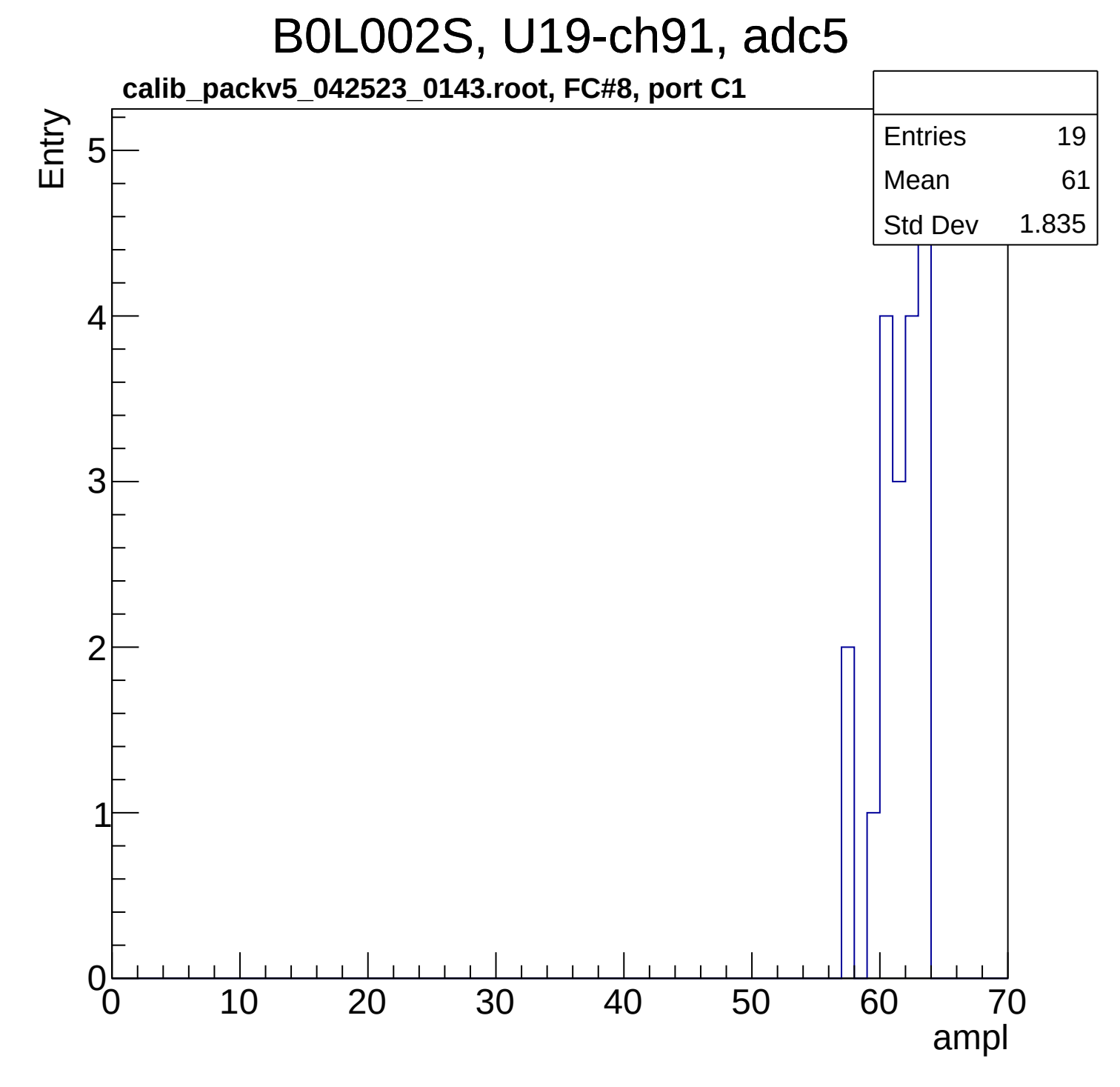

B0L002S, U19-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.22
Std Dev 3.573
Gaus mean : 32.3011
Gaus Width: 3.5272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

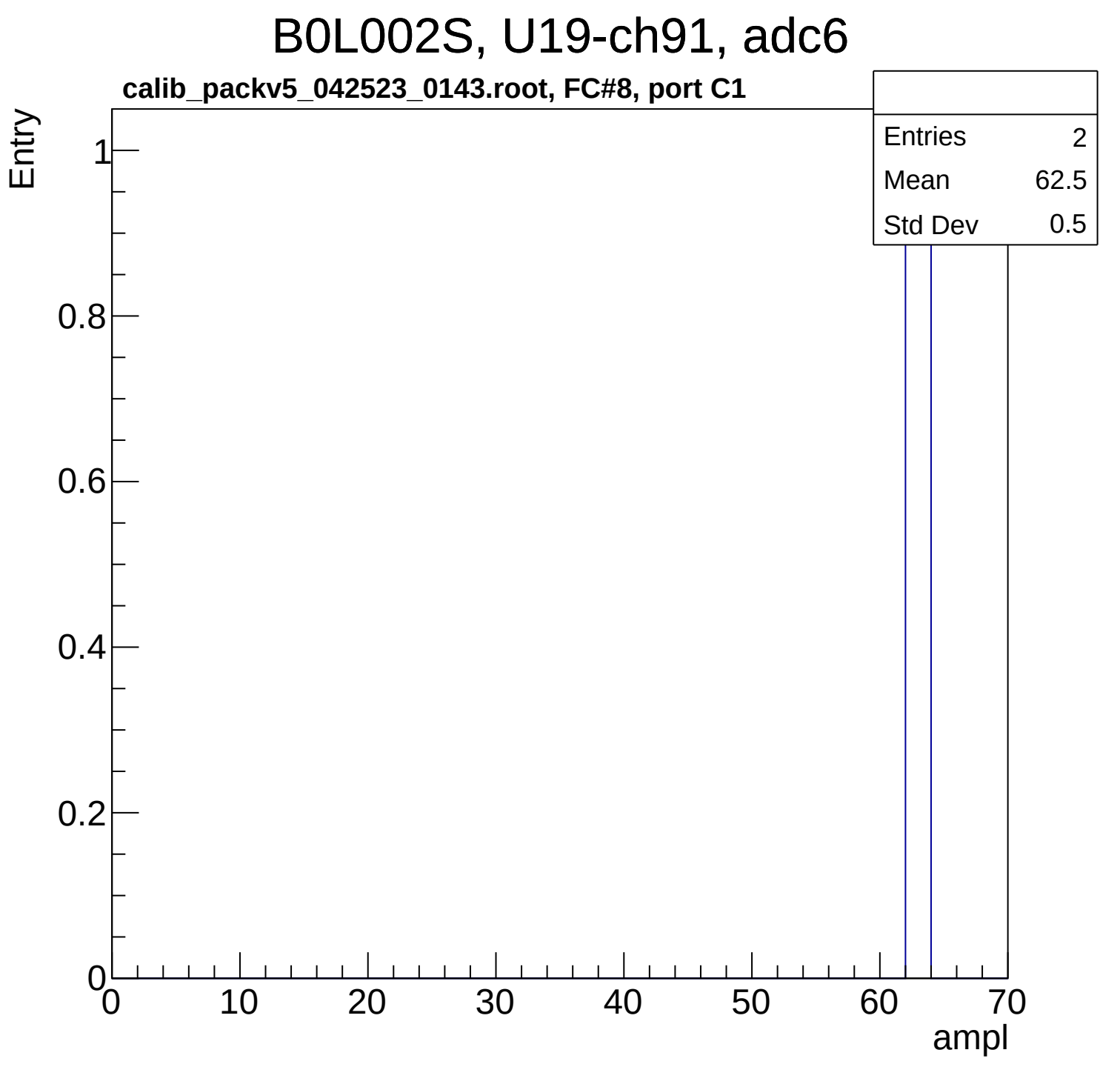

B0L002S, U19-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 39.22
Std Dev 3.393
Gaus mean : 40.0871
Gaus Width: 3.8040
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

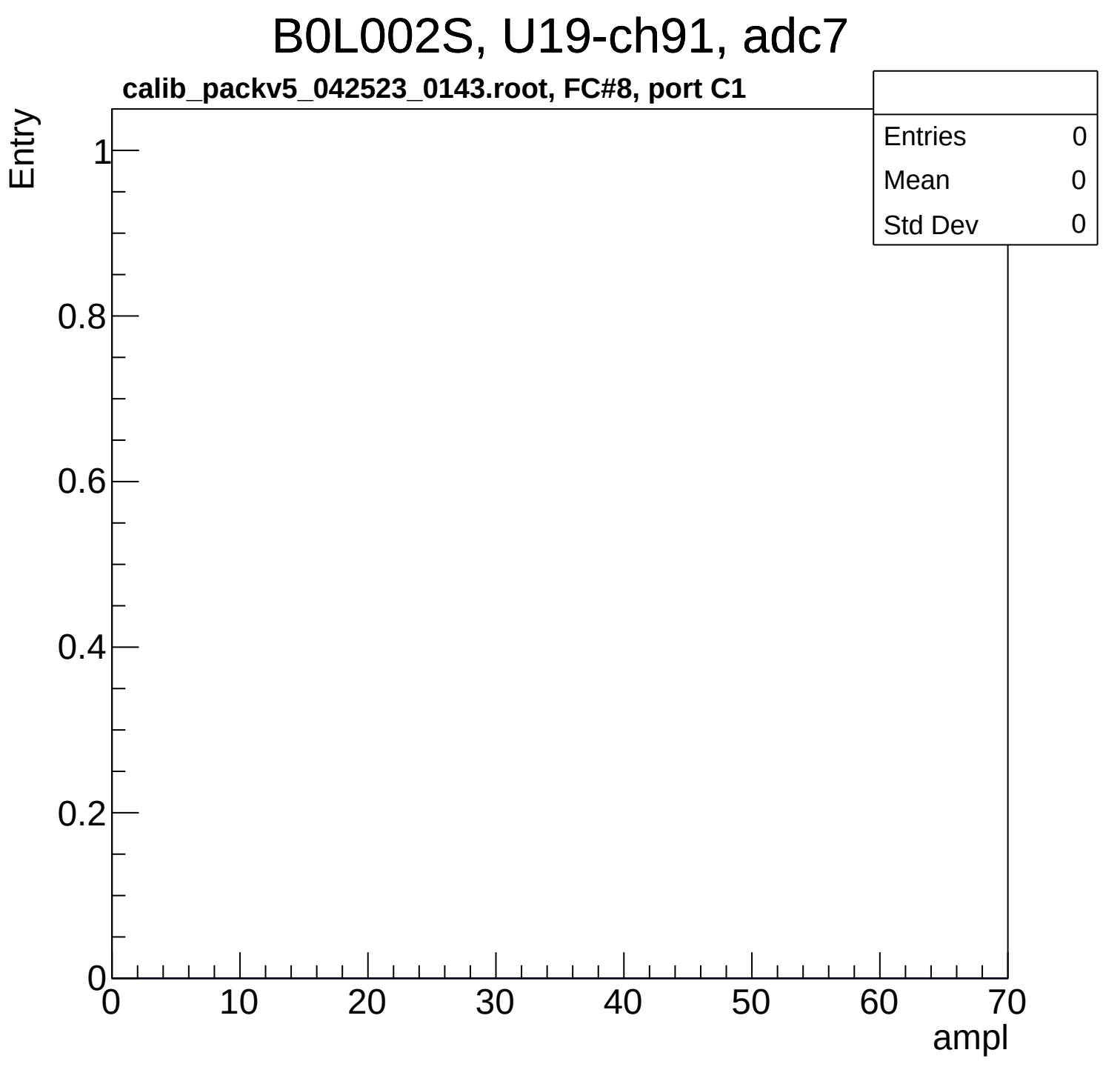

B0L002S, U19-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 46.43
Std Dev 3.412
Gaus mean : 47.1032
Gaus Width: 3.3945
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

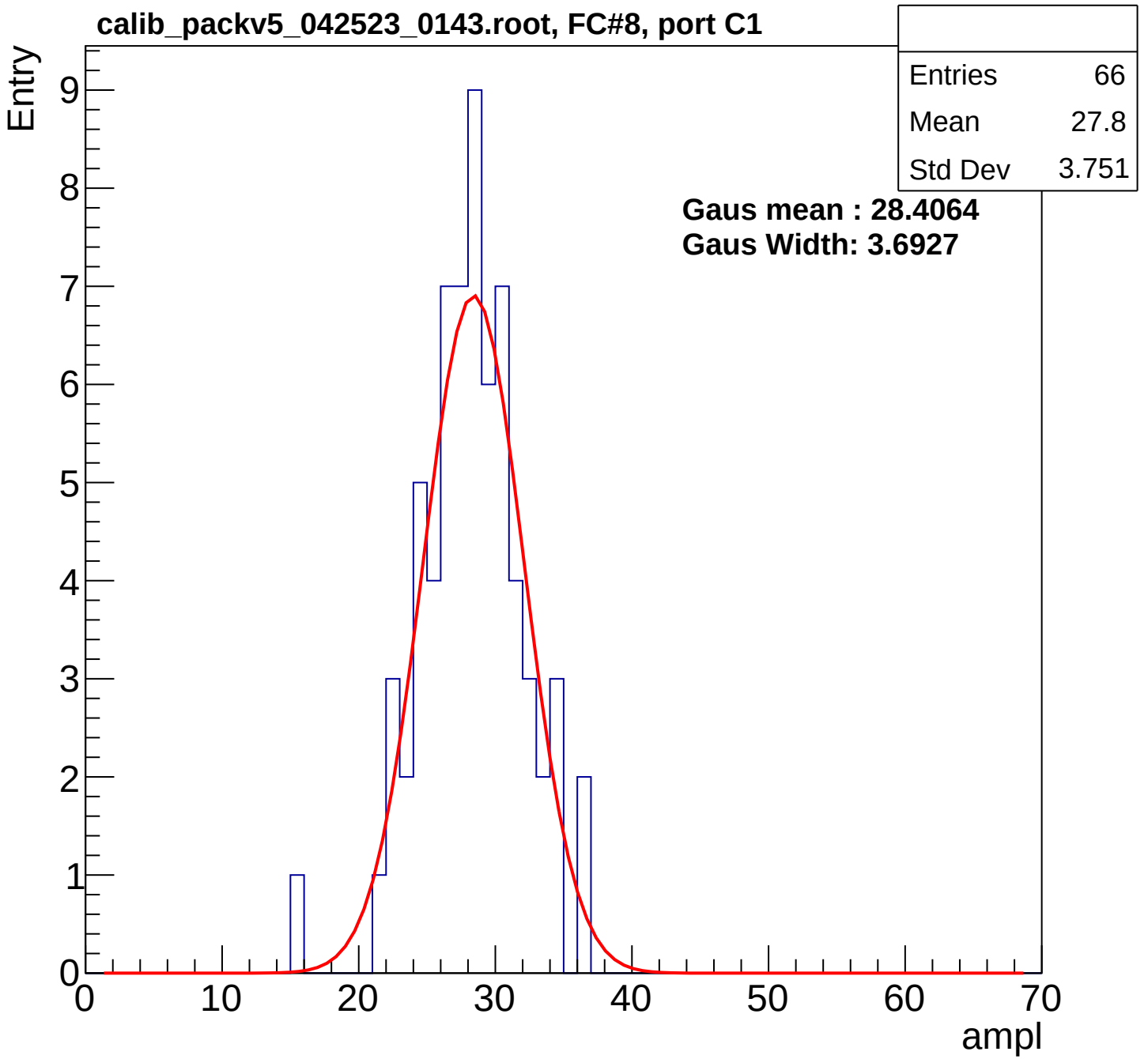

B0L002S, U19-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 52.7
Std Dev 3.107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

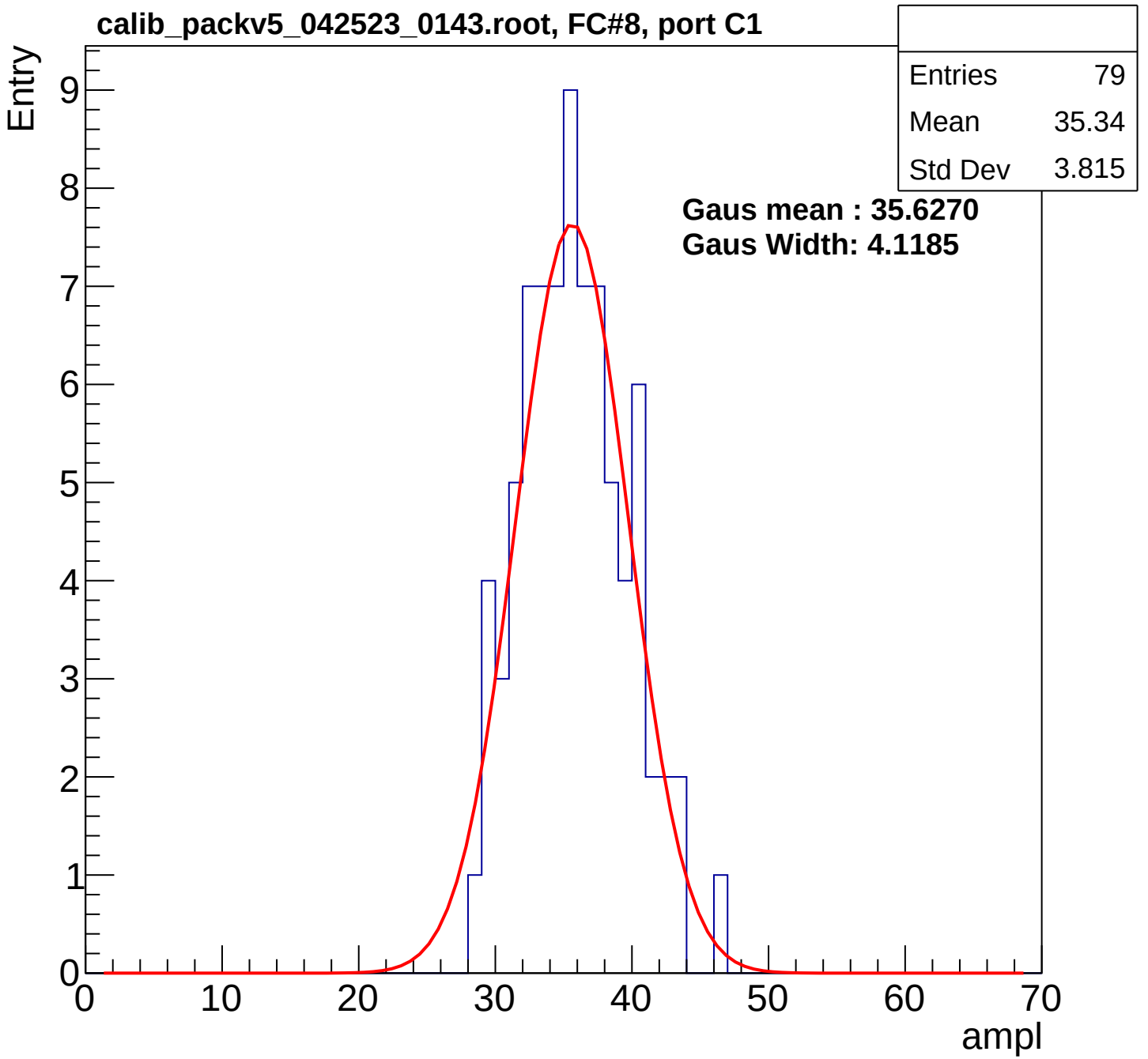

B0L002S, U19-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.33
Std Dev 7.905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

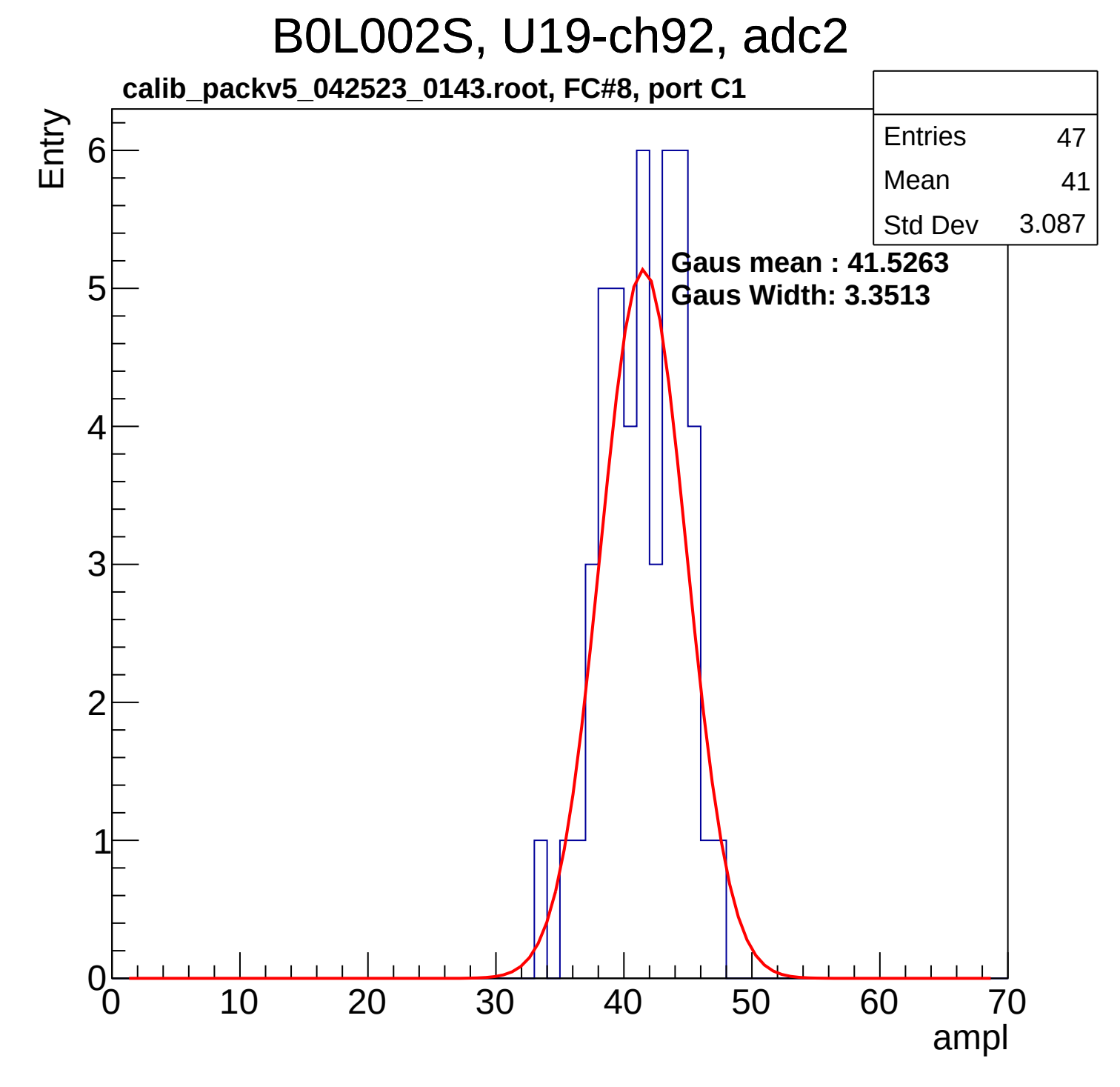

B0L002S, U19-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61
Std Dev 1.835
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

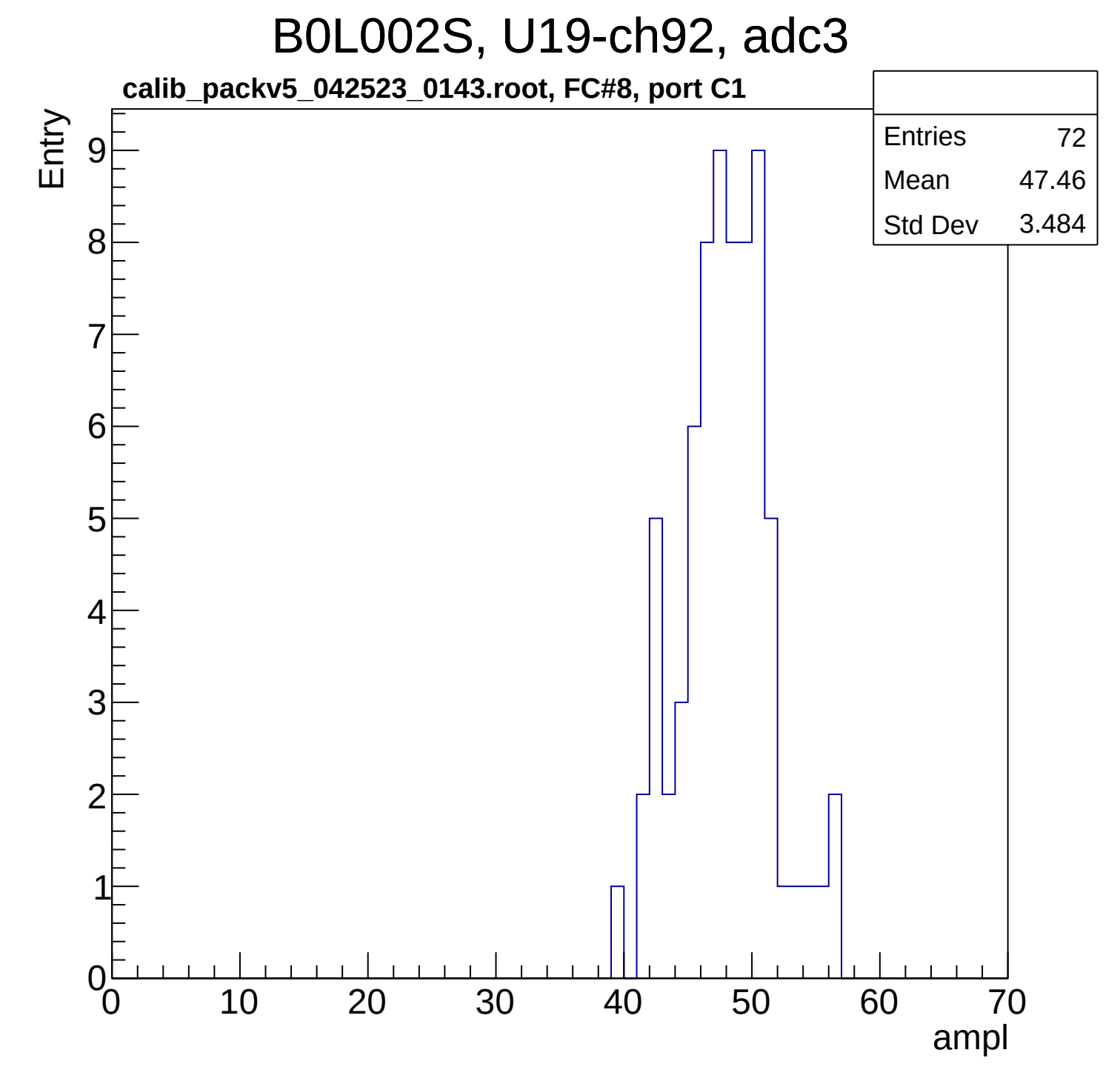

B0L002S, U19-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

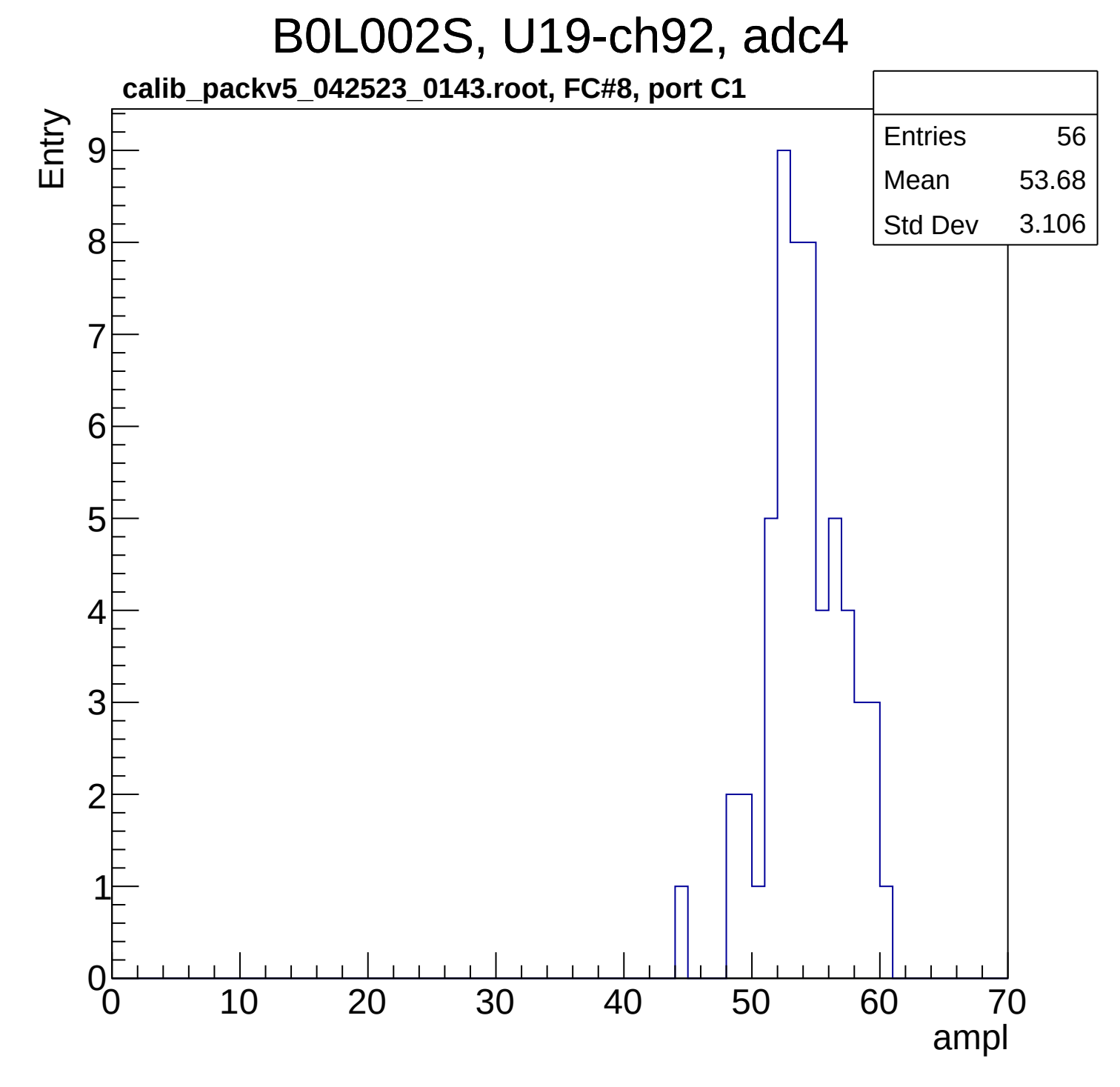

B0L002S, U19-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

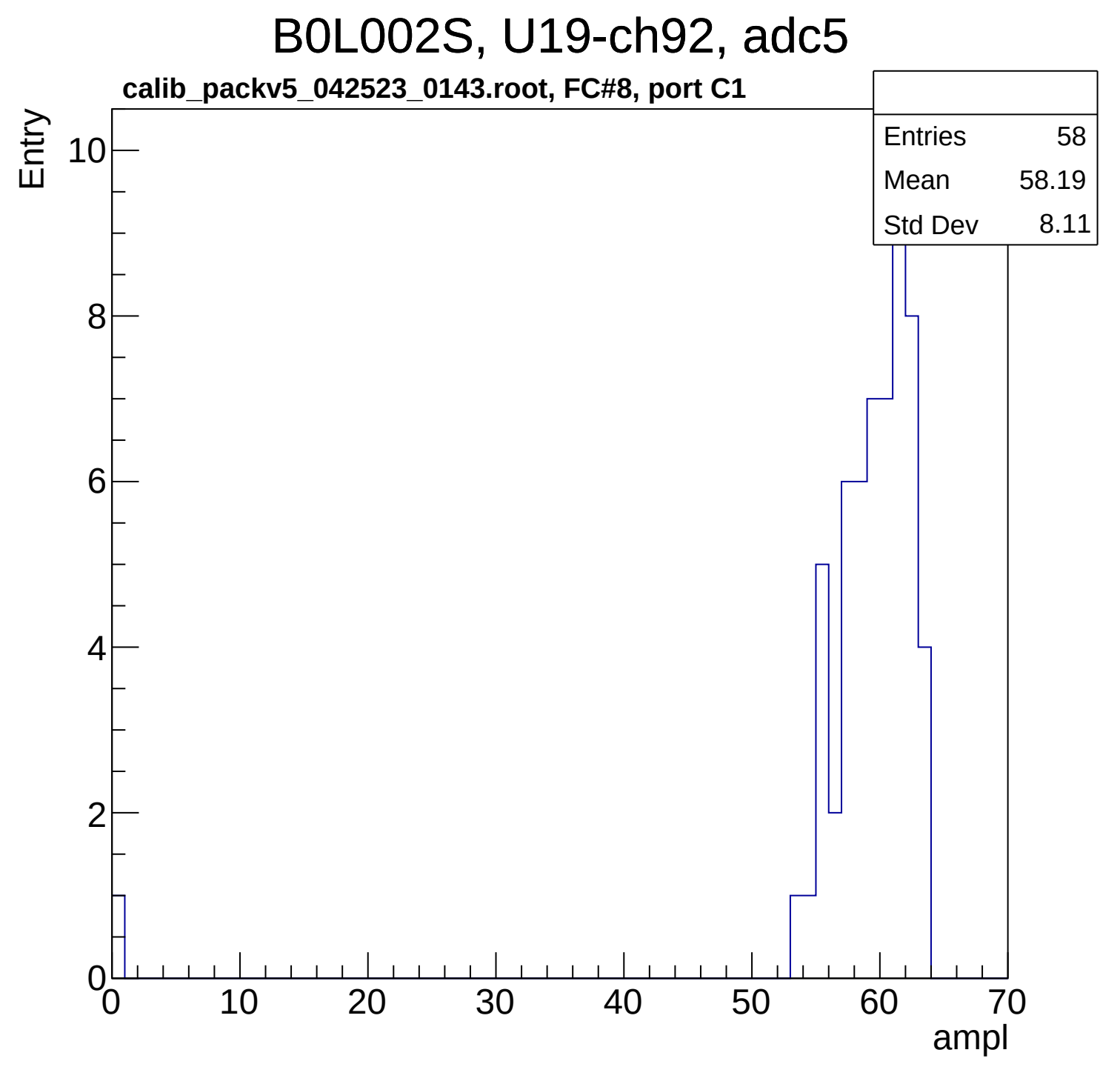

B0L002S, U19-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 27.8
Std Dev 3.751
Gaus mean : 28.4064
Gaus Width: 3.6927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

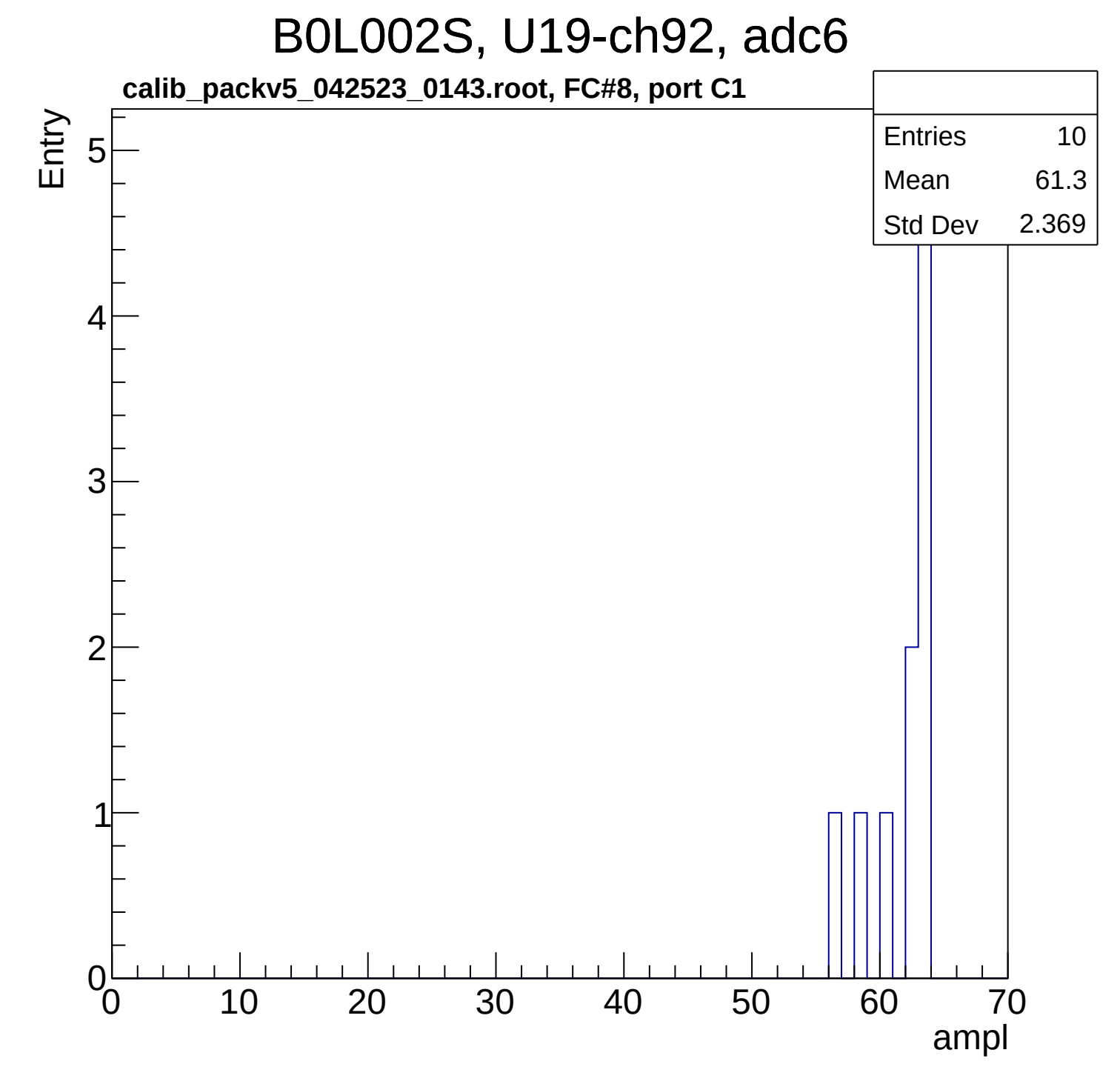

B0L002S, U19-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 35.34
Std Dev 3.815
Gaus mean : 35.6270
Gaus Width: 4.1185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

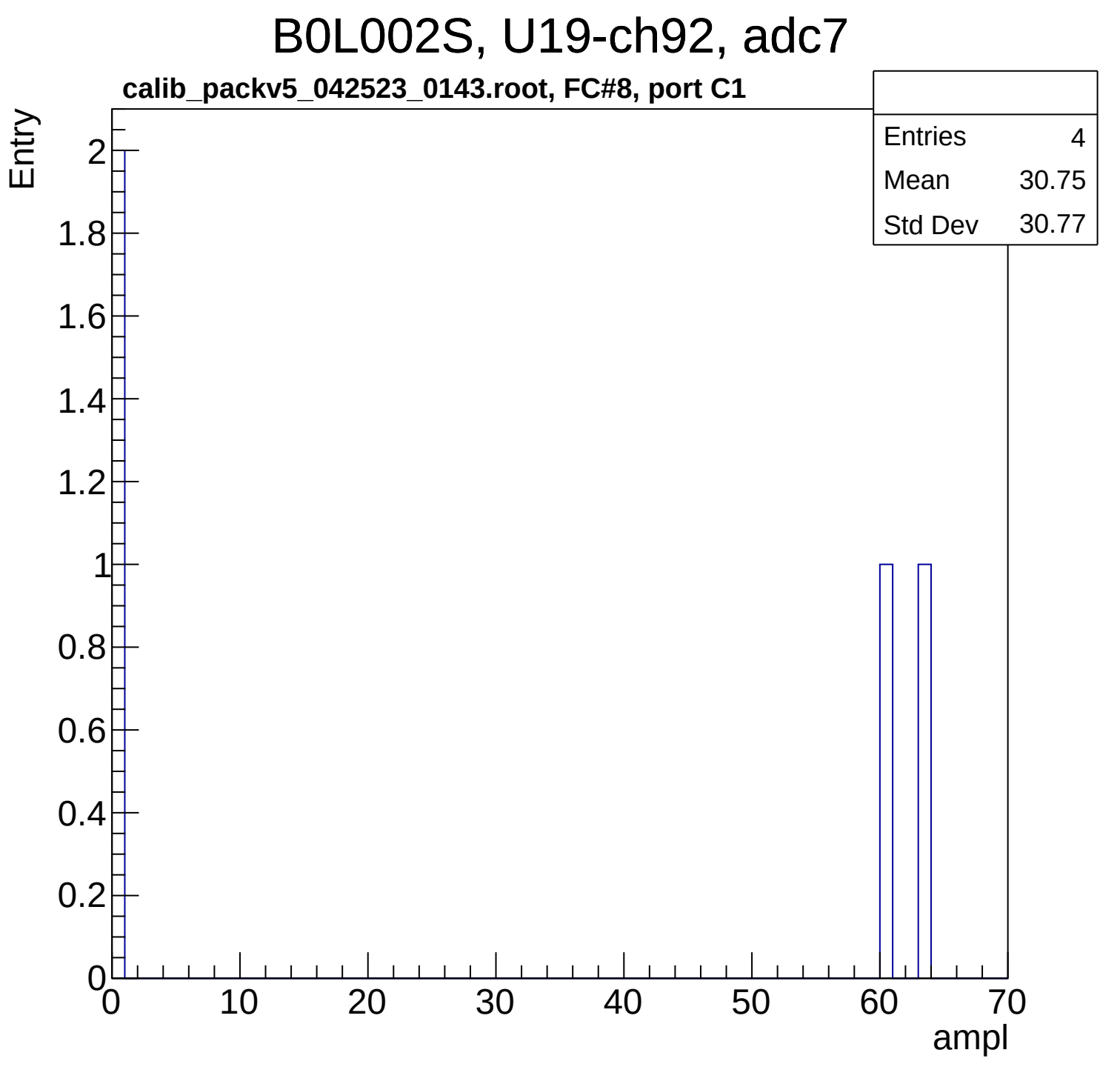

B0L002S, U19-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 41
Std Dev 3.087
Gaus mean : 41.5263
Gaus Width: 3.3513
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

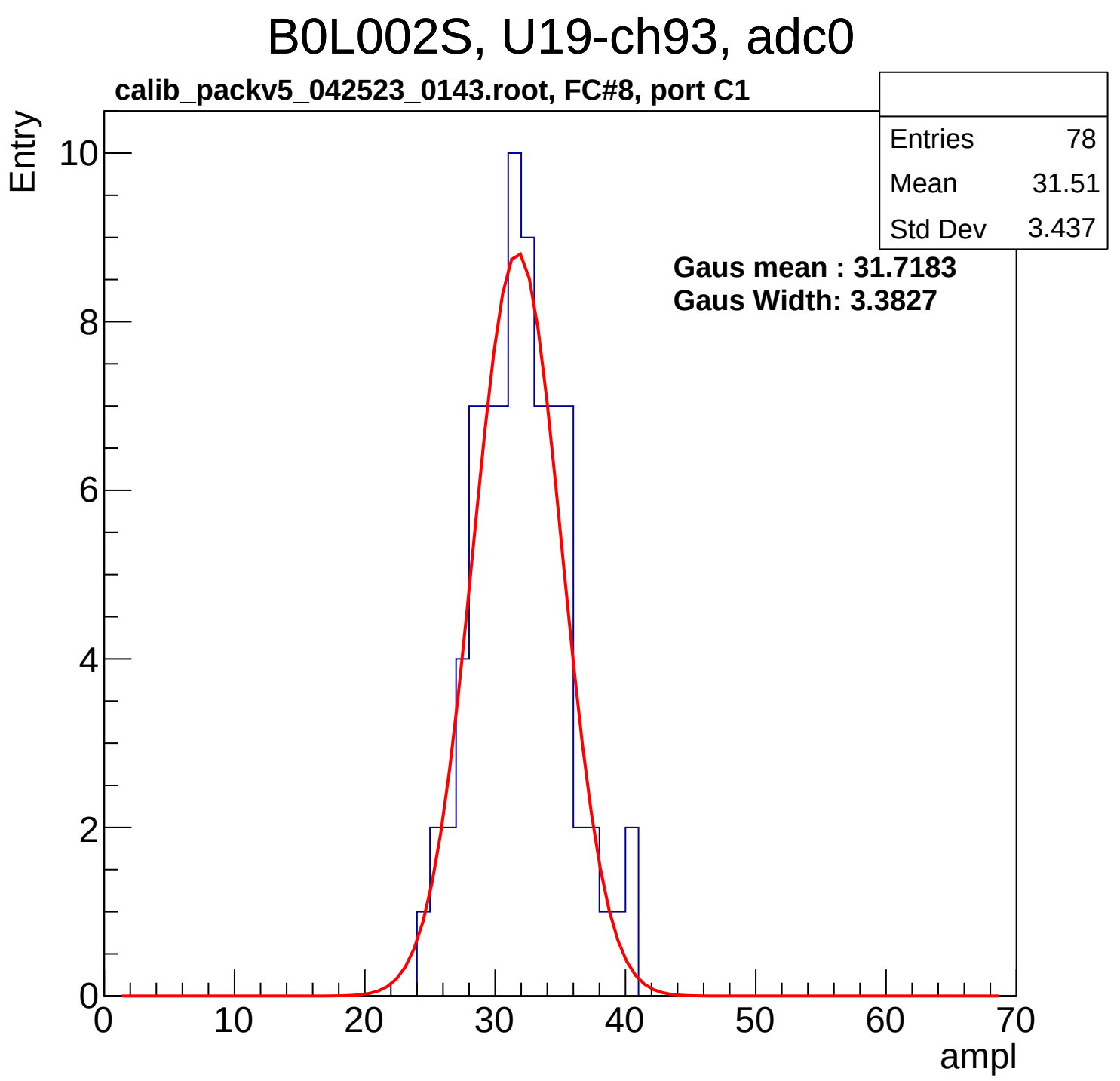

B0L002S, U19-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 47.46
Std Dev 3.484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

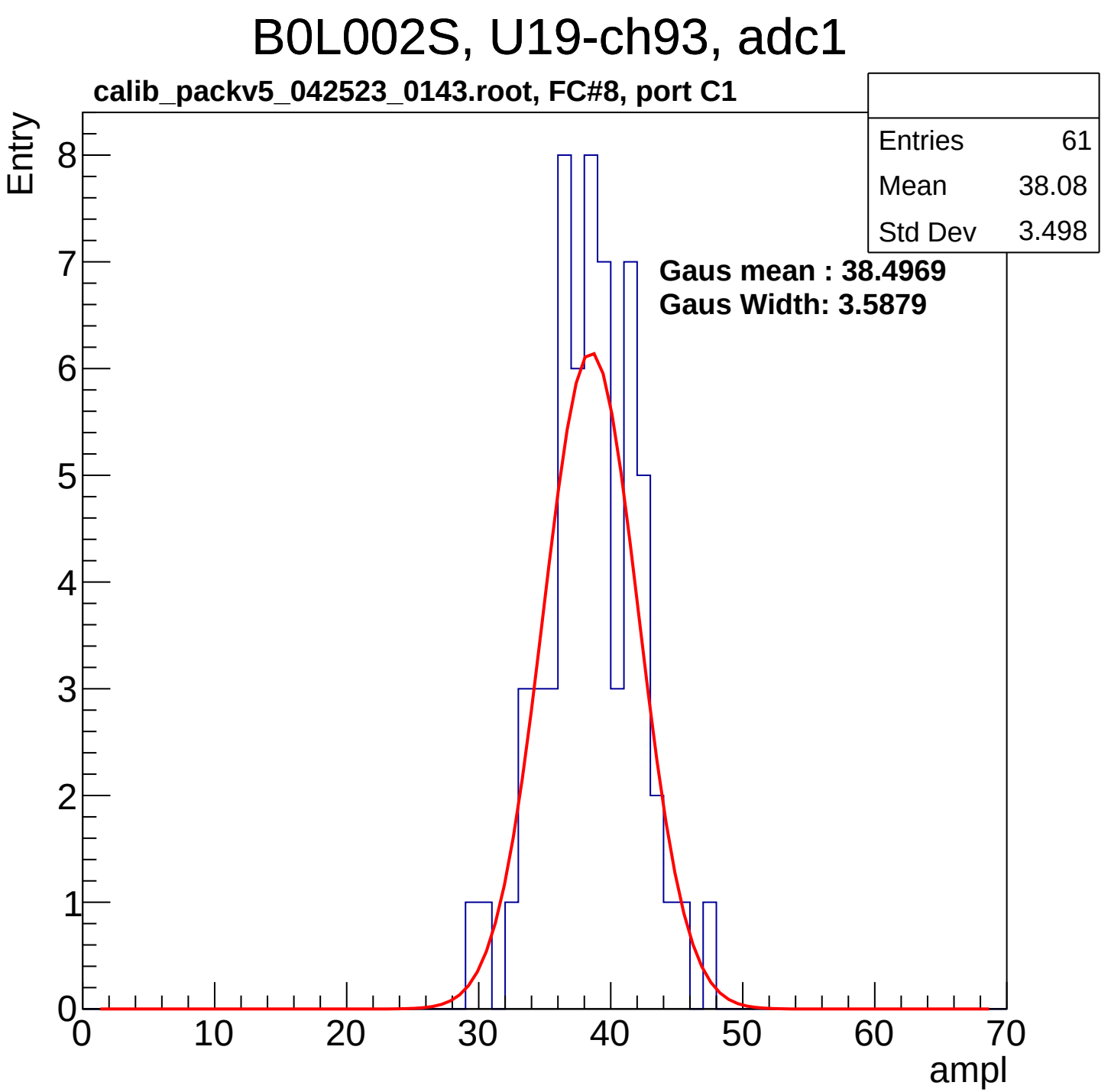

B0L002S, U19-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 53.68
Std Dev 3.106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

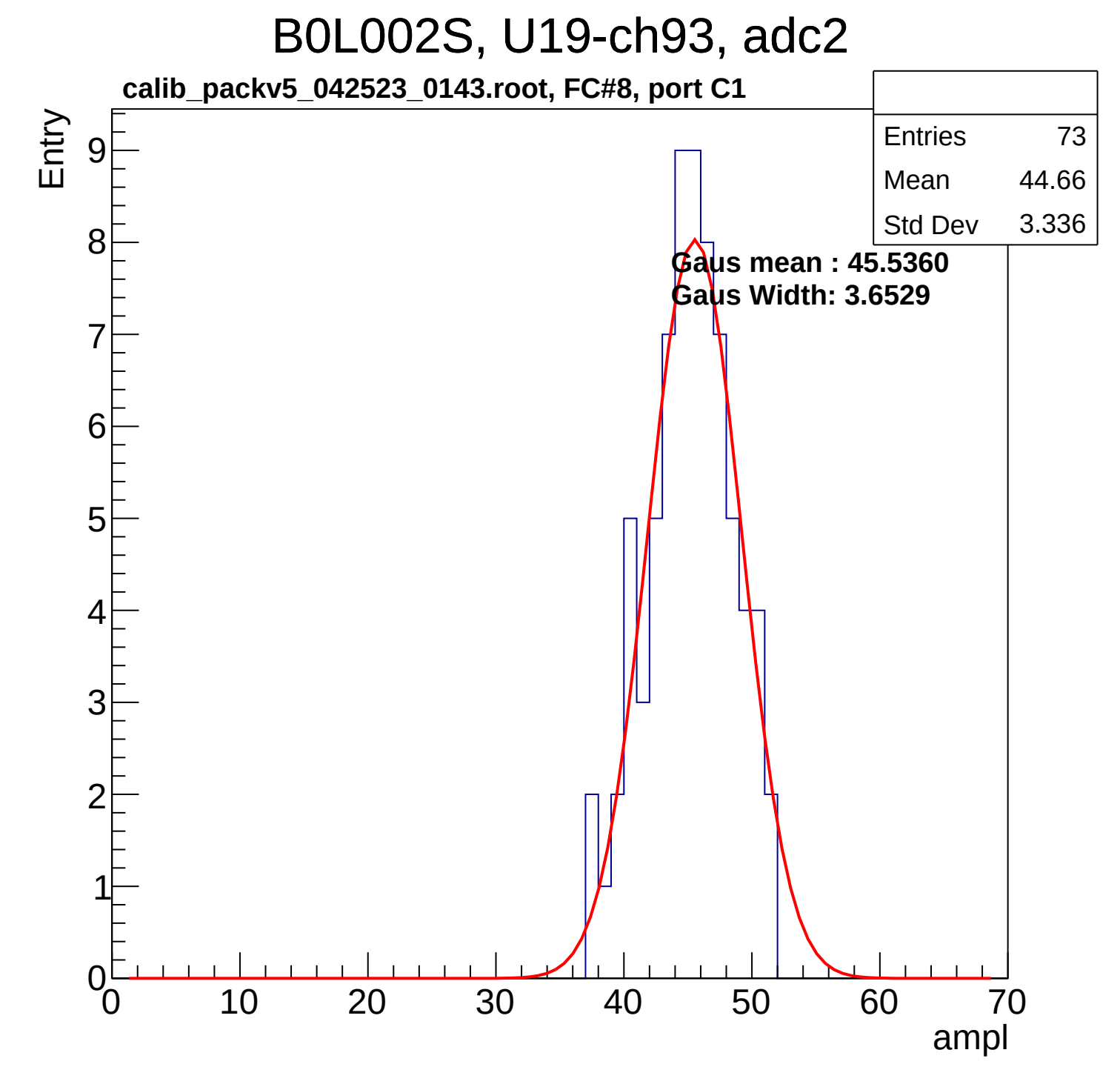

B0L002S, U19-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 58.19
Std Dev 8.11
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

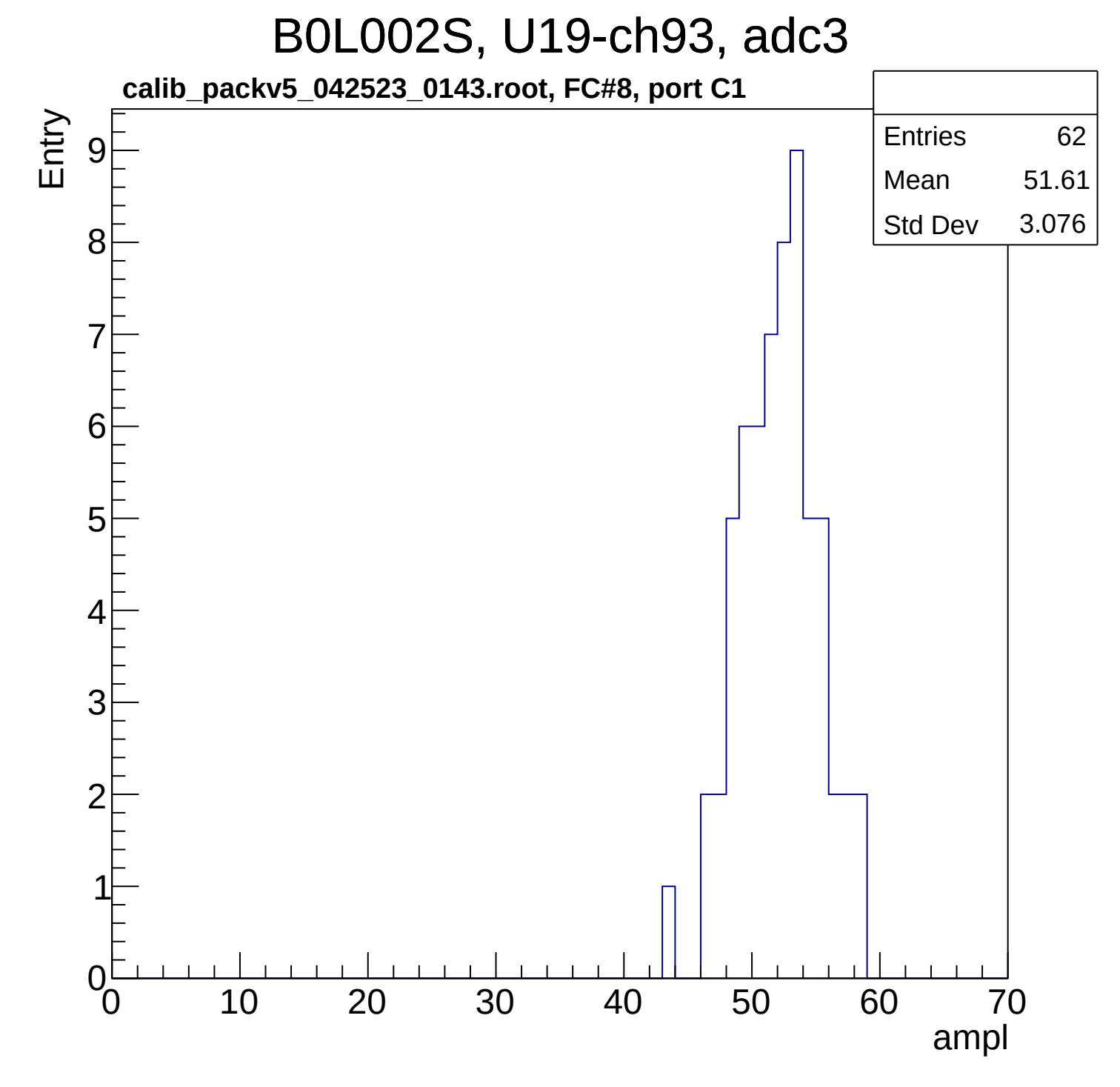

B0L002S, U19-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.3
Std Dev 2.369
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

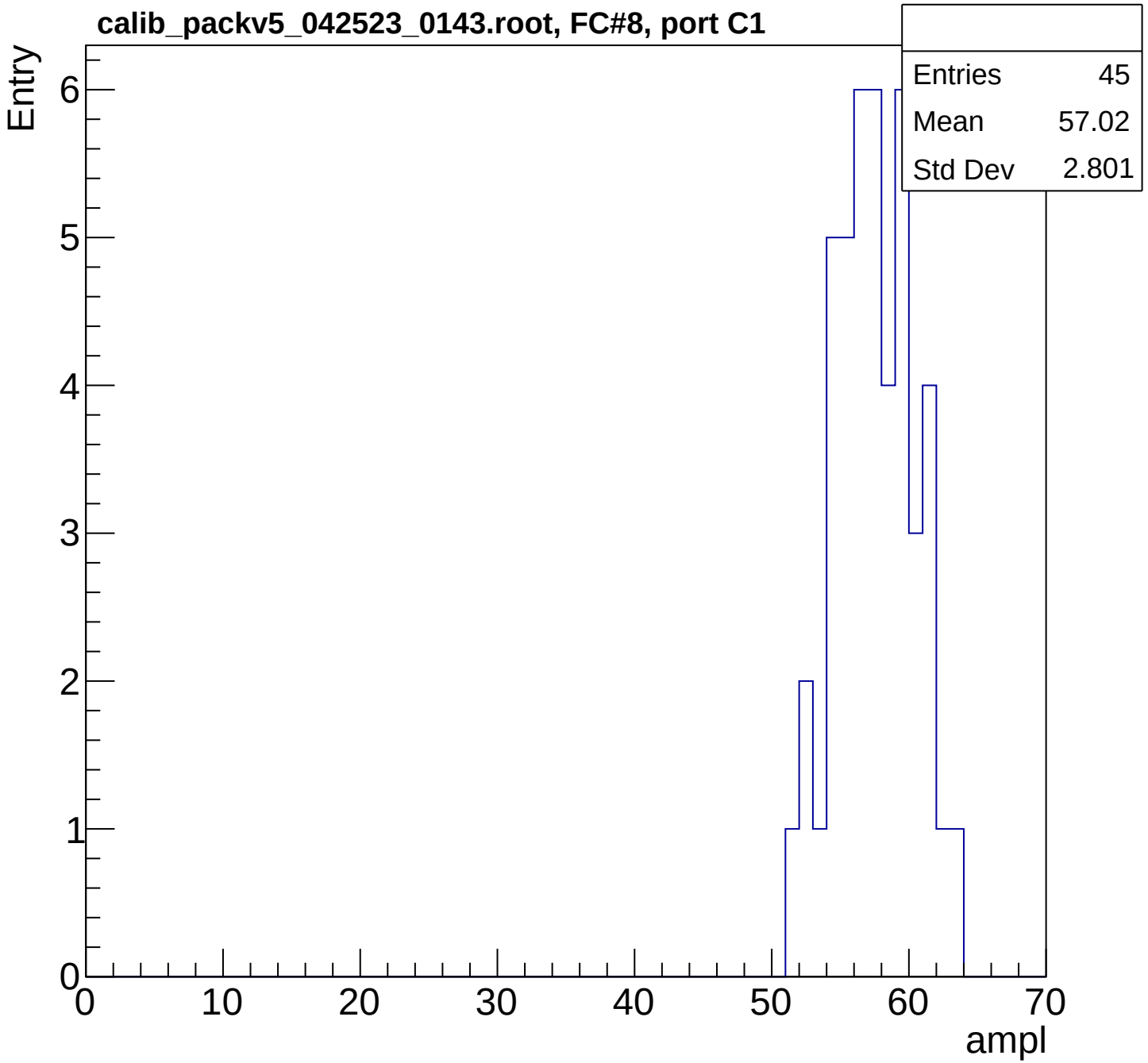

B0L002S, U19-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 30.75
Std Dev 30.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

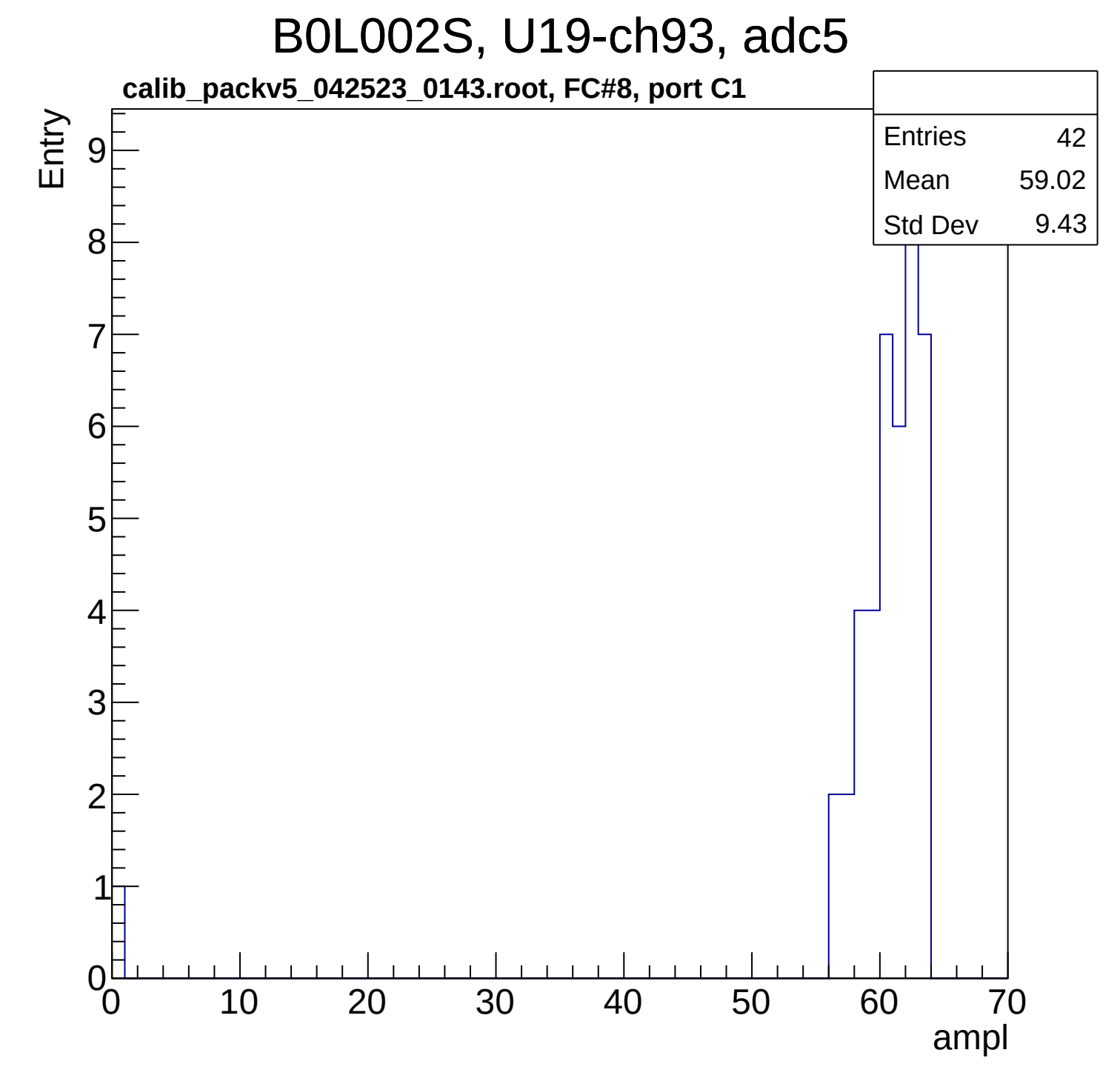

B0L002S, U19-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 31.51
Std Dev 3.437
Gaus mean : 31.7183
Gaus Width: 3.3827
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

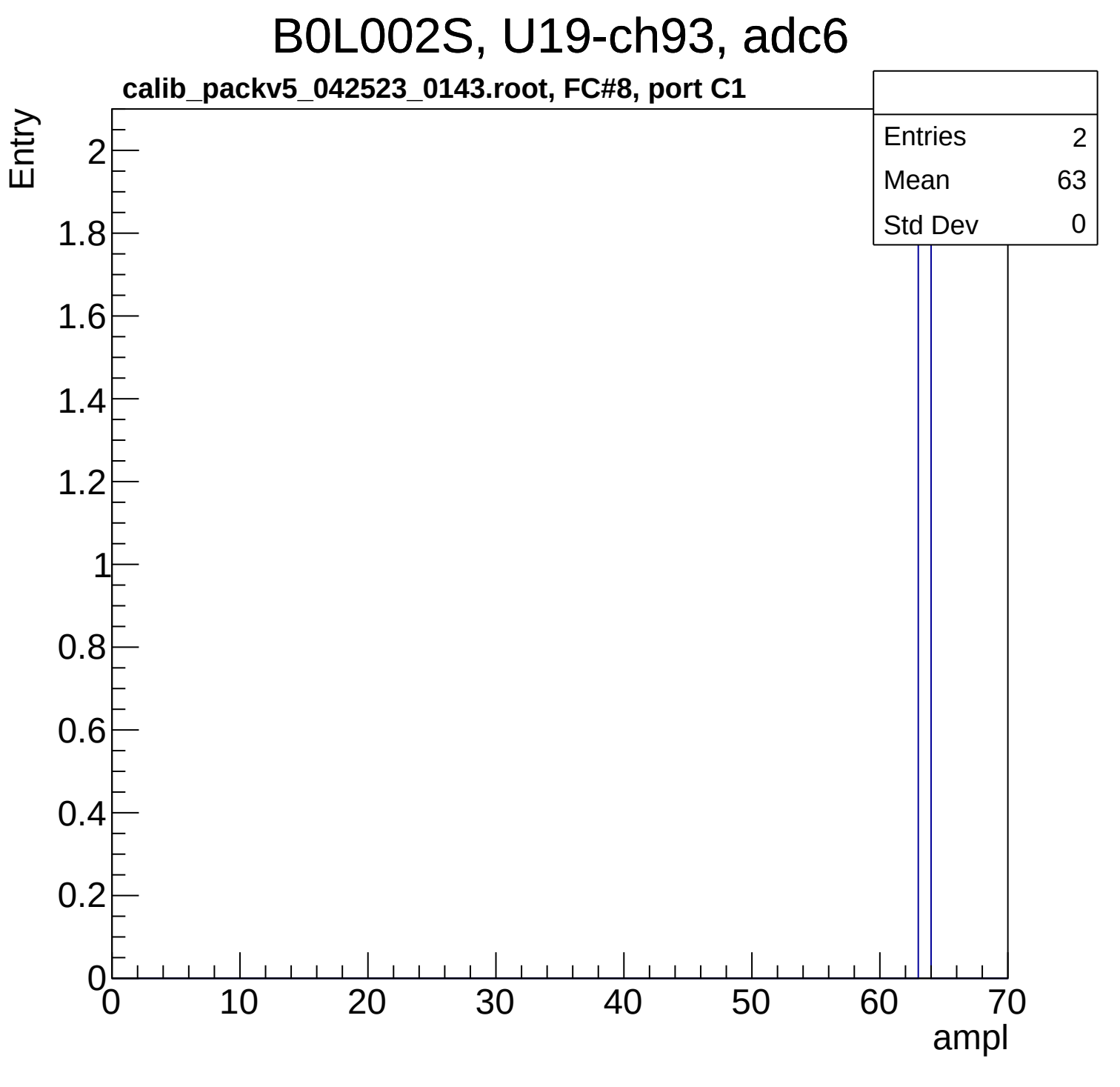

B0L002S, U19-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.08
Std Dev 3.498
Gaus mean : 38.4969
Gaus Width: 3.5879
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

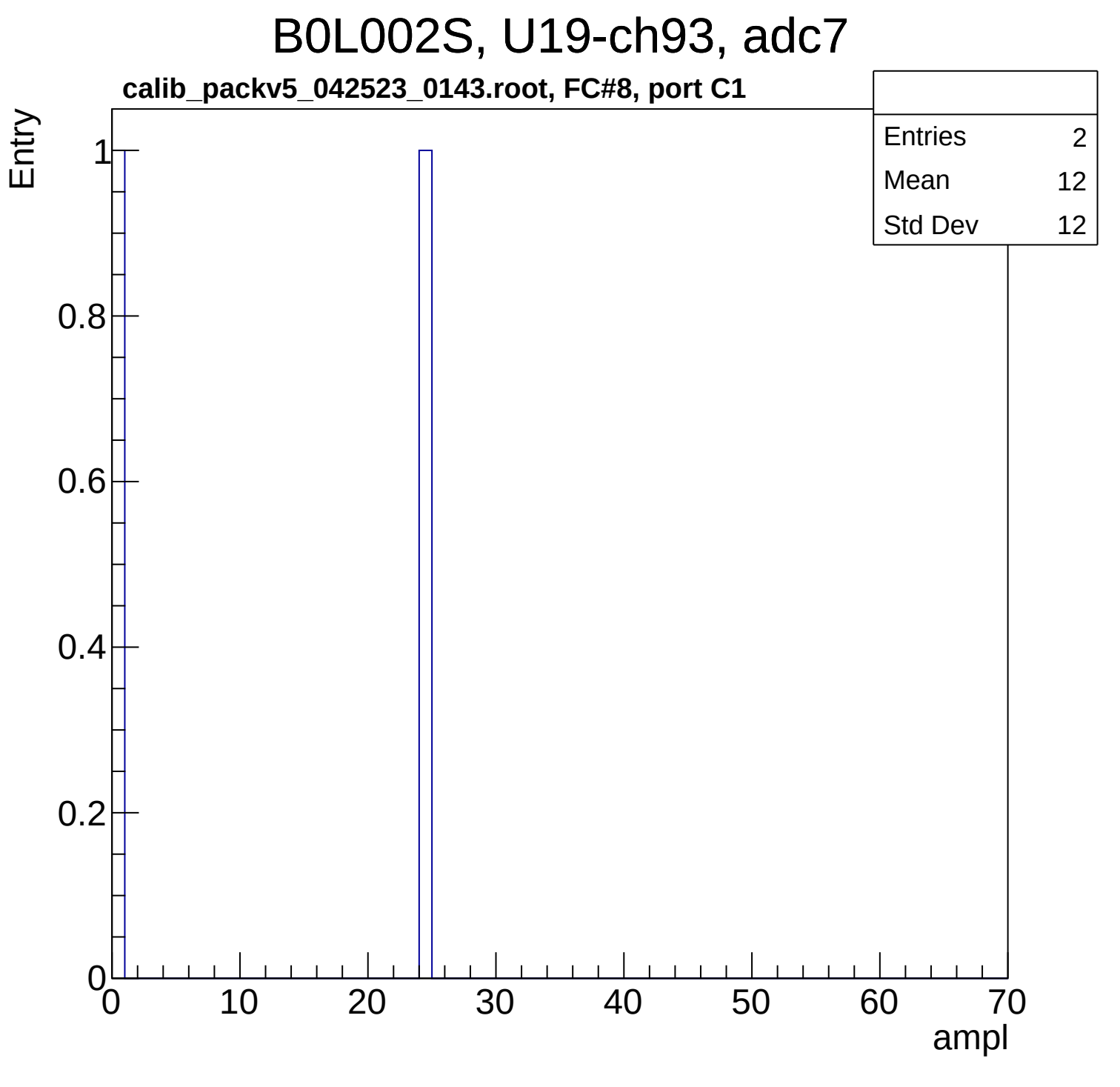

B0L002S, U19-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.66
Std Dev 3.336
Gaus mean : 45.5360
Gaus Width: 3.6529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

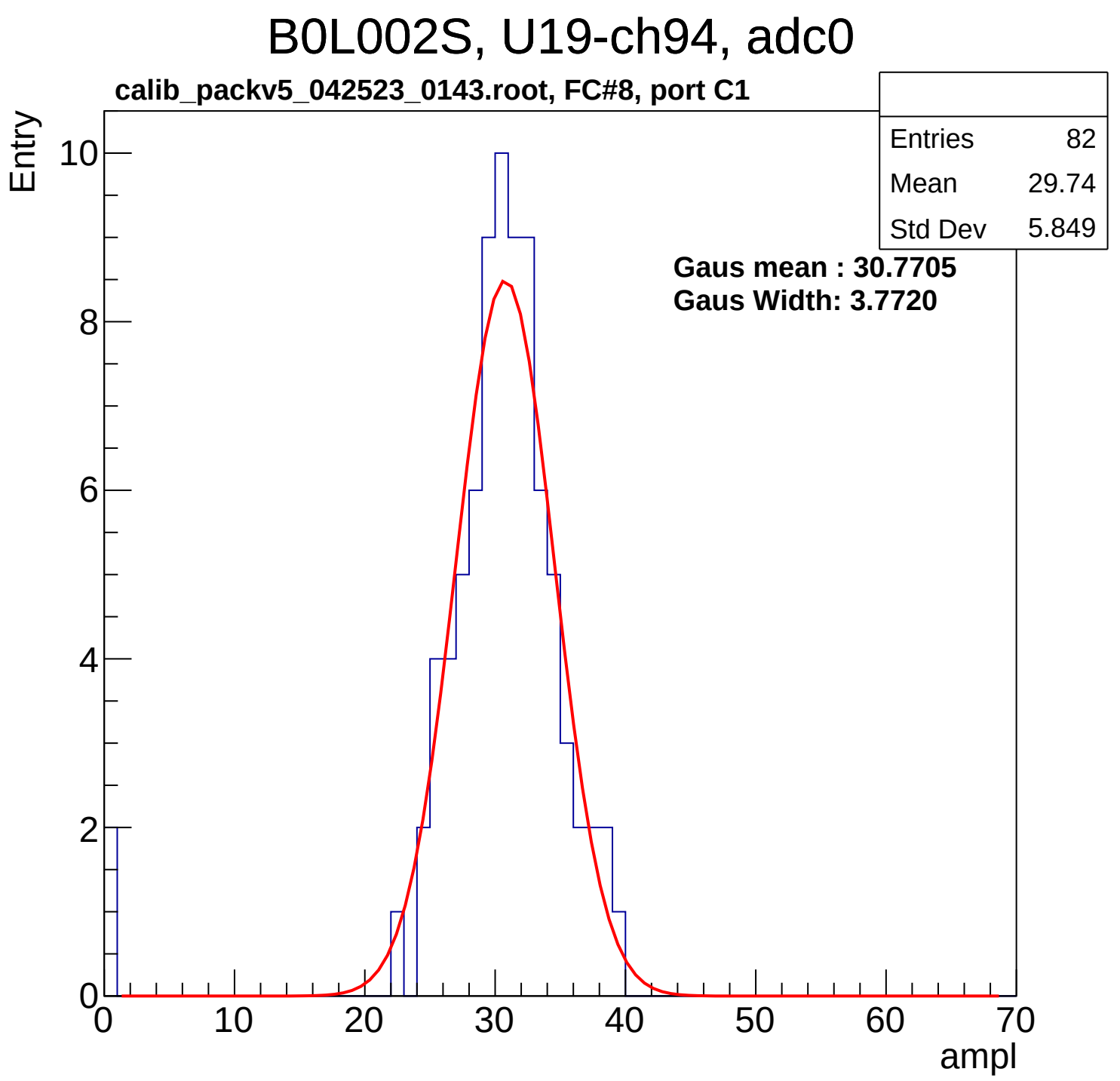

B0L002S, U19-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.61
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

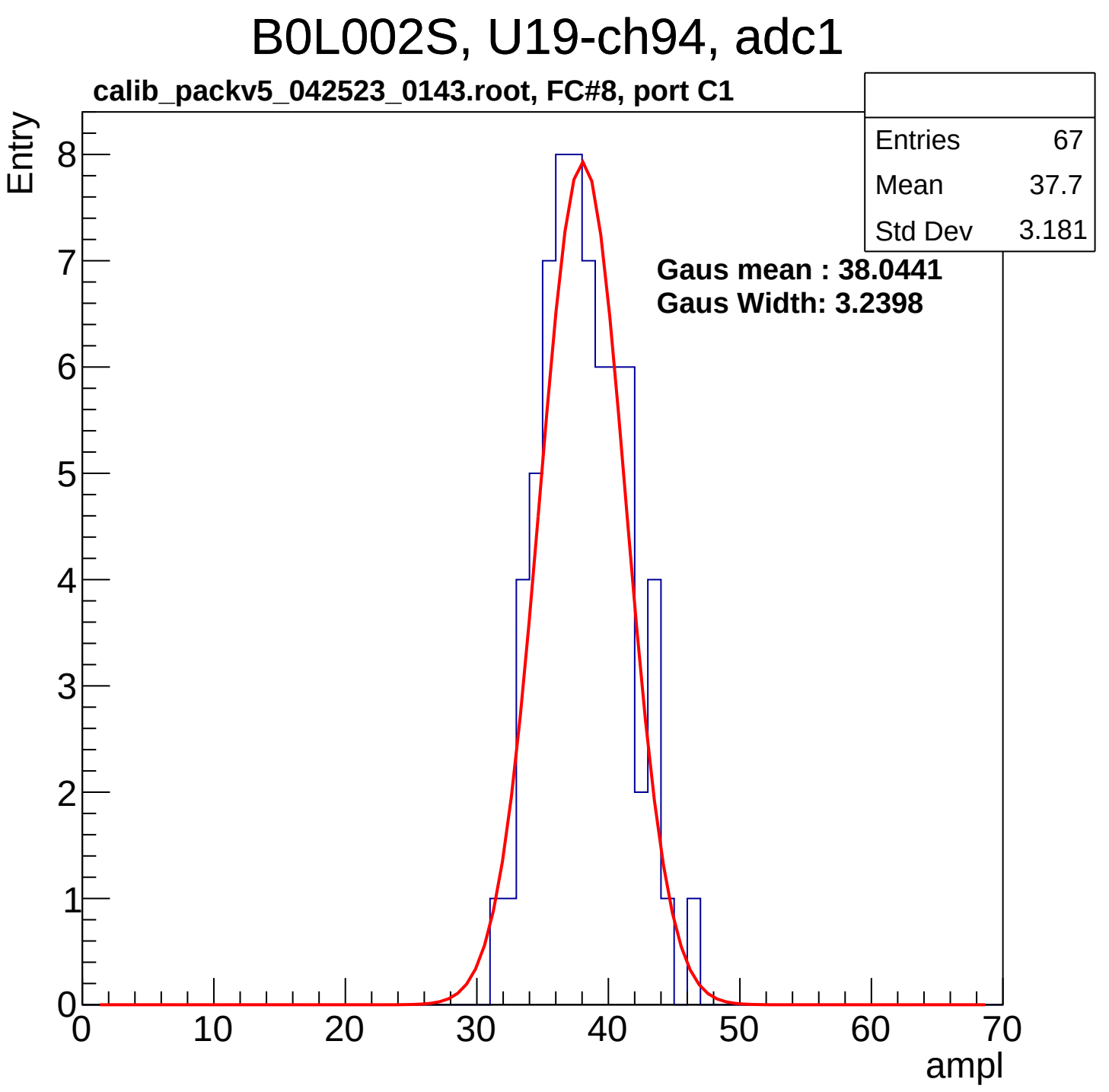

B0L002S, U19-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57.02
Std Dev 2.801
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

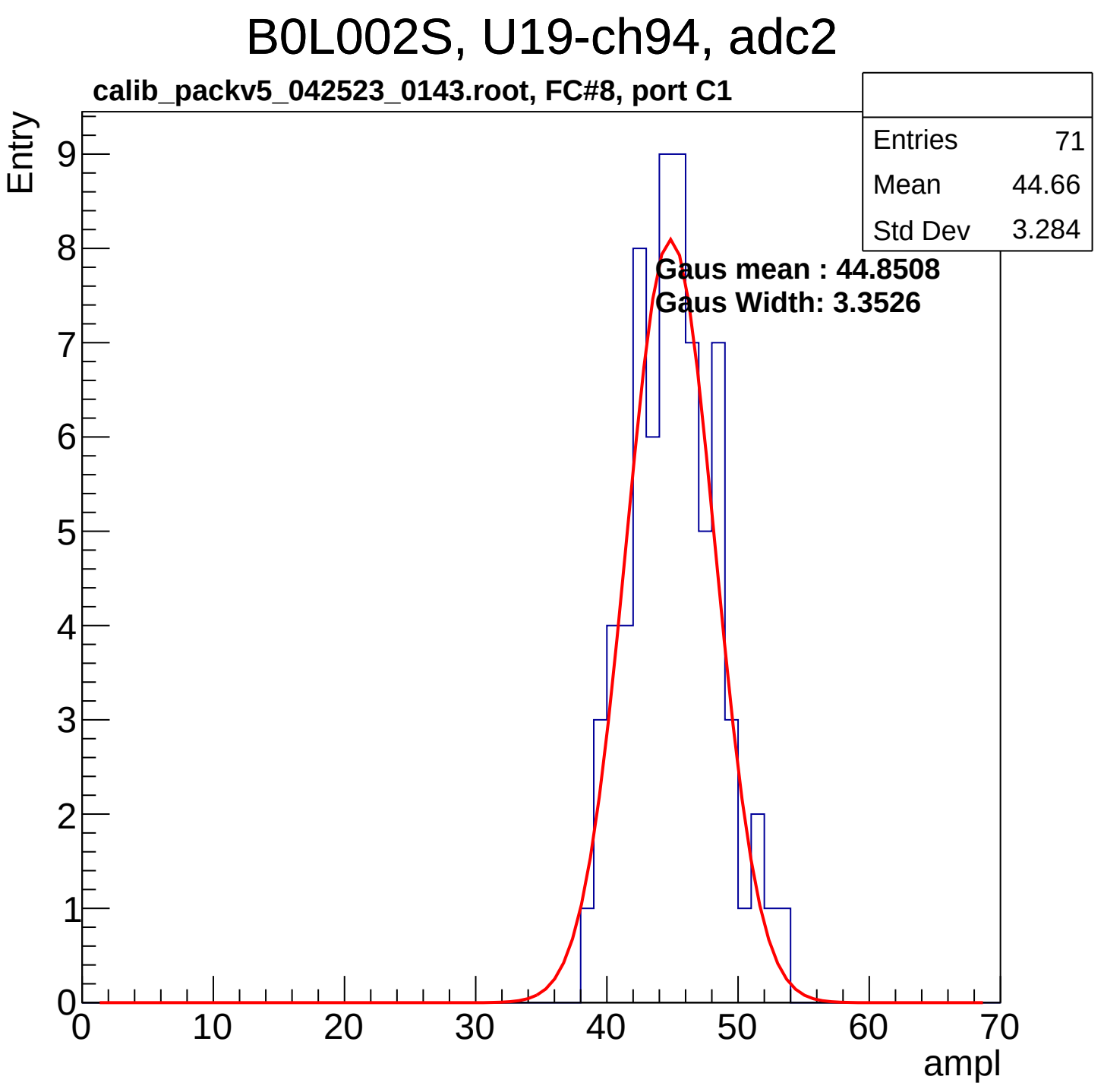

B0L002S, U19-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.02
Std Dev 9.43
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

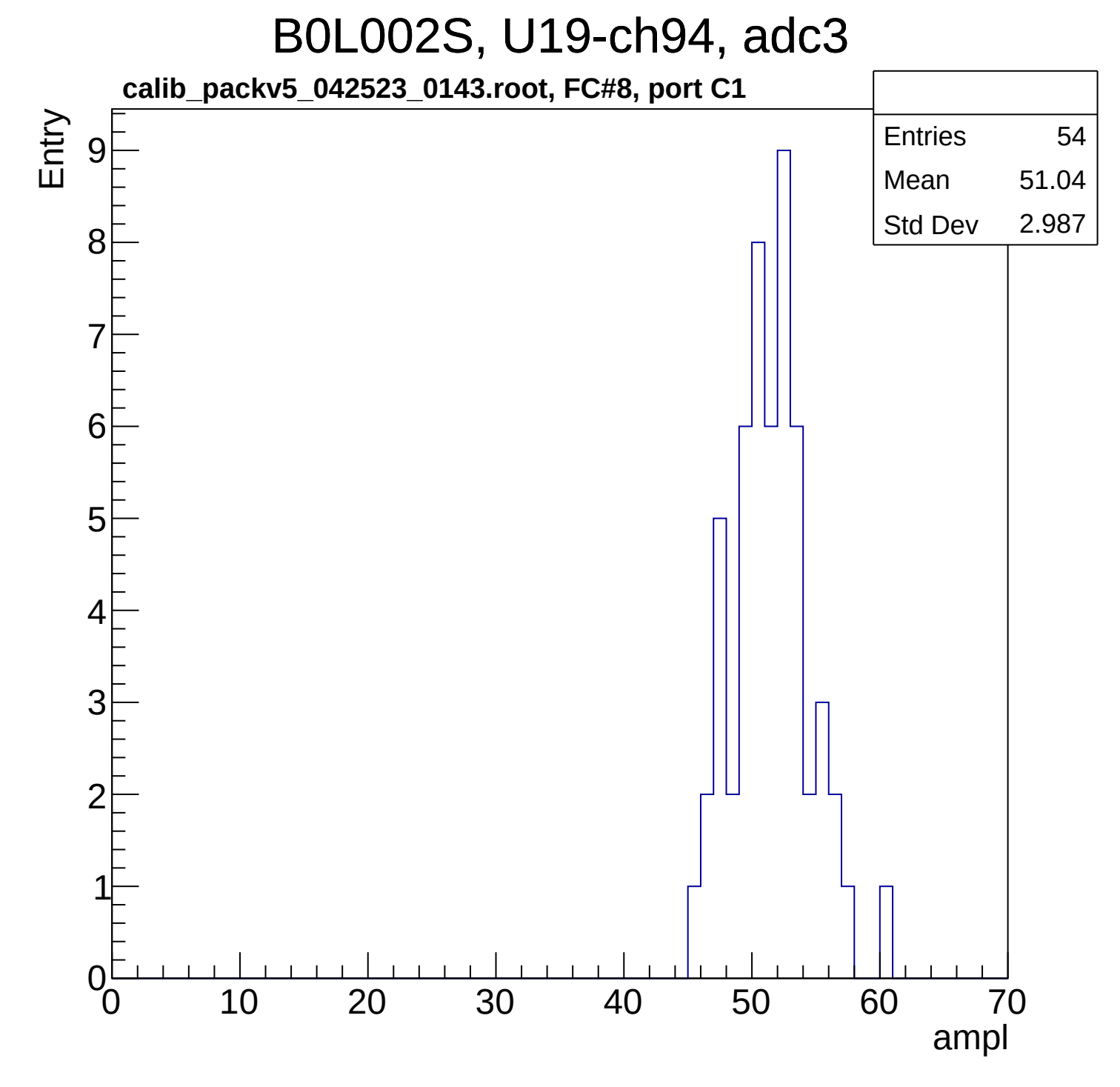

B0L002S, U19-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

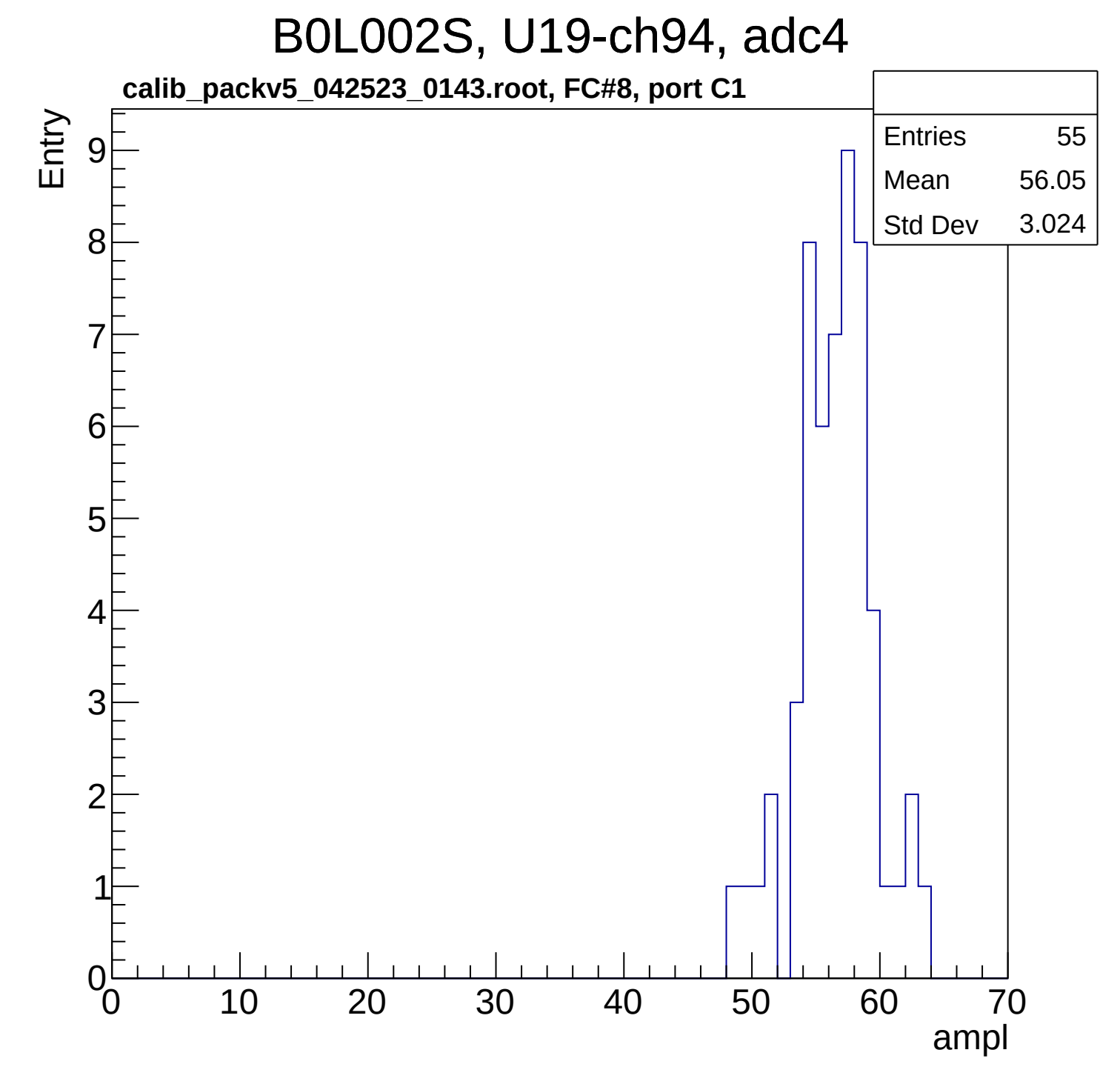

B0L002S, U19-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

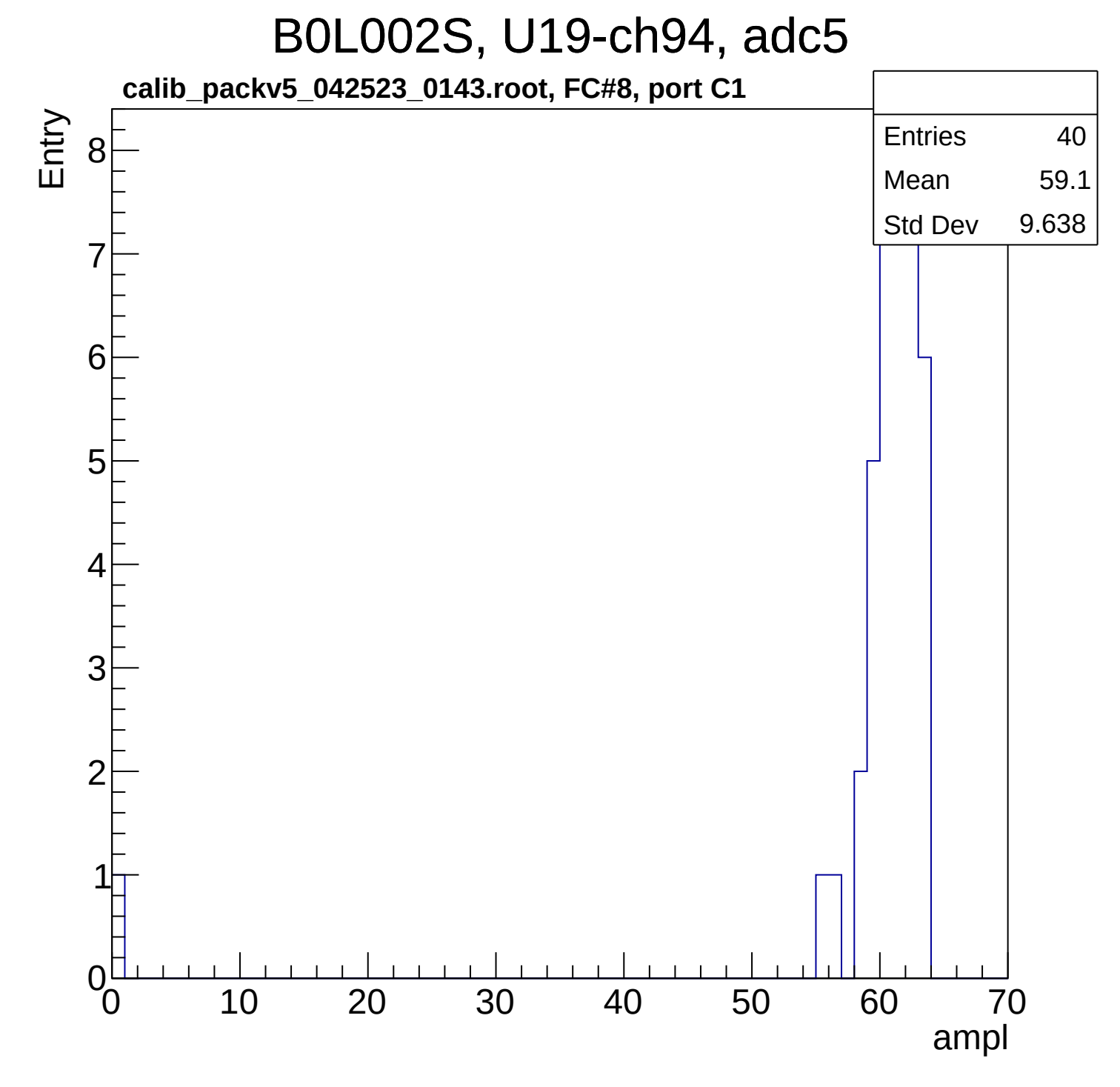

B0L002S, U19-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 29.74
Std Dev 5.849
Gaus mean : 30.7705
Gaus Width: 3.7720
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

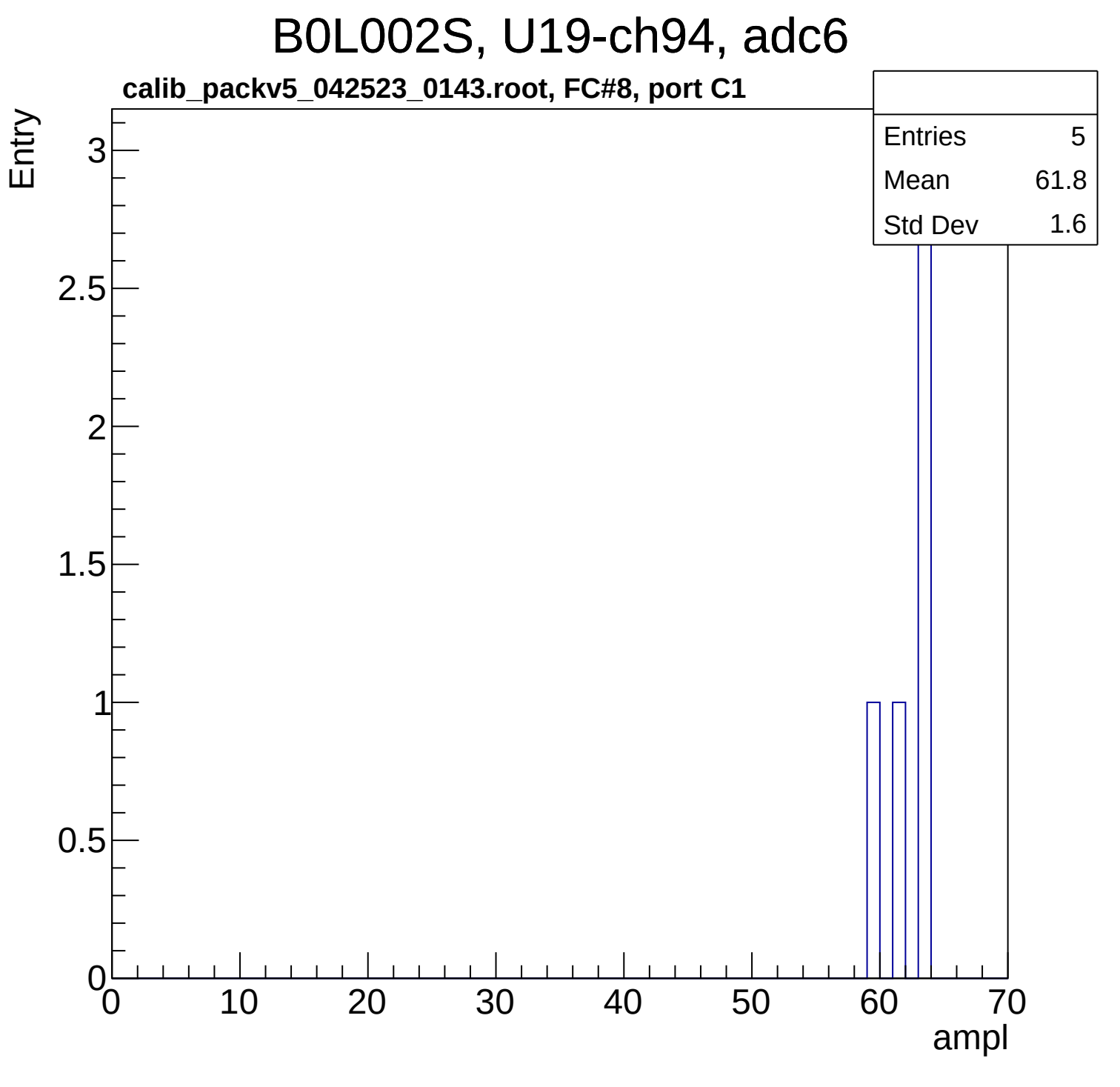

B0L002S, U19-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.7
Std Dev 3.181
Gaus mean : 38.0441
Gaus Width: 3.2398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

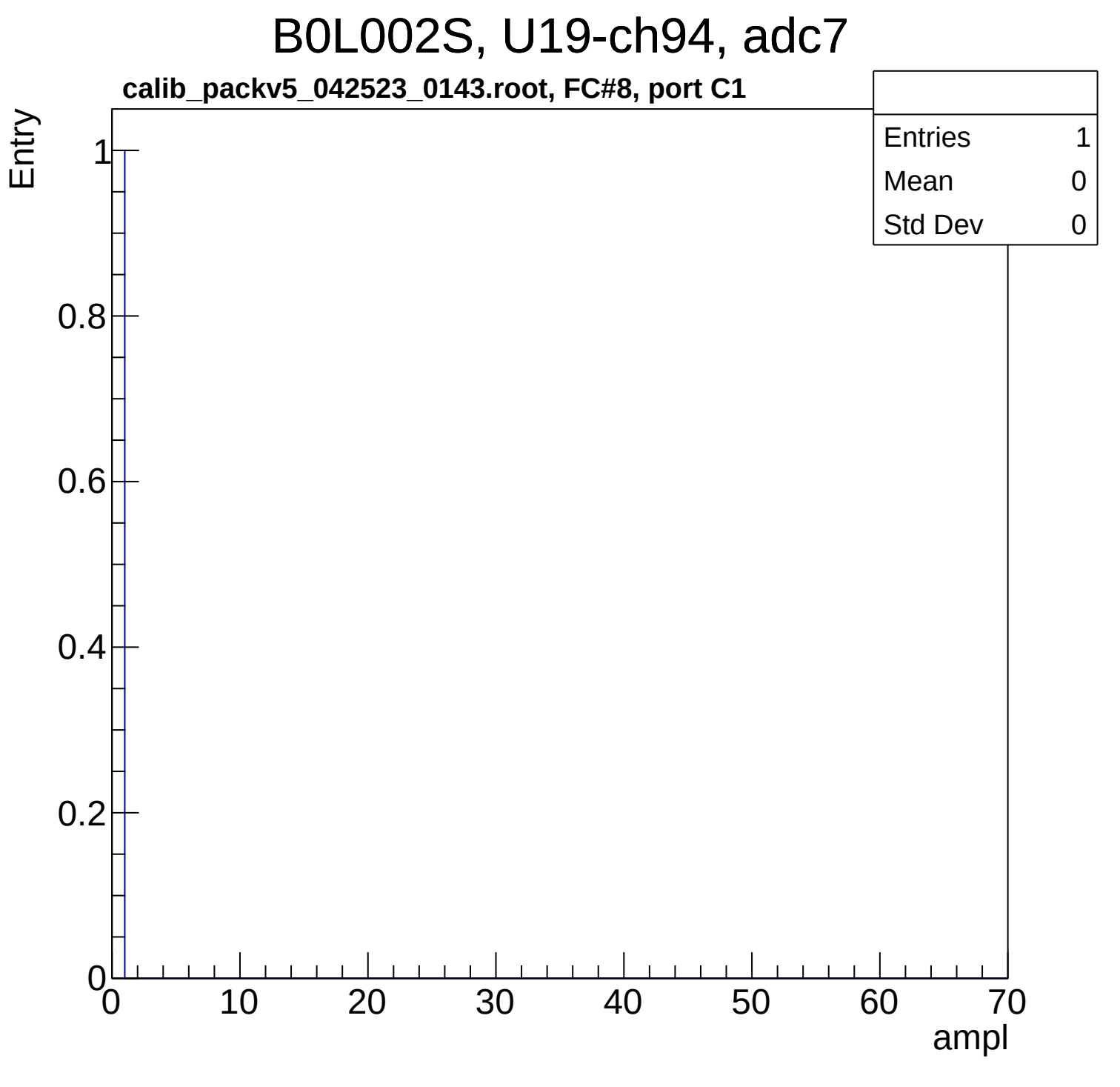

B0L002S, U19-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 44.66
Std Dev 3.284
Gaus mean : 44.8508
Gaus Width: 3.3526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

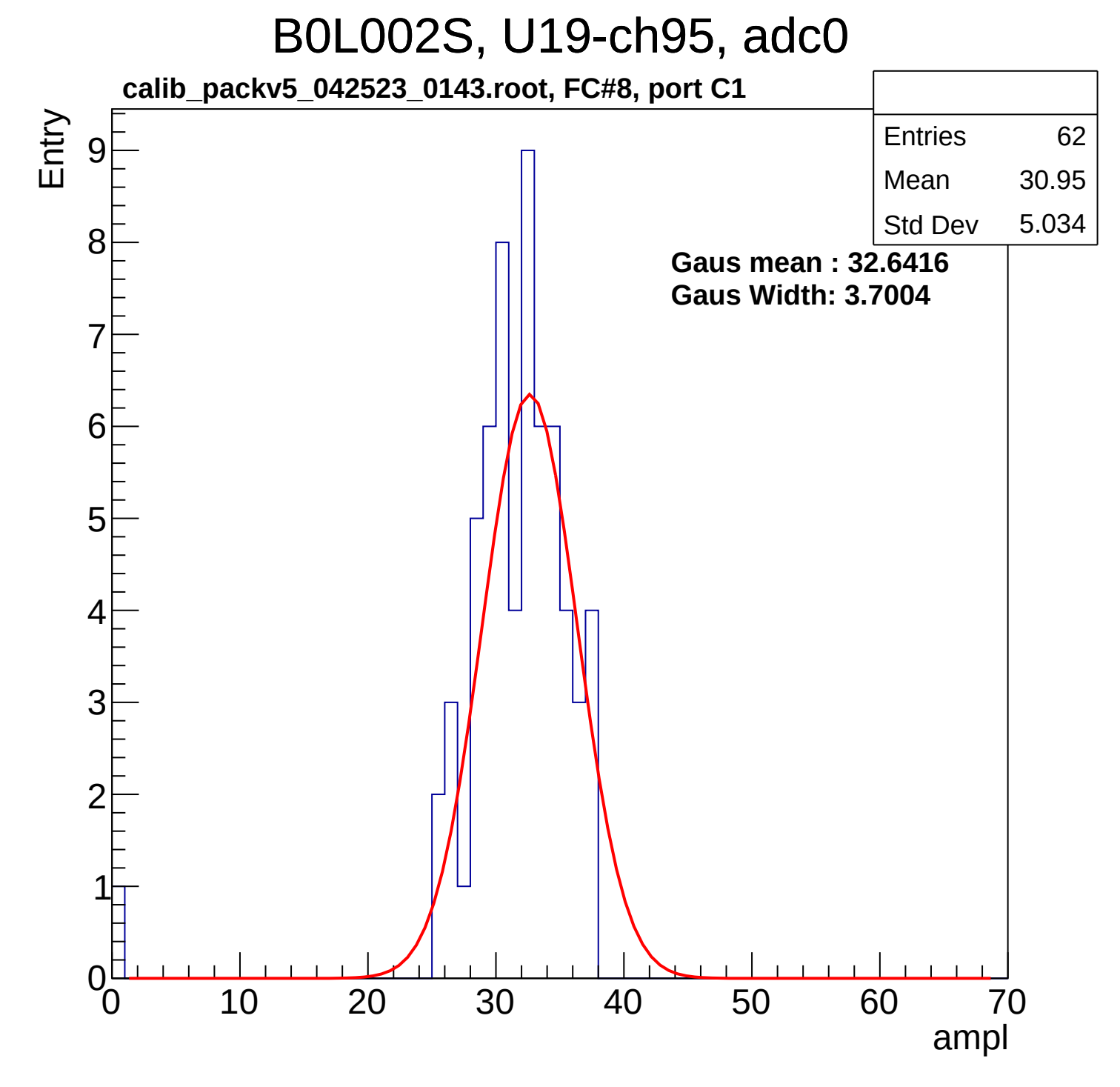

B0L002S, U19-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 51.04
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

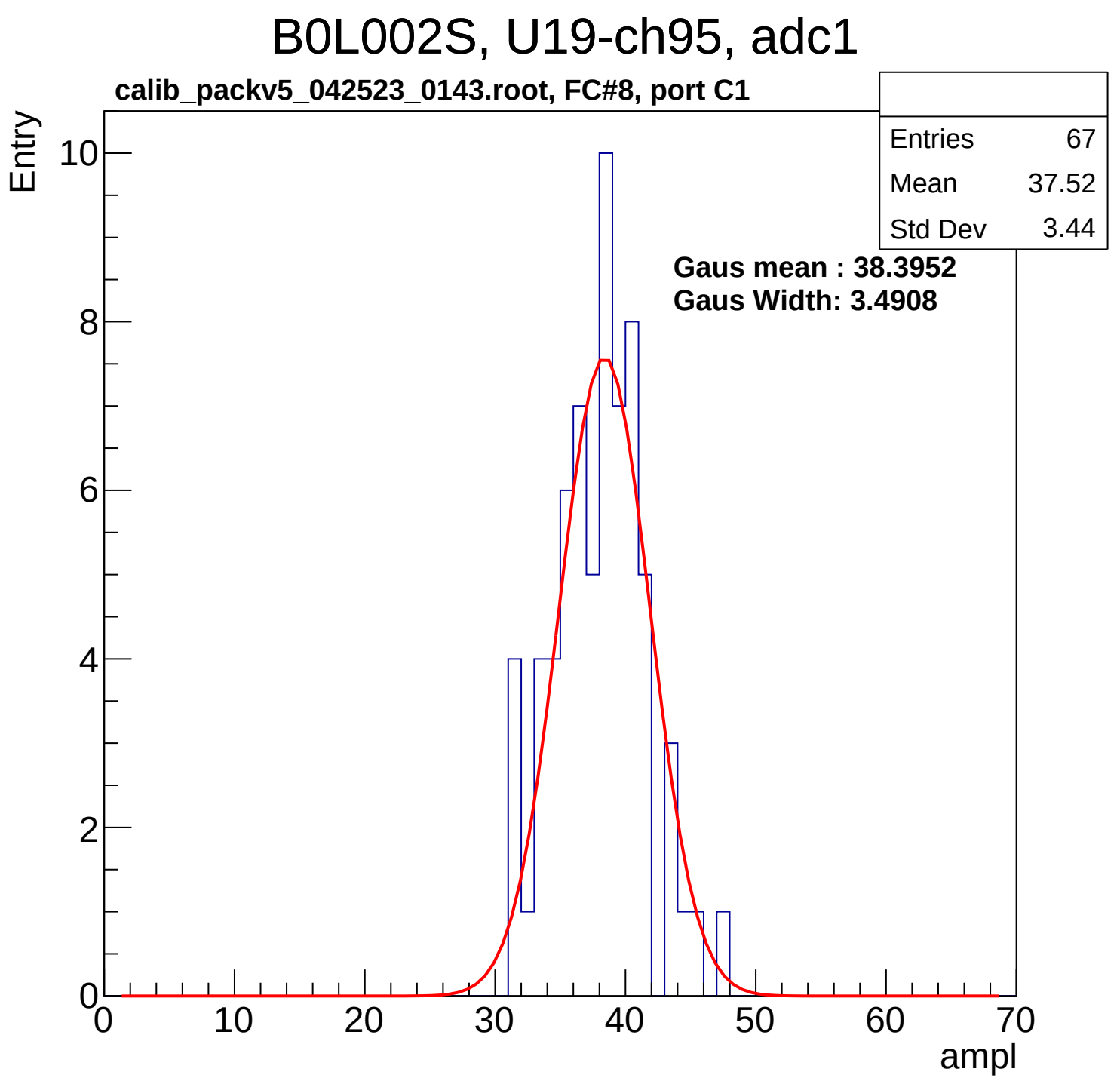

B0L002S, U19-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.05
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

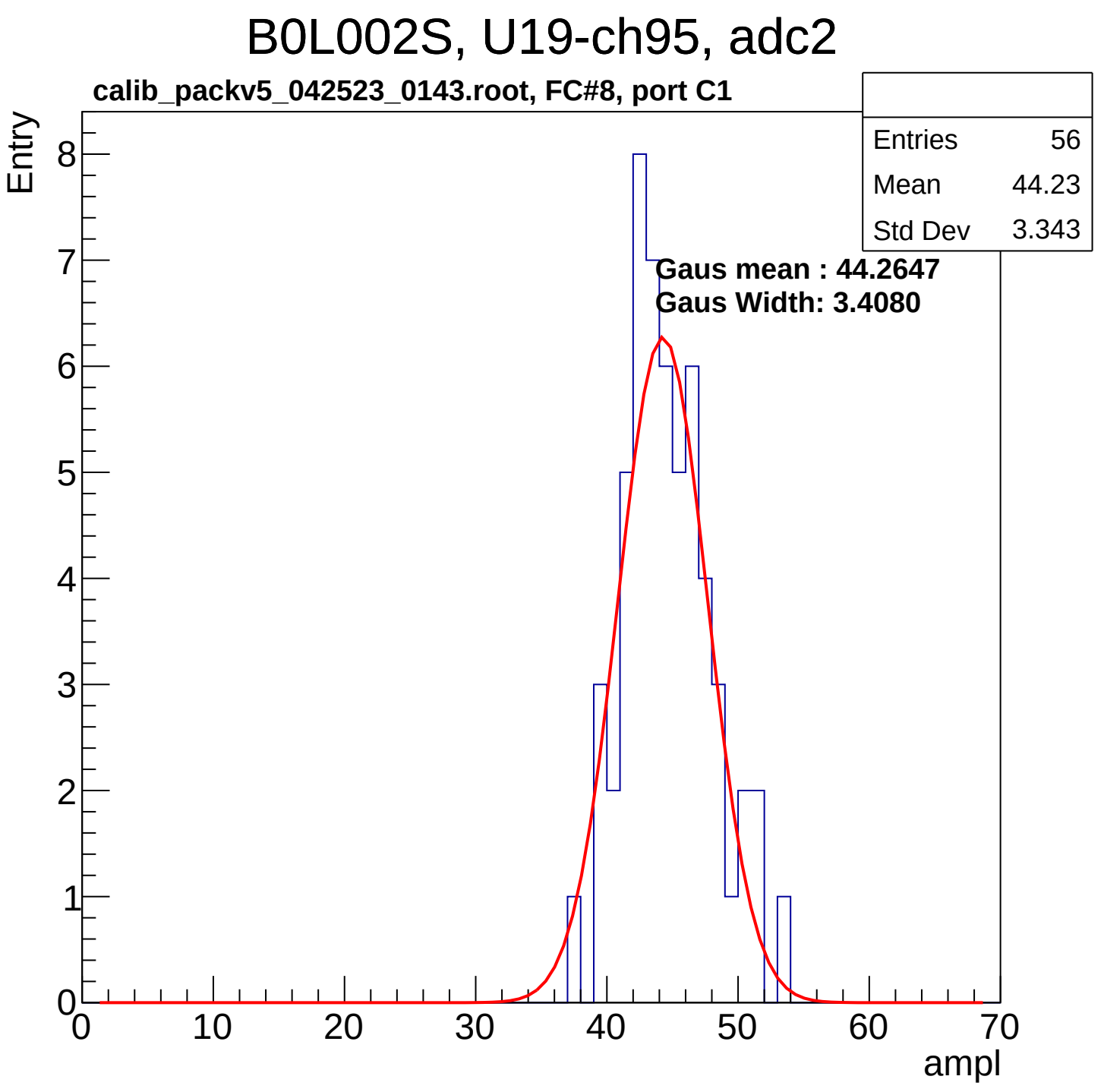

B0L002S, U19-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.1
Std Dev 9.638
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

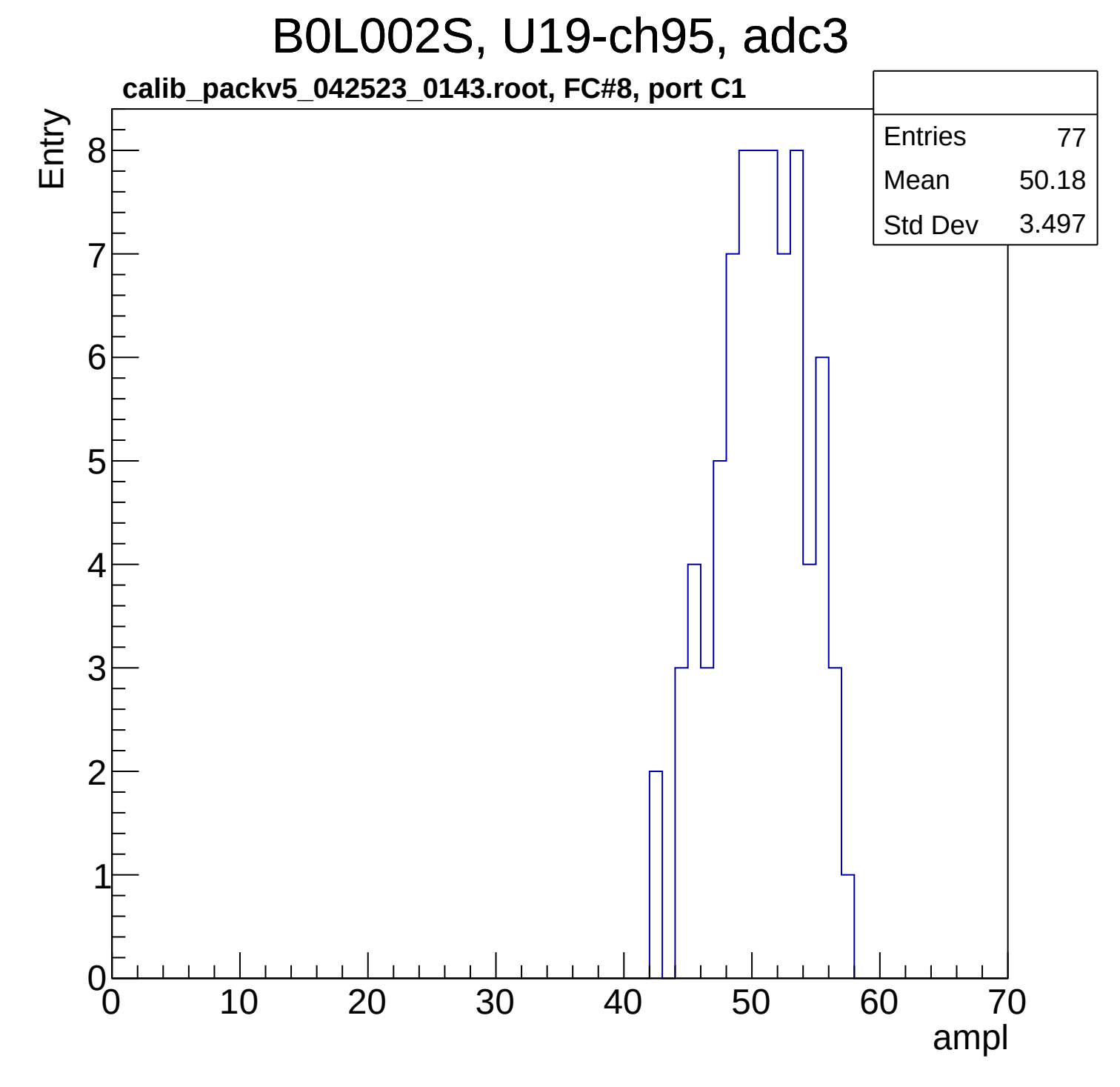

B0L002S, U19-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.8
Std Dev 1.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

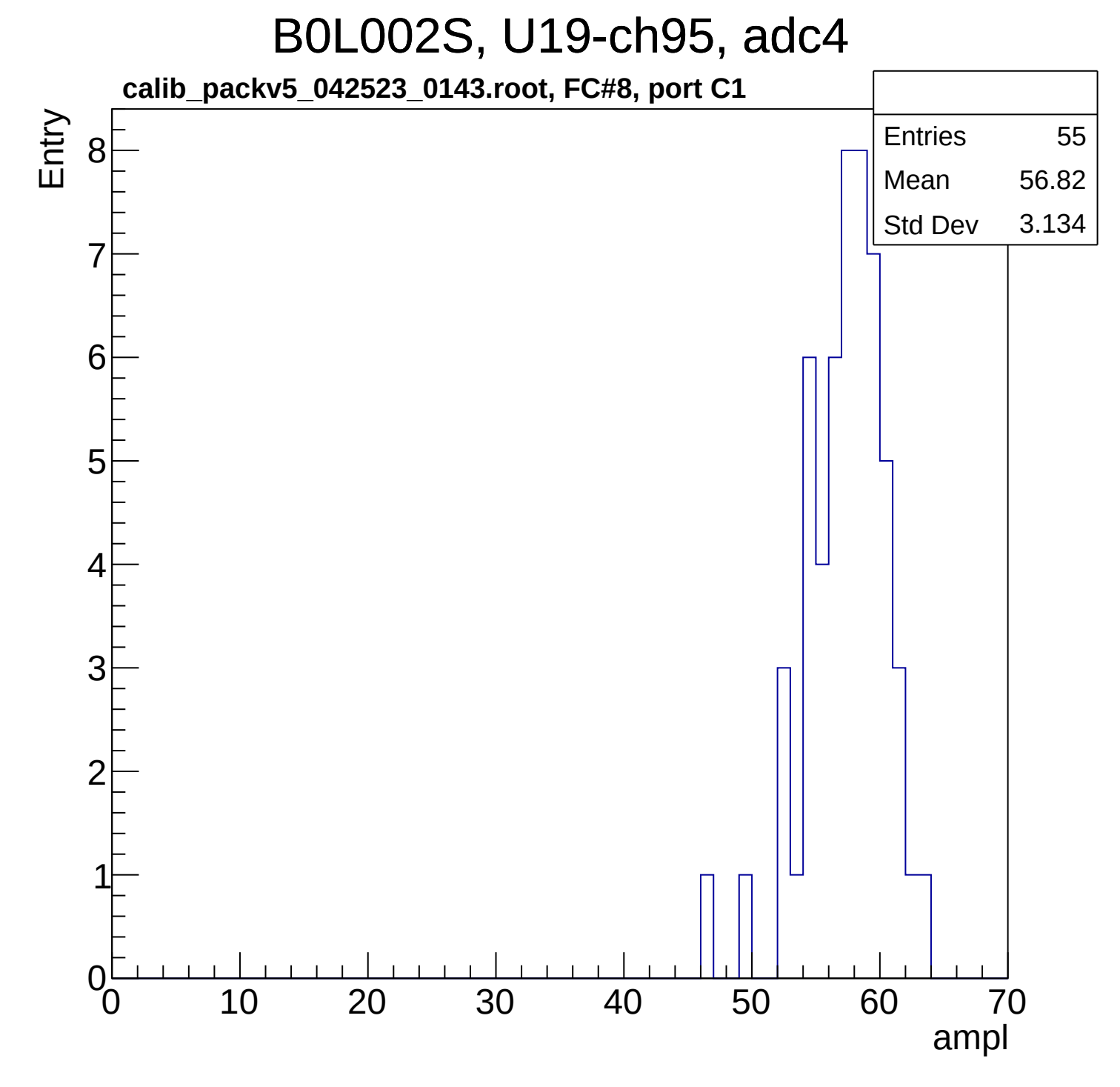

B0L002S, U19-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

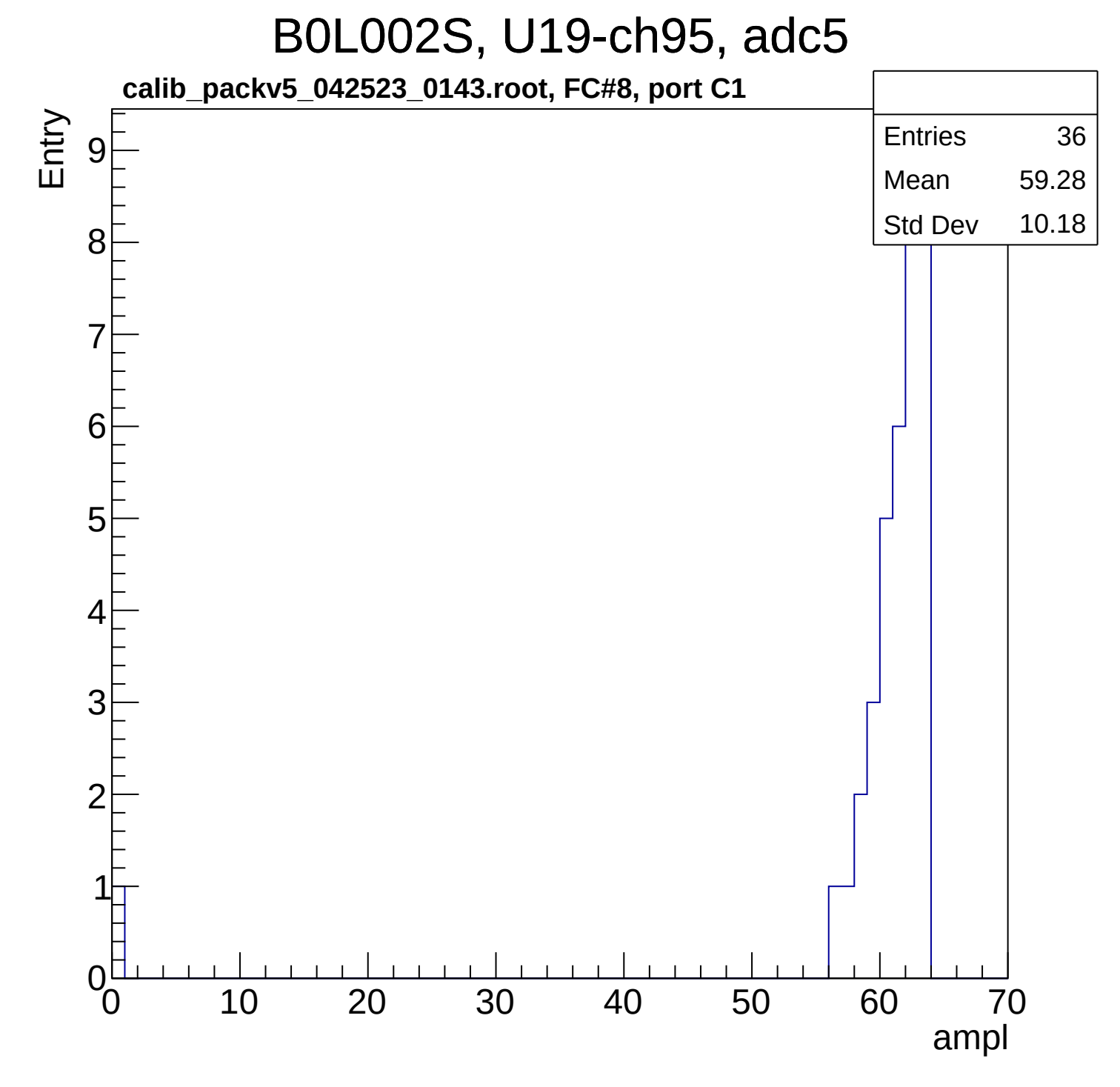

B0L002S, U19-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.95
Std Dev 5.034
Gaus mean : 32.6416
Gaus Width: 3.7004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

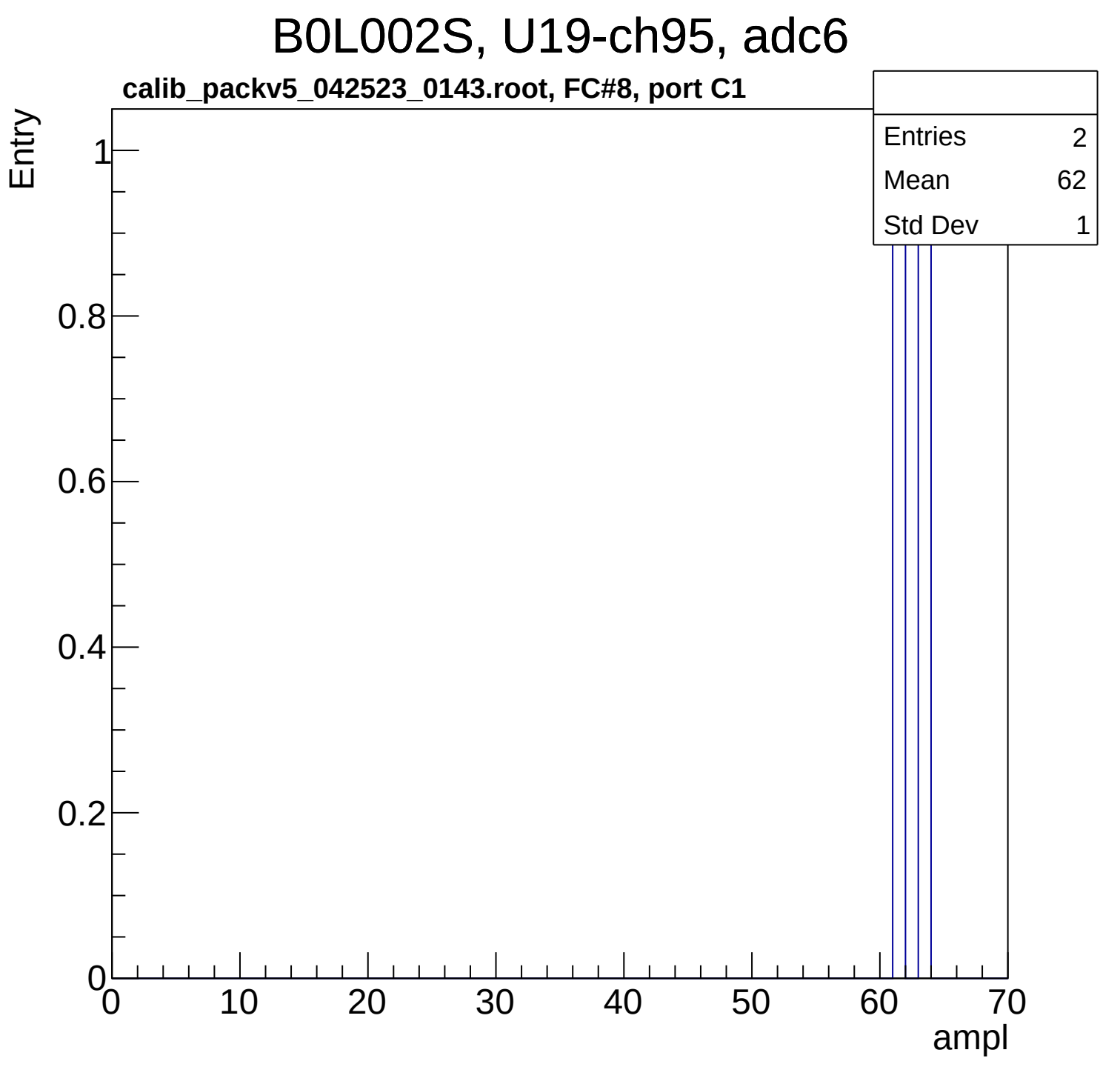

B0L002S, U19-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.52
Std Dev 3.44
Gaus mean : 38.3952
Gaus Width: 3.4908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

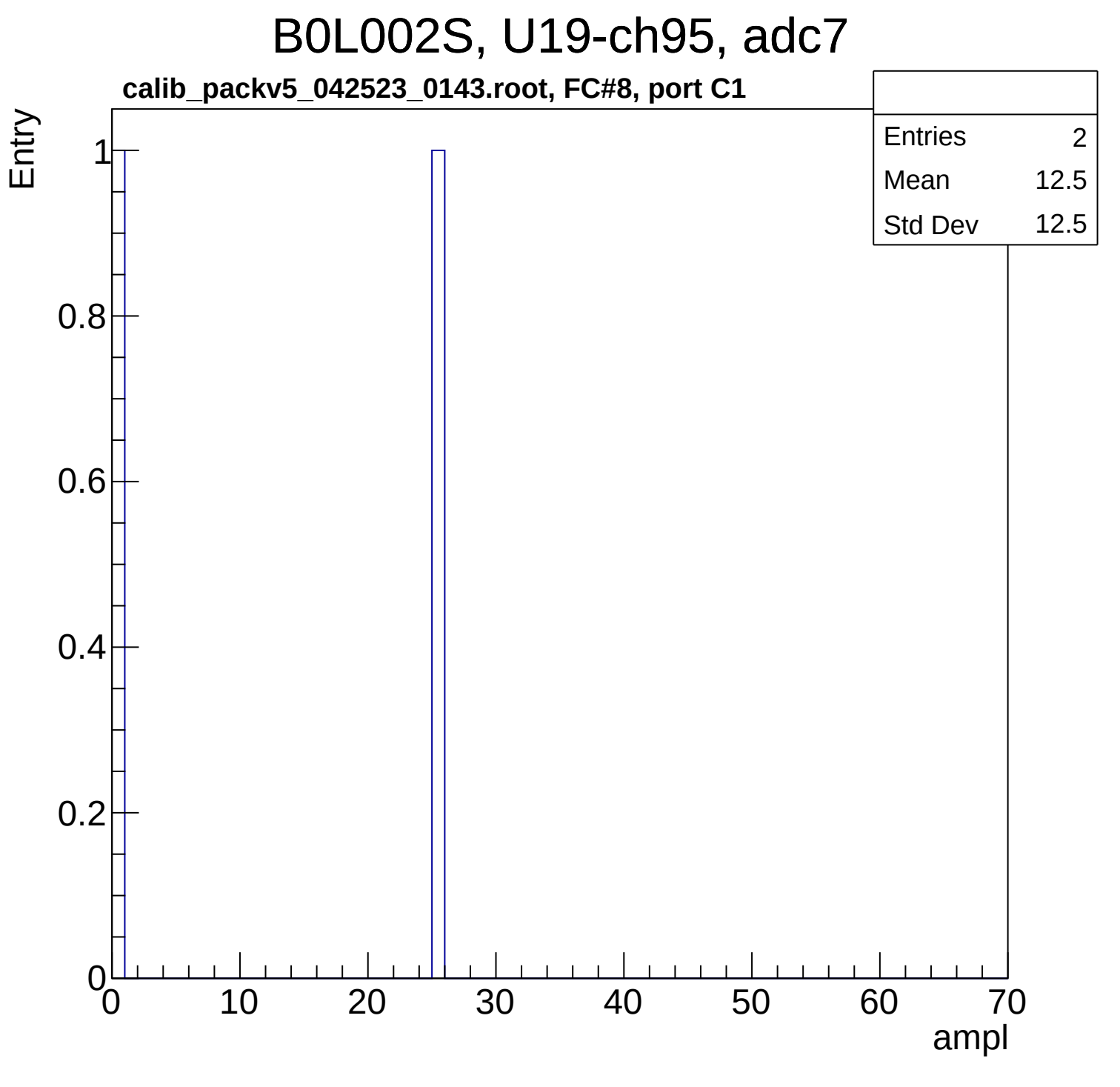

B0L002S, U19-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.23
Std Dev 3.343
Gaus mean : 44.2647
Gaus Width: 3.4080
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

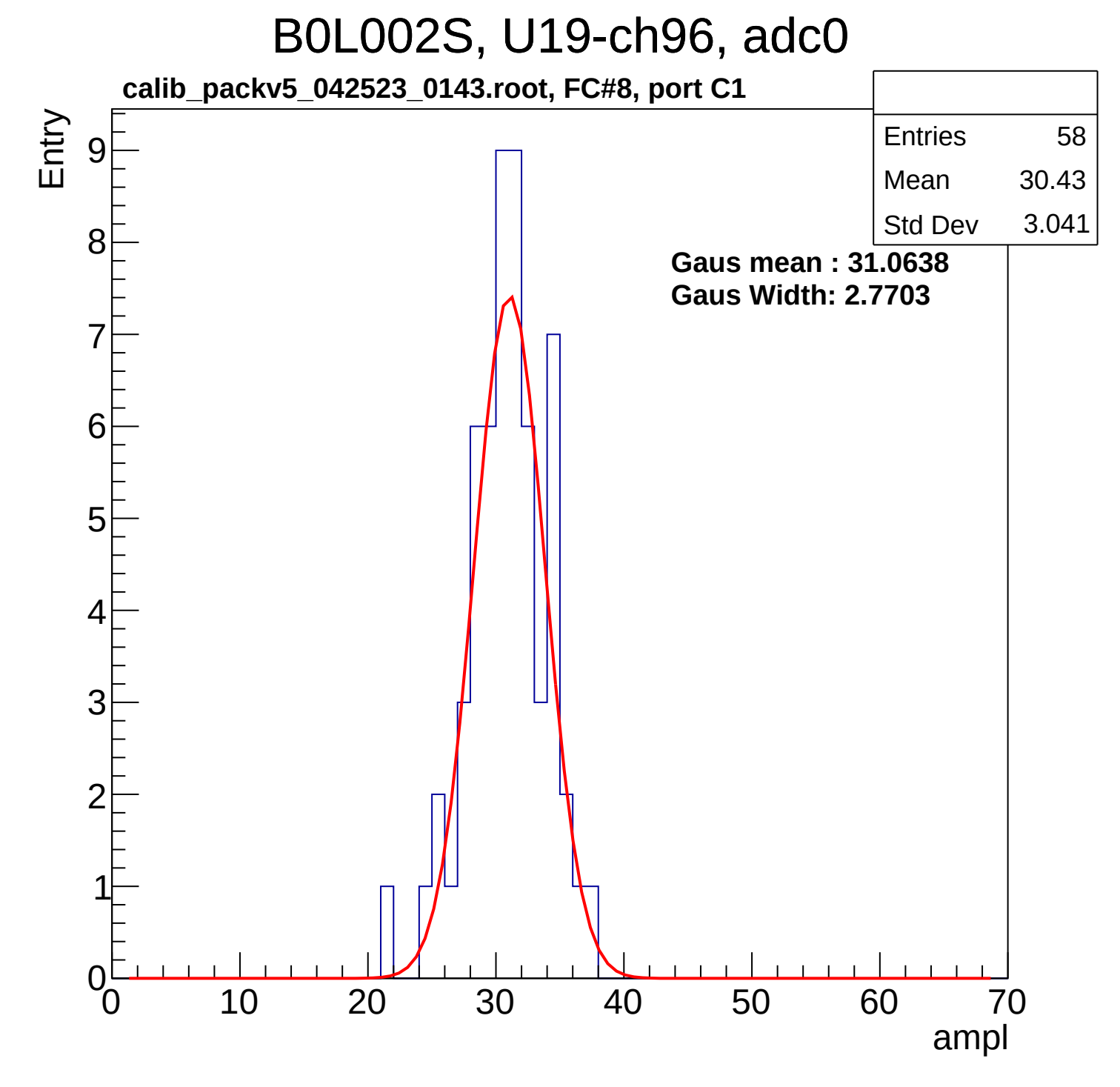

B0L002S, U19-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 50.18
Std Dev 3.497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

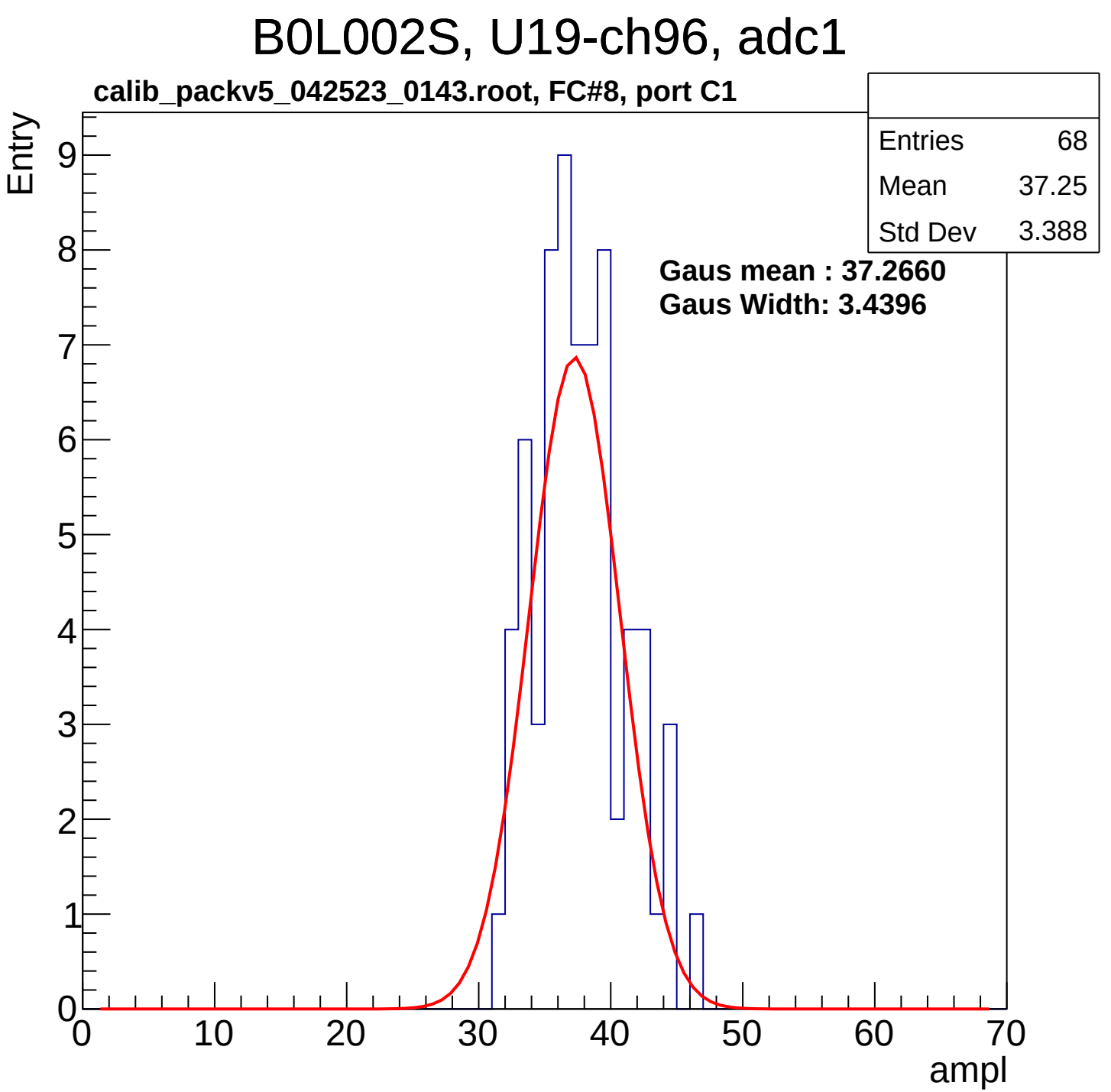

B0L002S, U19-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.82
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

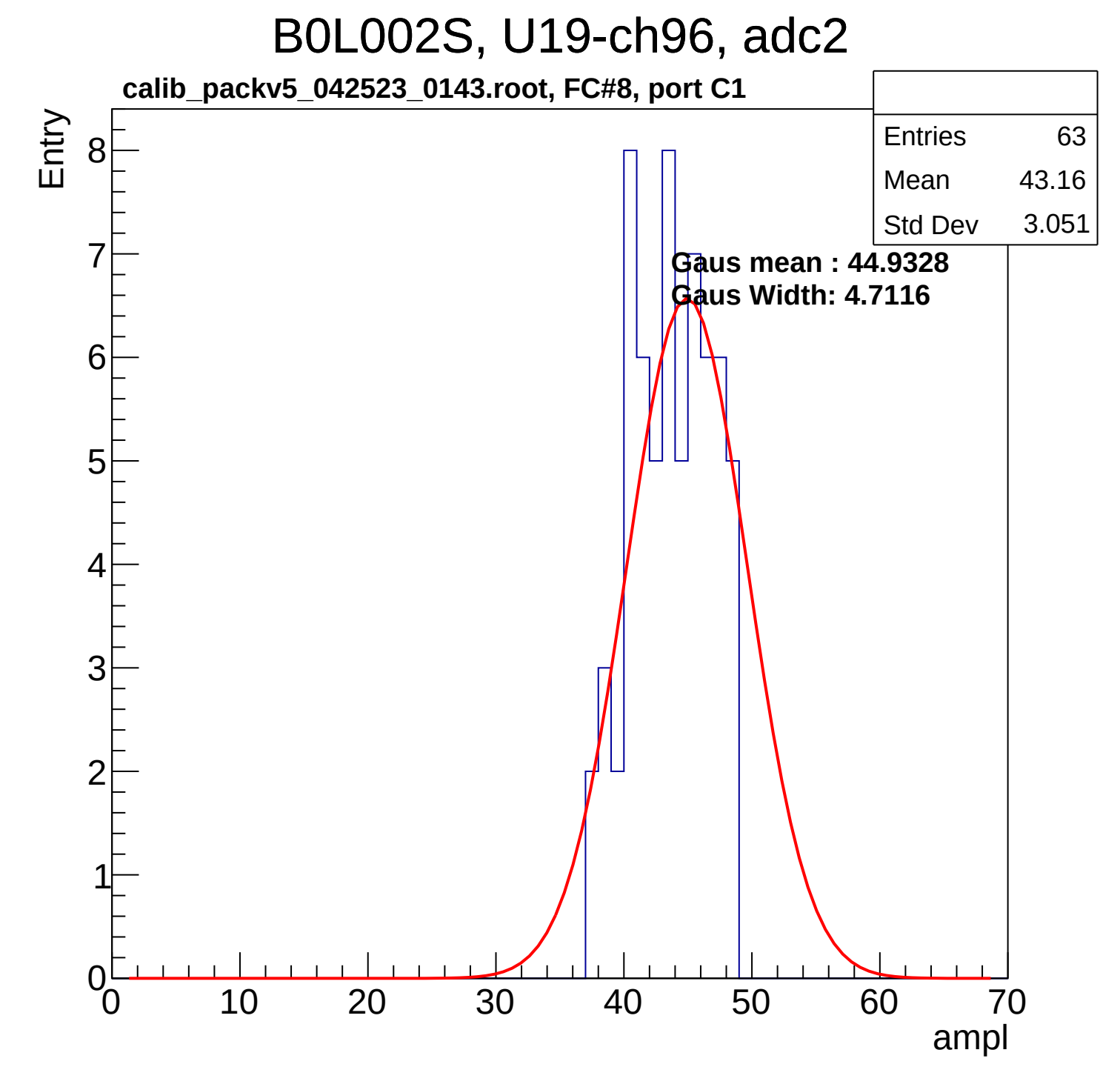

B0L002S, U19-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.28
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

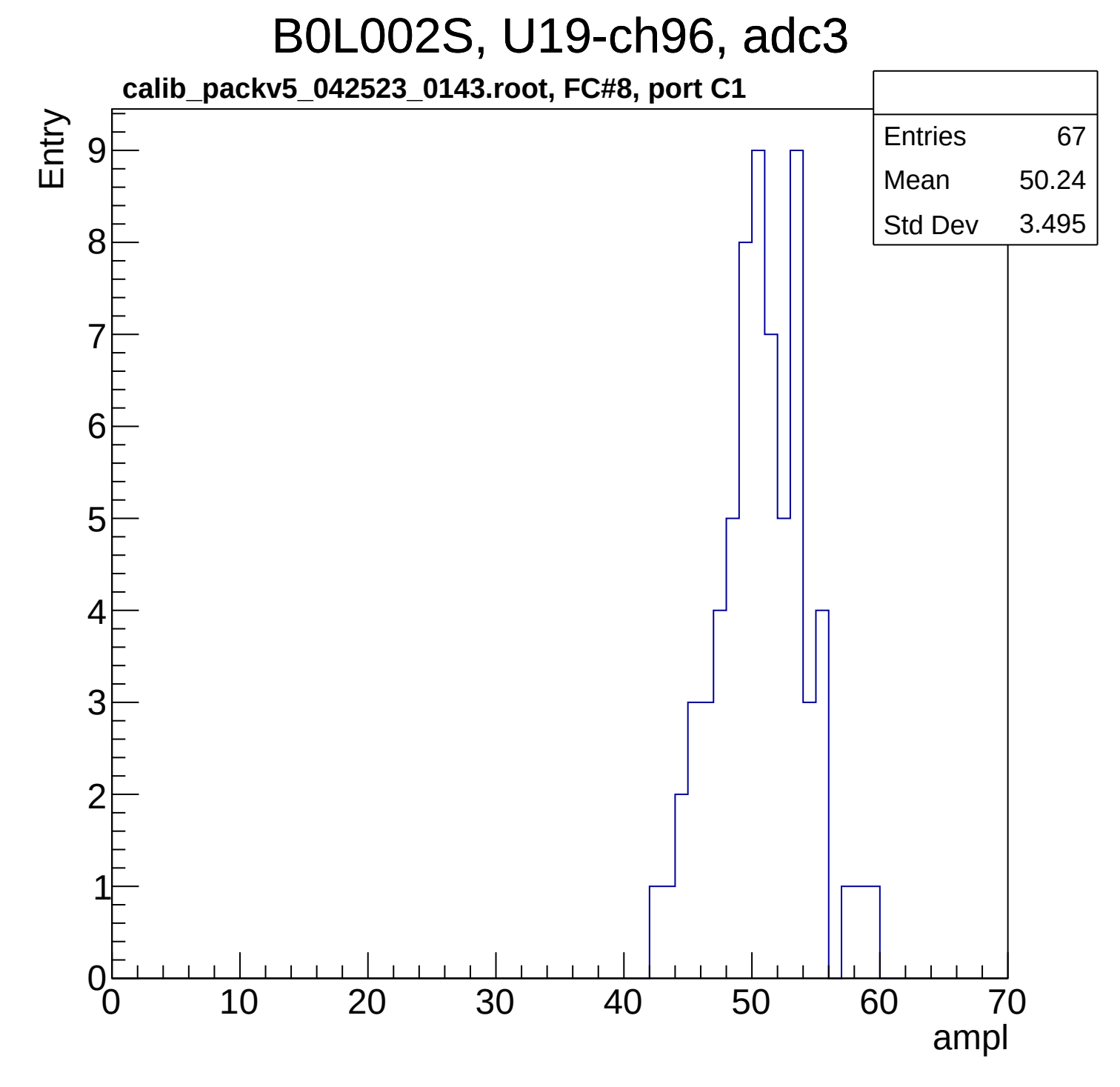

B0L002S, U19-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

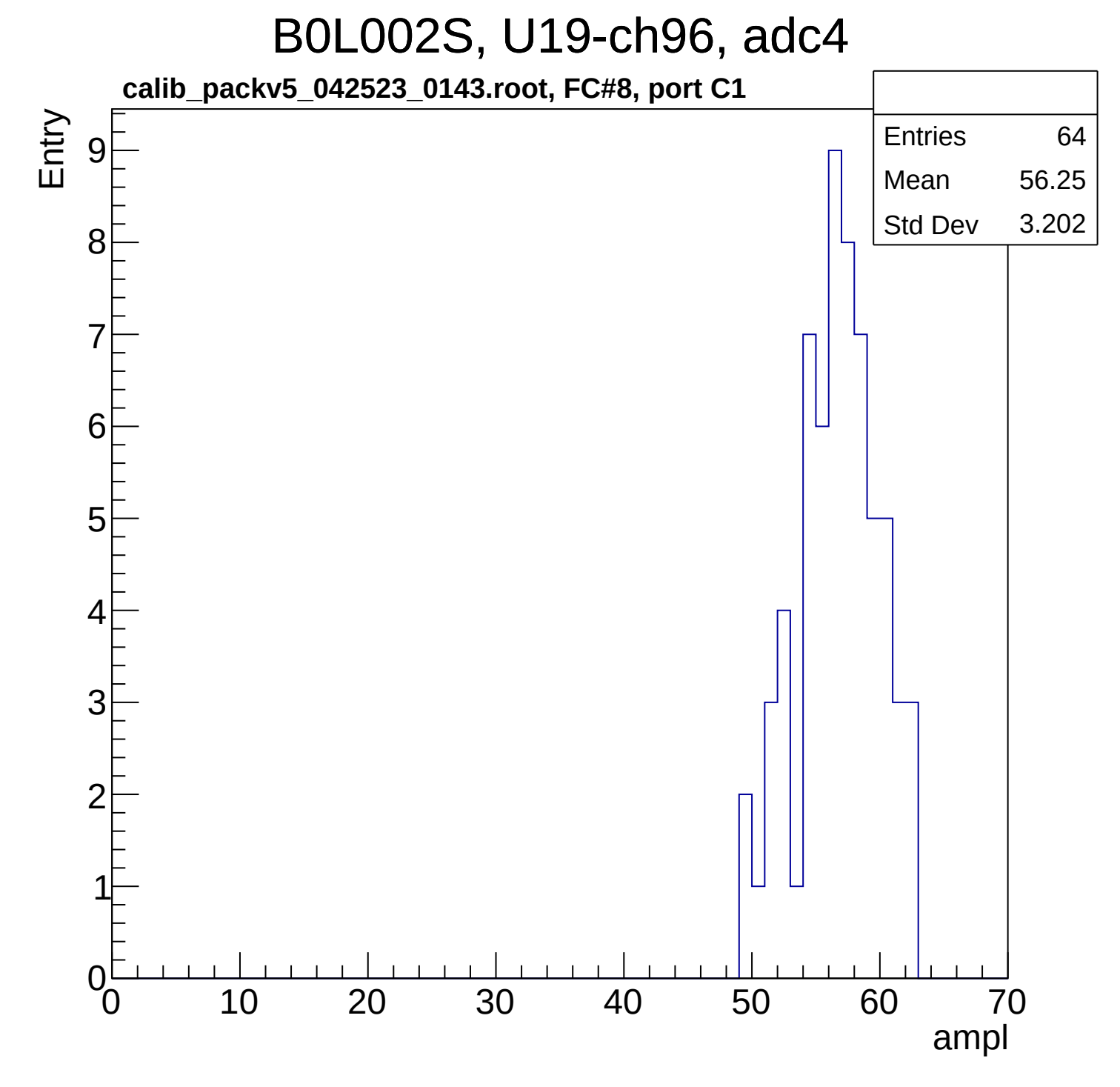

B0L002S, U19-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

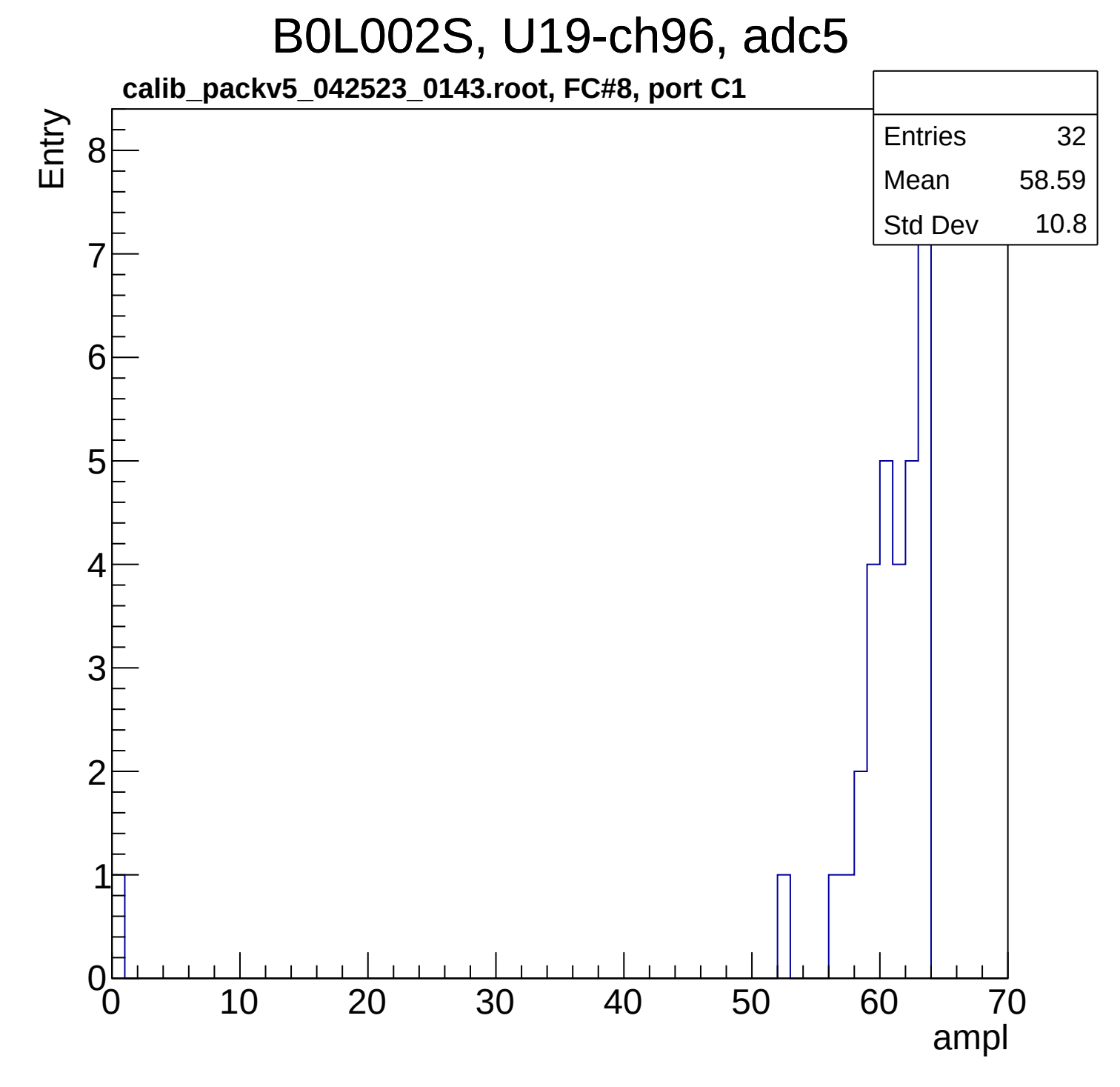

B0L002S, U19-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.43
Std Dev 3.041
Gaus mean : 31.0638
Gaus Width: 2.7703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

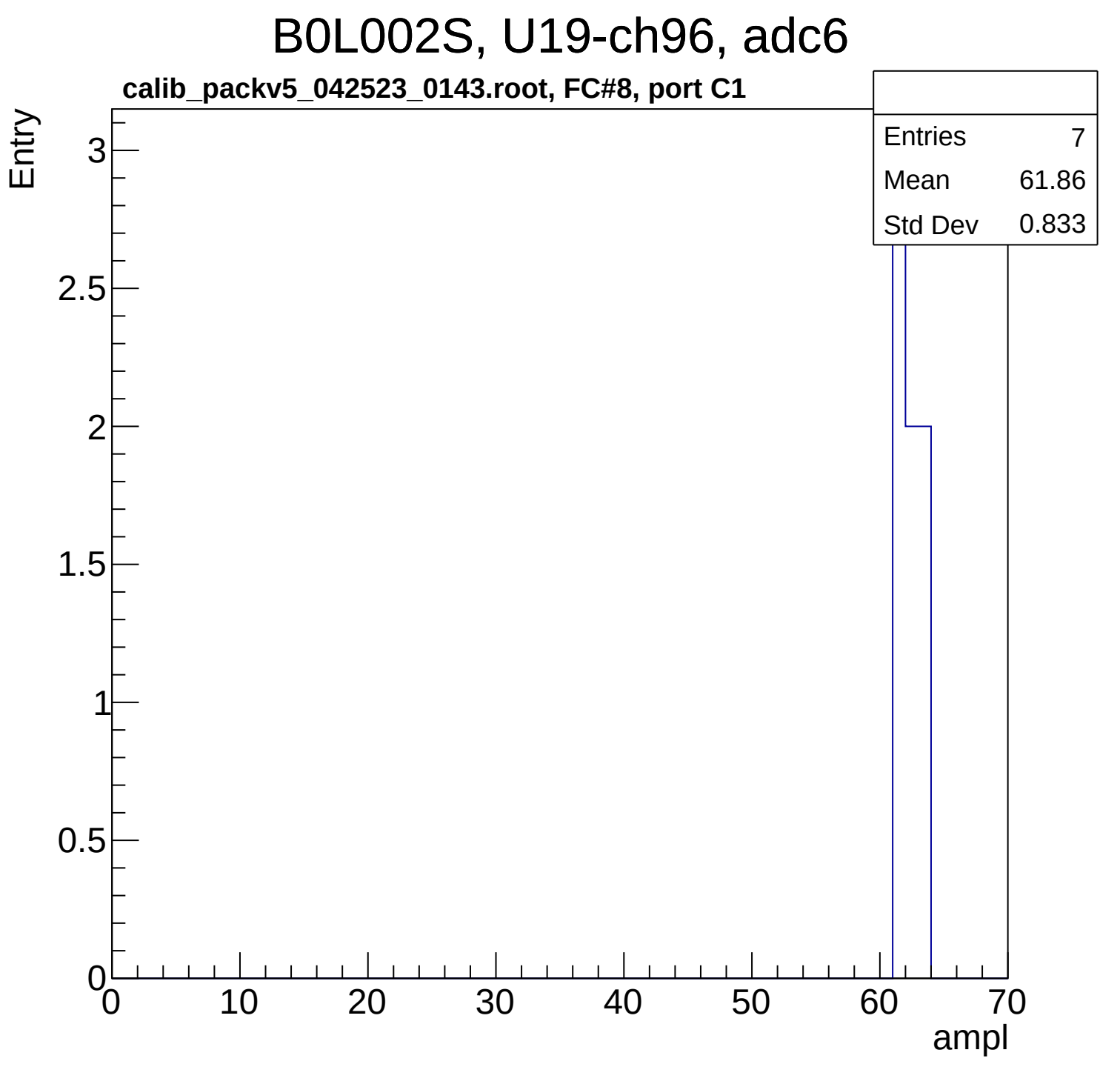

B0L002S, U19-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.25
Std Dev 3.388
Gaus mean : 37.2660
Gaus Width: 3.4396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

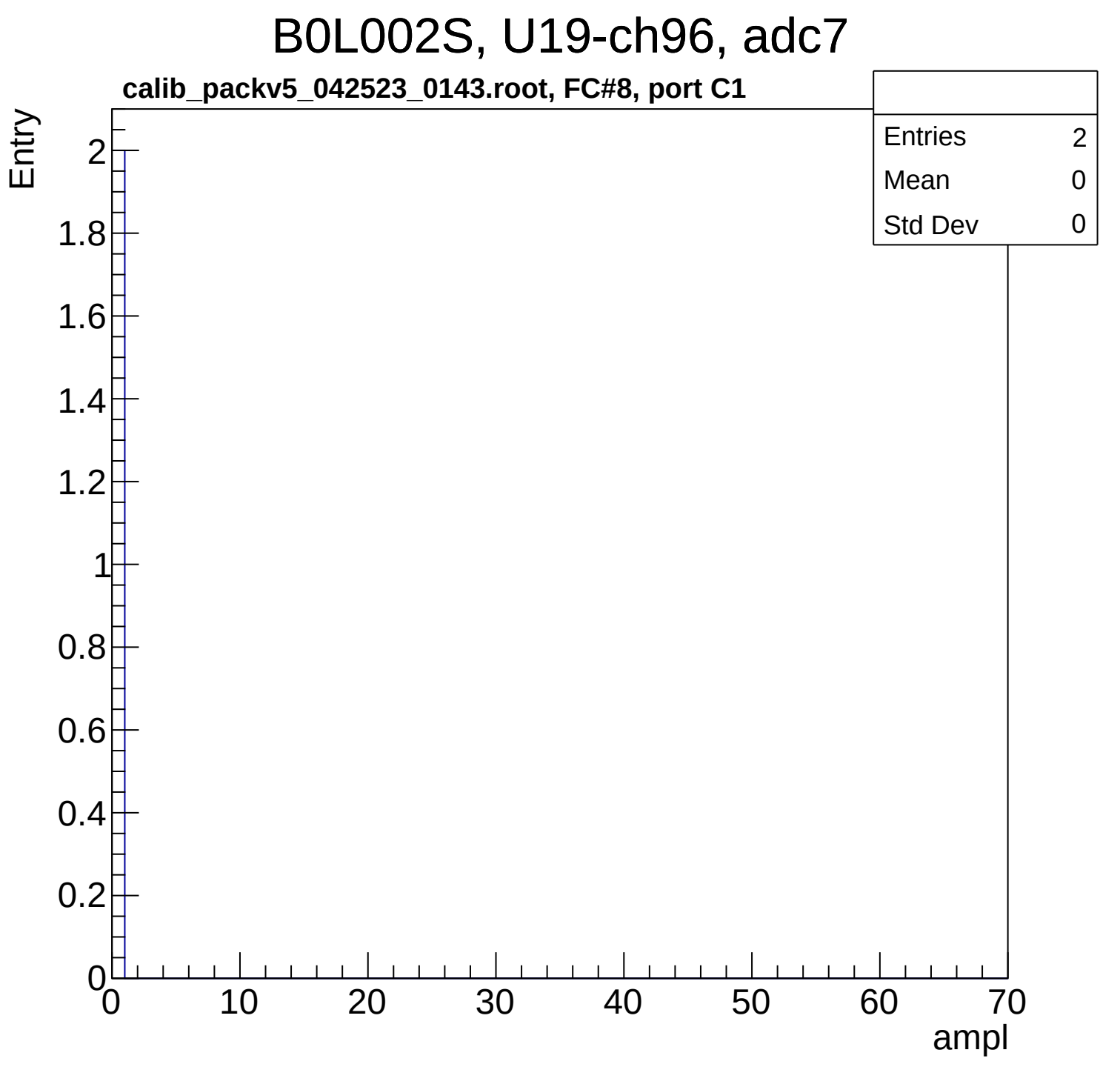

B0L002S, U19-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 43.16
Std Dev 3.051
Gaus mean : 44.9328
Gaus Width: 4.7116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

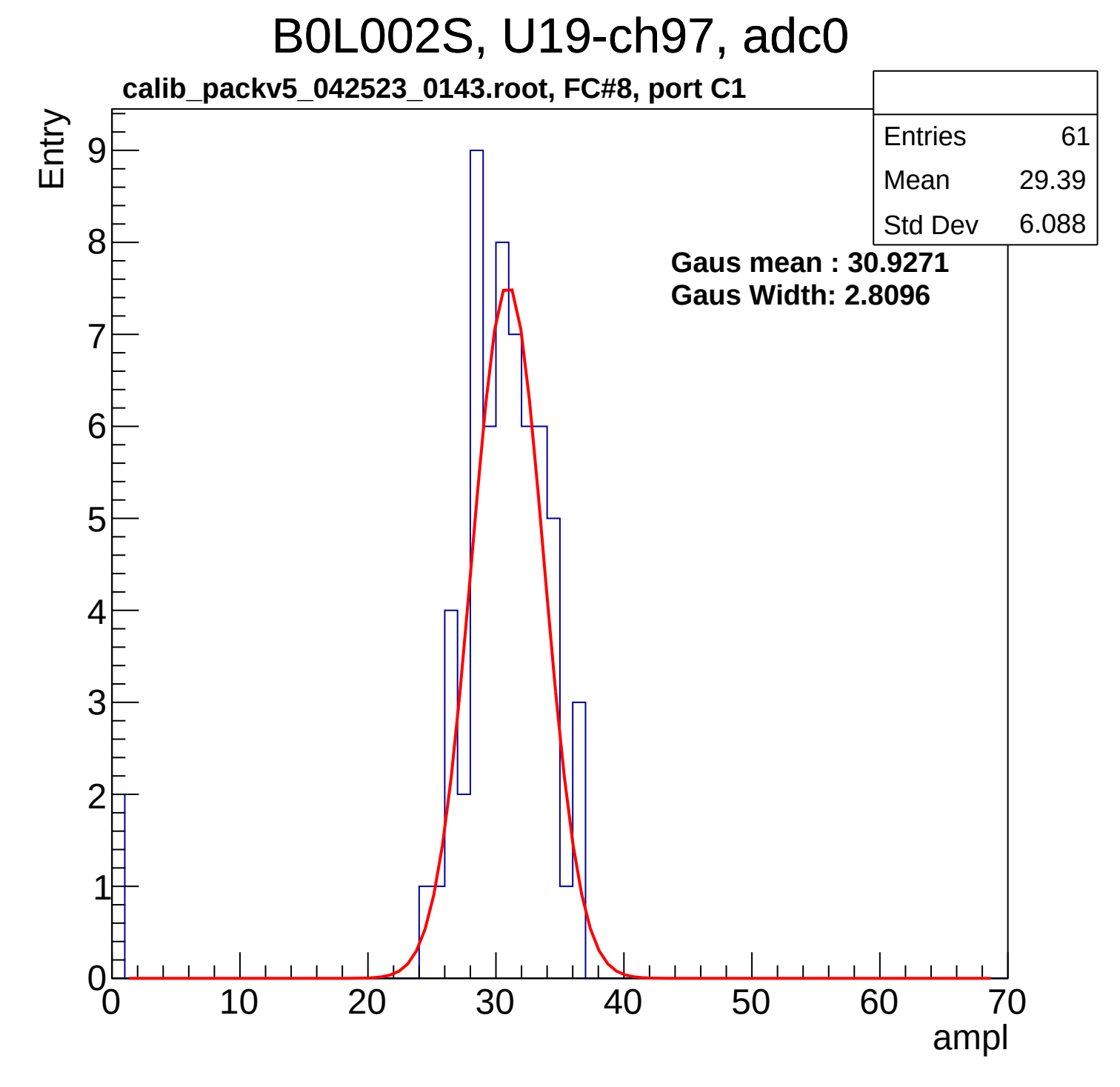

B0L002S, U19-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.24
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

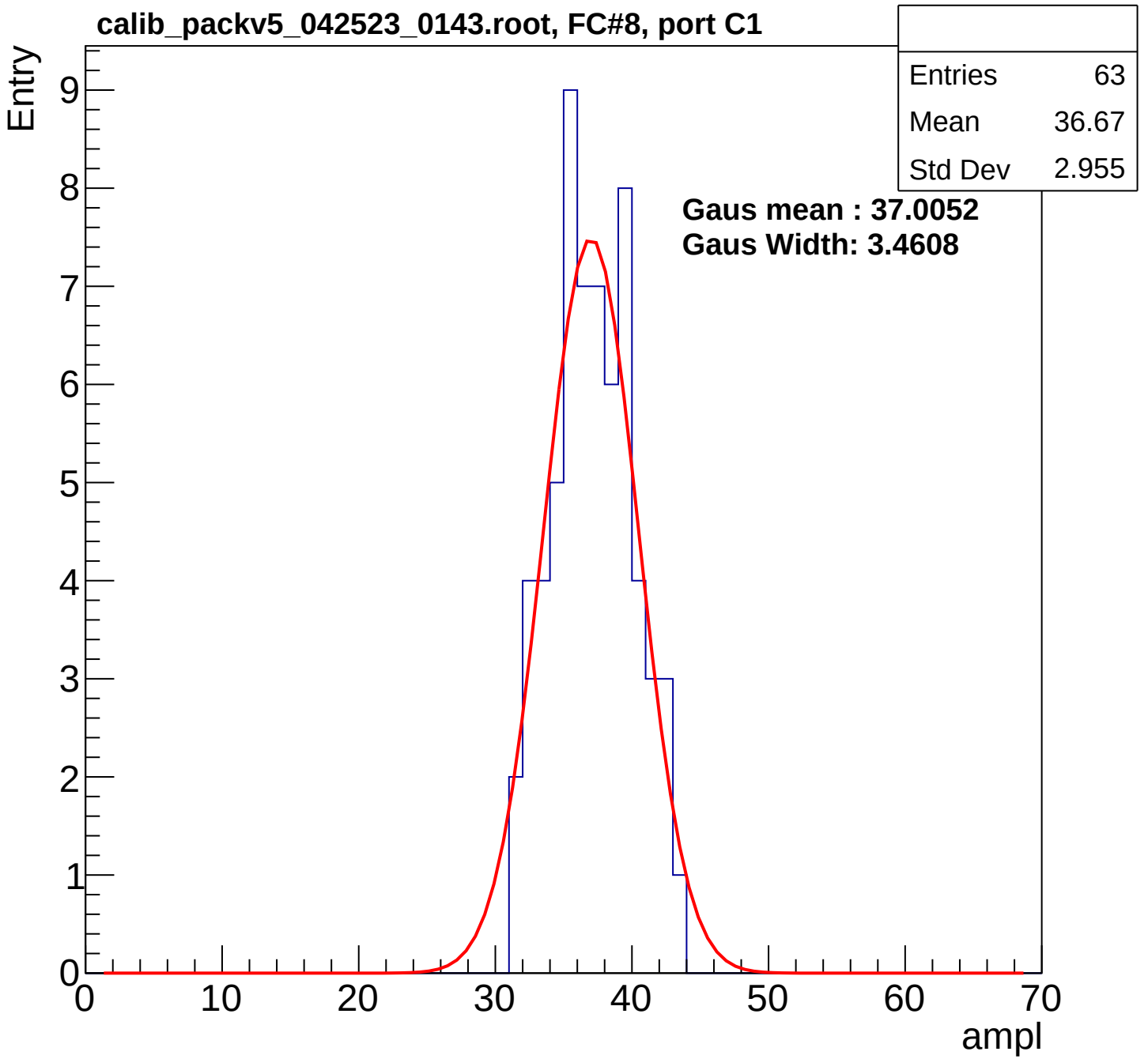

B0L002S, U19-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.25
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

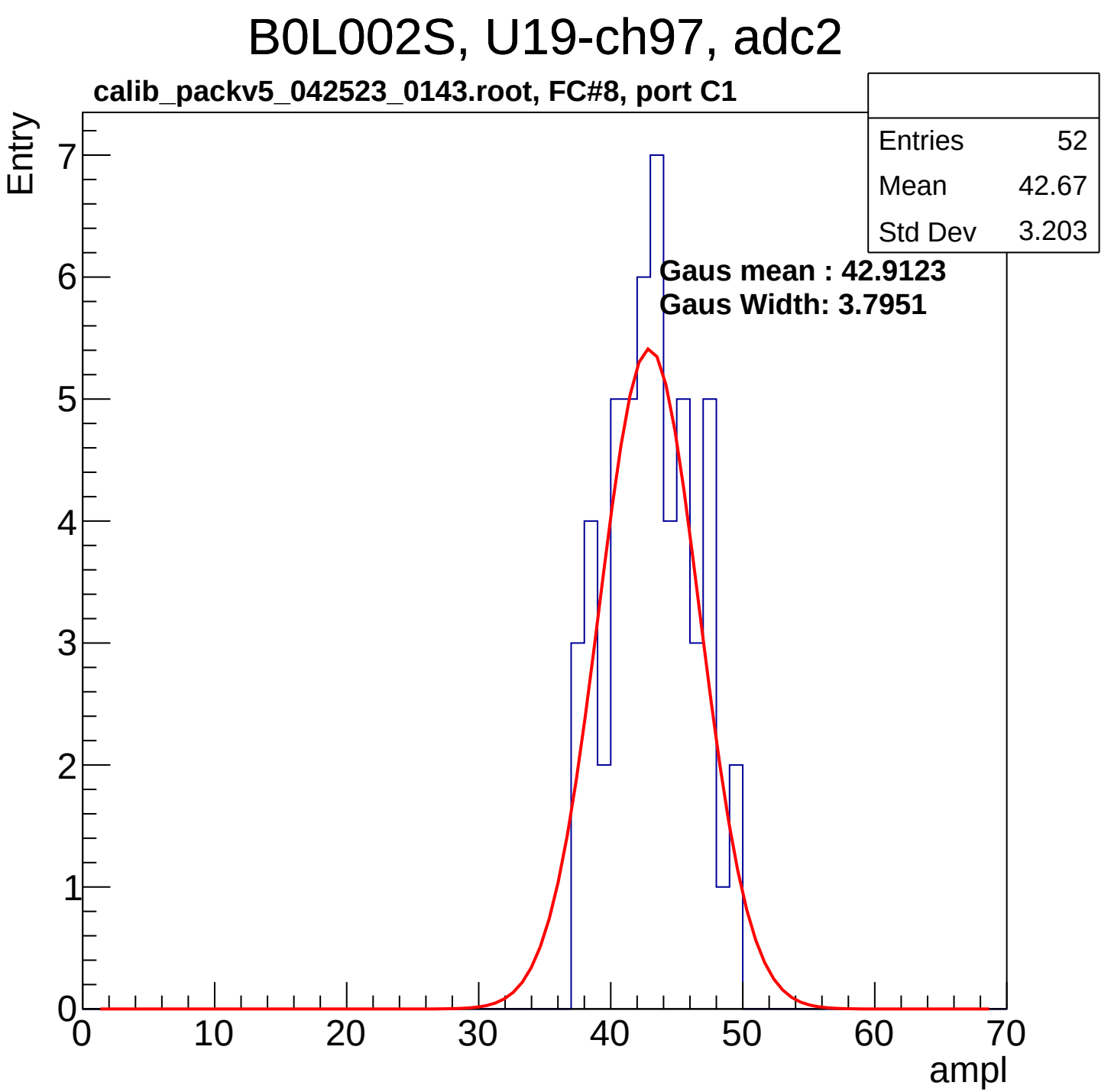

B0L002S, U19-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.59
Std Dev 10.8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

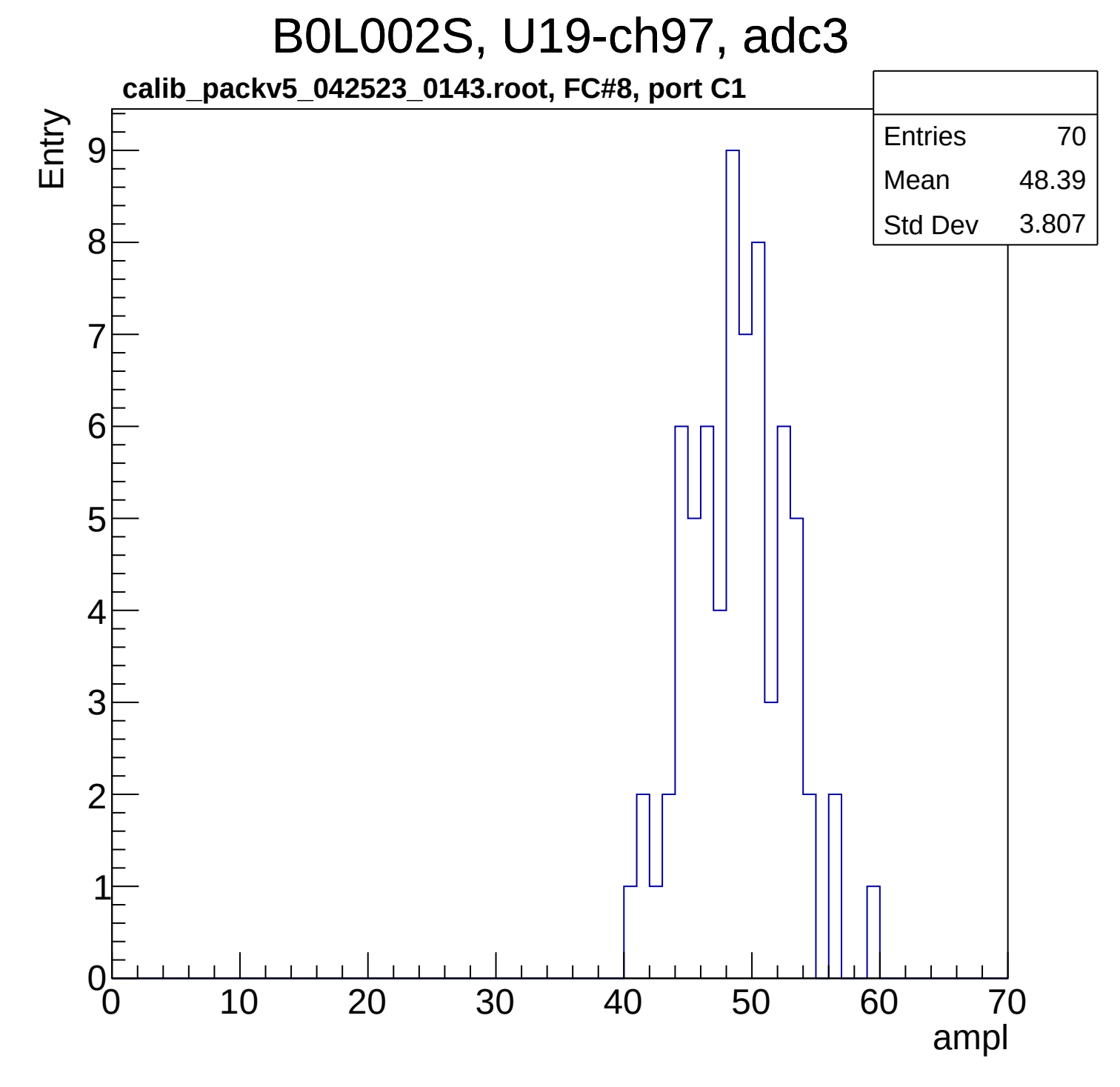

B0L002S, U19-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 61.86
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

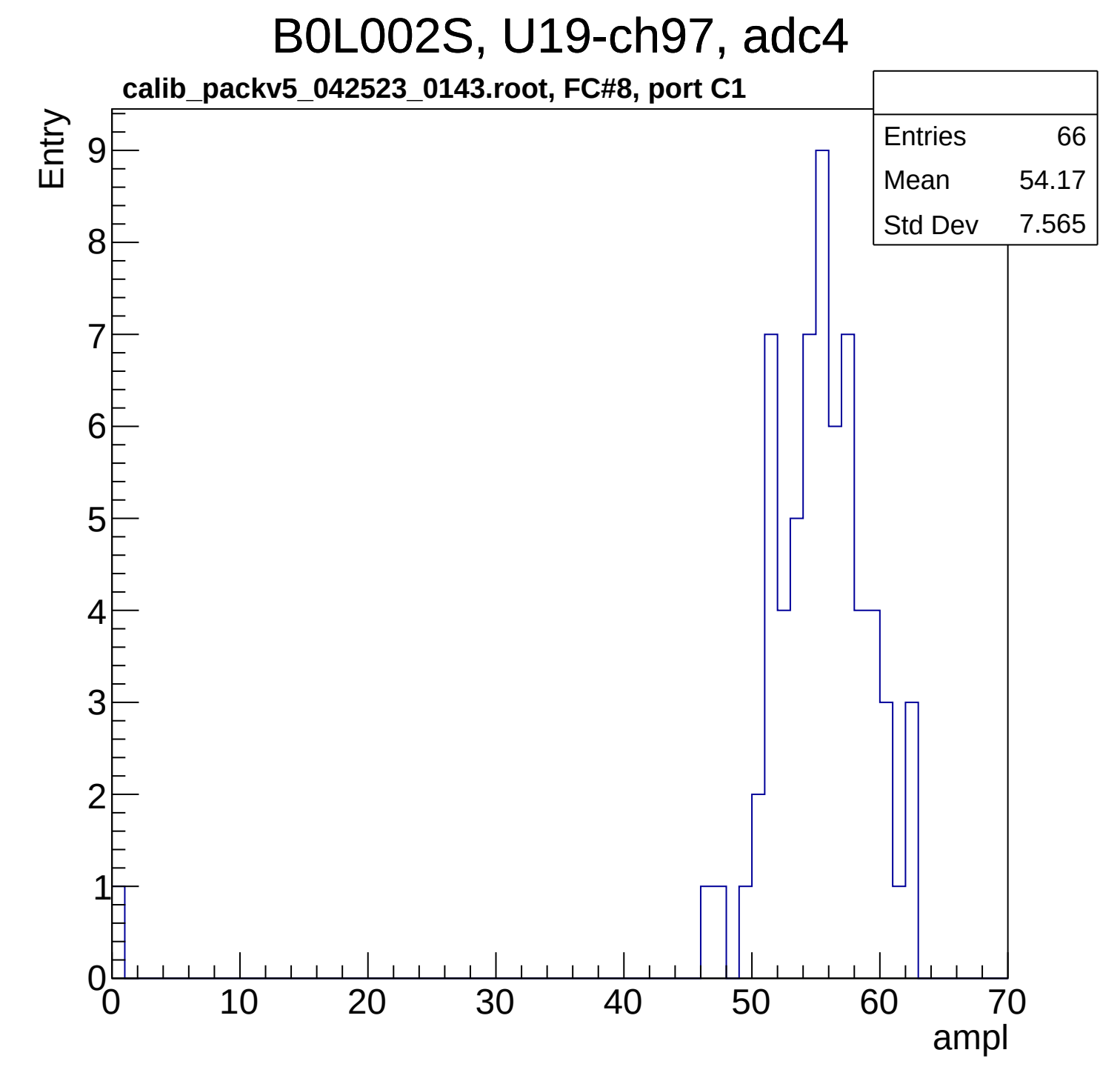

B0L002S, U19-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

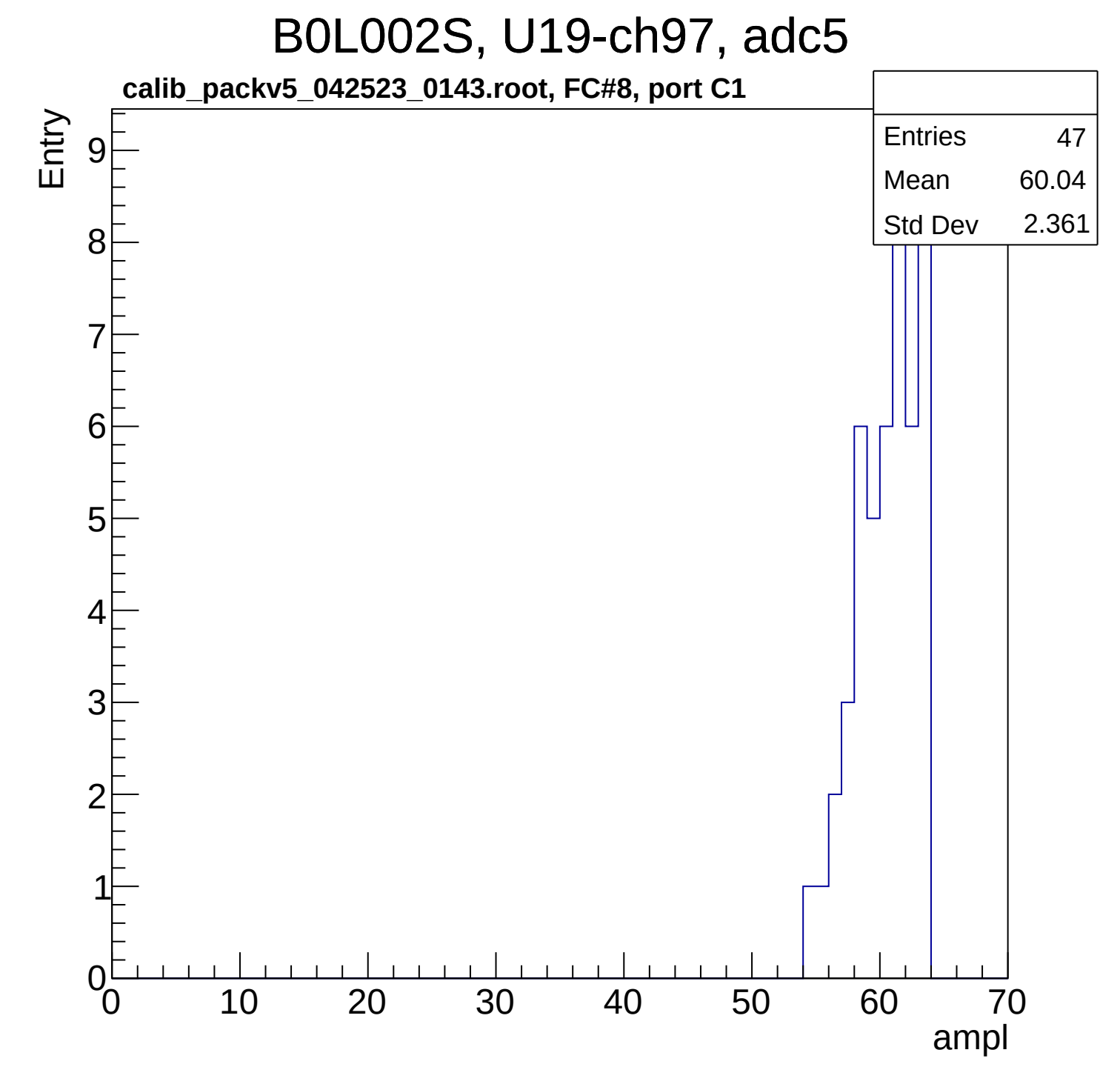

B0L002S, U19-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.39
Std Dev 6.088
Gaus mean : 30.9271
Gaus Width: 2.8096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

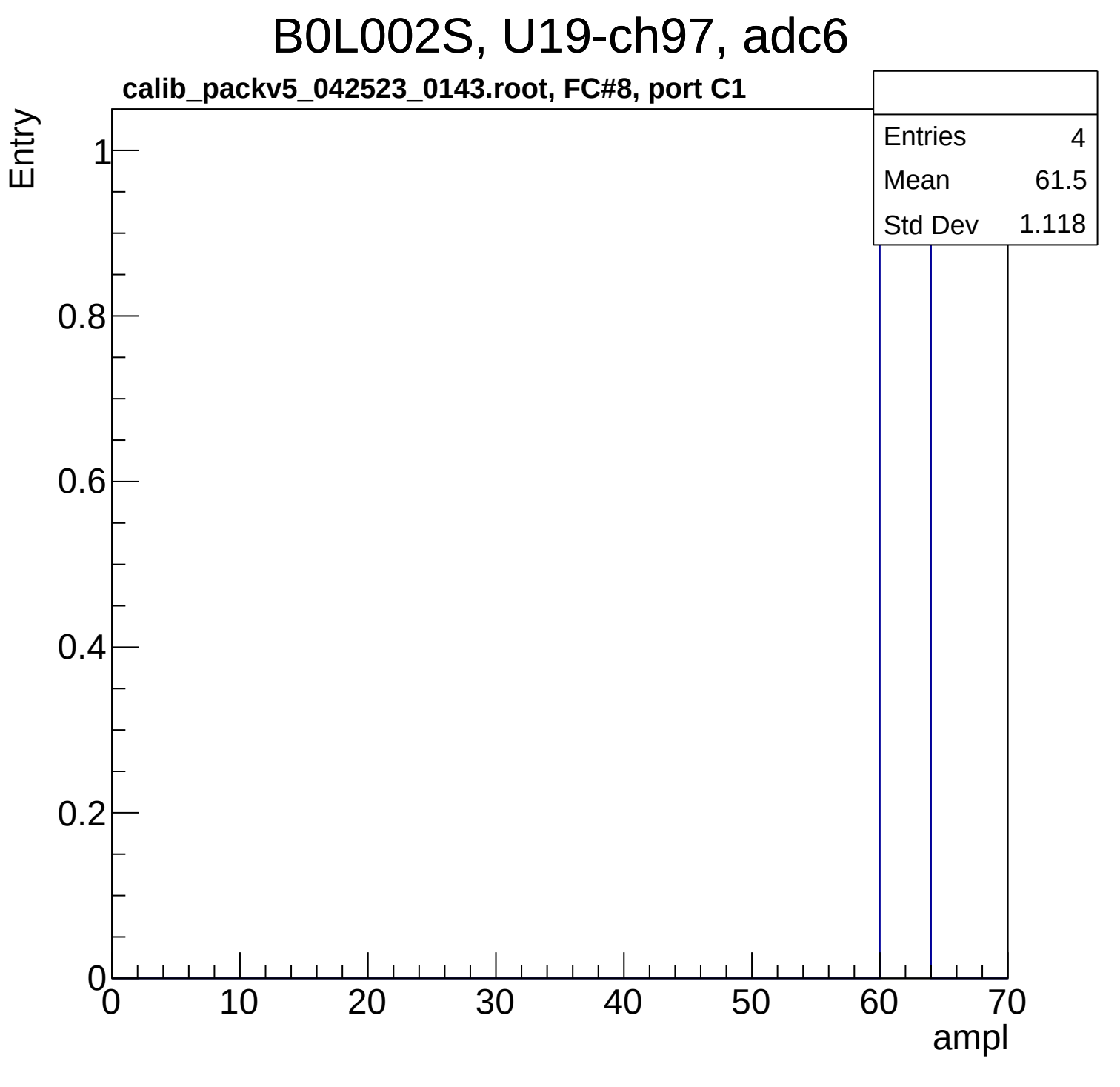

B0L002S, U19-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 36.67
Std Dev 2.955
Gaus mean : 37.0052
Gaus Width: 3.4608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

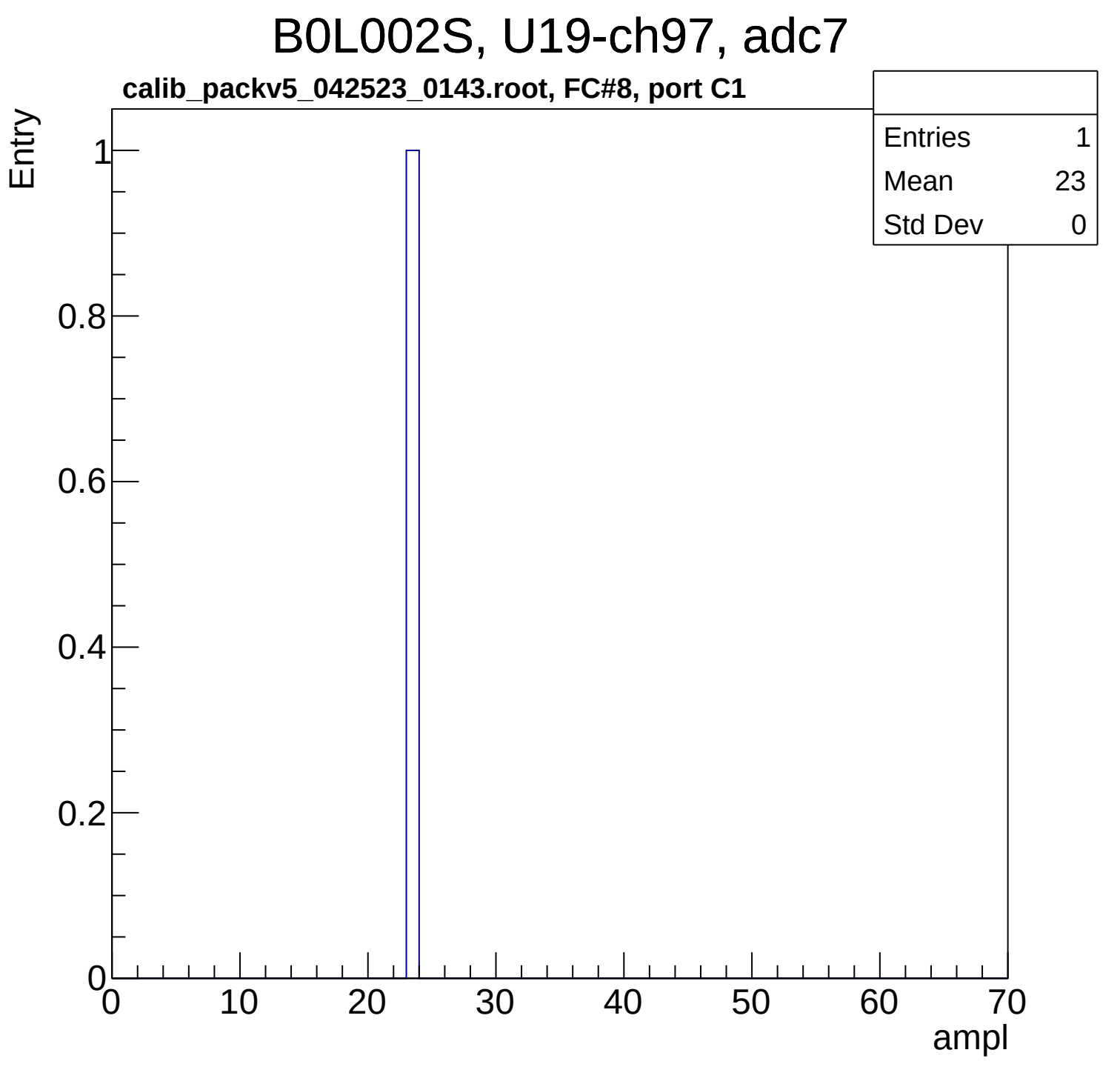

B0L002S, U19-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 42.67
Std Dev 3.203
Gaus mean : 42.9123
Gaus Width: 3.7951
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

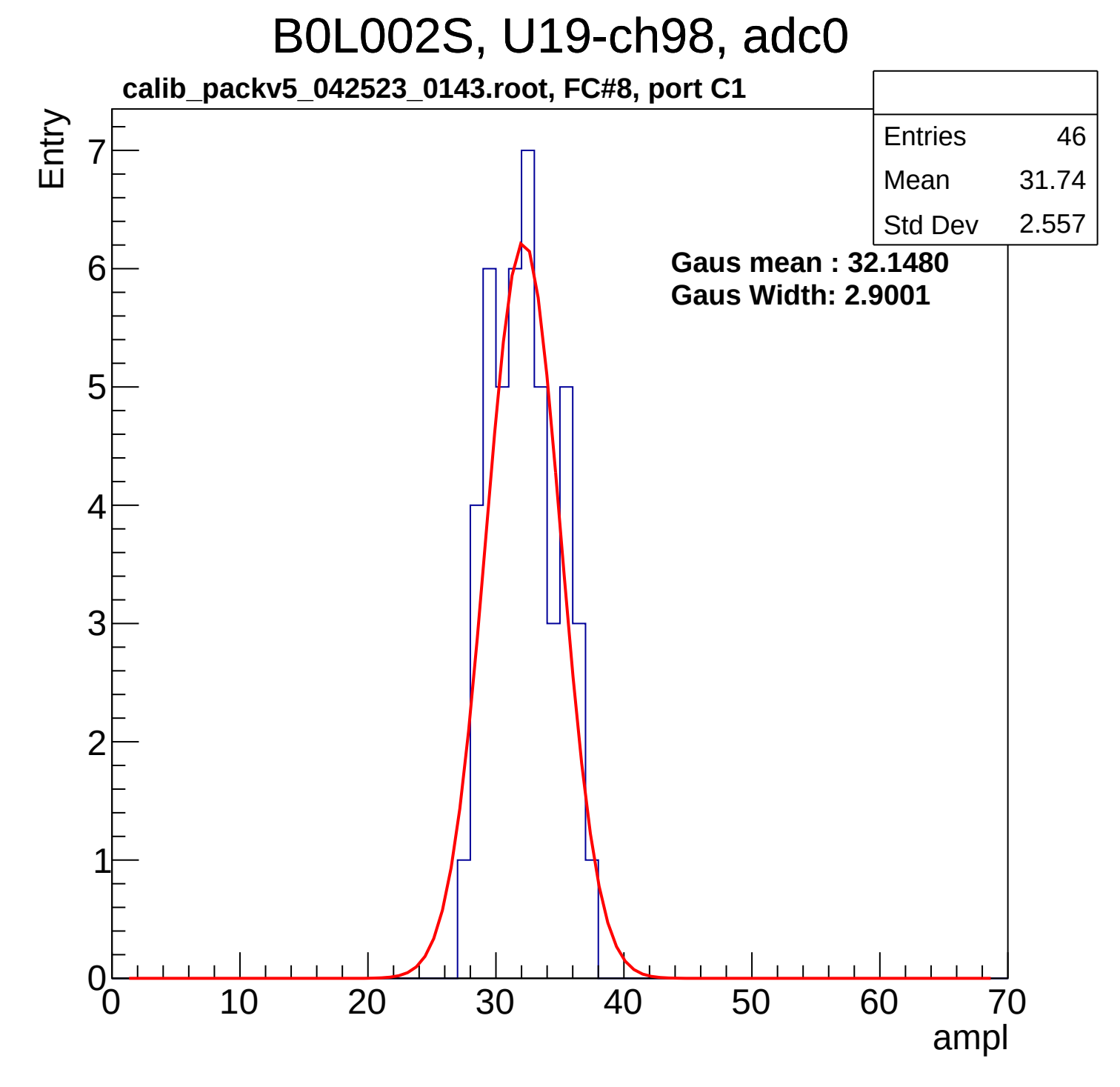

B0L002S, U19-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 48.39
Std Dev 3.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

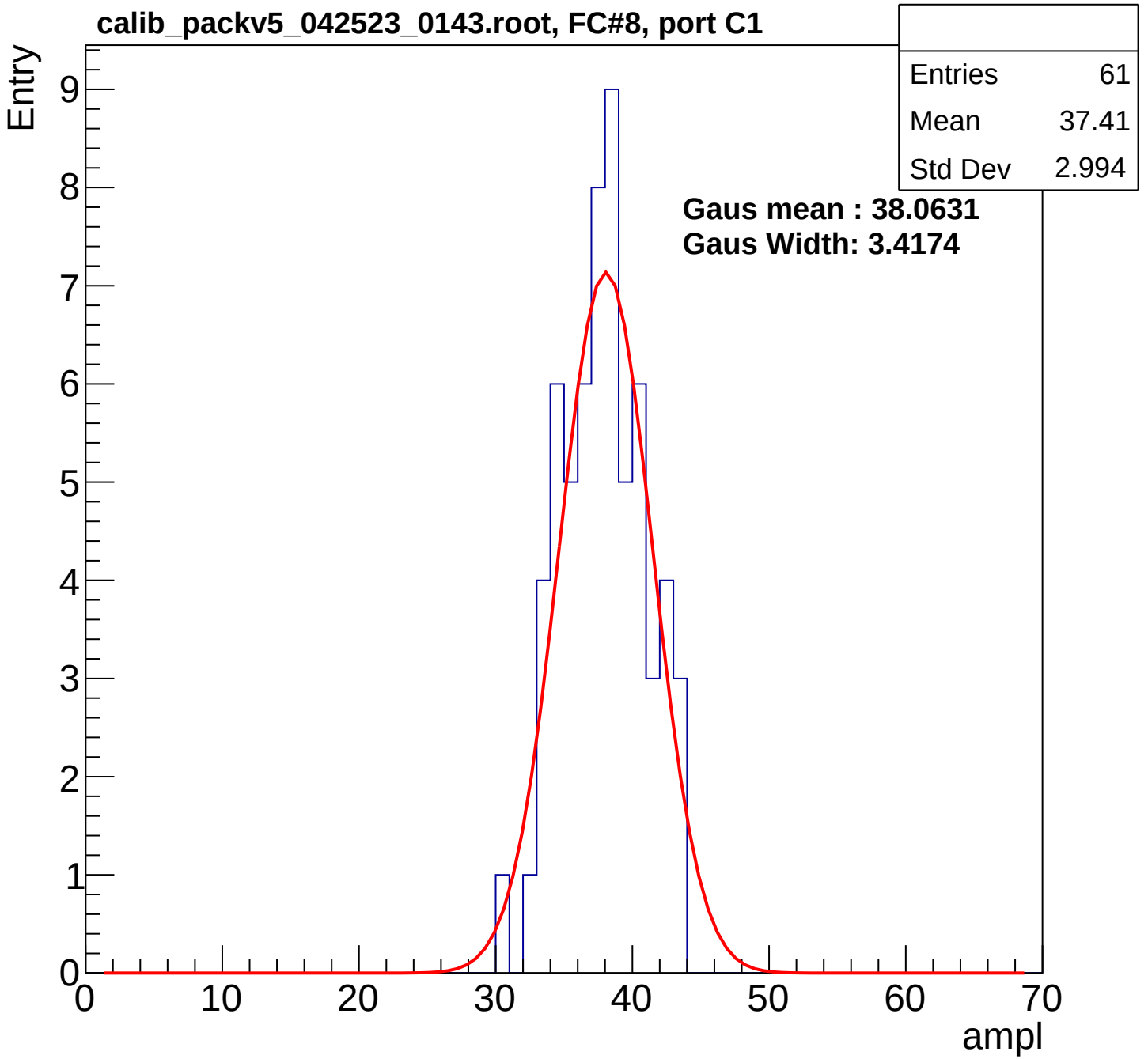

B0L002S, U19-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 54.17
Std Dev 7.565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

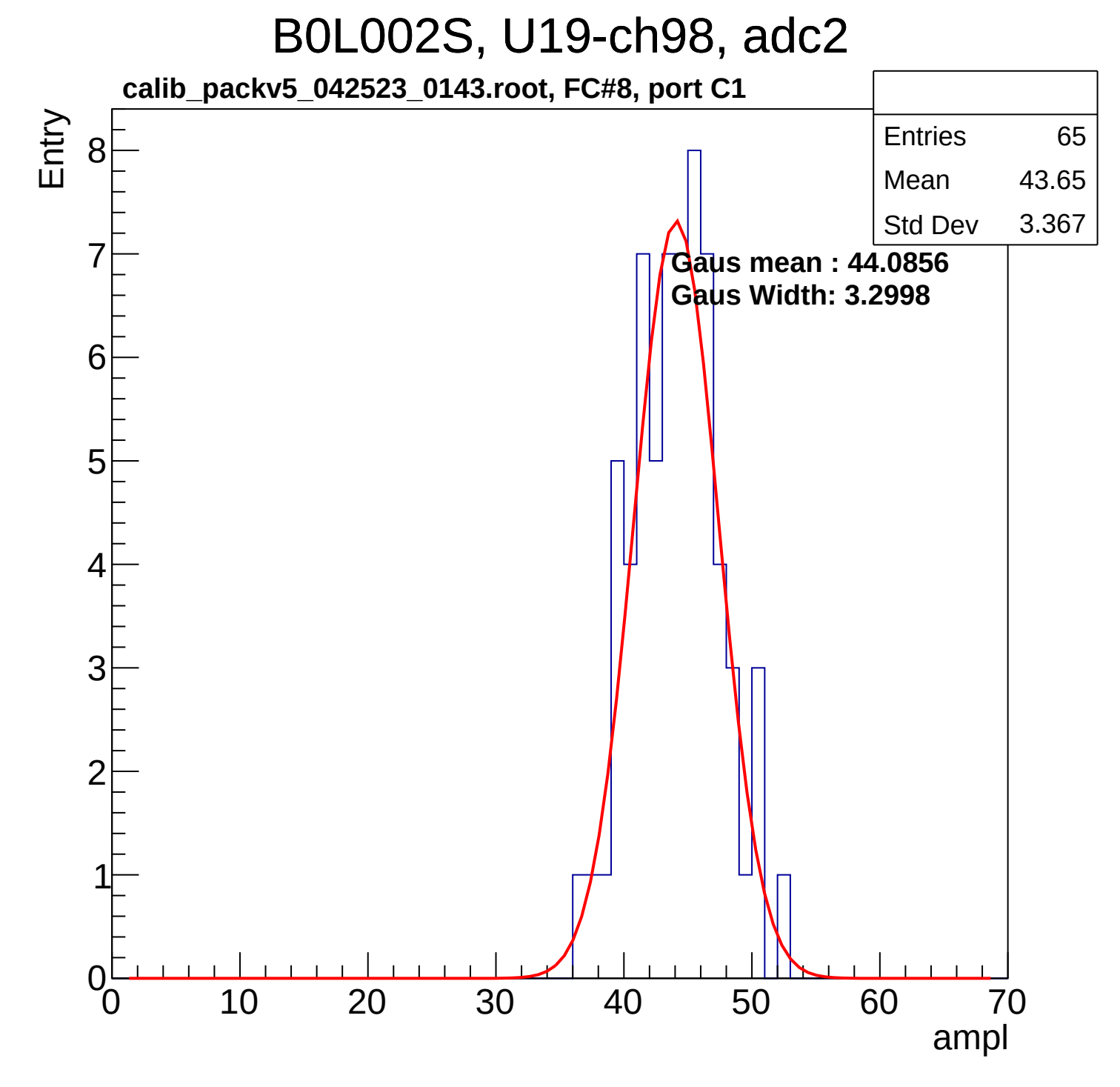

B0L002S, U19-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 60.04
Std Dev 2.361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

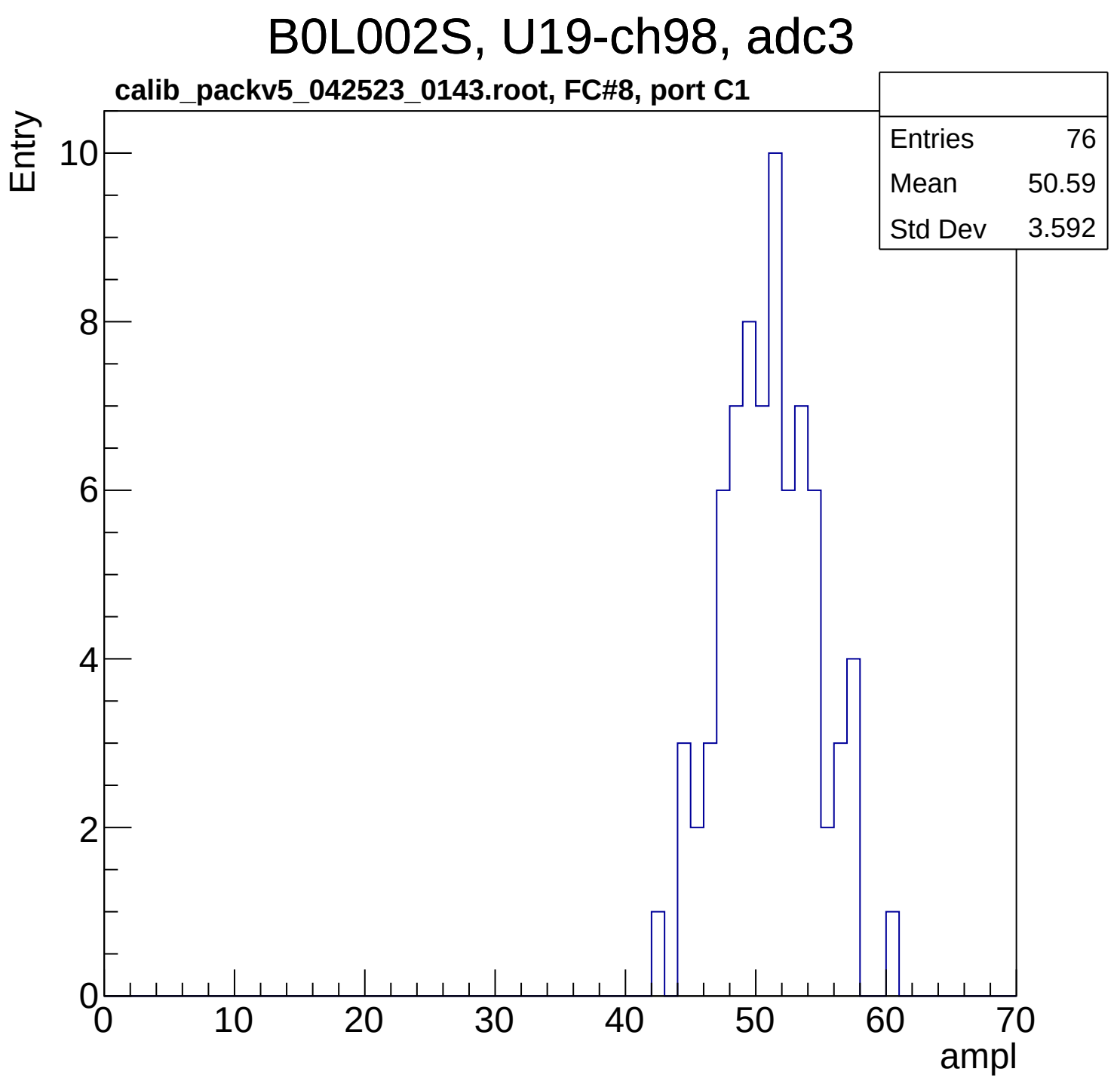

B0L002S, U19-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

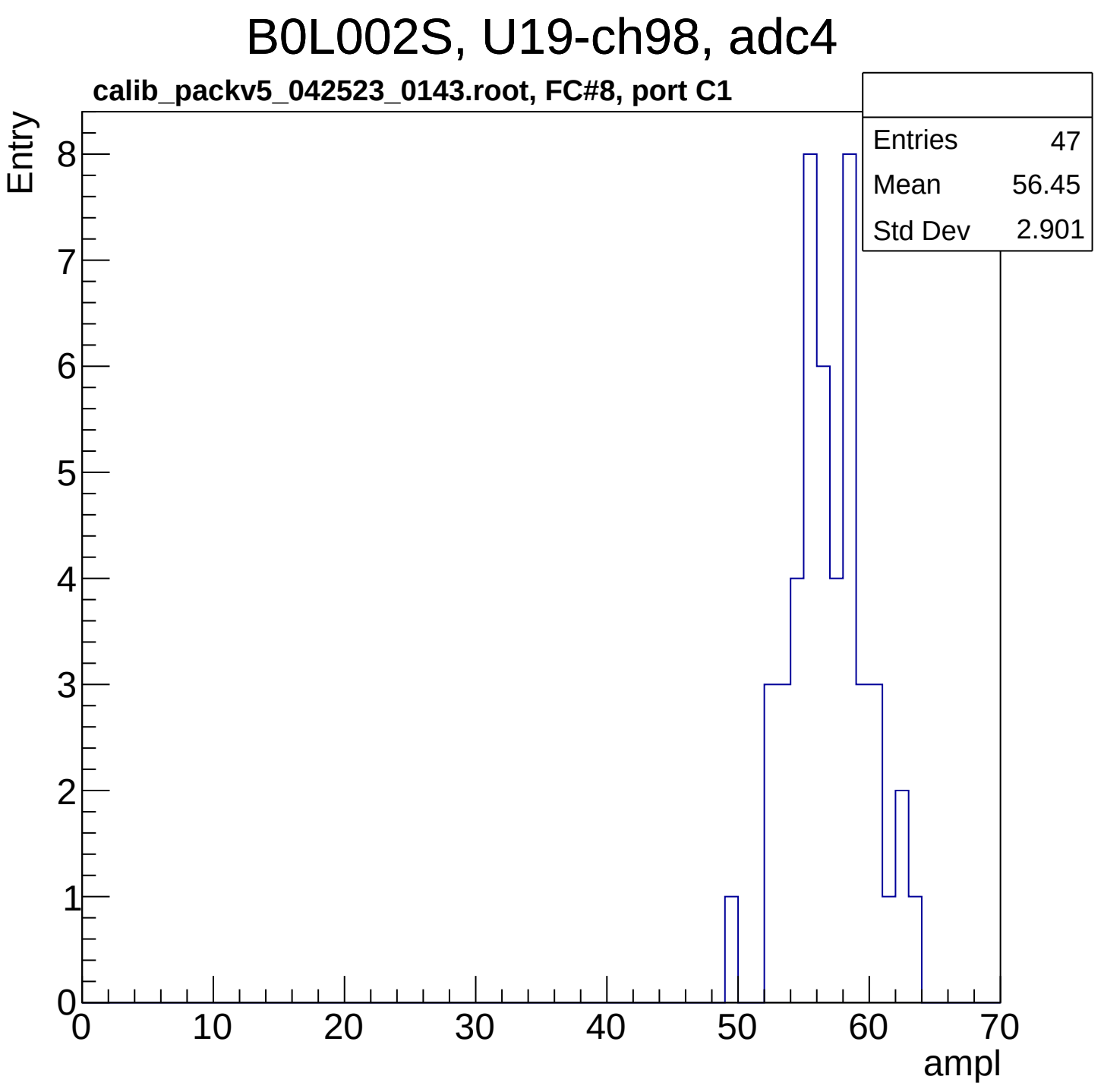

B0L002S, U19-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

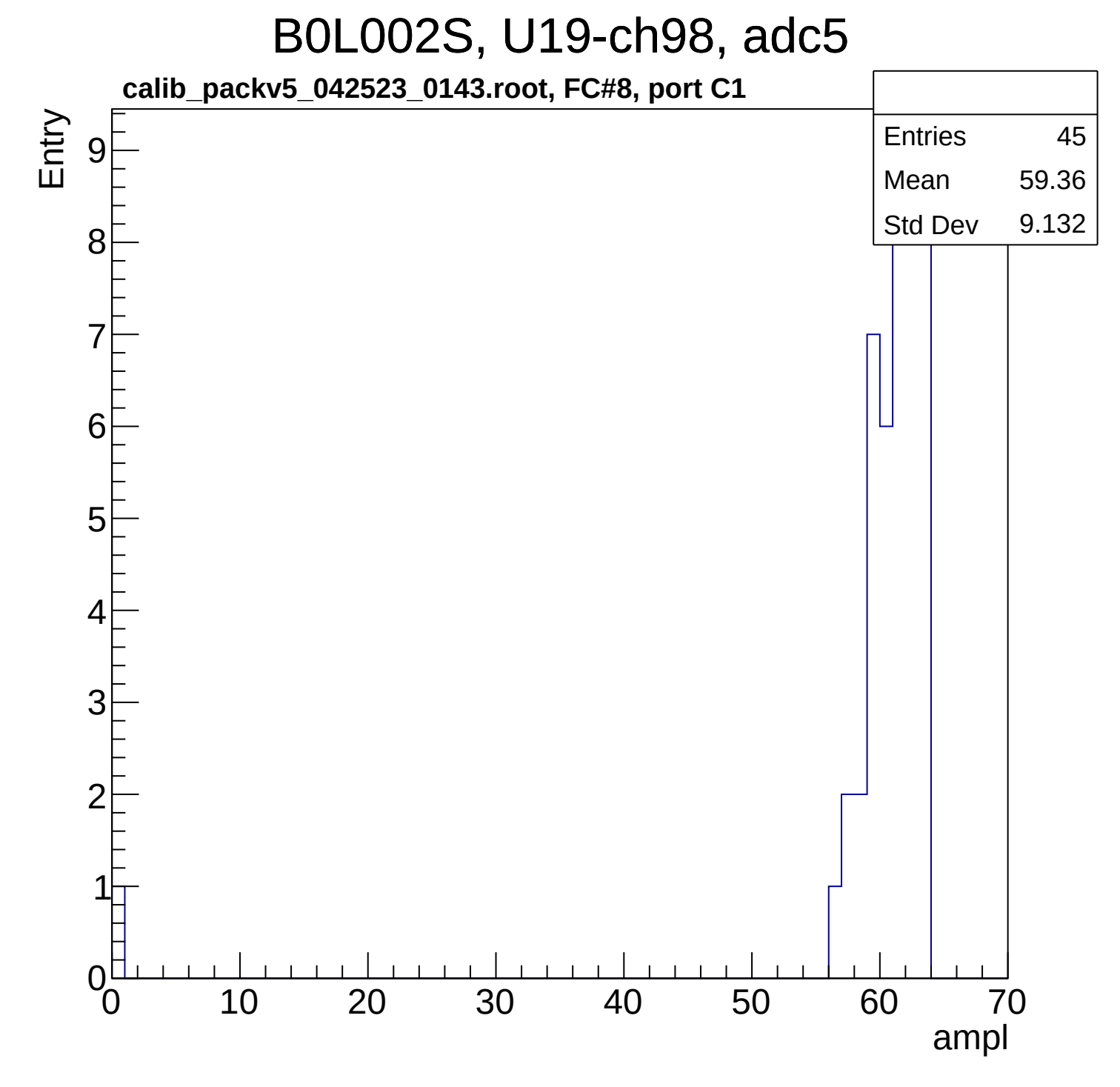

B0L002S, U19-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 31.74
Std Dev 2.557
Gaus mean : 32.1480
Gaus Width: 2.9001
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

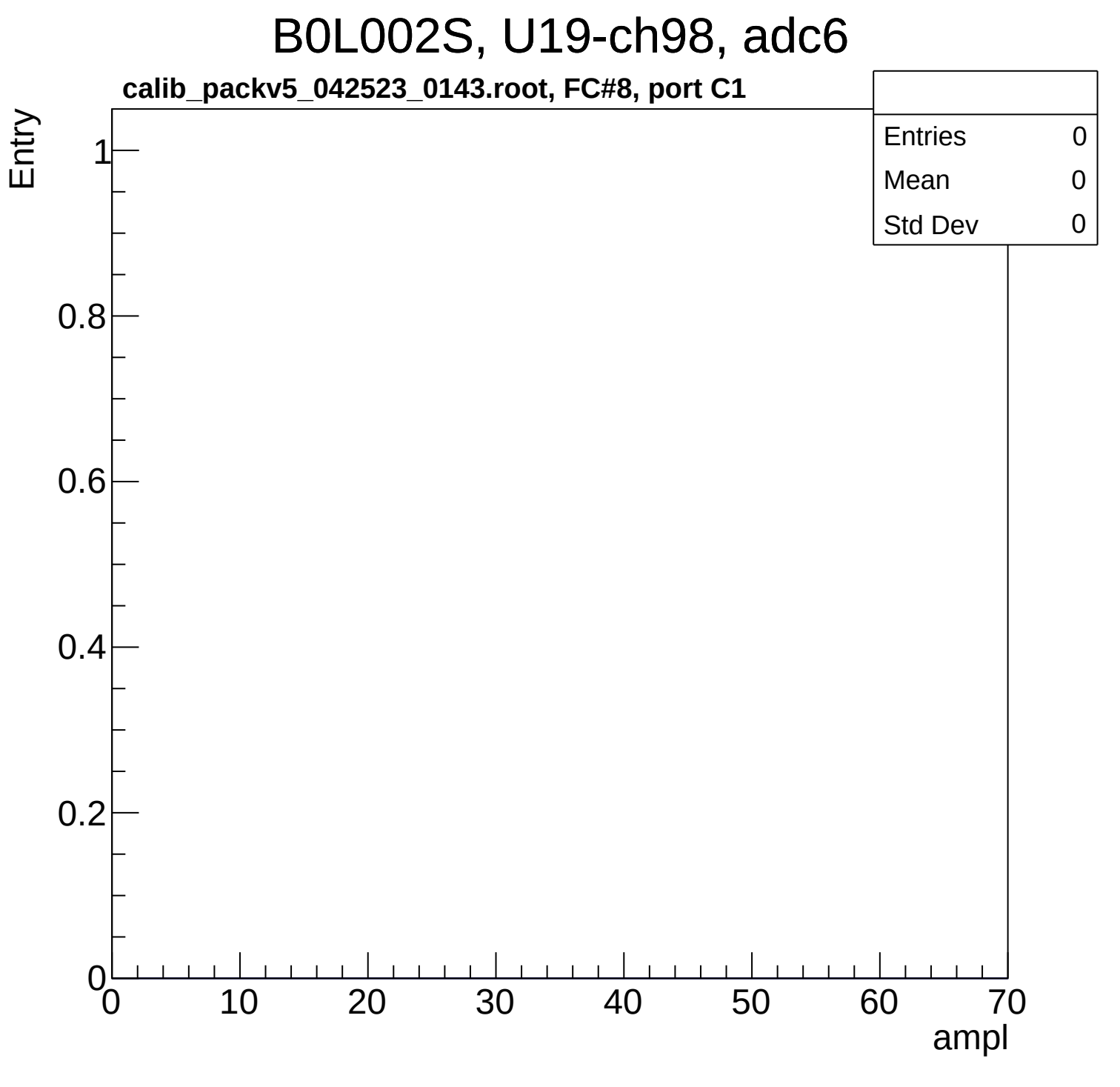

B0L002S, U19-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.41
Std Dev 2.994
Gaus mean : 38.0631
Gaus Width: 3.4174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

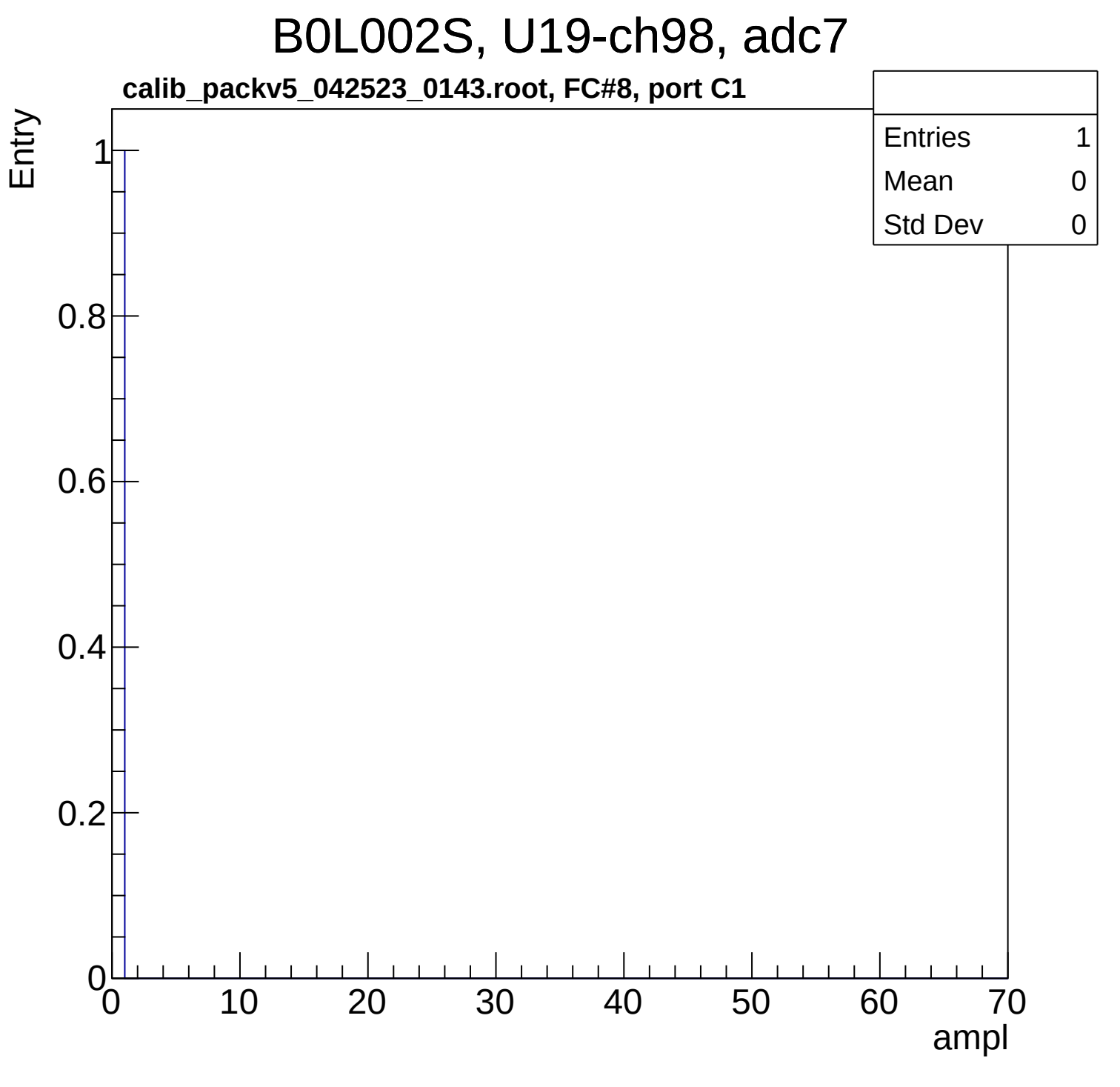

B0L002S, U19-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.65
Std Dev 3.367
Gaus mean : 44.0856
Gaus Width: 3.2998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

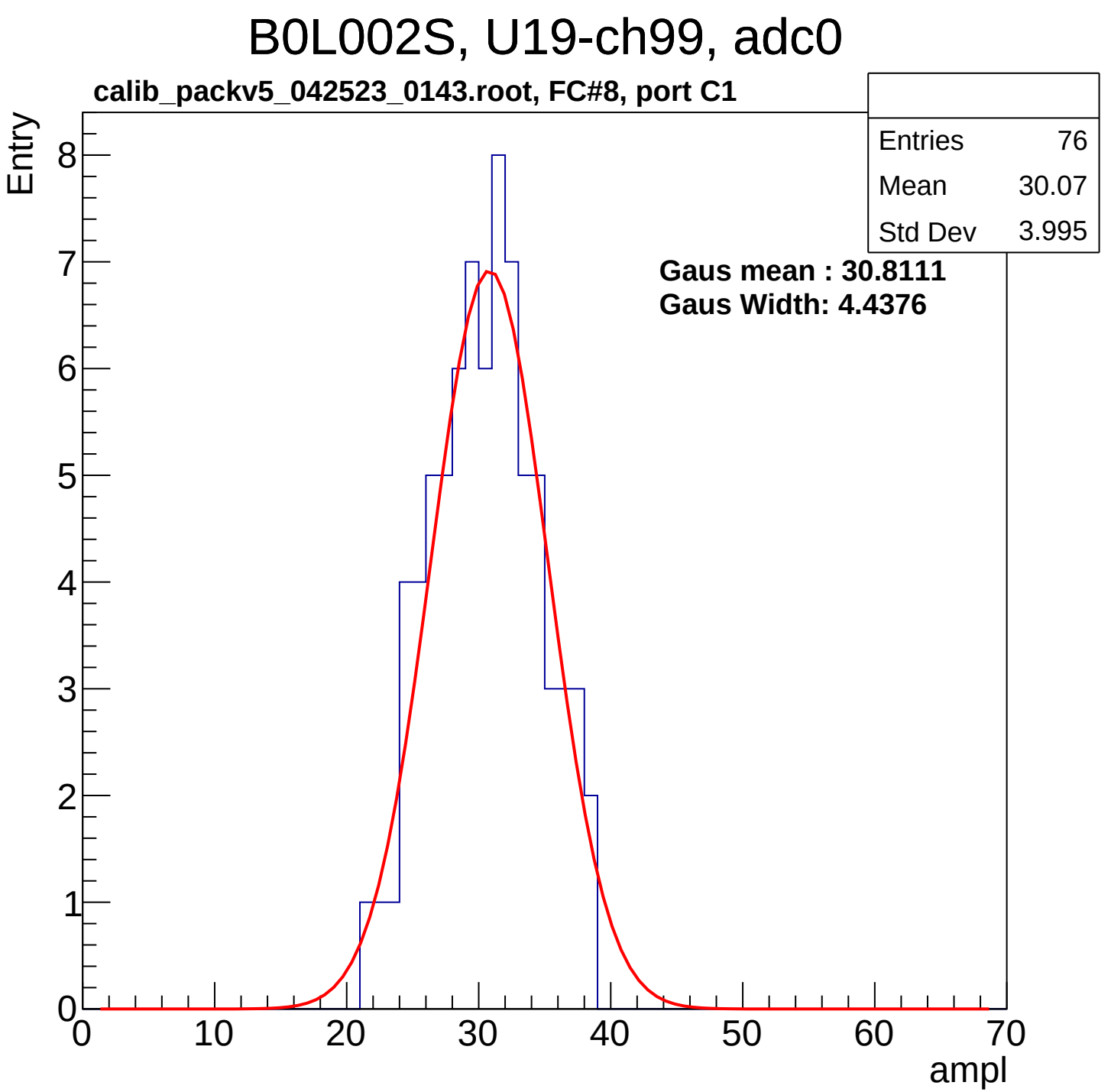

B0L002S, U19-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 50.59
Std Dev 3.592
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

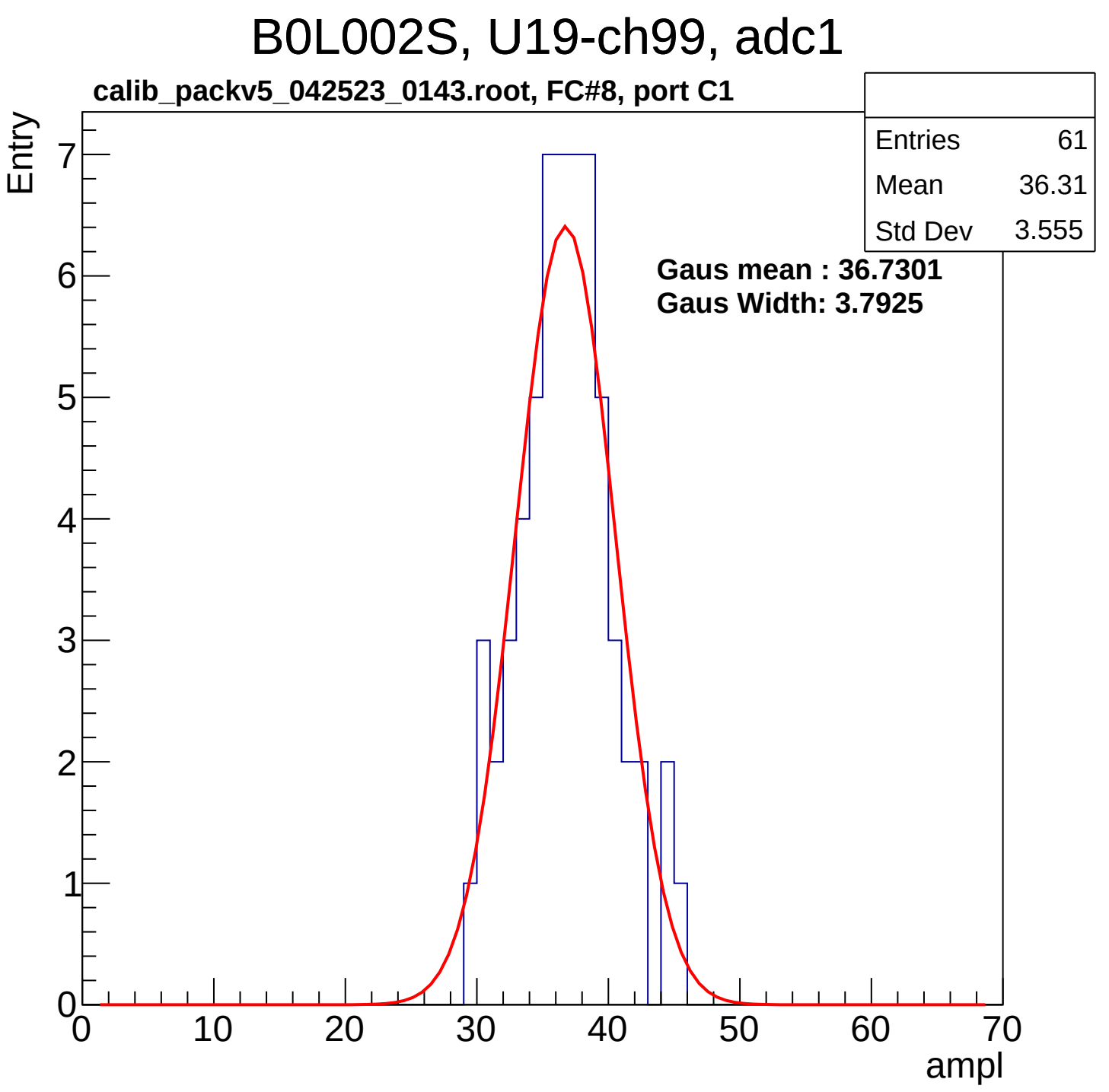

B0L002S, U19-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 56.45
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

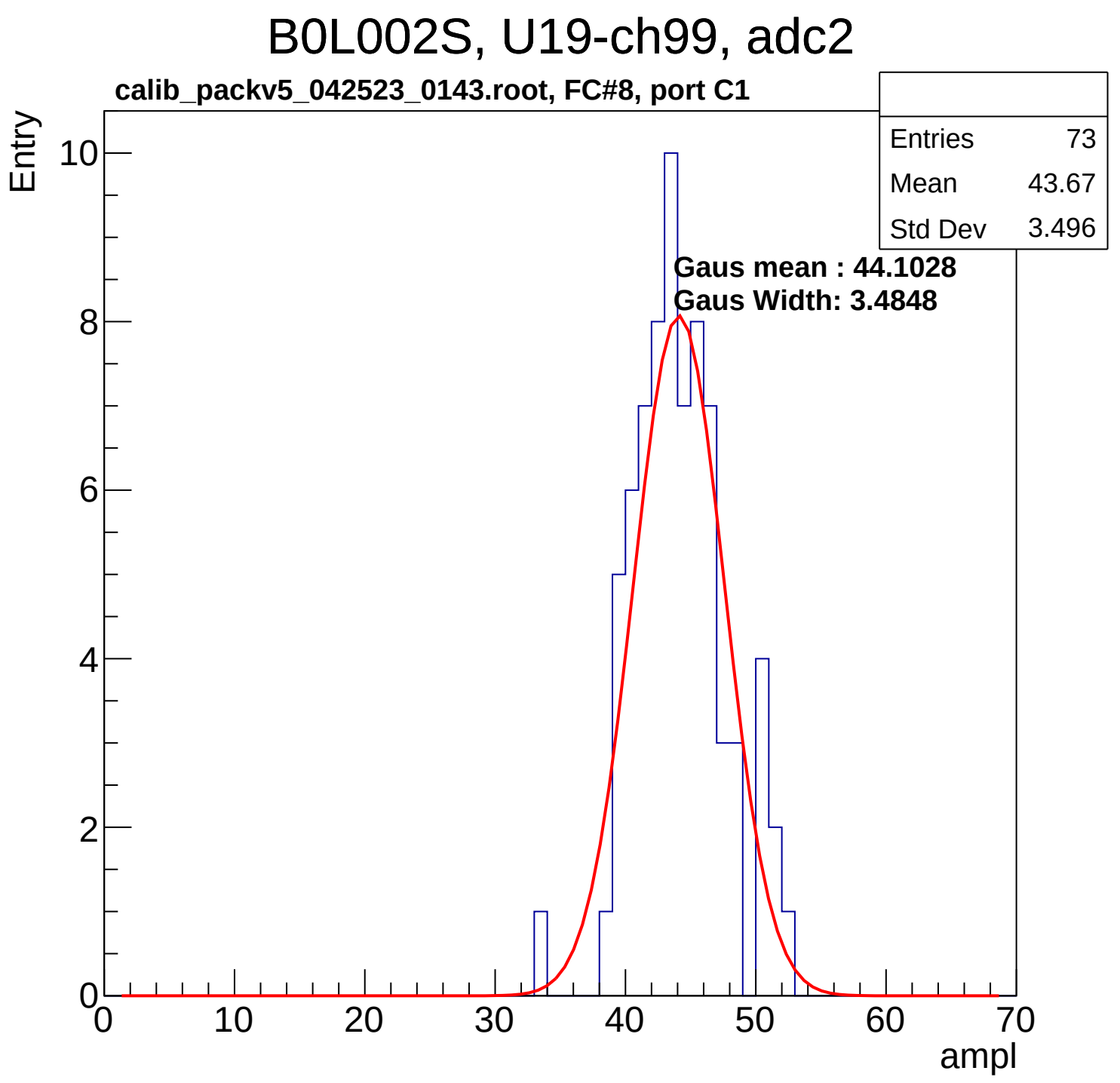

B0L002S, U19-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.36
Std Dev 9.132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

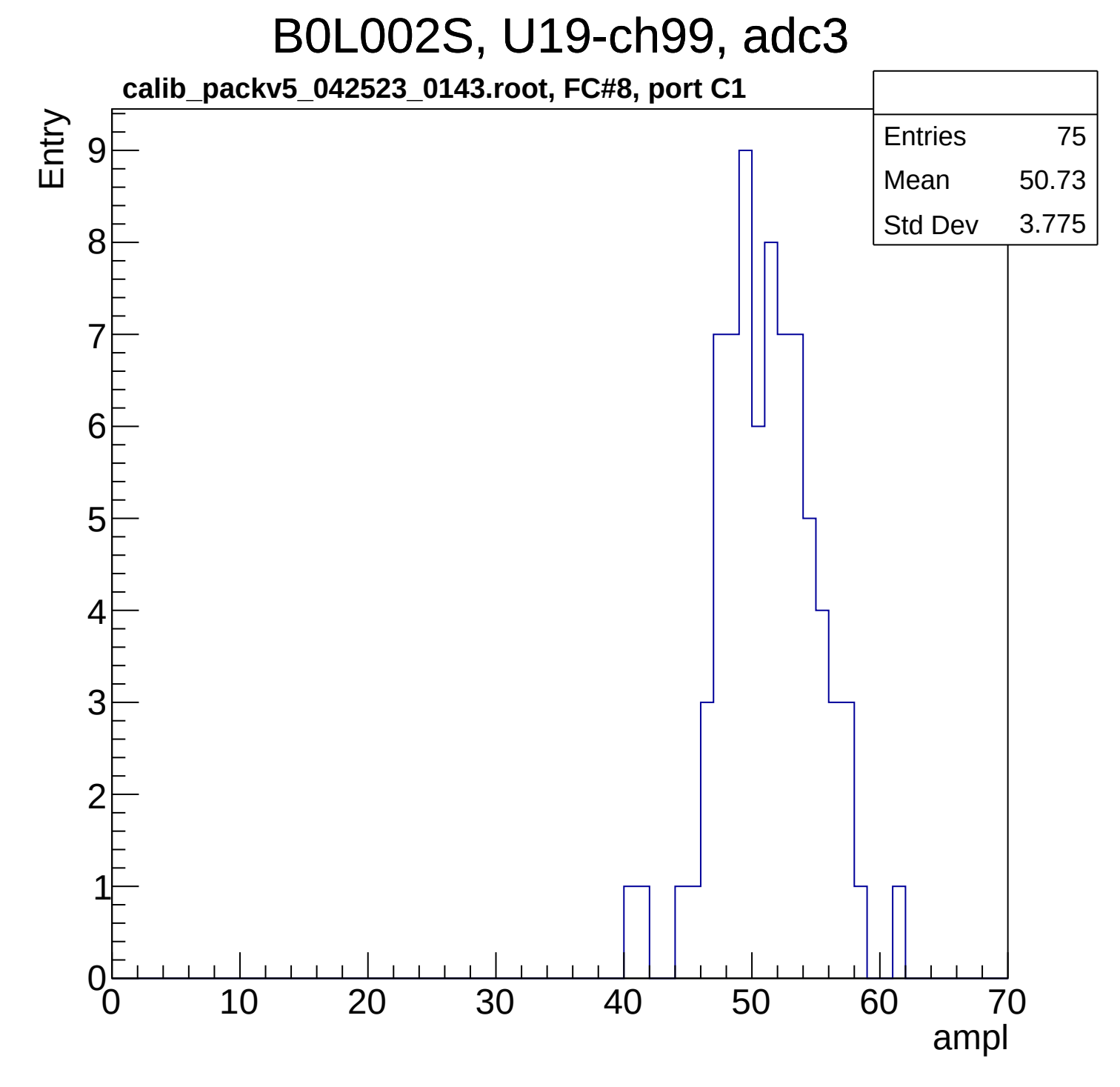

B0L002S, U19-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

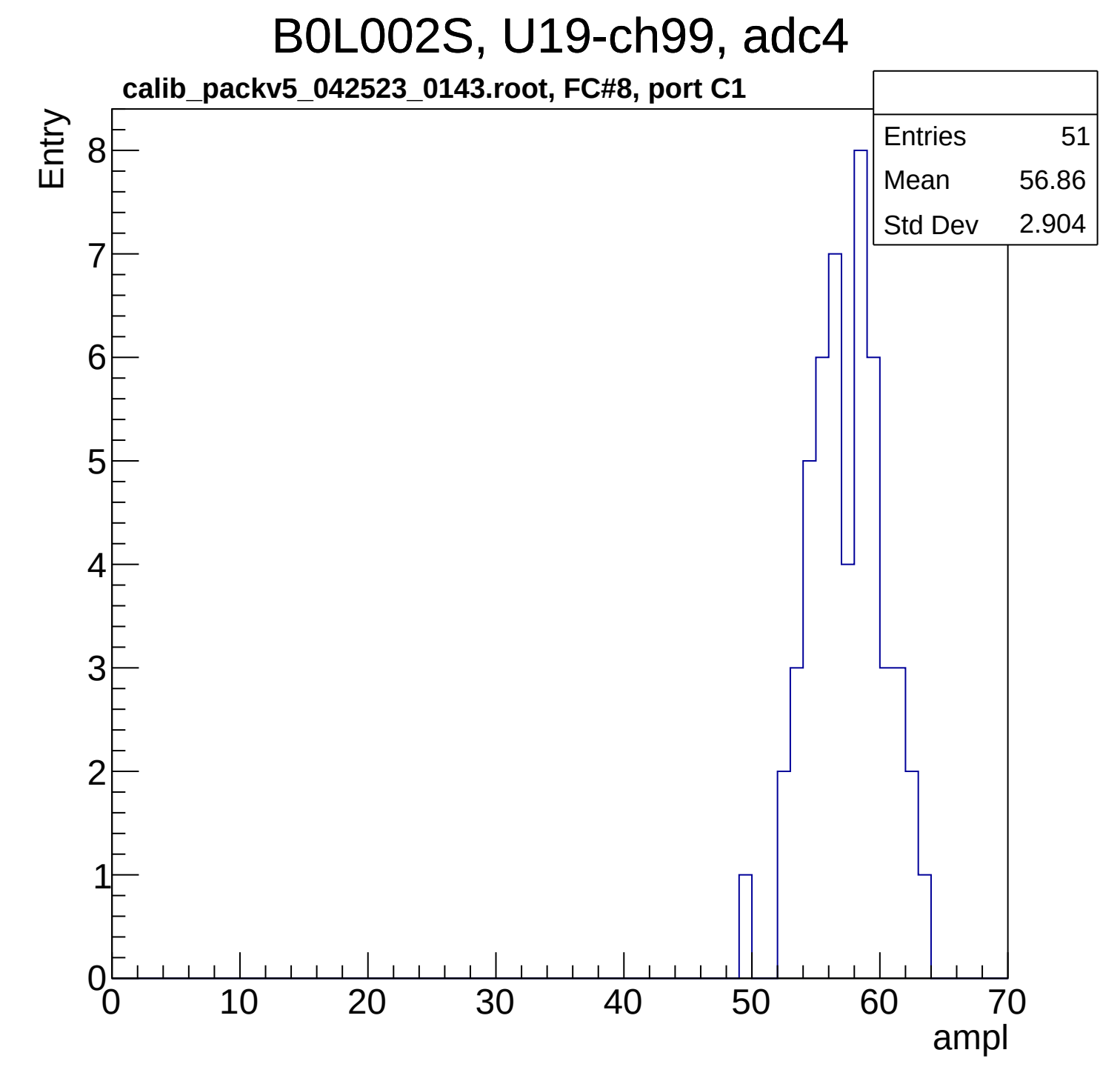

B0L002S, U19-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

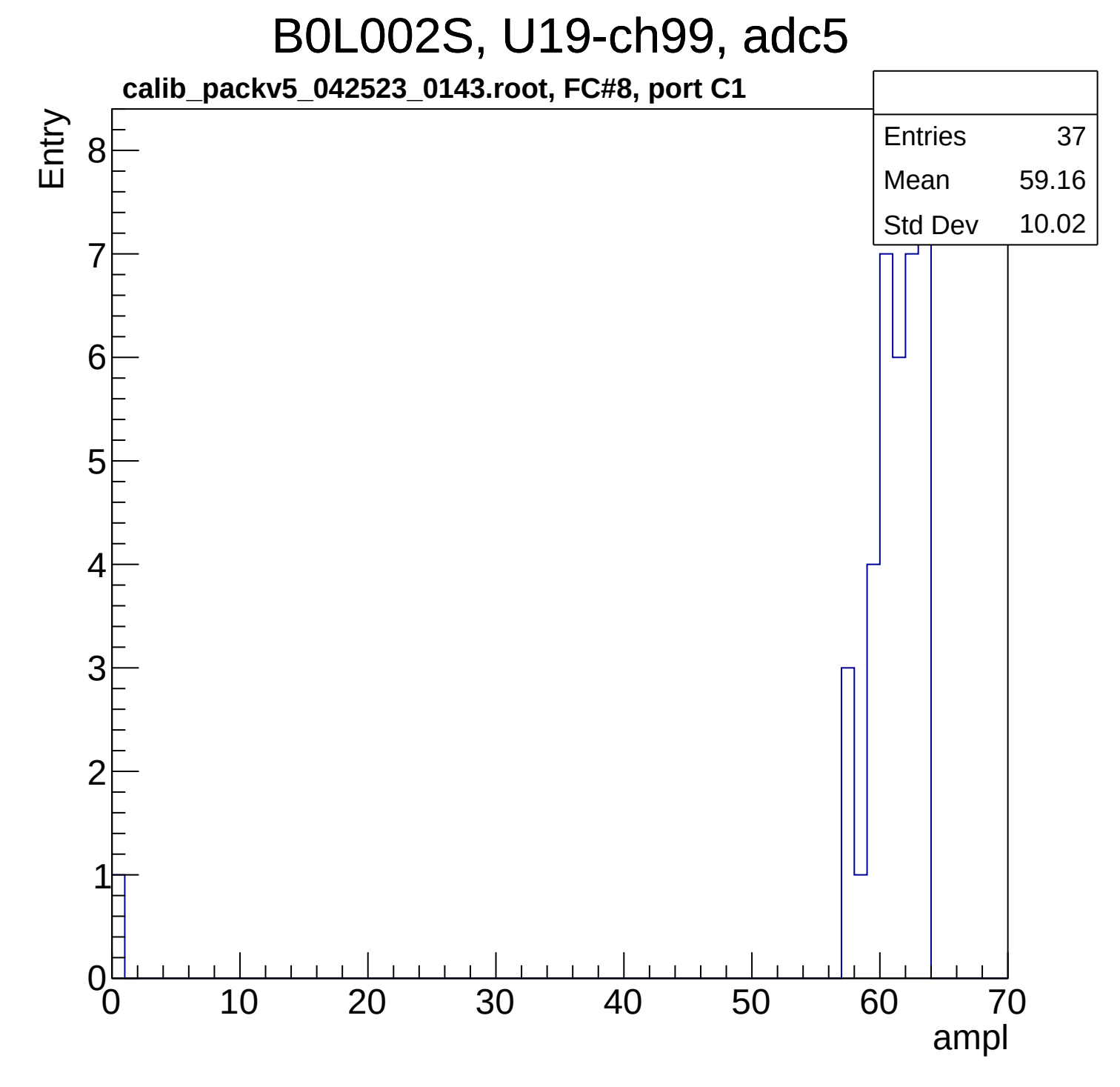

B0L002S, U19-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.07
Std Dev 3.995
Gaus mean : 30.8111
Gaus Width: 4.4376
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

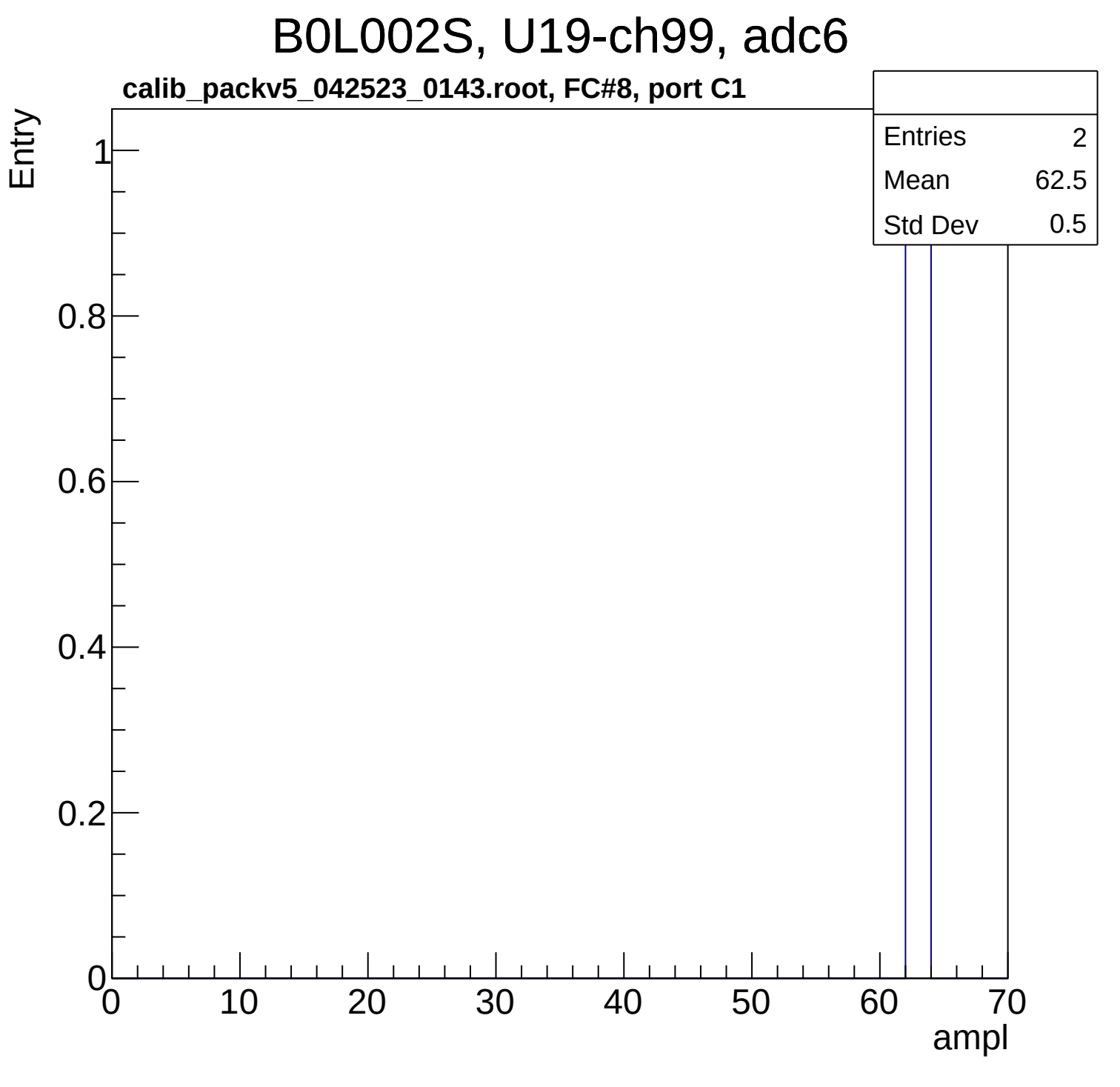

B0L002S, U19-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.31
Std Dev 3.555
Gaus mean : 36.7301
Gaus Width: 3.7925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

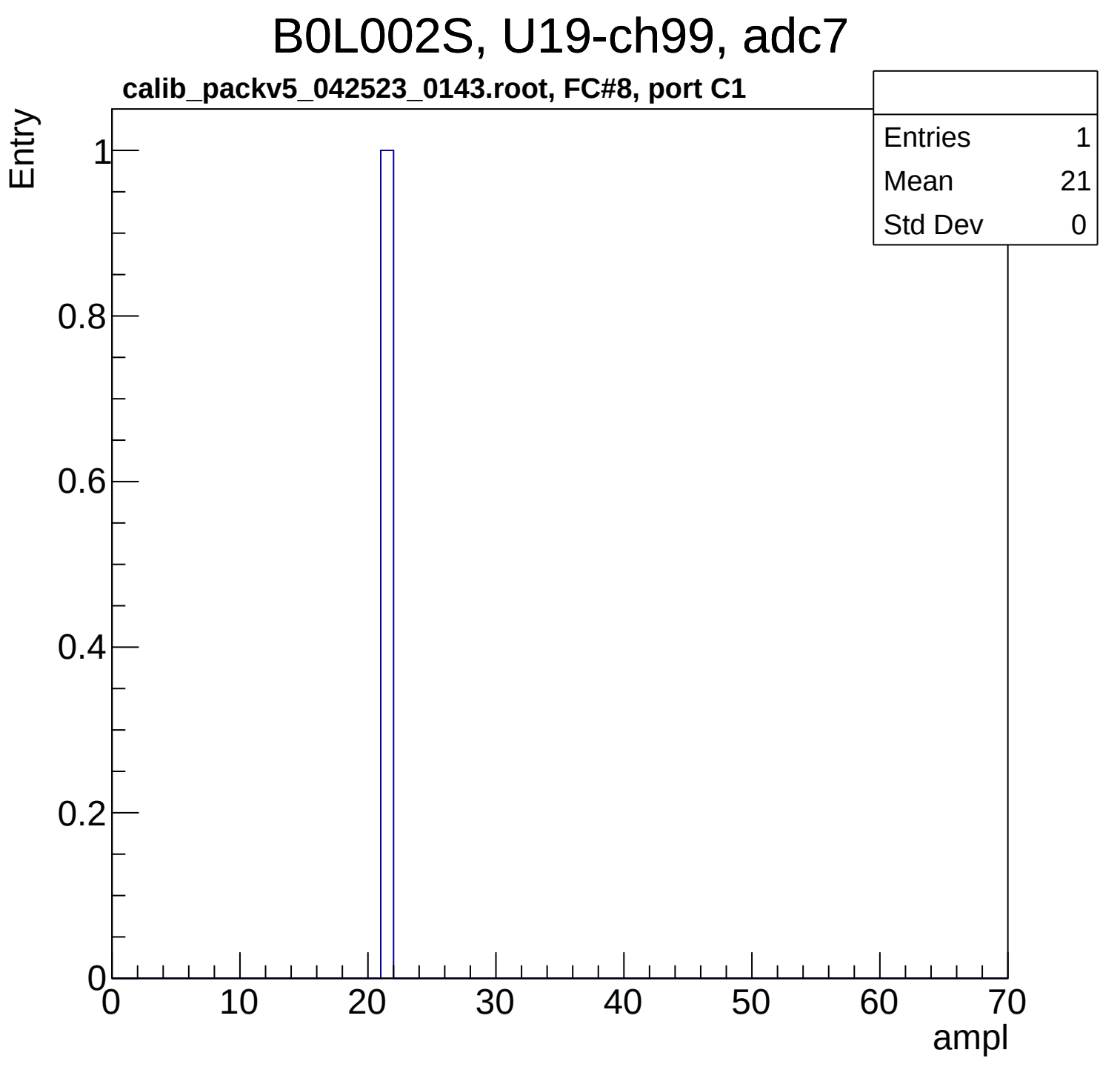

B0L002S, U19-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.67
Std Dev 3.496
Gaus mean : 44.1028
Gaus Width: 3.4848
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

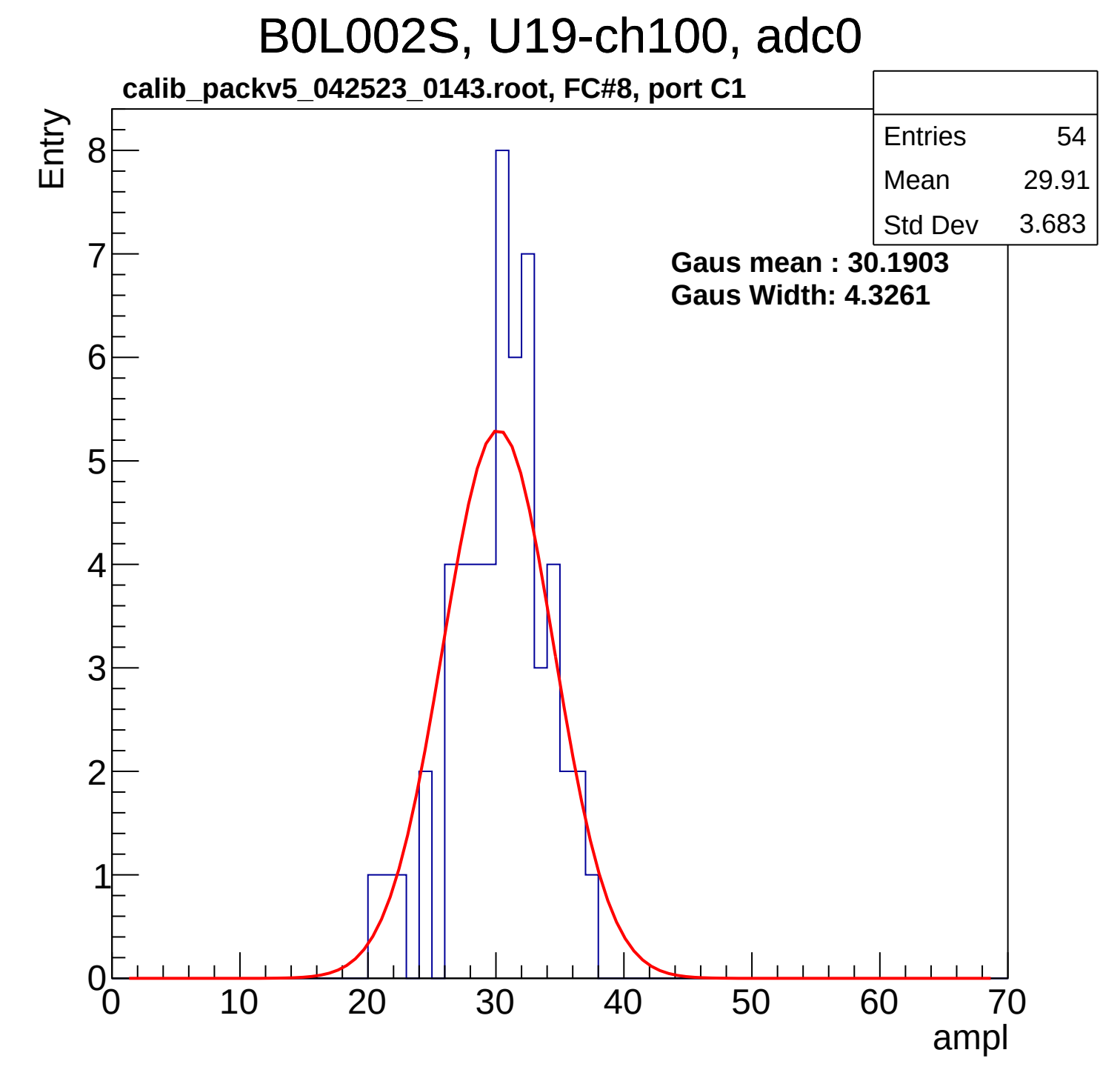

B0L002S, U19-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.73
Std Dev 3.775
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

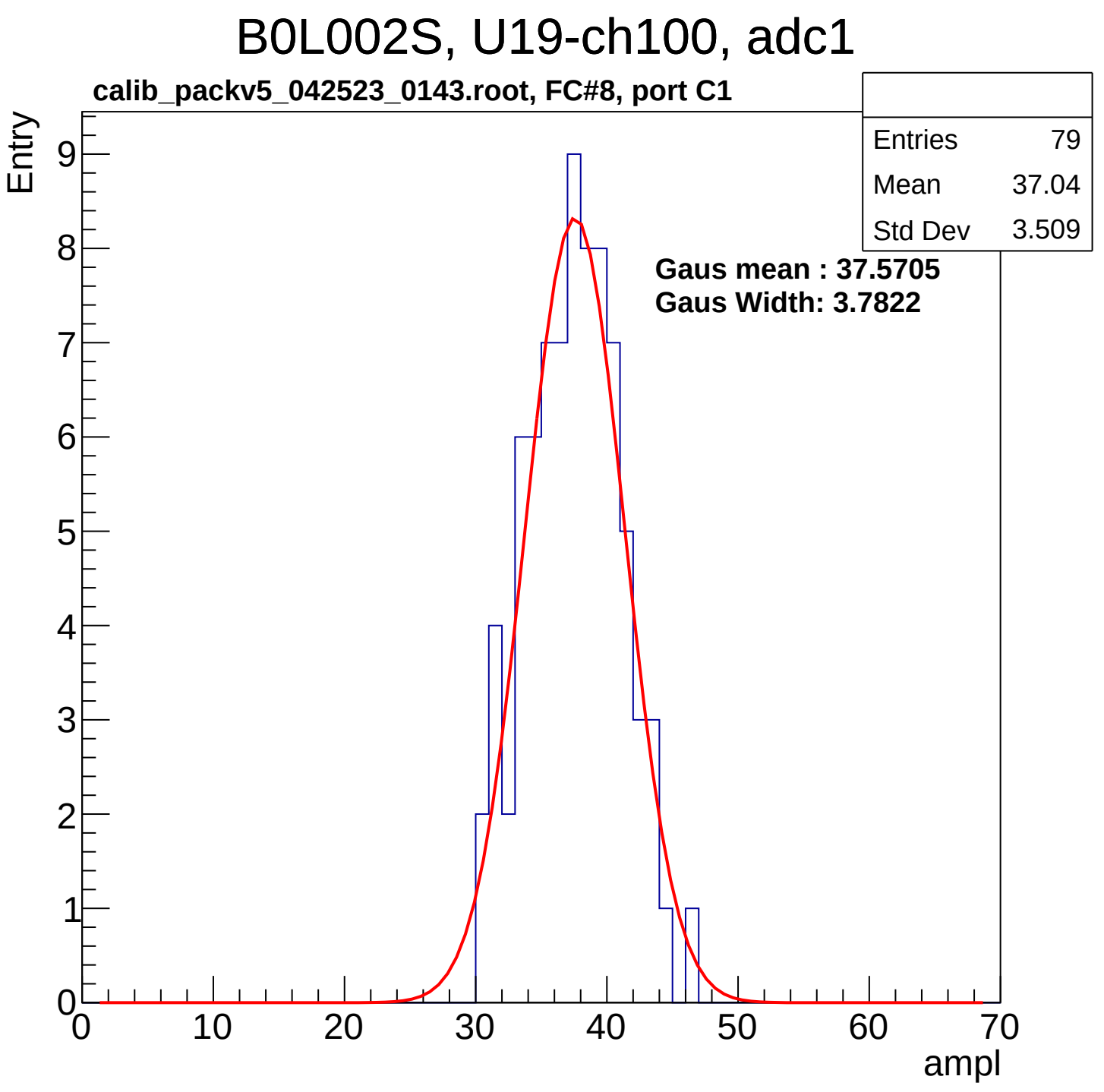

B0L002S, U19-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.86
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

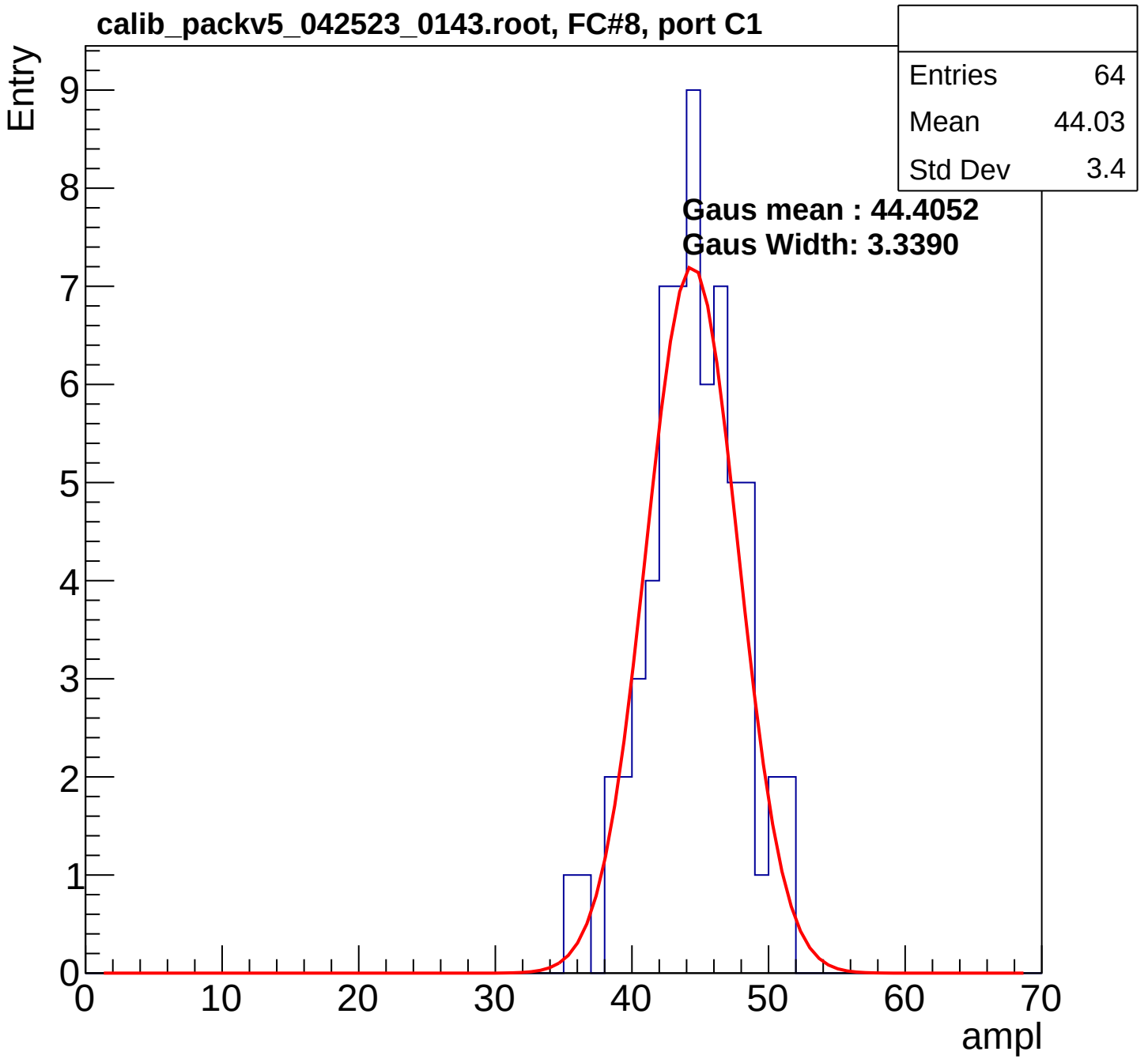

B0L002S, U19-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.16
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

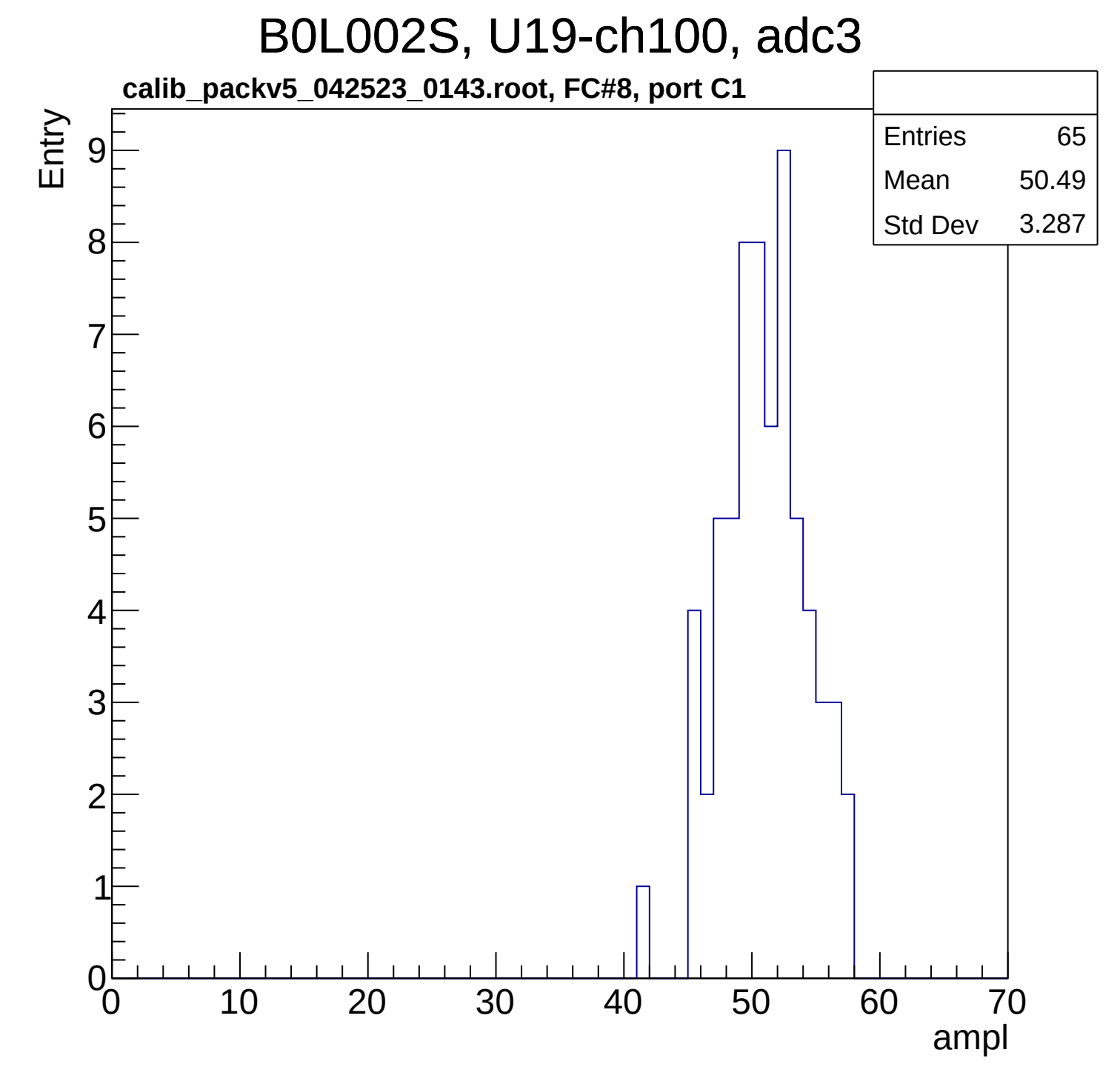

B0L002S, U19-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

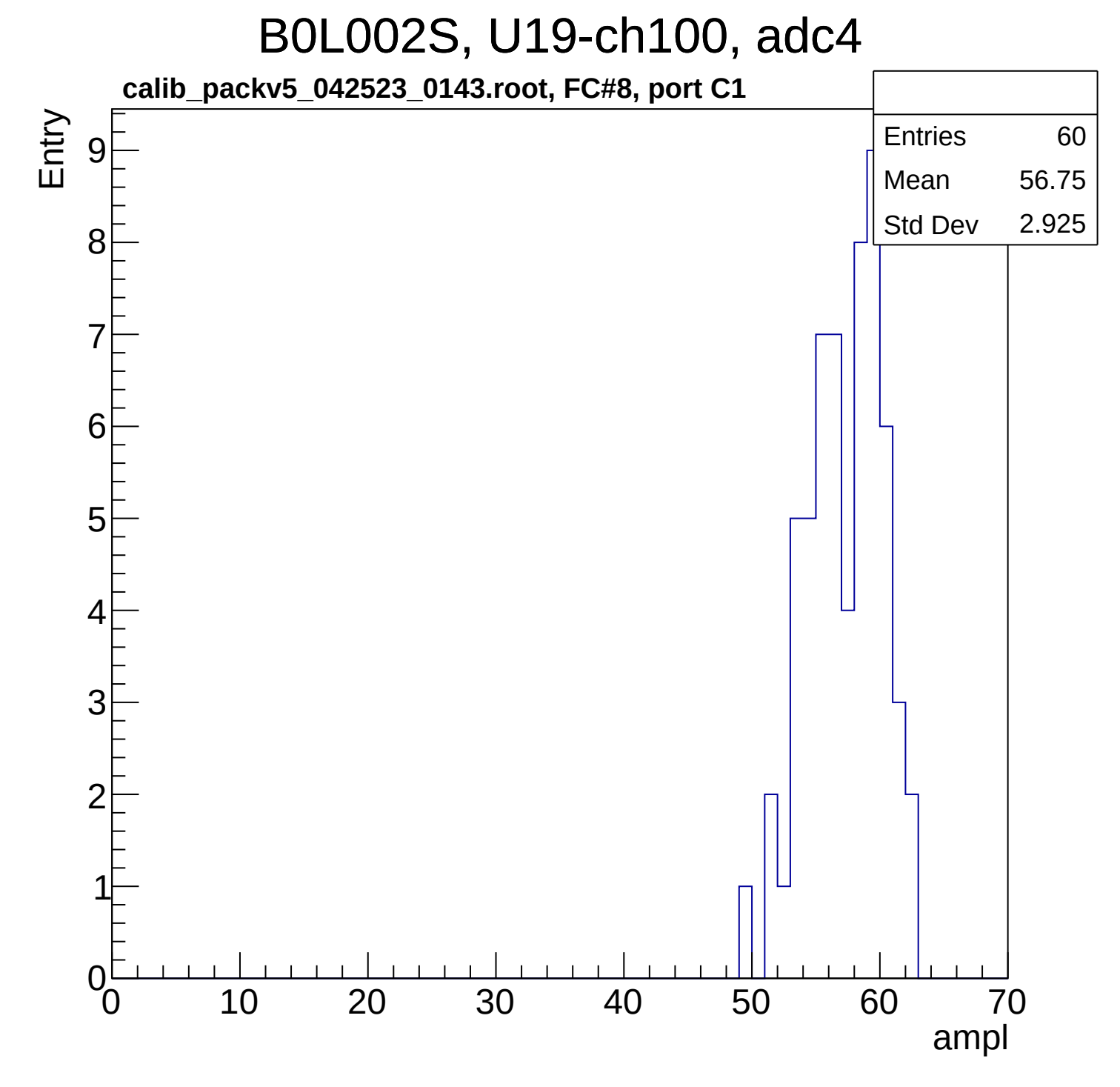

B0L002S, U19-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

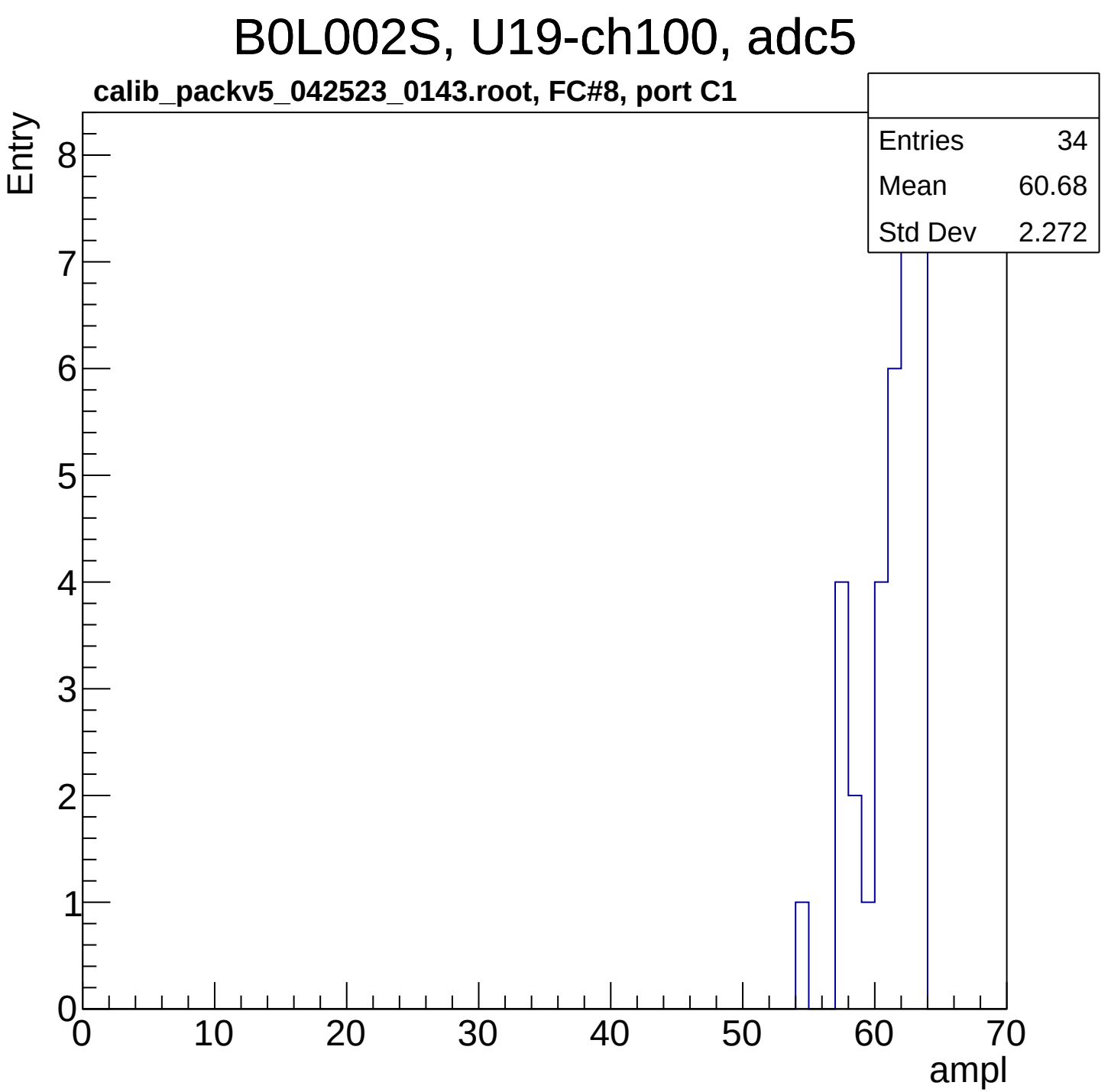

B0L002S, U19-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 29.91
Std Dev 3.683
Gaus mean : 30.1903
Gaus Width: 4.3261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

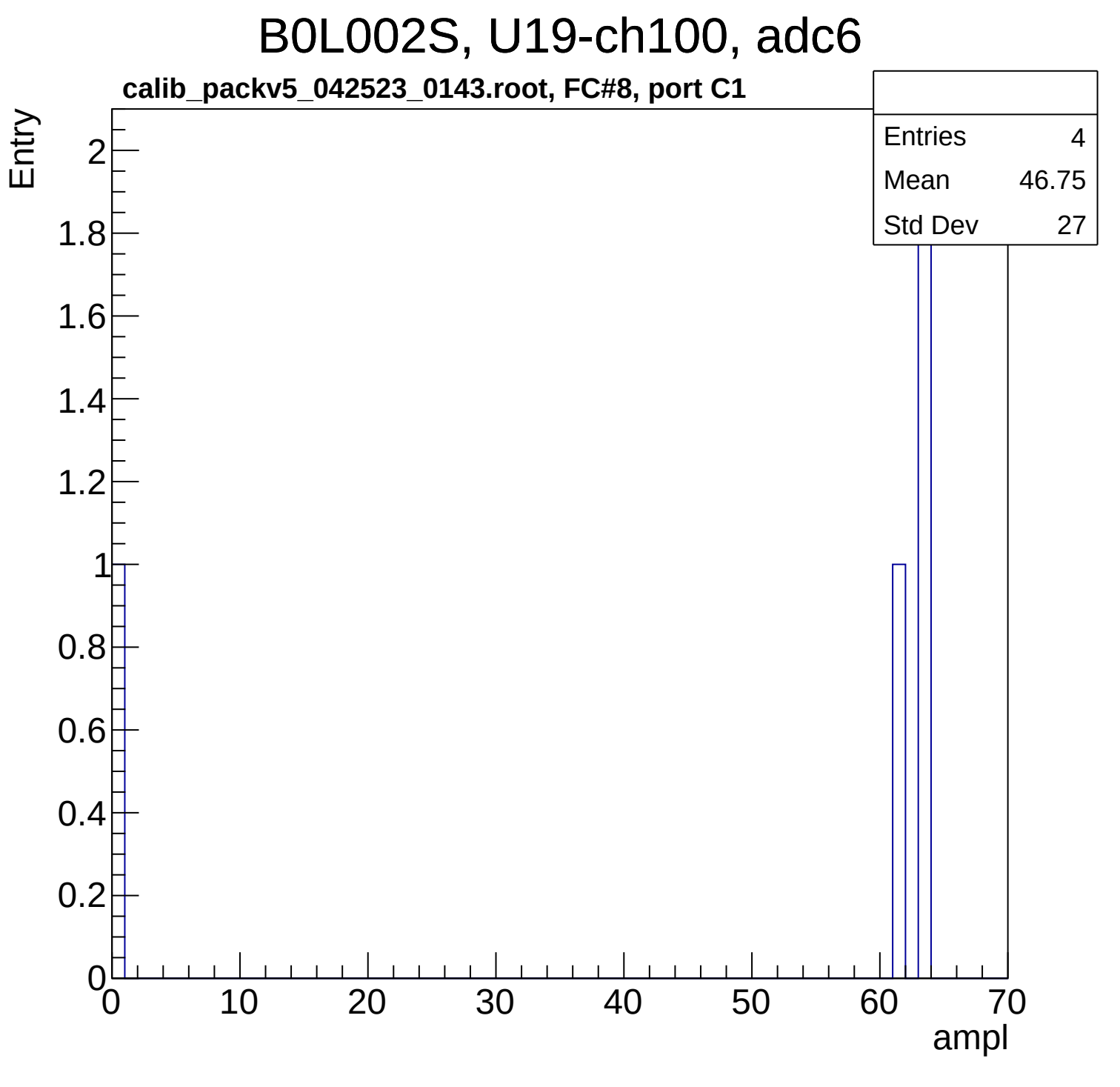

B0L002S, U19-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 37.04
Std Dev 3.509
Gaus mean : 37.5705
Gaus Width: 3.7822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

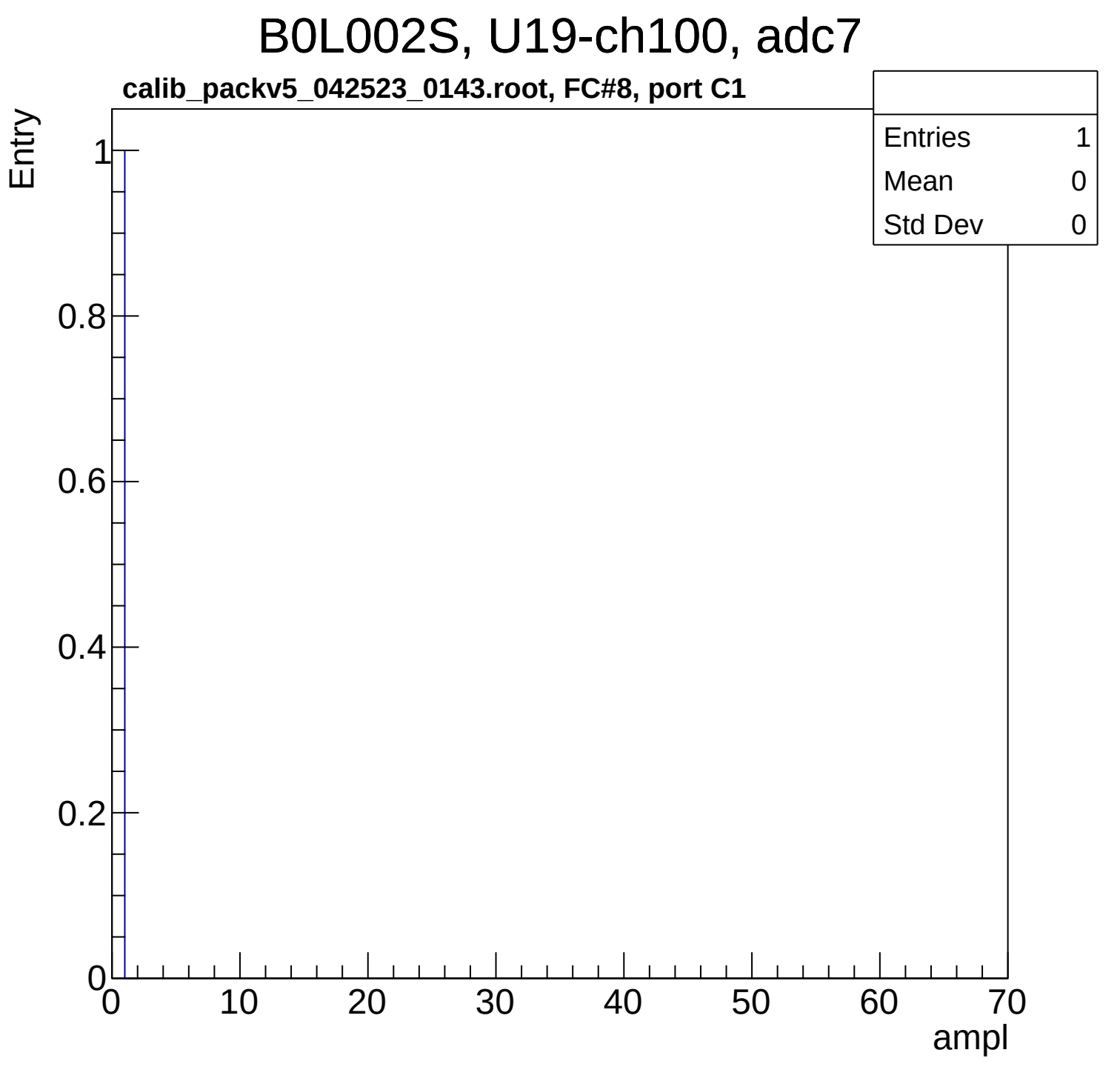

B0L002S, U19-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.03
Std Dev 3.4
Gaus mean : 44.4052
Gaus Width: 3.3390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

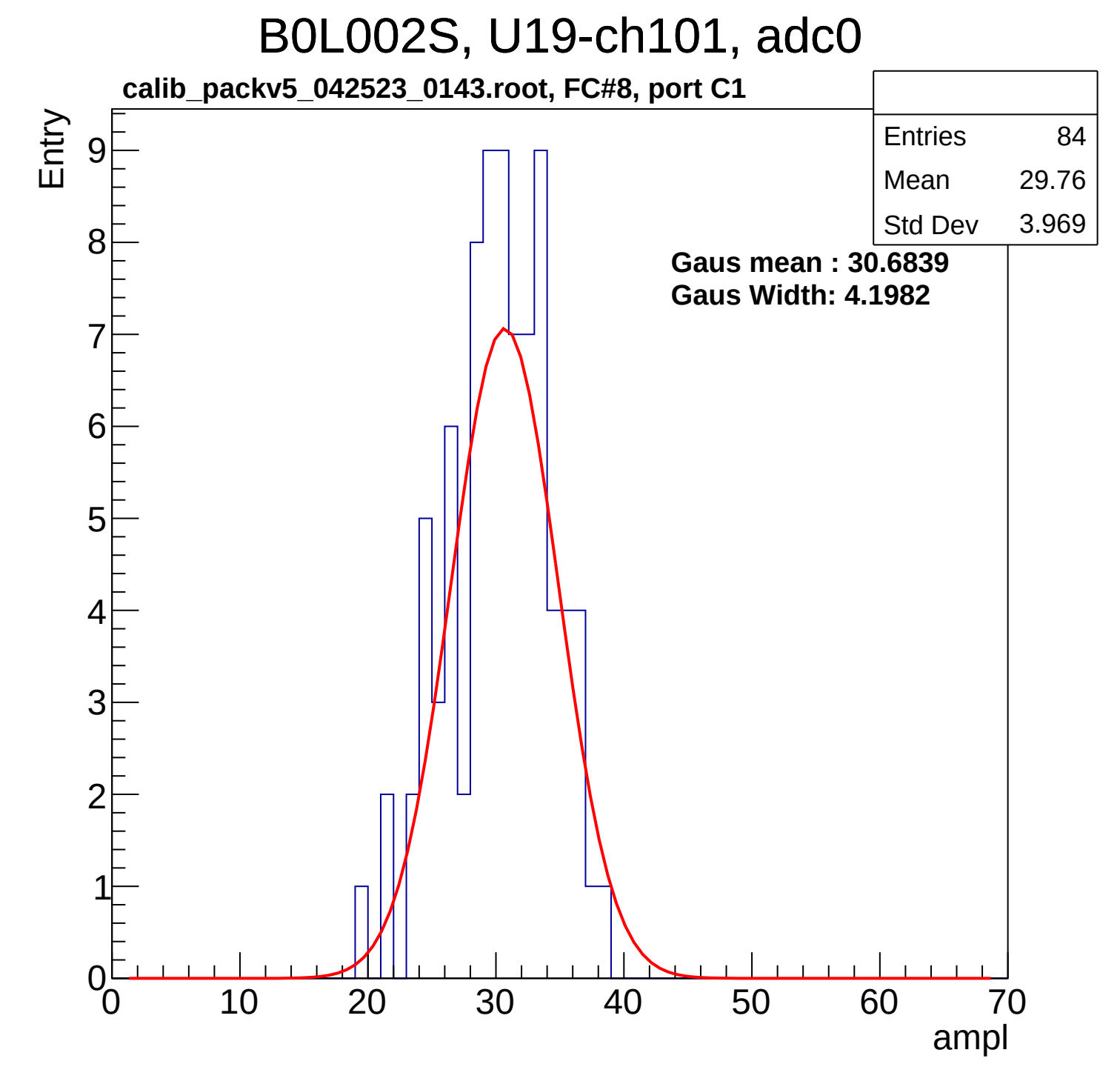

B0L002S, U19-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.49
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

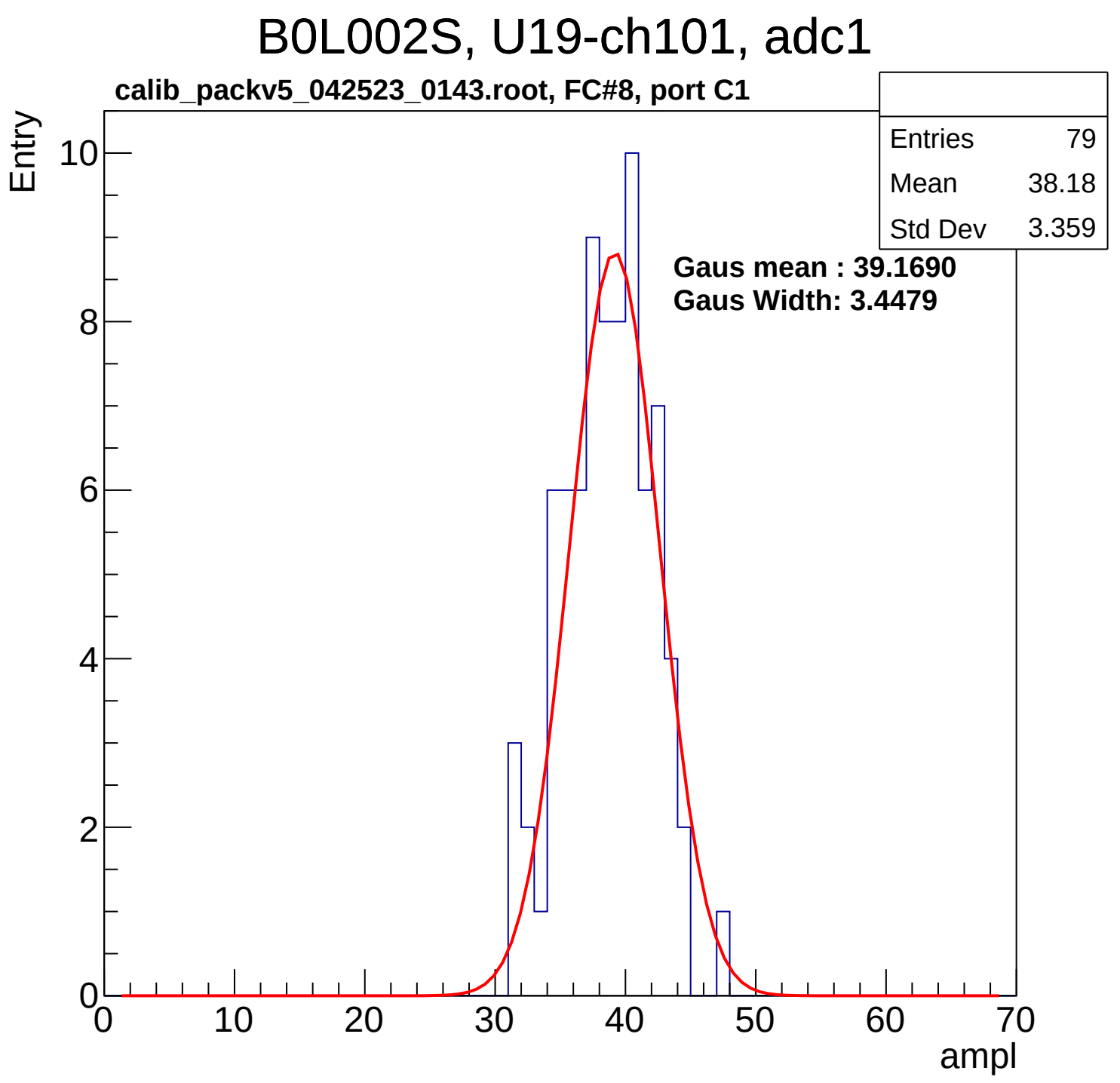

B0L002S, U19-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.75
Std Dev 2.925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

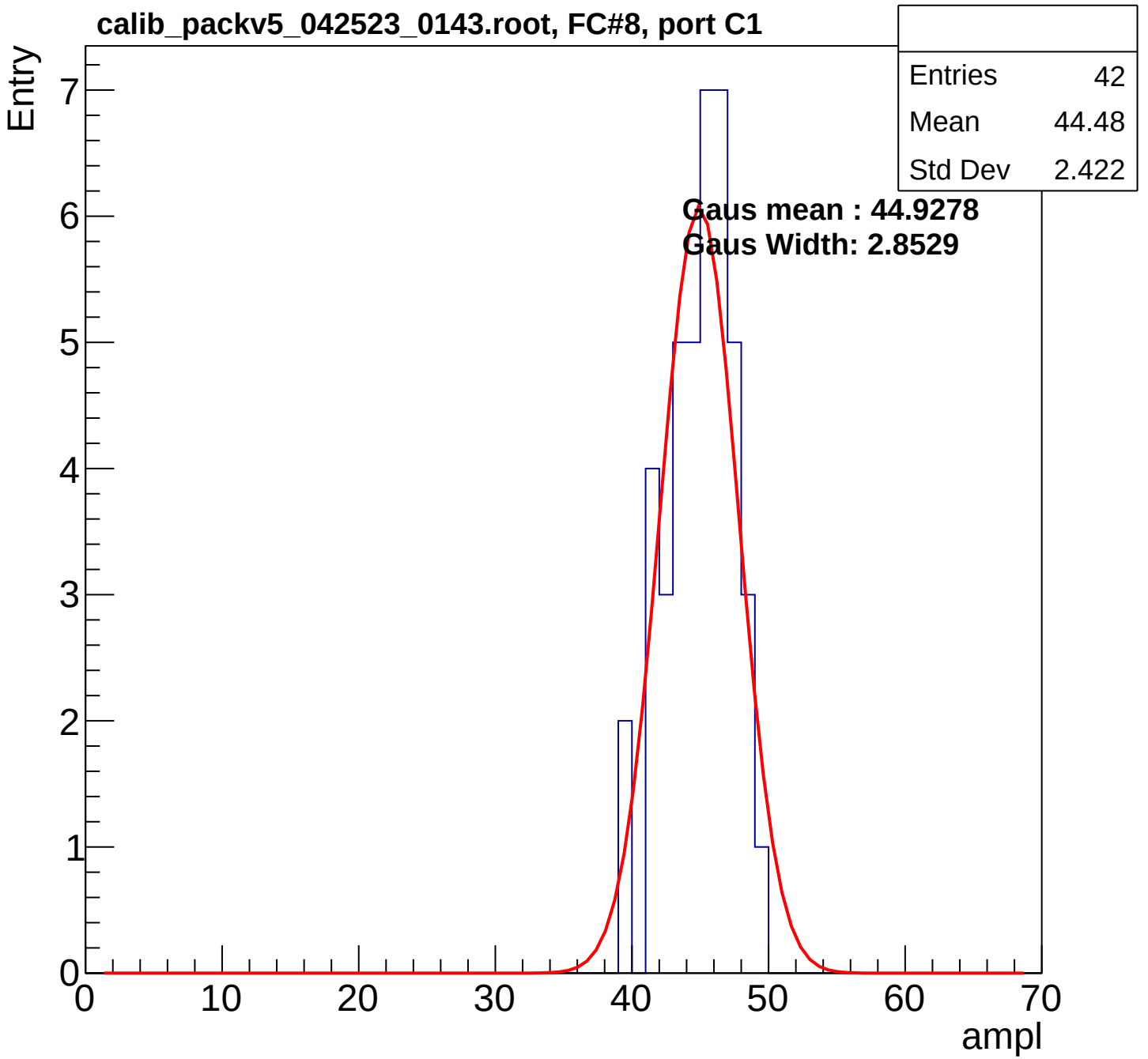

B0L002S, U19-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.68
Std Dev 2.272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

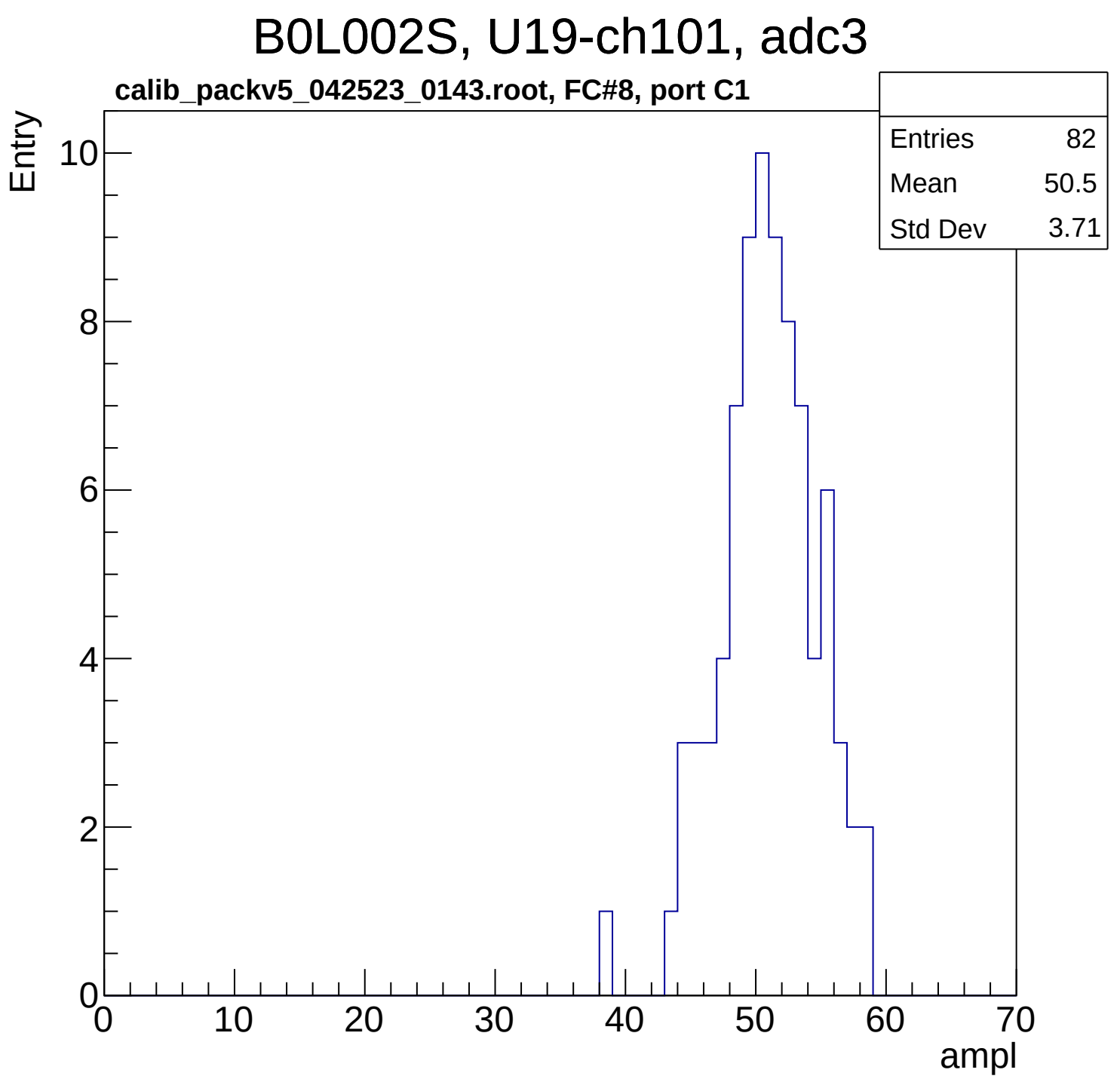

B0L002S, U19-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

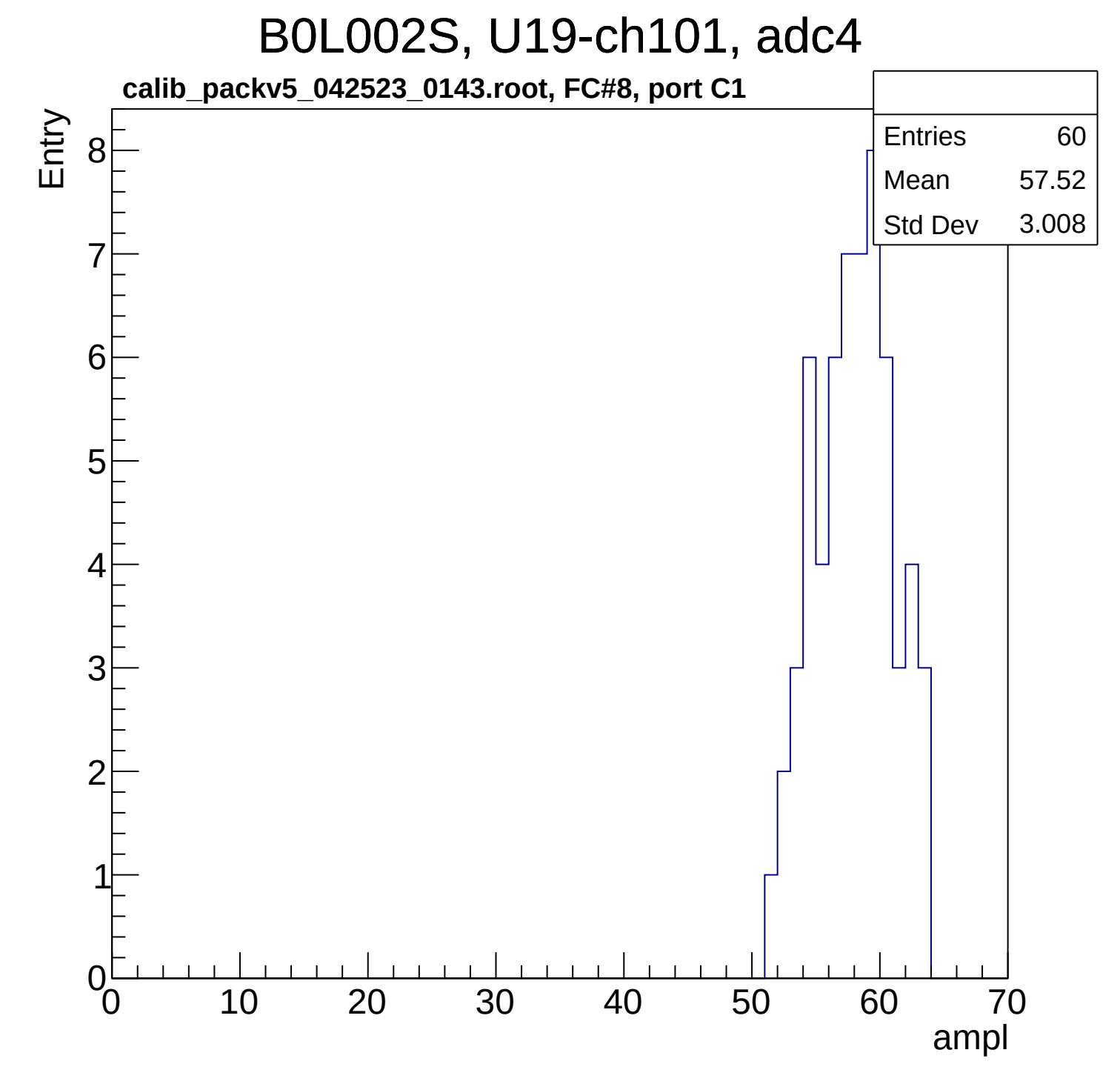

B0L002S, U19-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

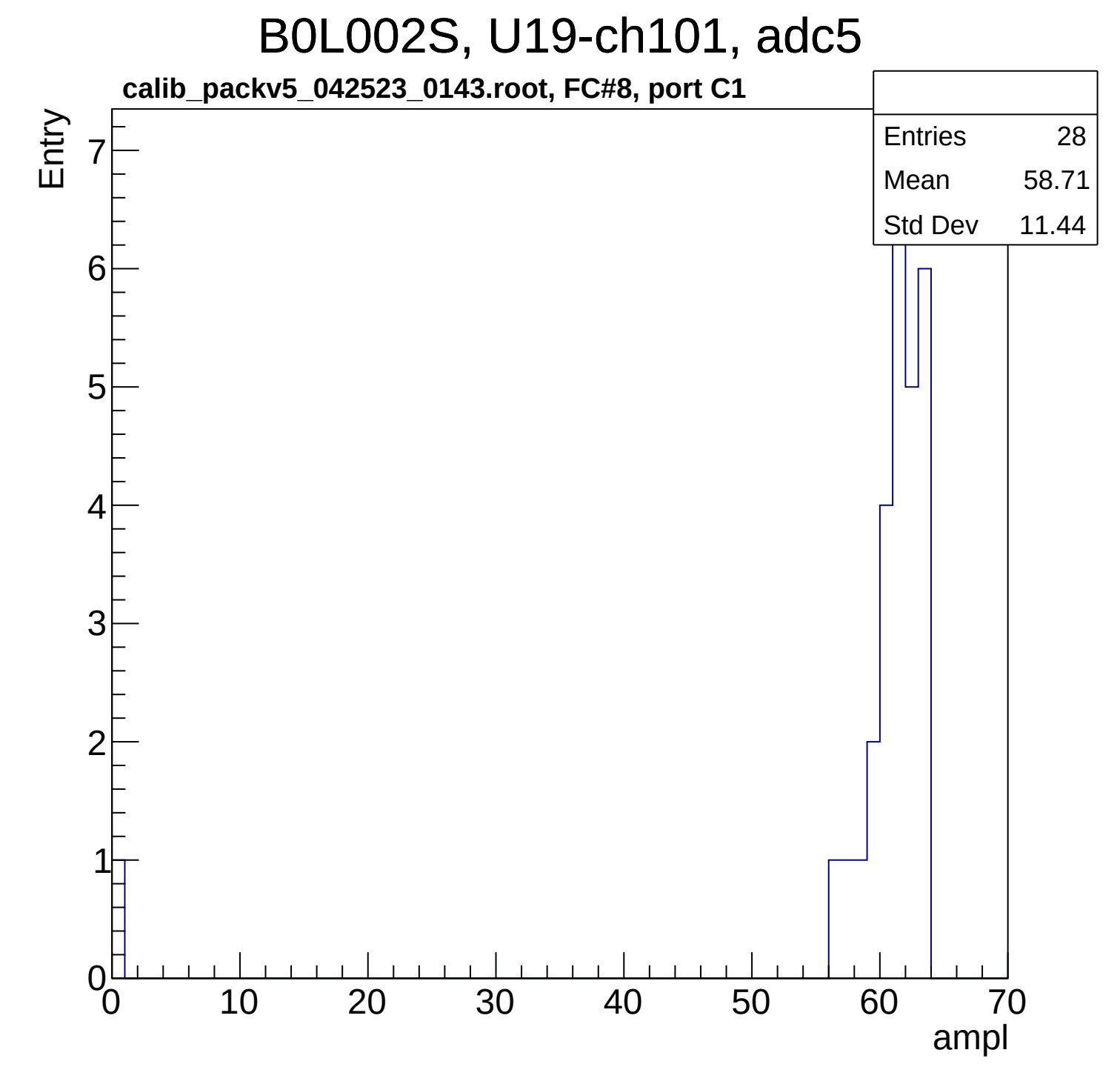

B0L002S, U19-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.76
Std Dev 3.969
Gaus mean : 30.6839
Gaus Width: 4.1982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

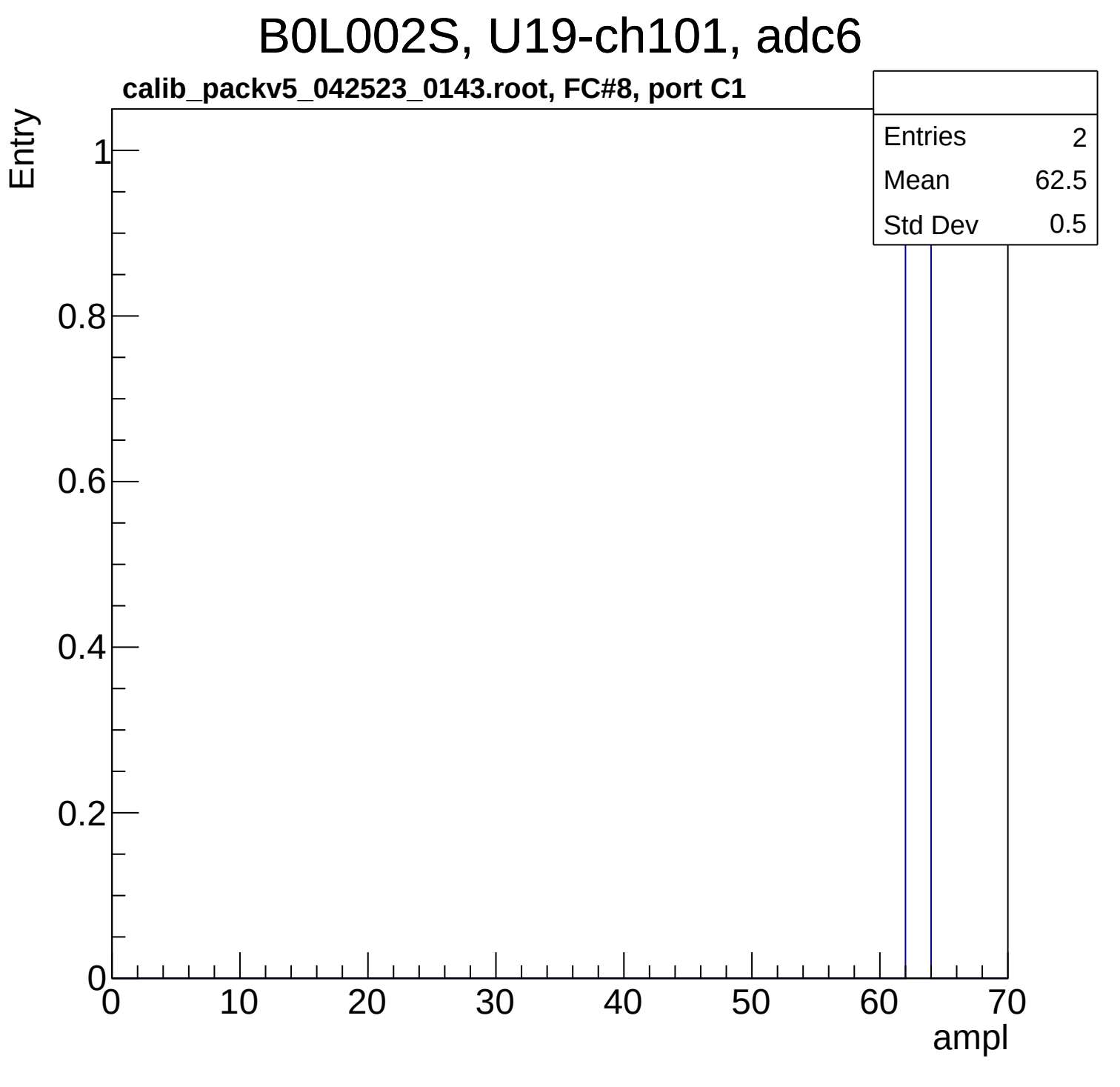

B0L002S, U19-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 38.18
Std Dev 3.359
Gaus mean : 39.1690
Gaus Width: 3.4479
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

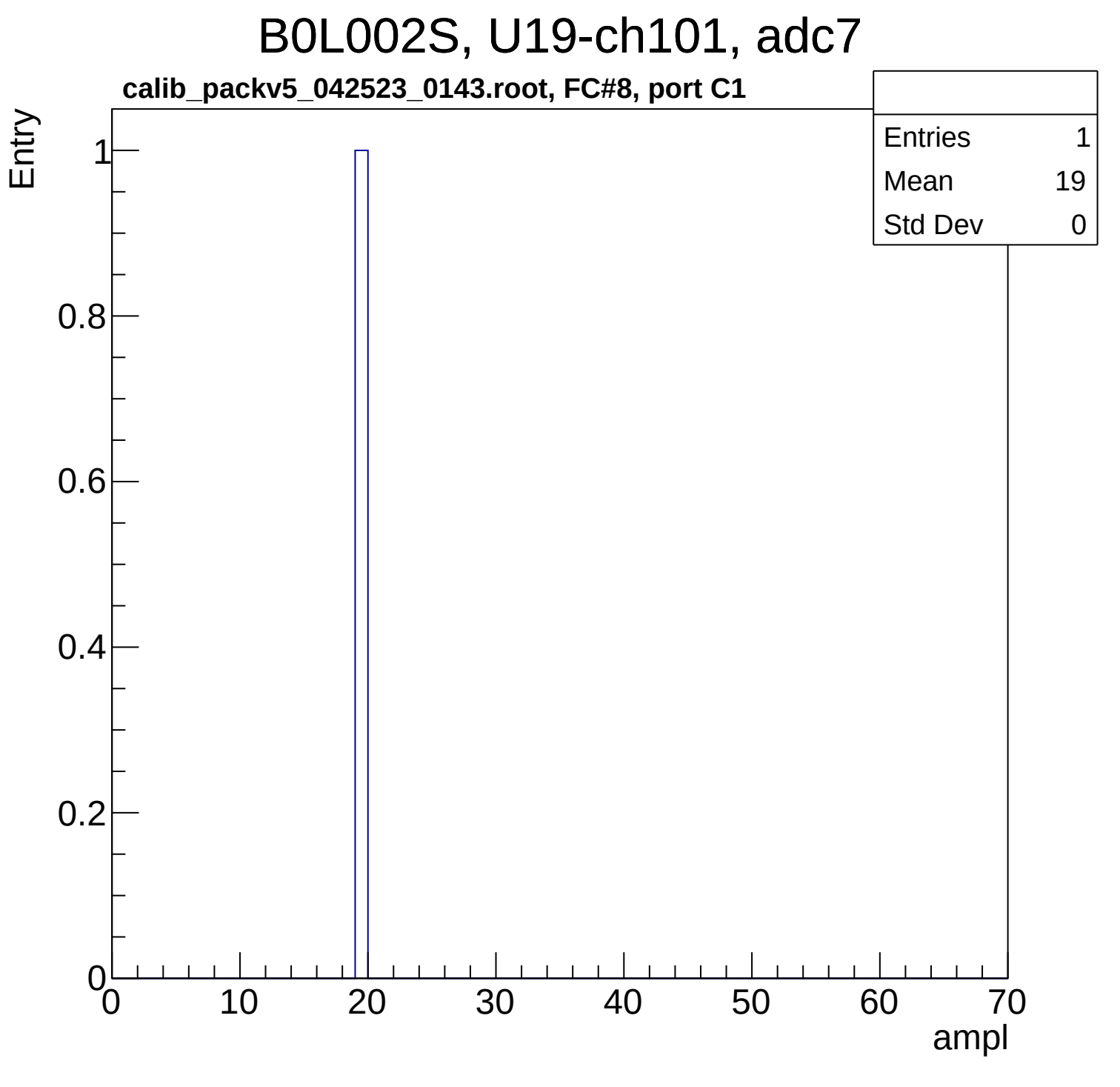

B0L002S, U19-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 44.48
Std Dev 2.422
Gaus mean : 44.9278
Gaus Width: 2.8529
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

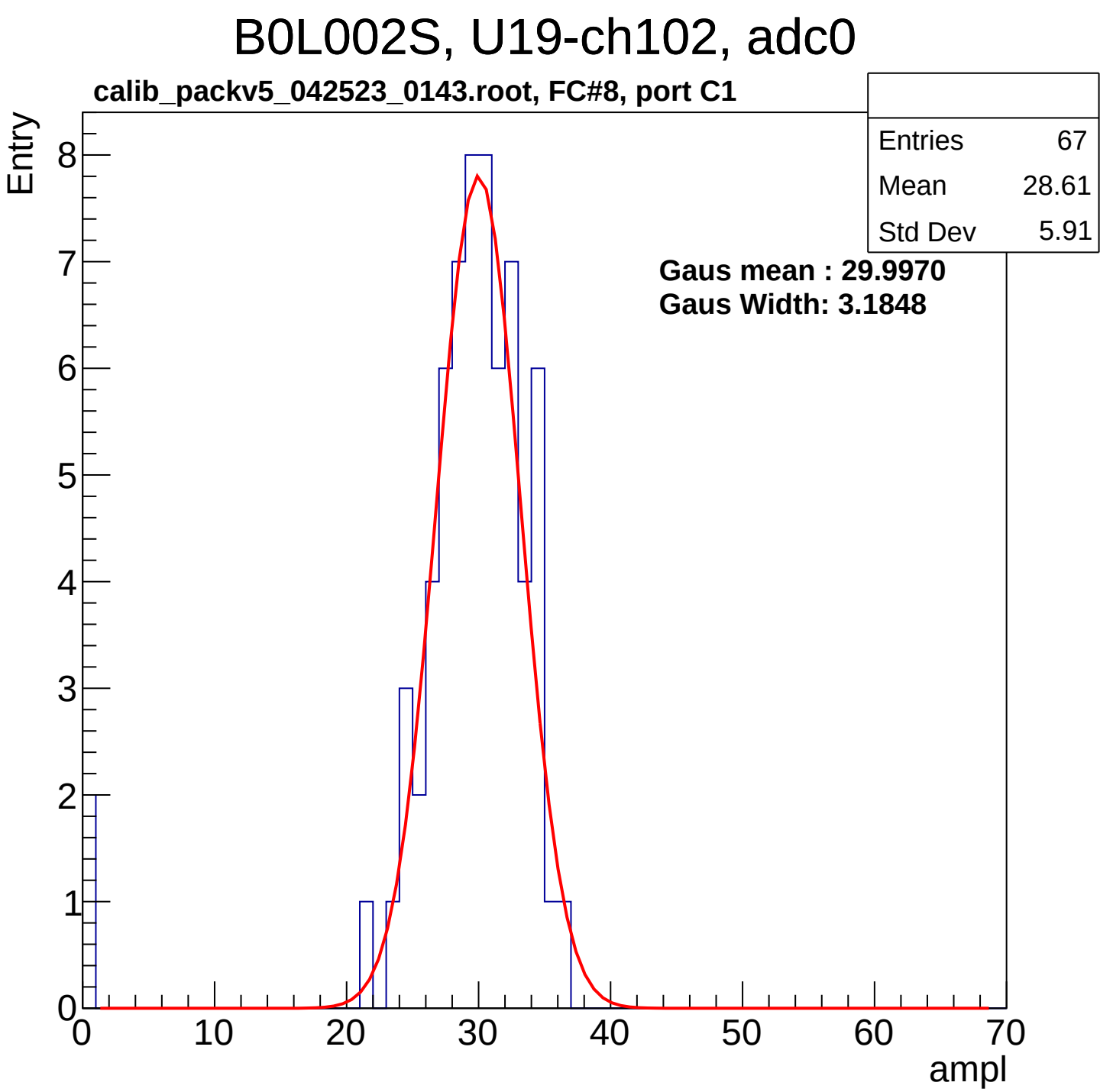

B0L002S, U19-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 50.5
Std Dev 3.71
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

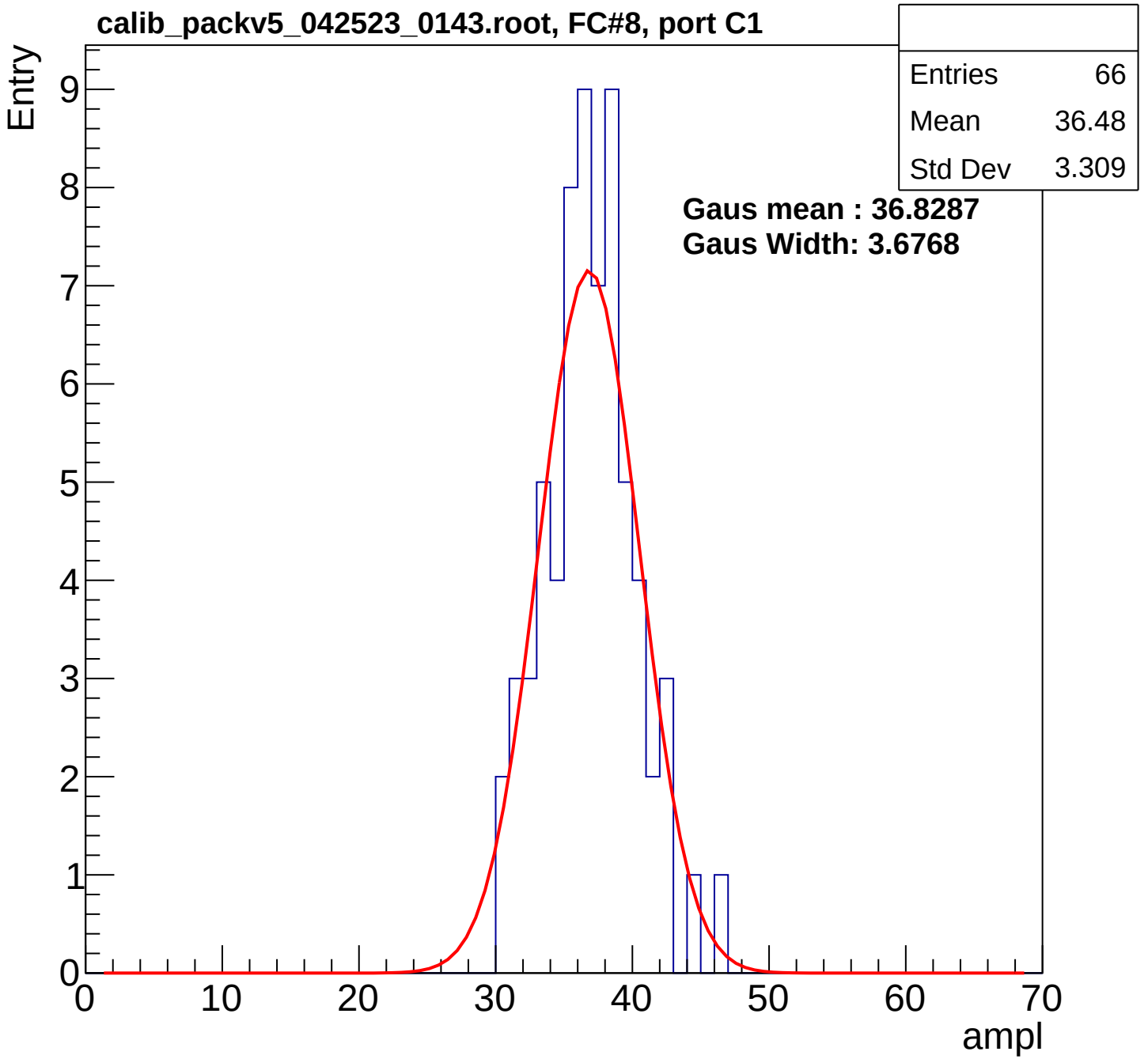

B0L002S, U19-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.52
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

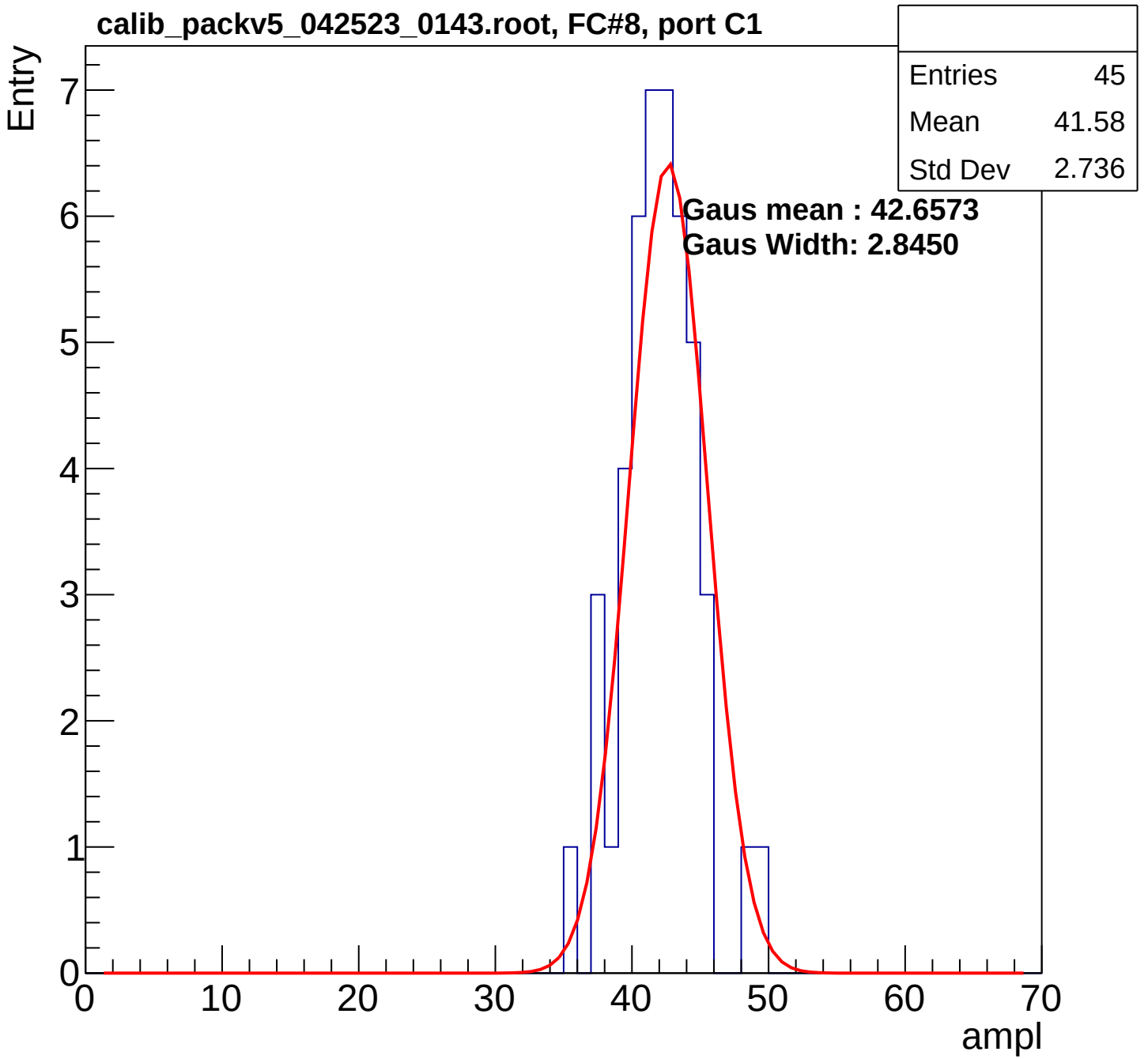

B0L002S, U19-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.71
Std Dev 11.44
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

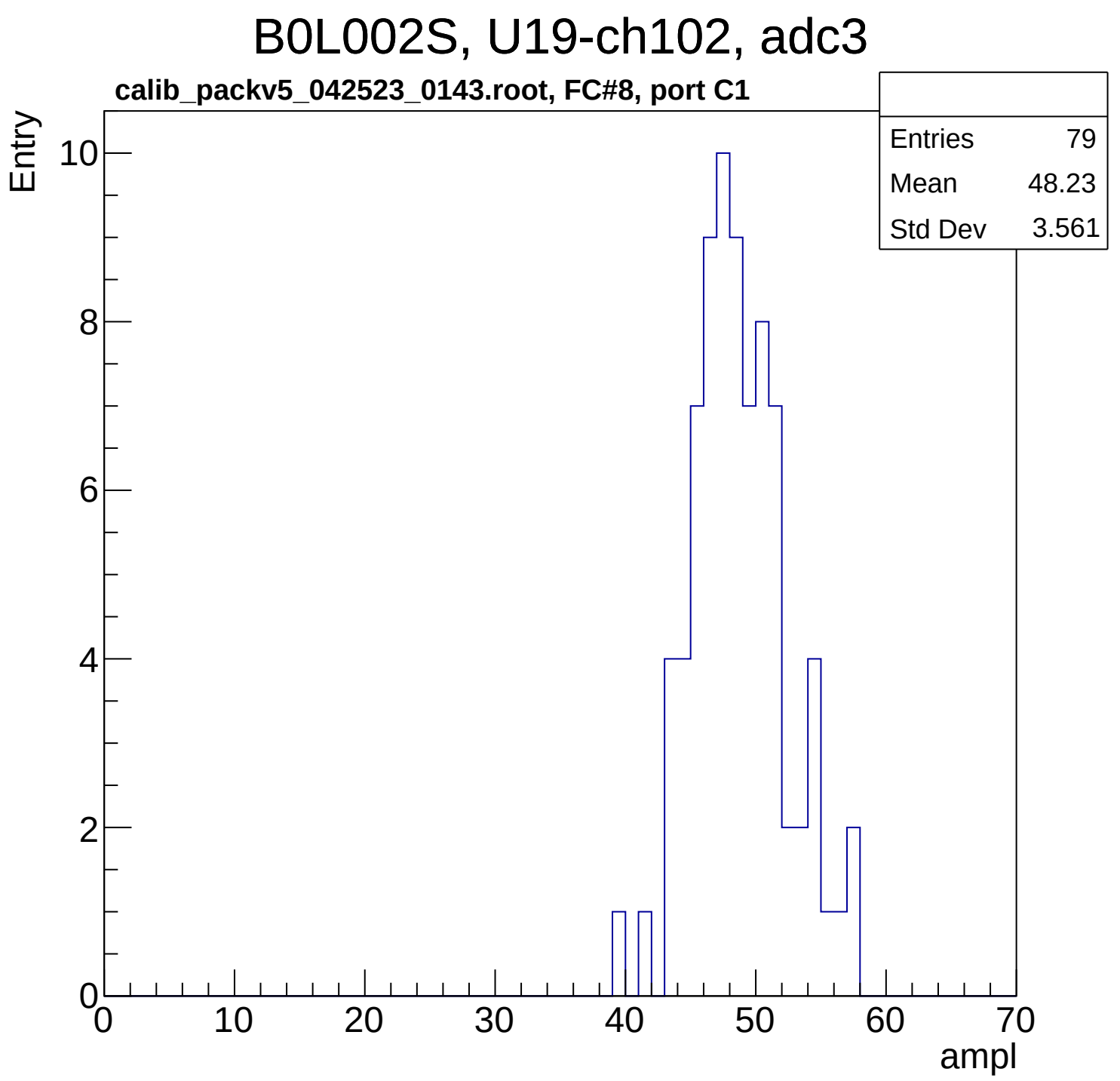

B0L002S, U19-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

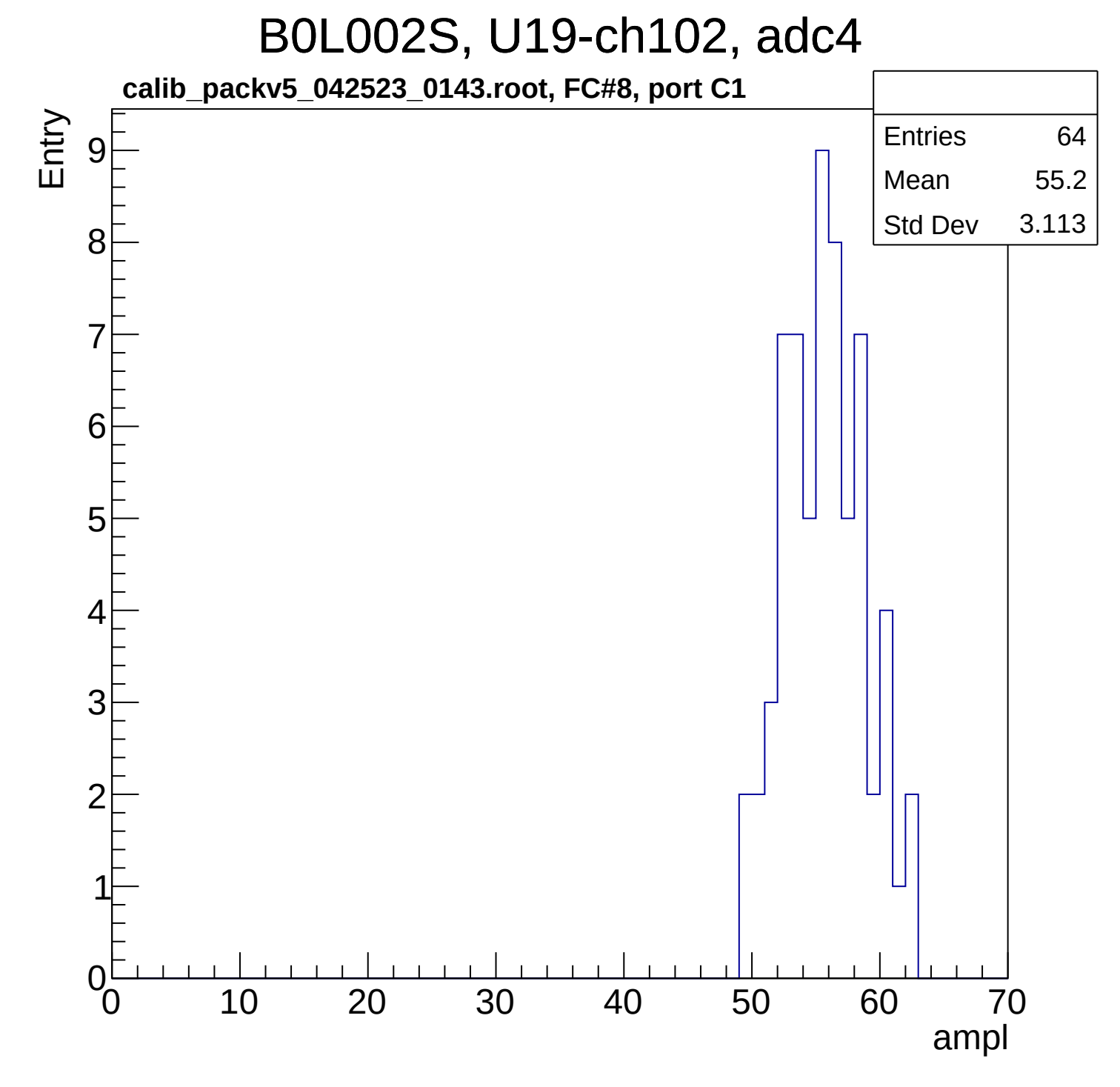

B0L002S, U19-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

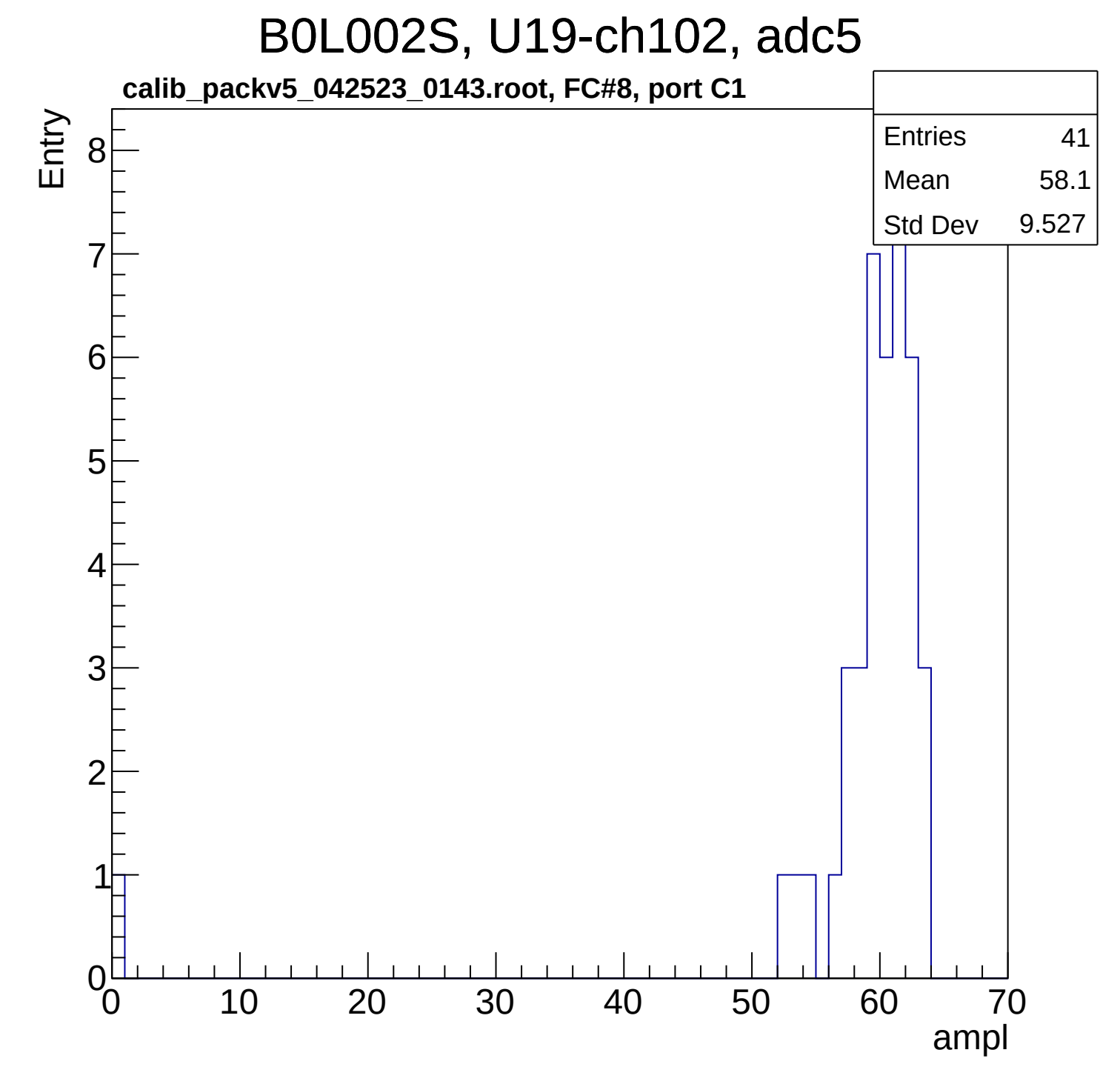

B0L002S, U19-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 28.61
Std Dev 5.91
Gaus mean : 29.9970
Gaus Width: 3.1848
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

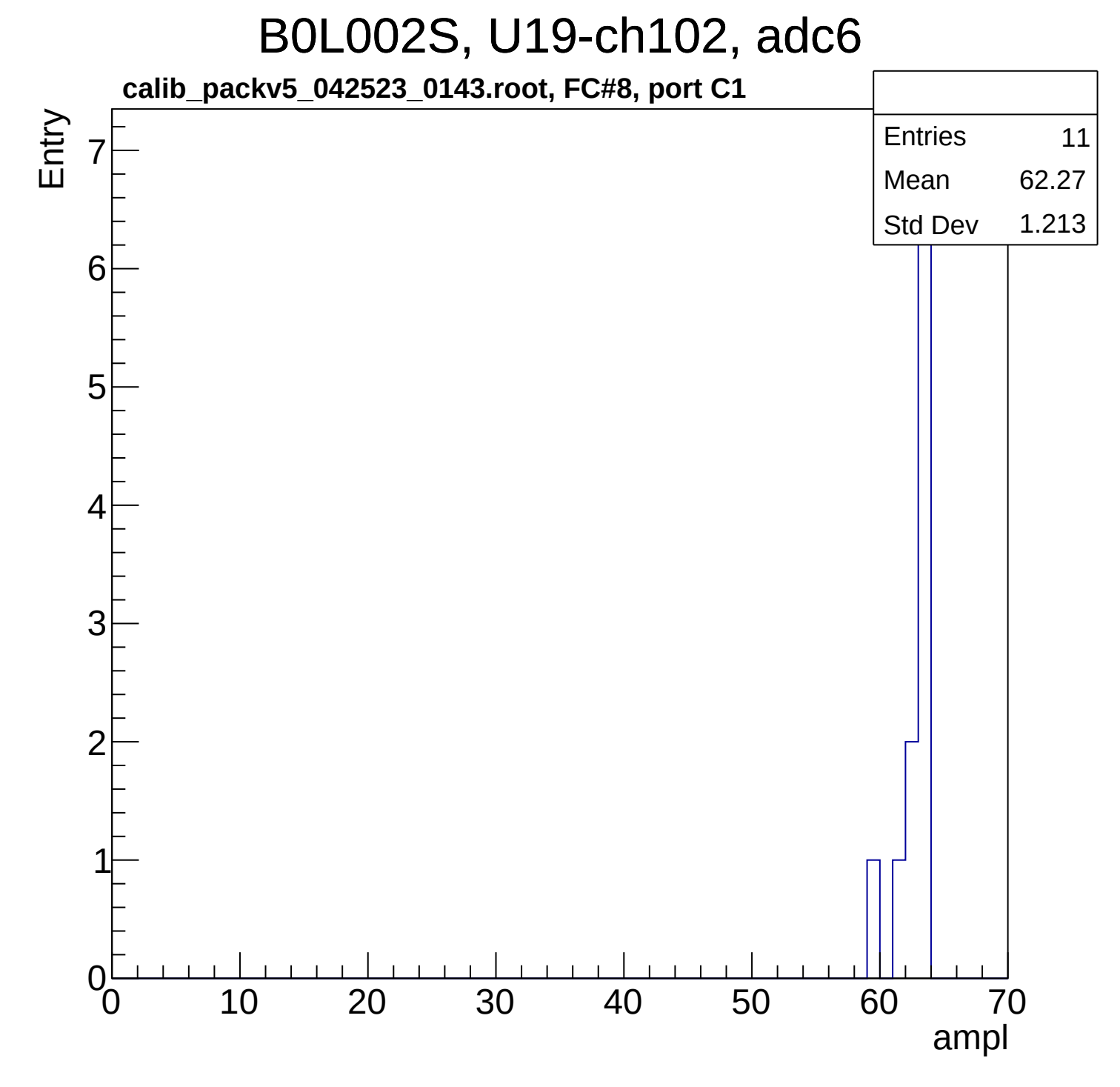

B0L002S, U19-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.48
Std Dev 3.309
Gaus mean : 36.8287
Gaus Width: 3.6768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

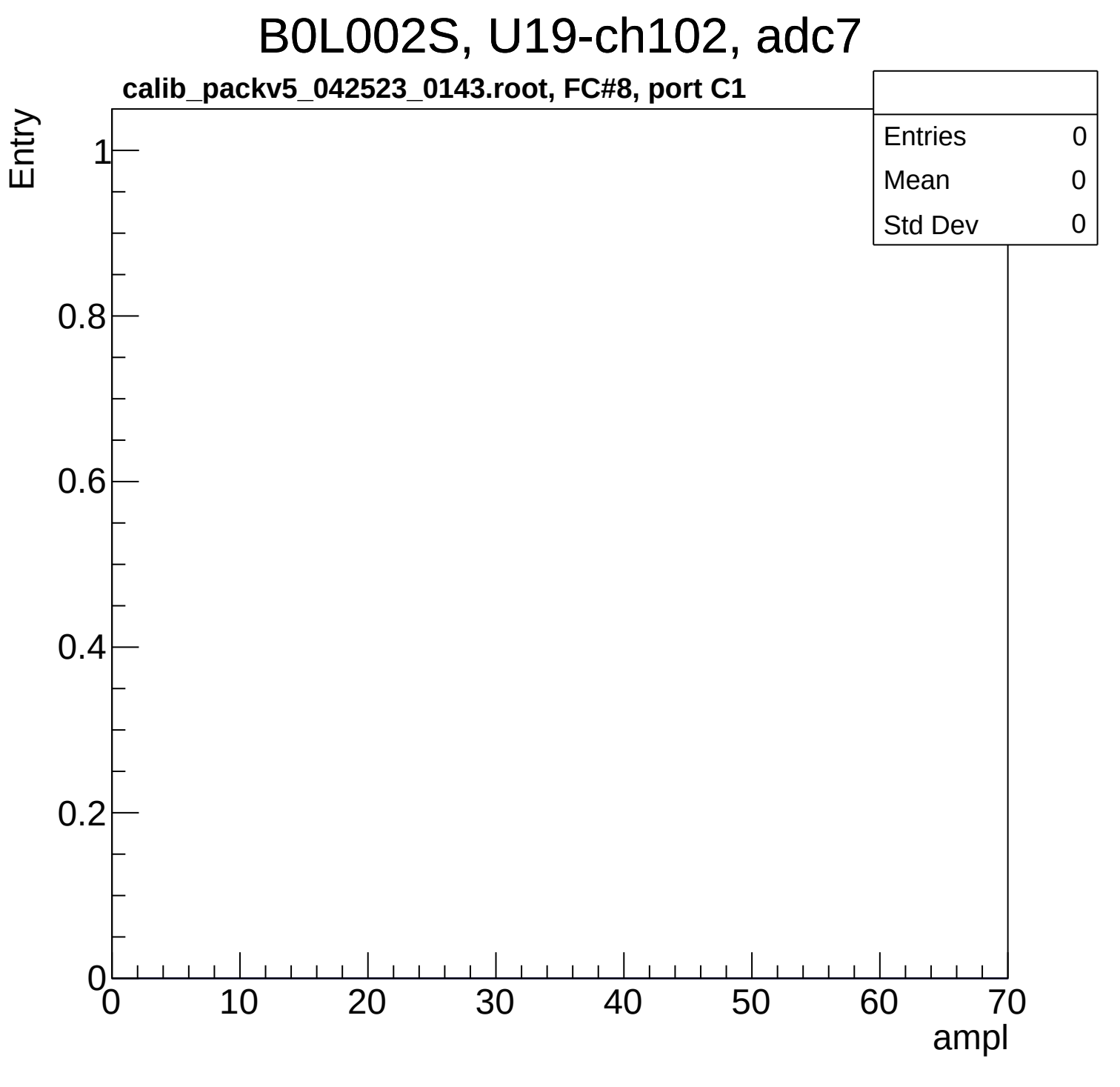

B0L002S, U19-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 41.58
Std Dev 2.736
Gaus mean : 42.6573
Gaus Width: 2.8450
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

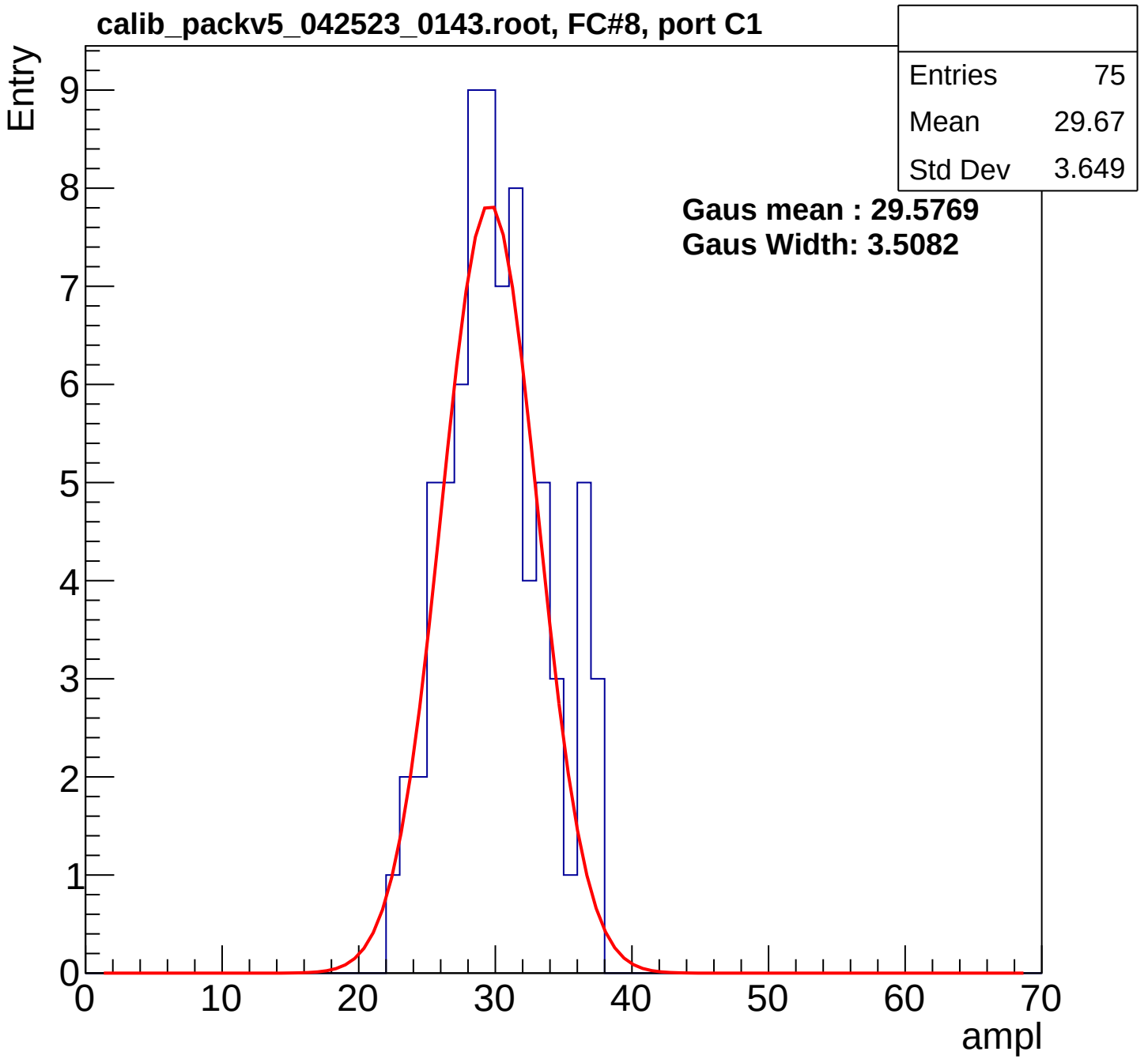

B0L002S, U19-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 48.23
Std Dev 3.561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

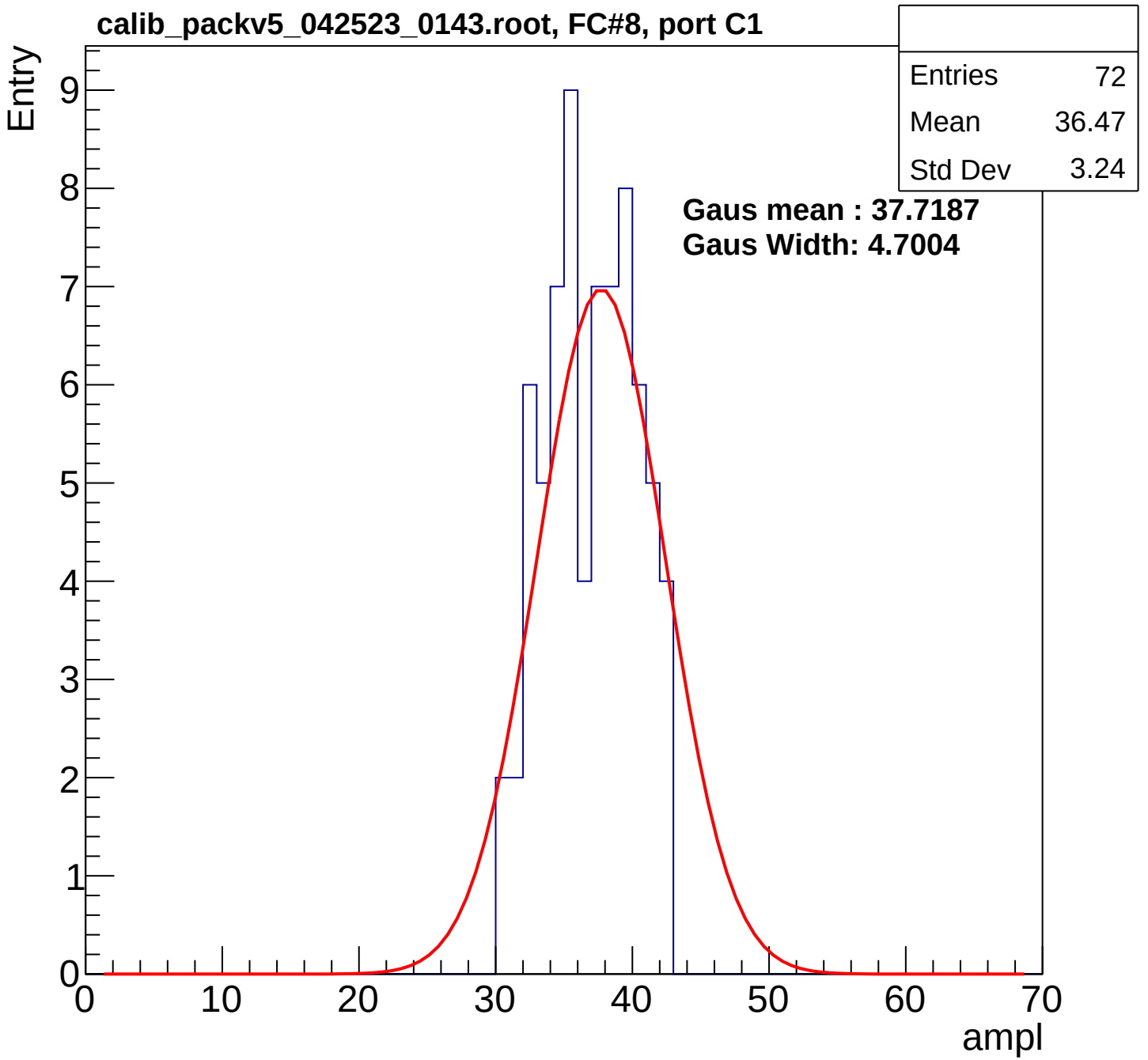

B0L002S, U19-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.2
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

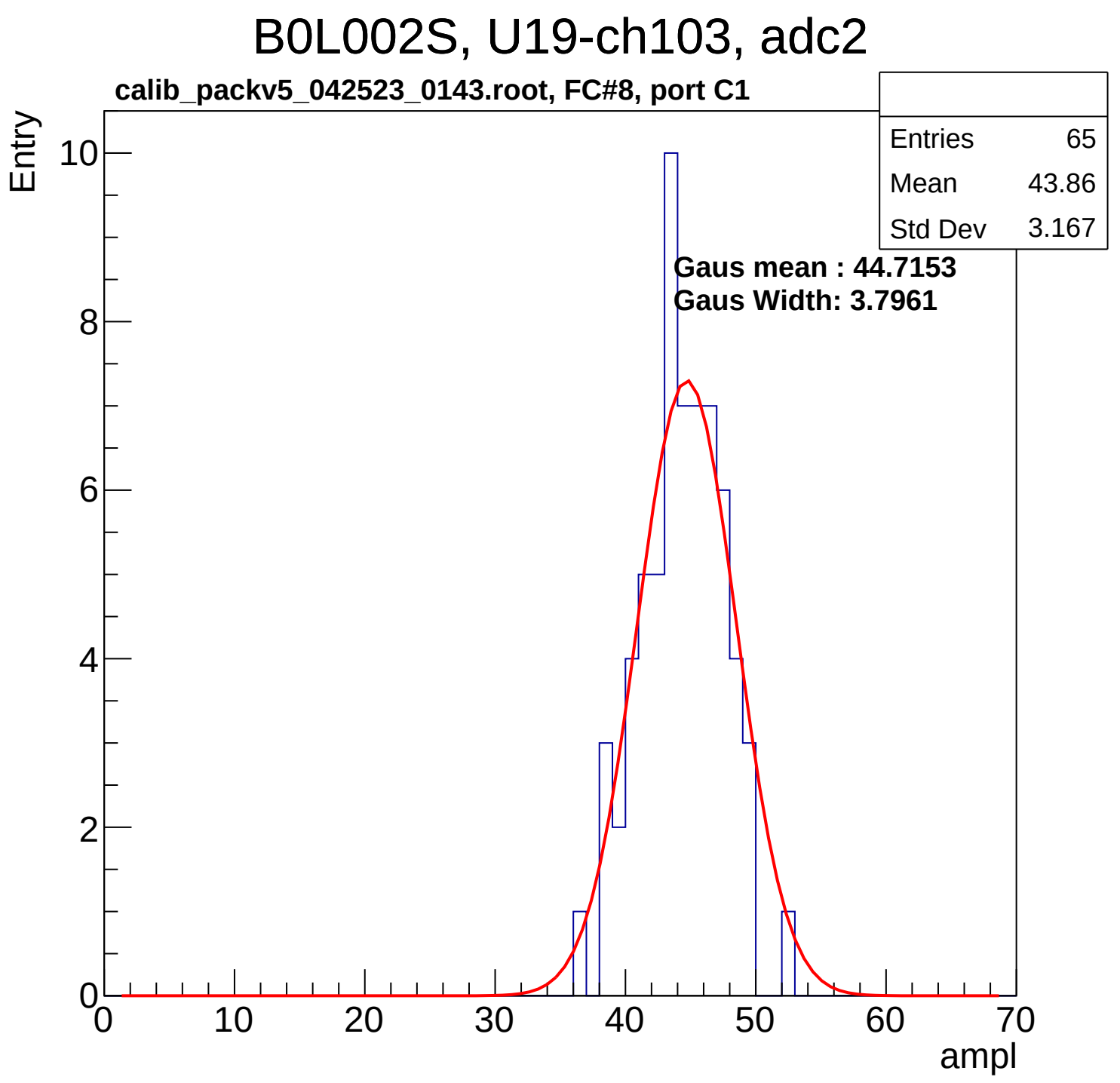

B0L002S, U19-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.1
Std Dev 9.527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

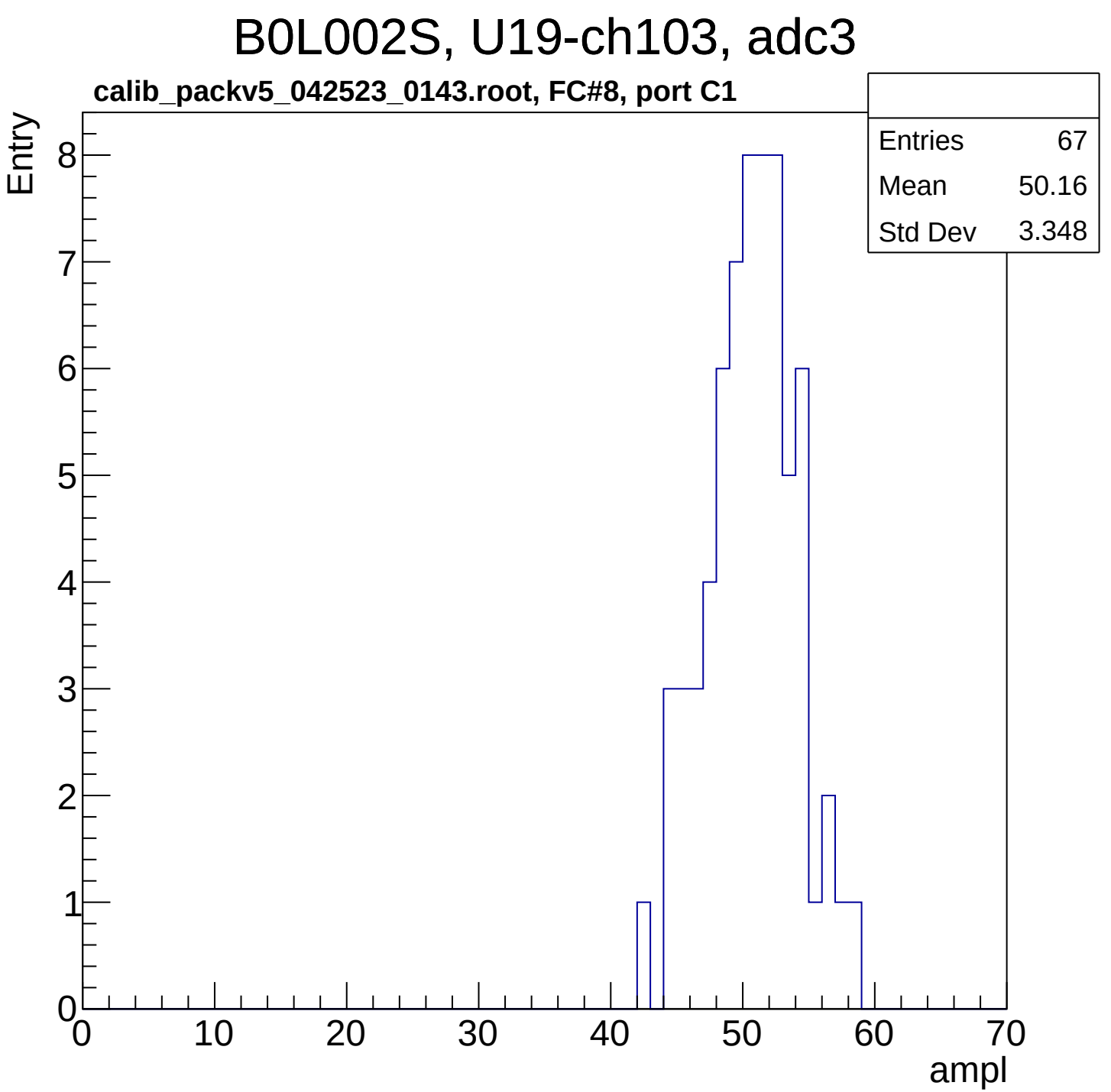

B0L002S, U19-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 11
Mean 62.27
Std Dev 1.213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

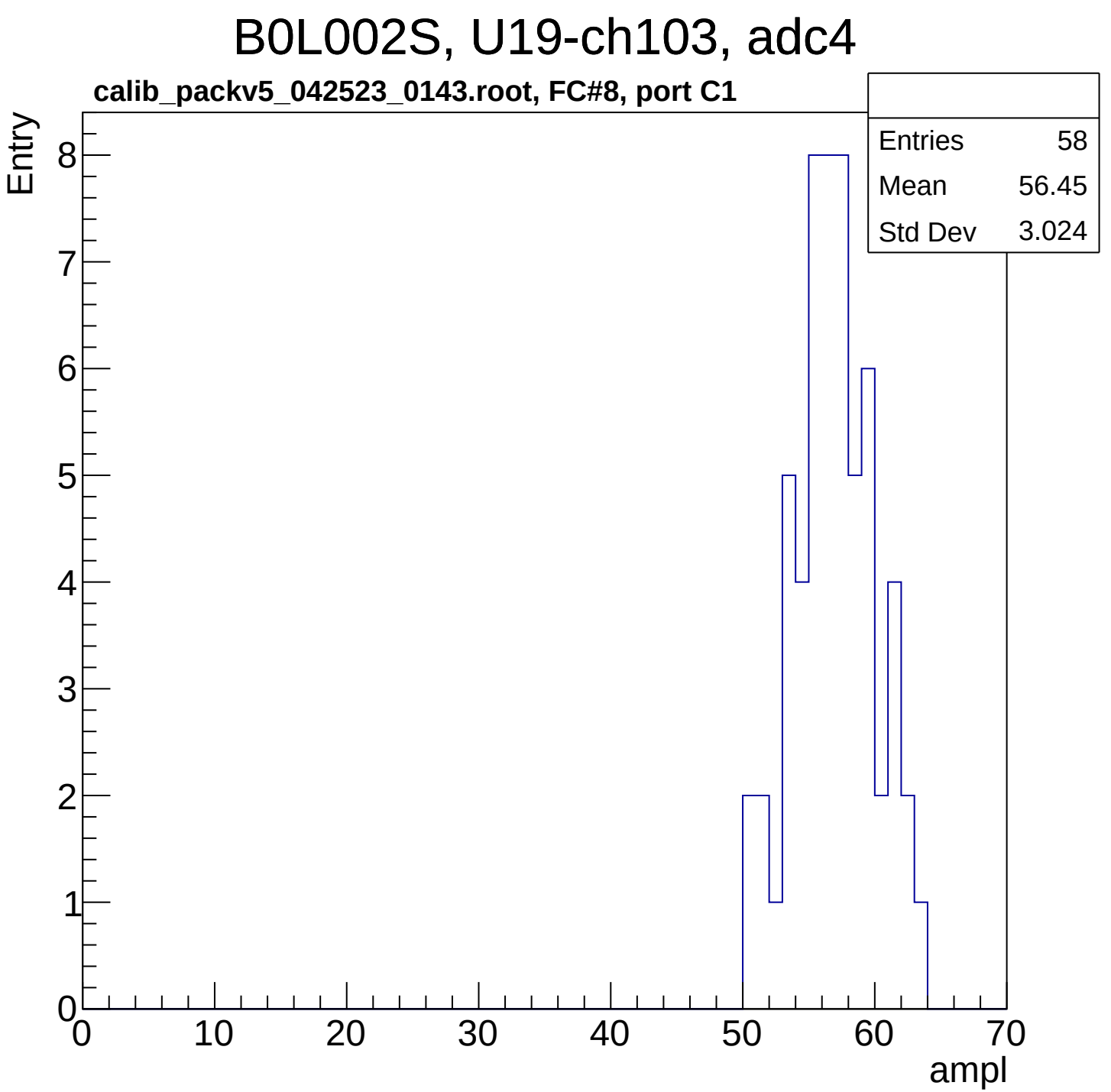

B0L002S, U19-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

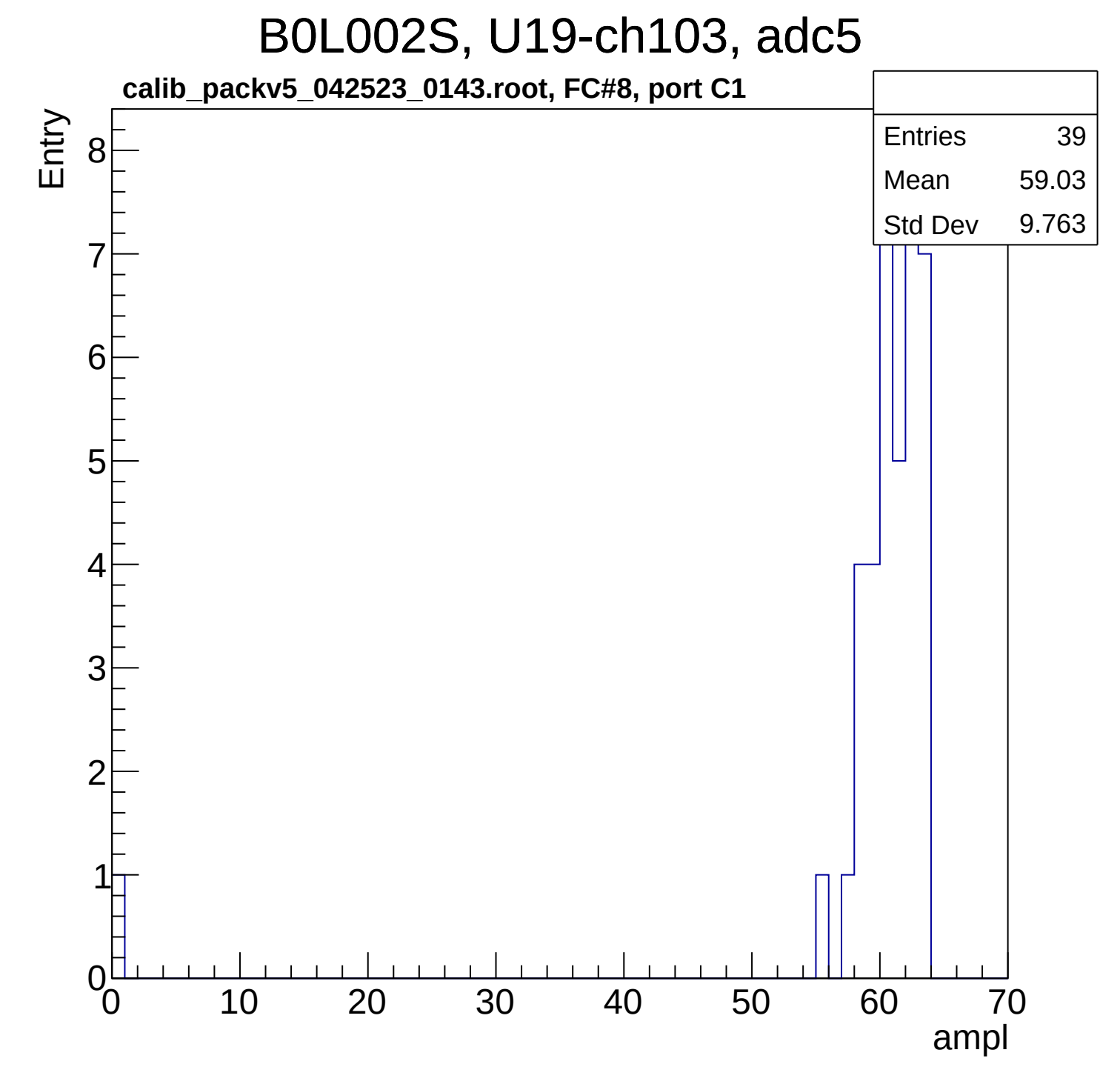

B0L002S, U19-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.67
Std Dev 3.649
Gaus mean : 29.5769
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

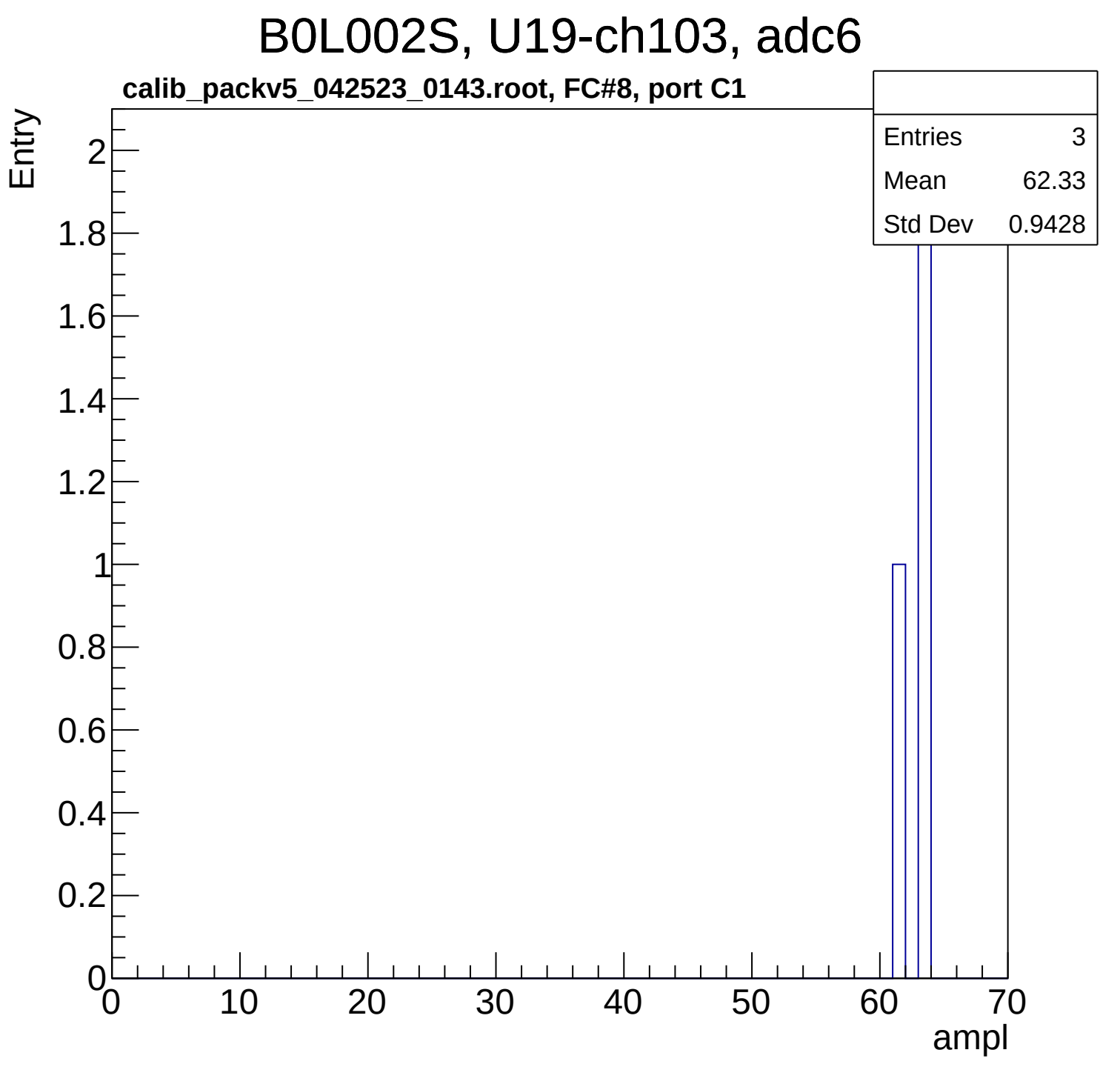

B0L002S, U19-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.47
Std Dev 3.24
Gaus mean : 37.7187
Gaus Width: 4.7004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

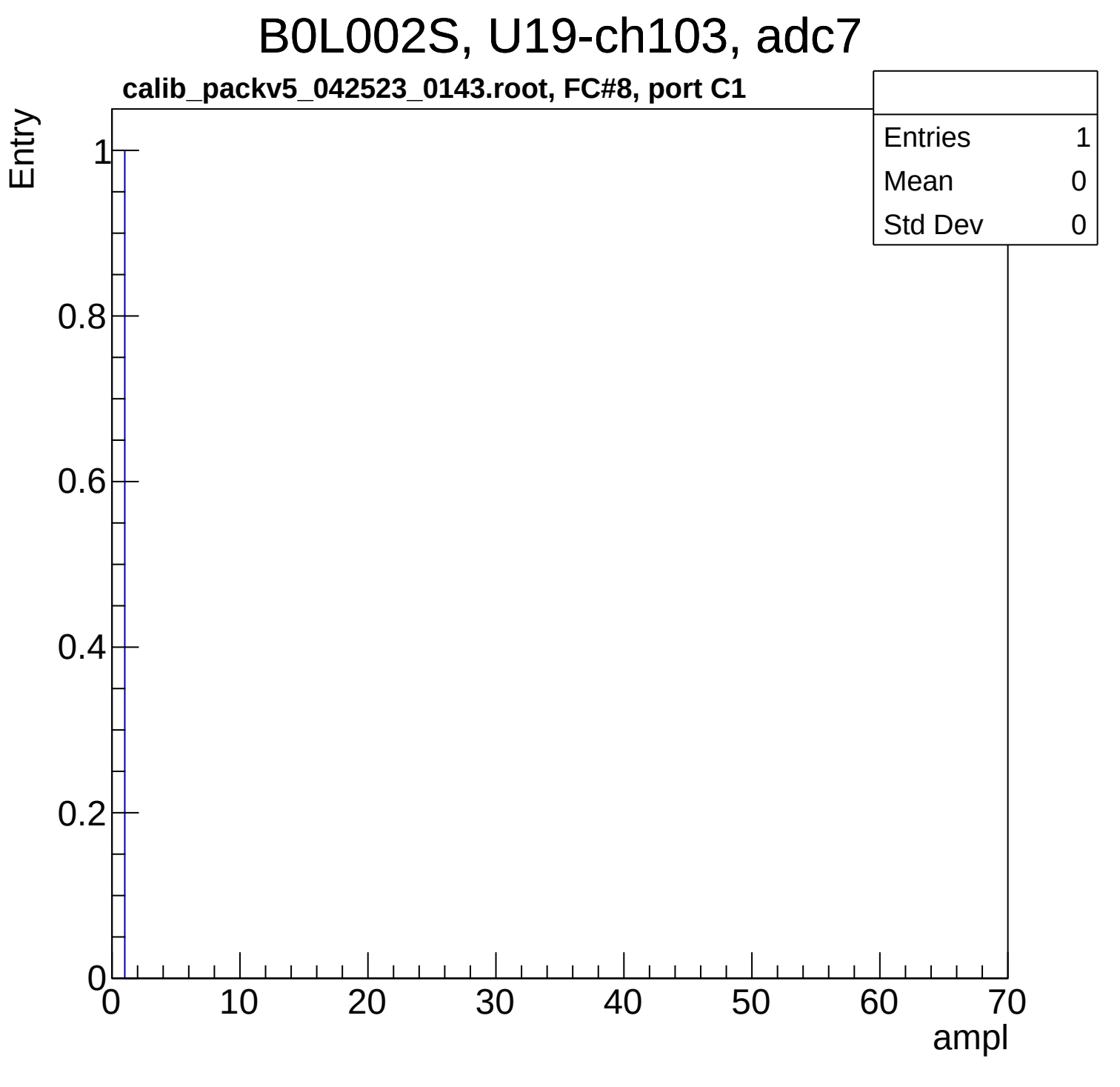

B0L002S, U19-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.86
Std Dev 3.167
Gaus mean : 44.7153
Gaus Width: 3.7961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

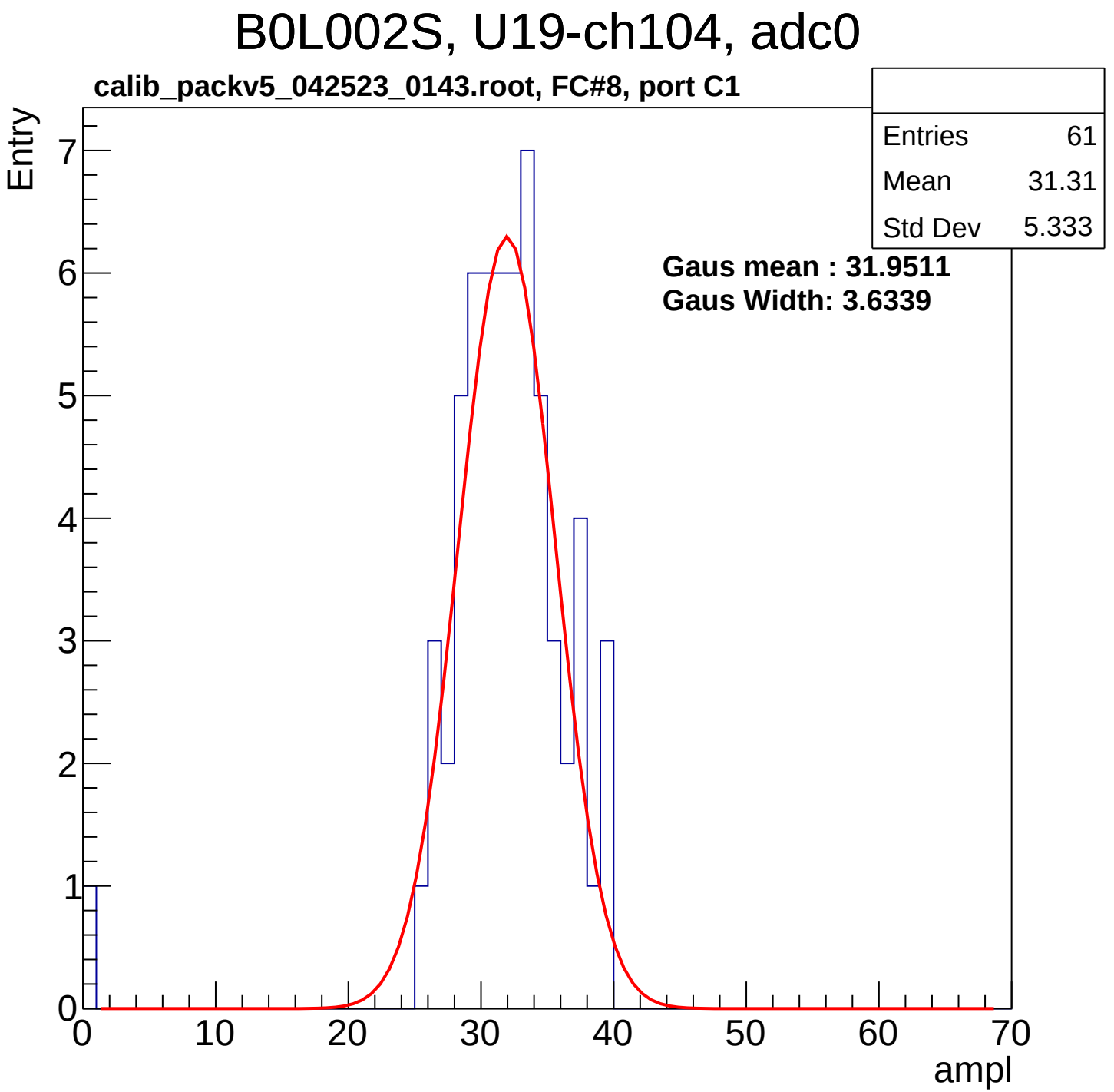

B0L002S, U19-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.16
Std Dev 3.348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

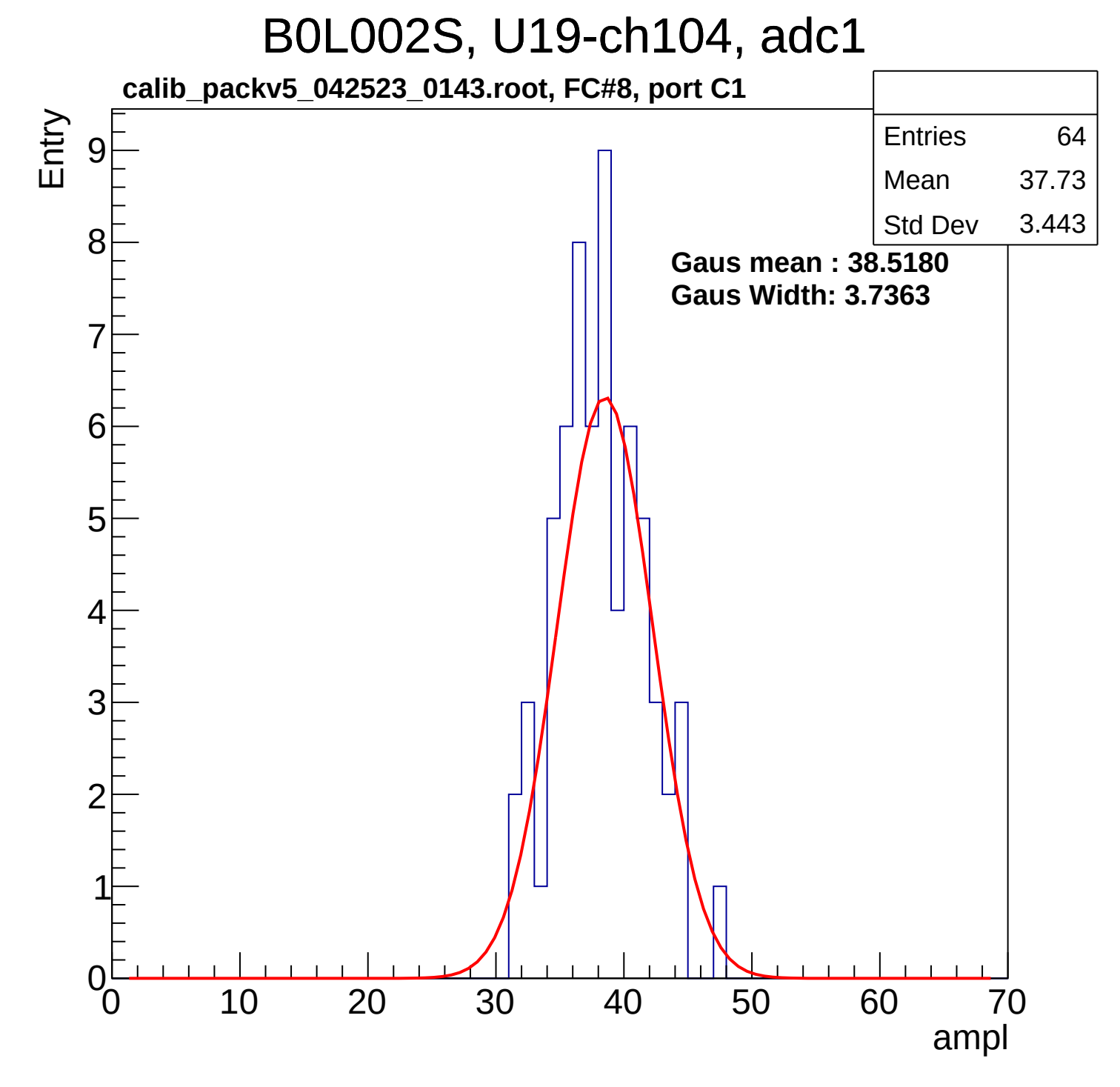

B0L002S, U19-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.45
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

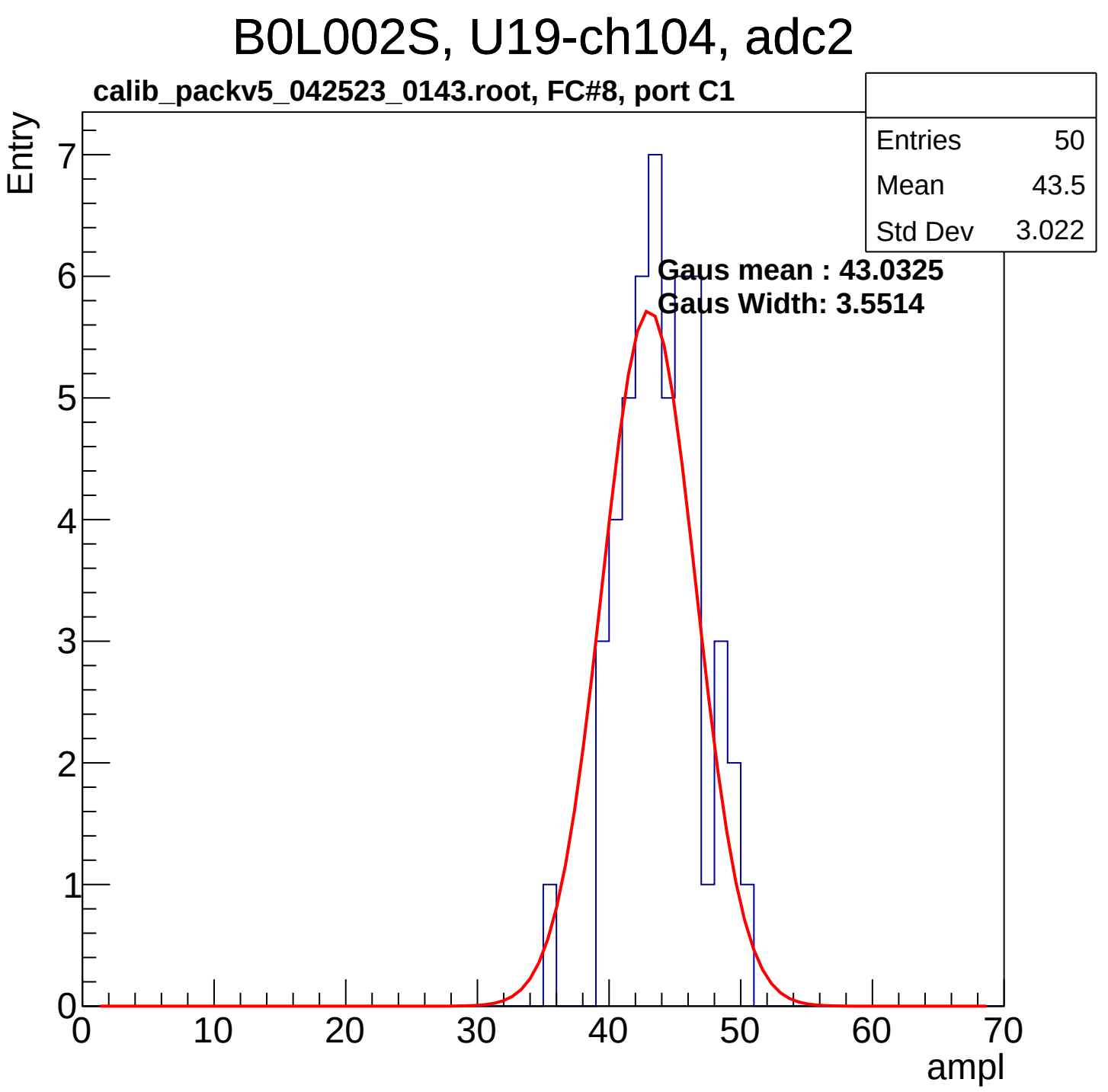

B0L002S, U19-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.03
Std Dev 9.763
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

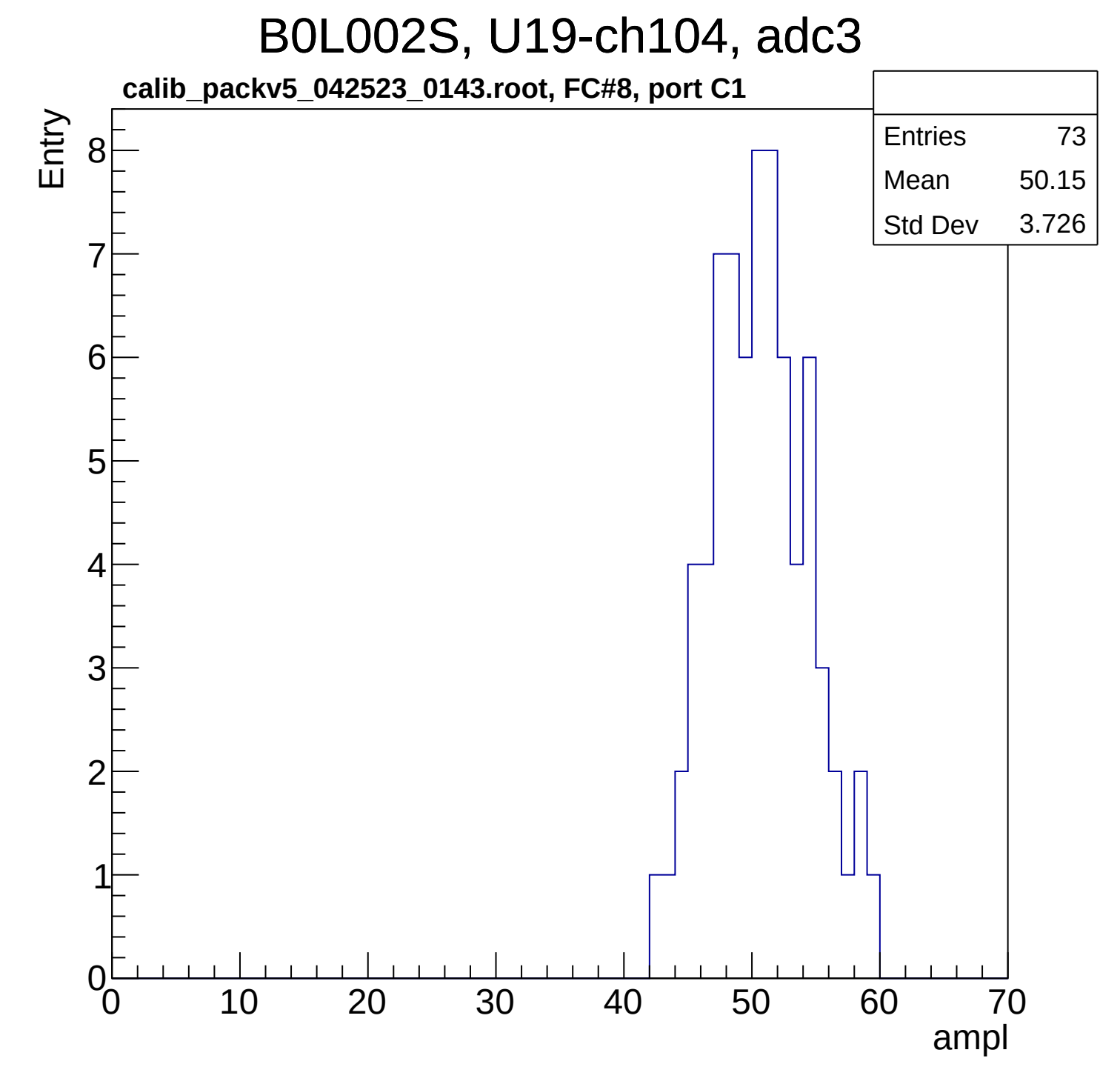

B0L002S, U19-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

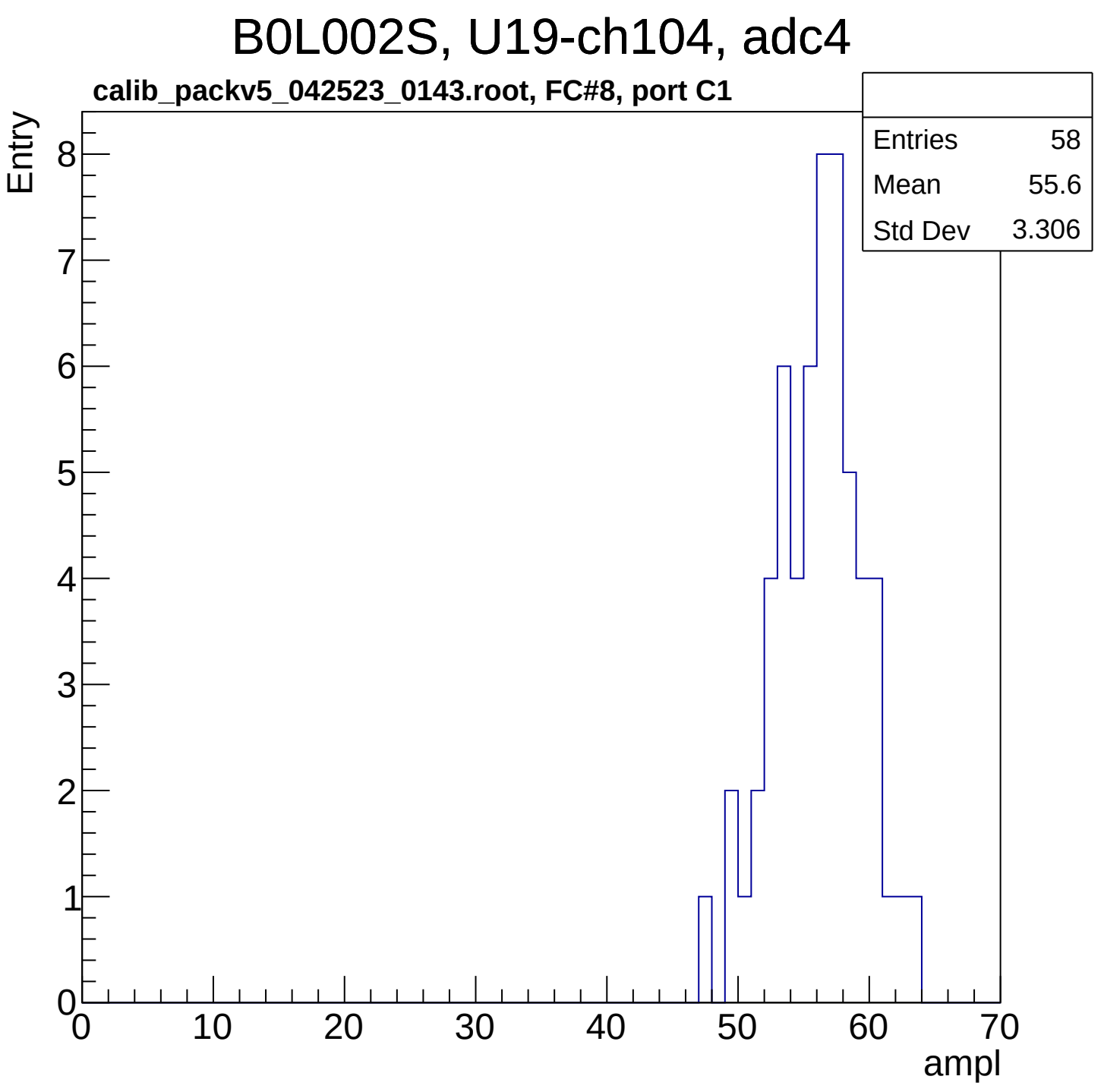

B0L002S, U19-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

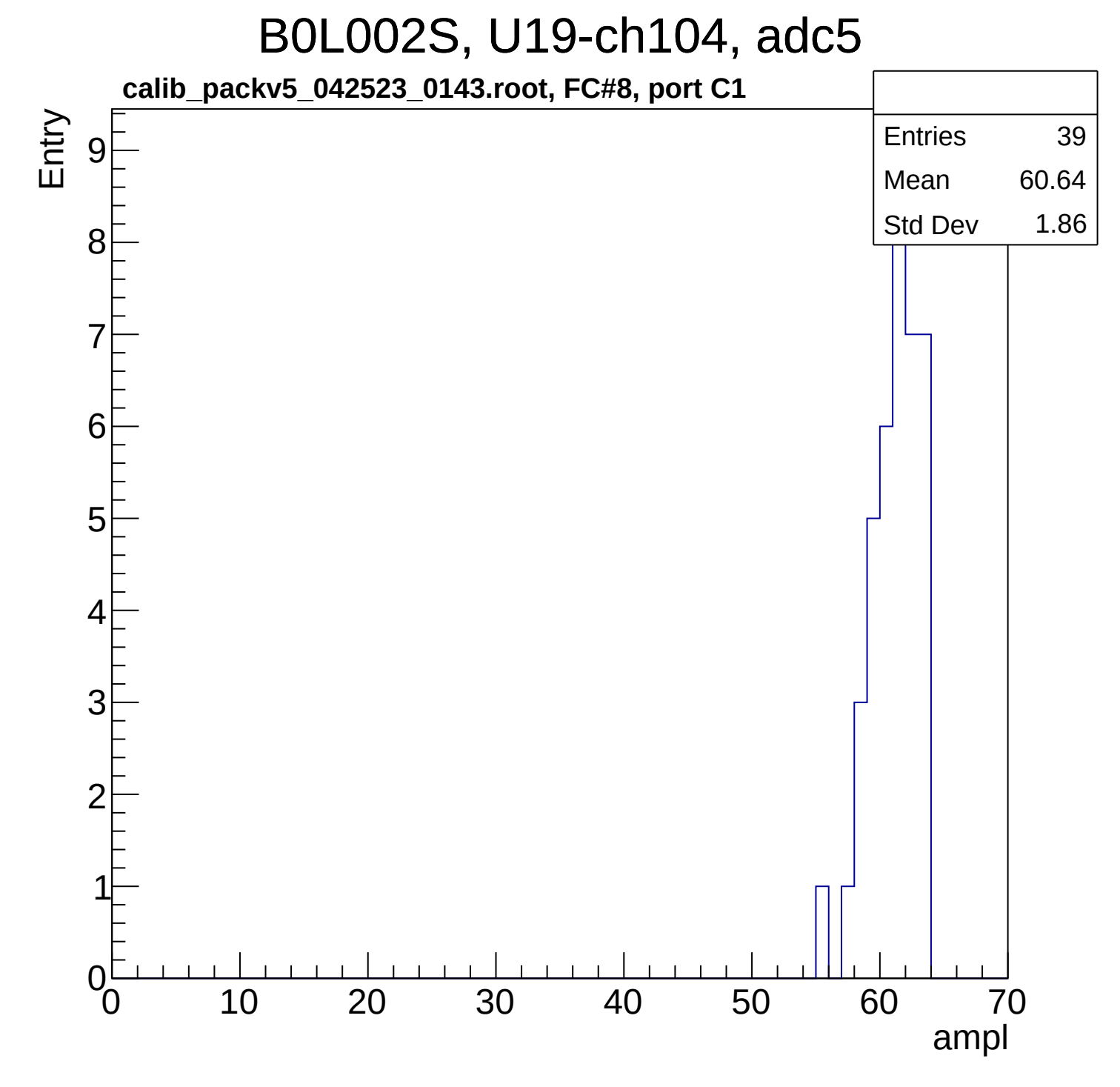

B0L002S, U19-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 31.31
Std Dev 5.333
Gaus mean : 31.9511
Gaus Width: 3.6339
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

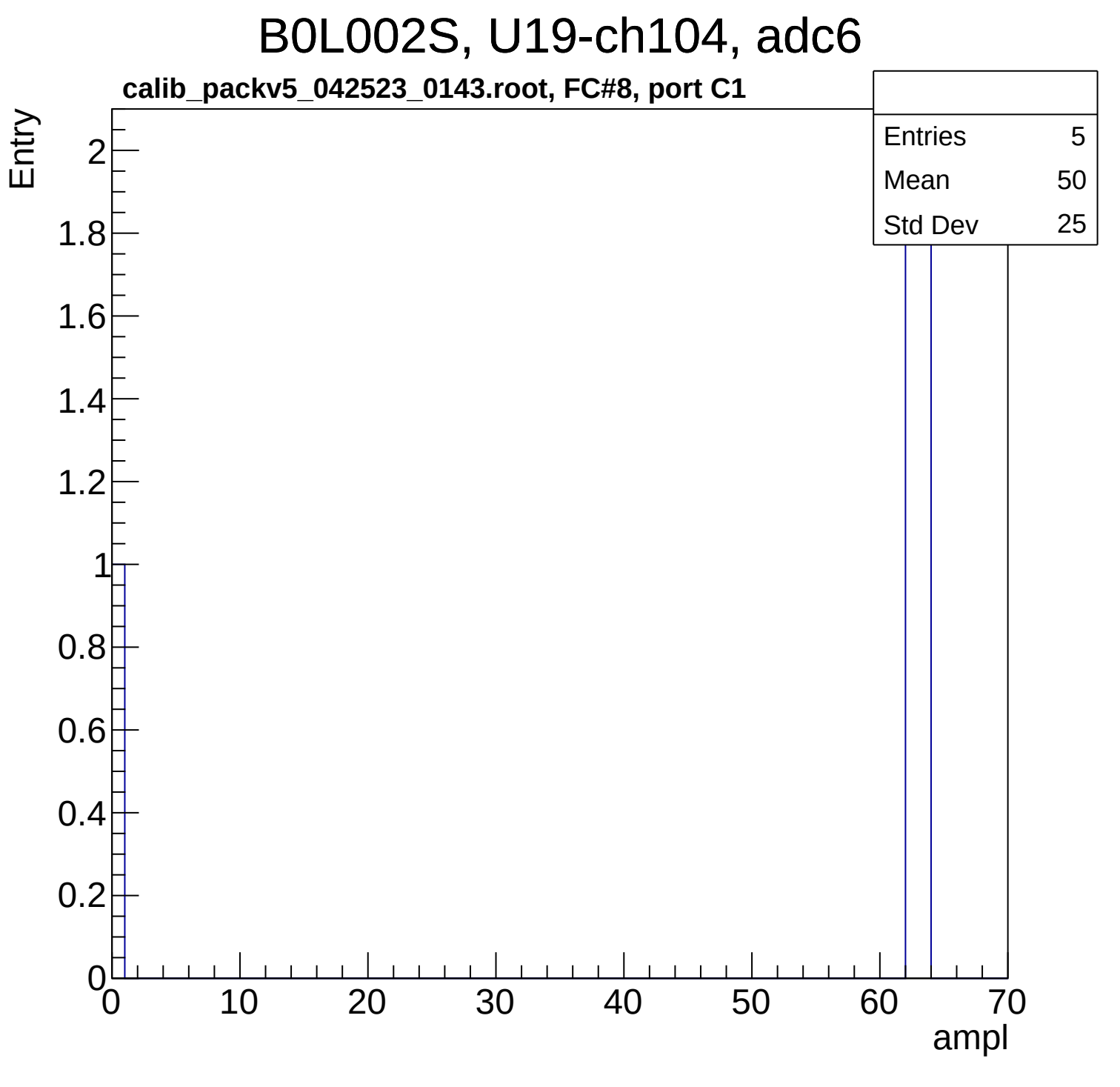

B0L002S, U19-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.73
Std Dev 3.443
Gaus mean : 38.5180
Gaus Width: 3.7363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

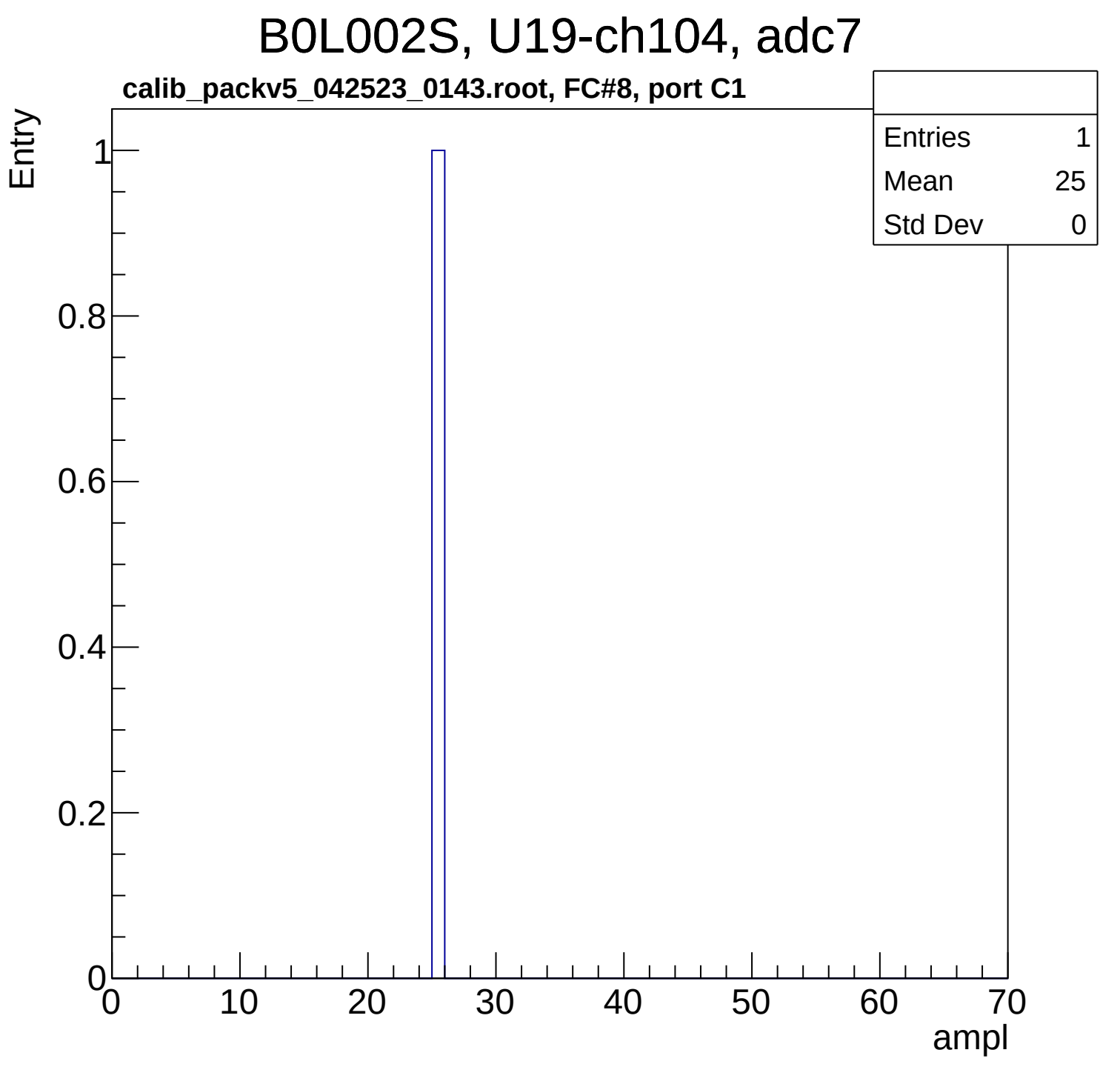

B0L002S, U19-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 43.5
Std Dev 3.022
Gaus mean : 43.0325
Gaus Width: 3.5514
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

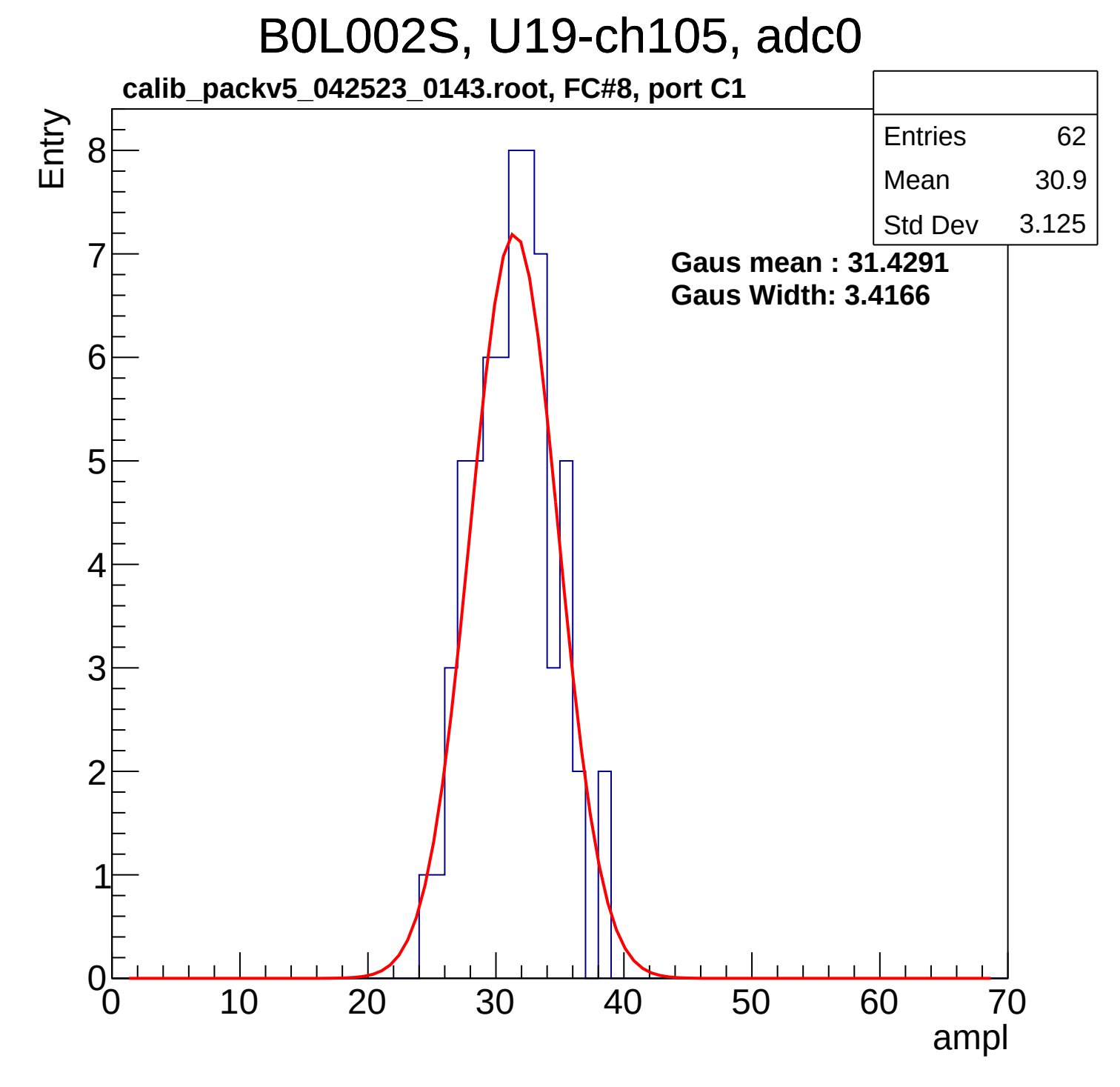

B0L002S, U19-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.15
Std Dev 3.726
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

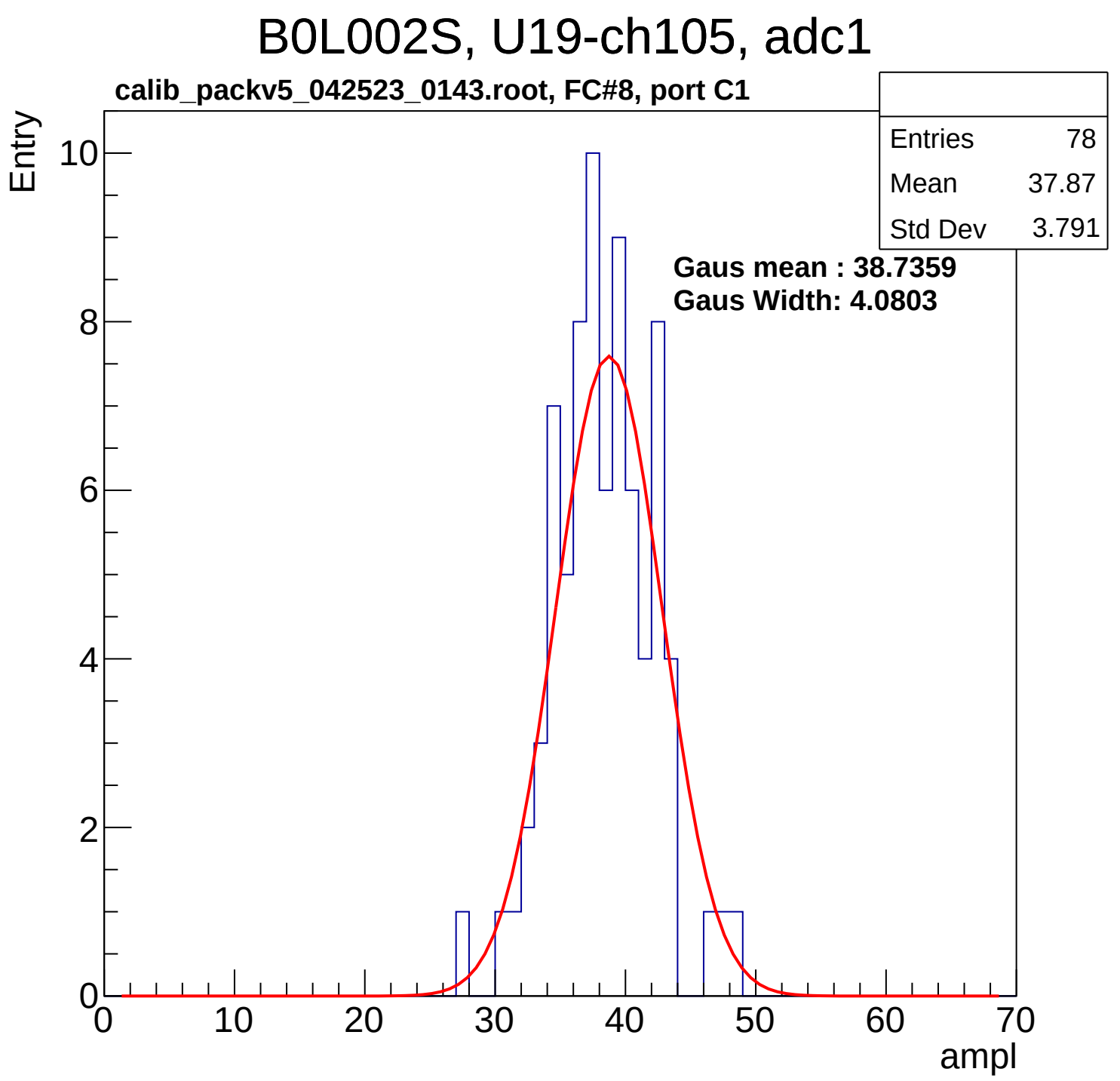

B0L002S, U19-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.6
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

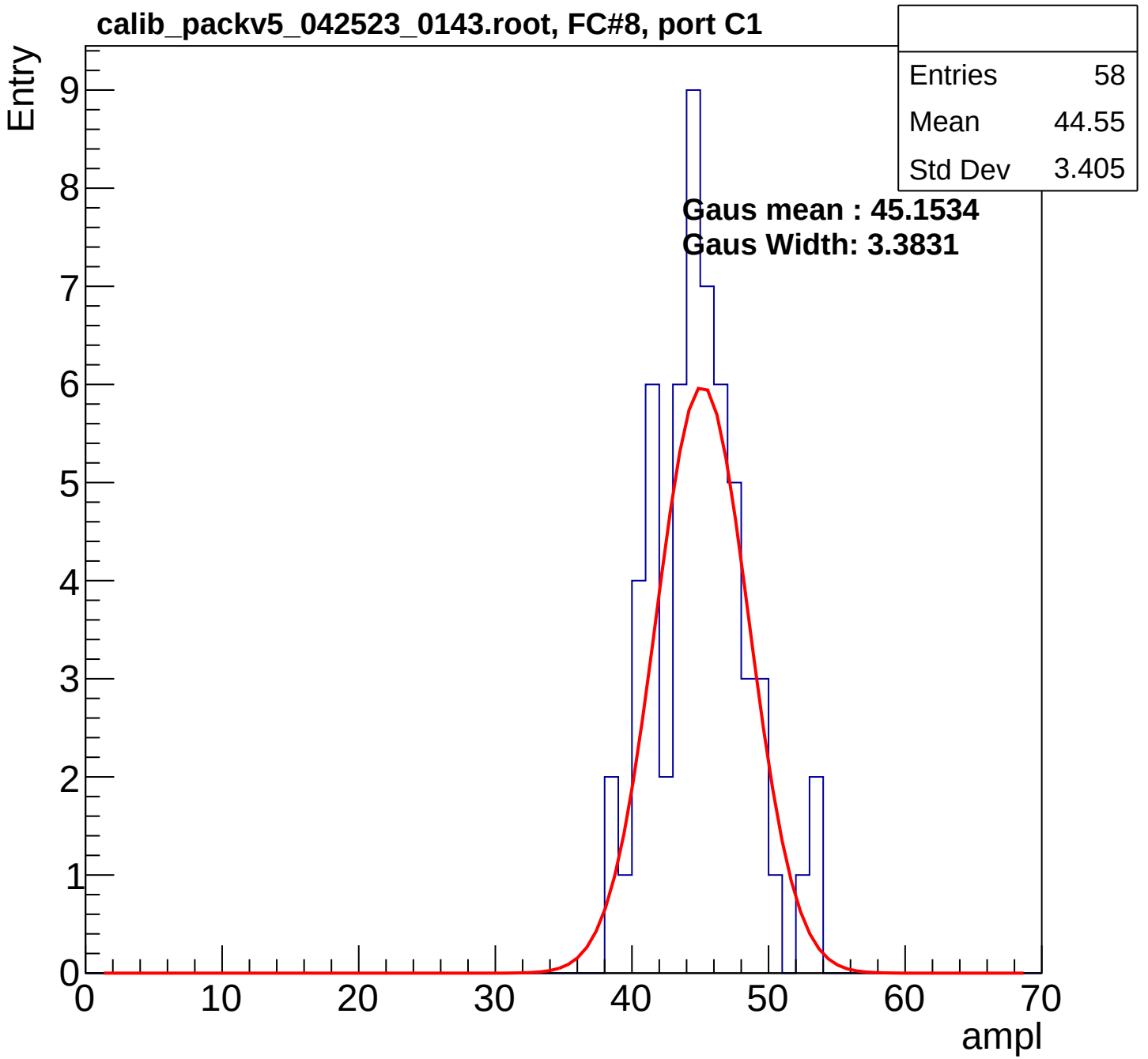

B0L002S, U19-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.64
Std Dev 1.86
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

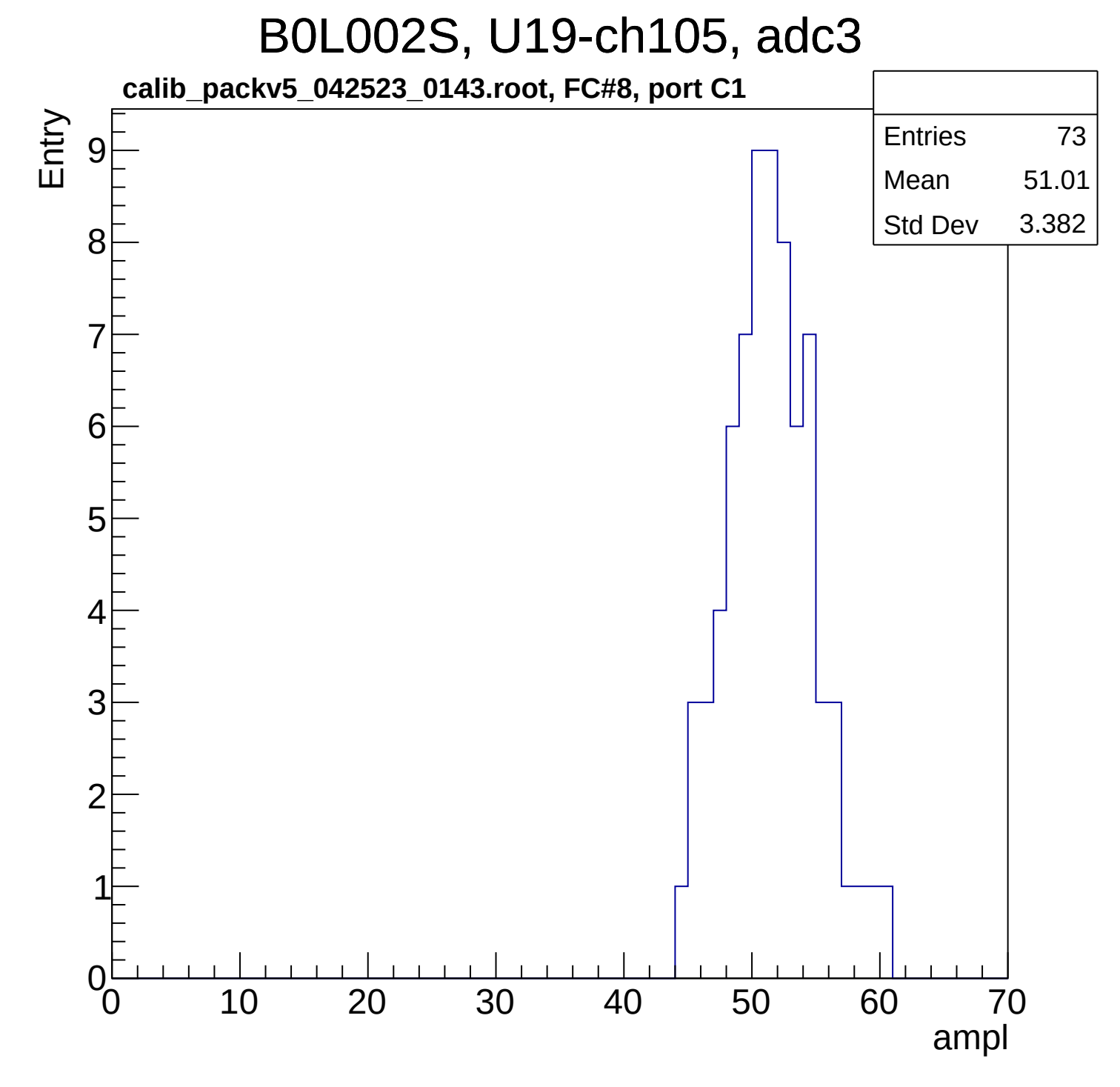

B0L002S, U19-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

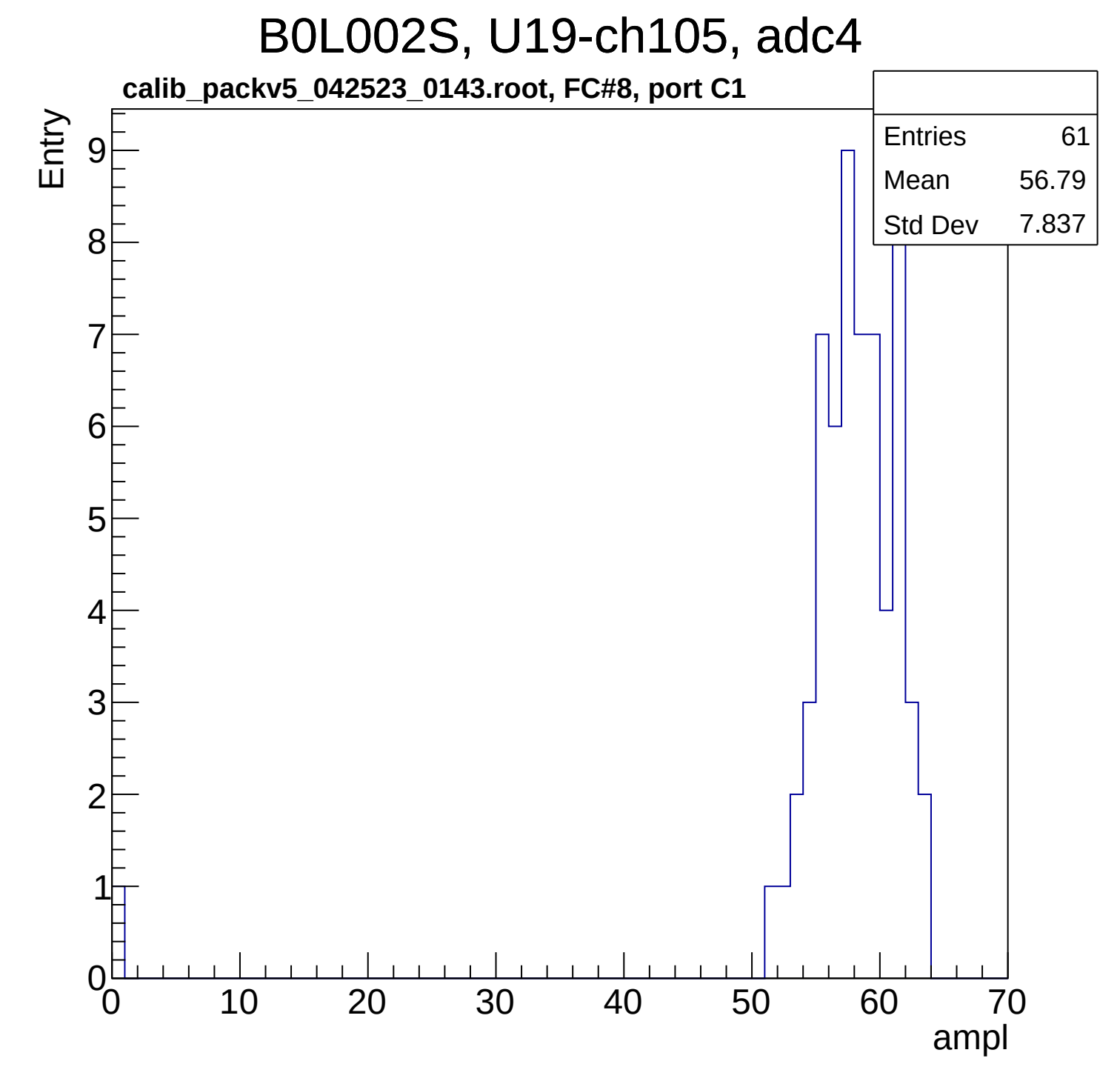

B0L002S, U19-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

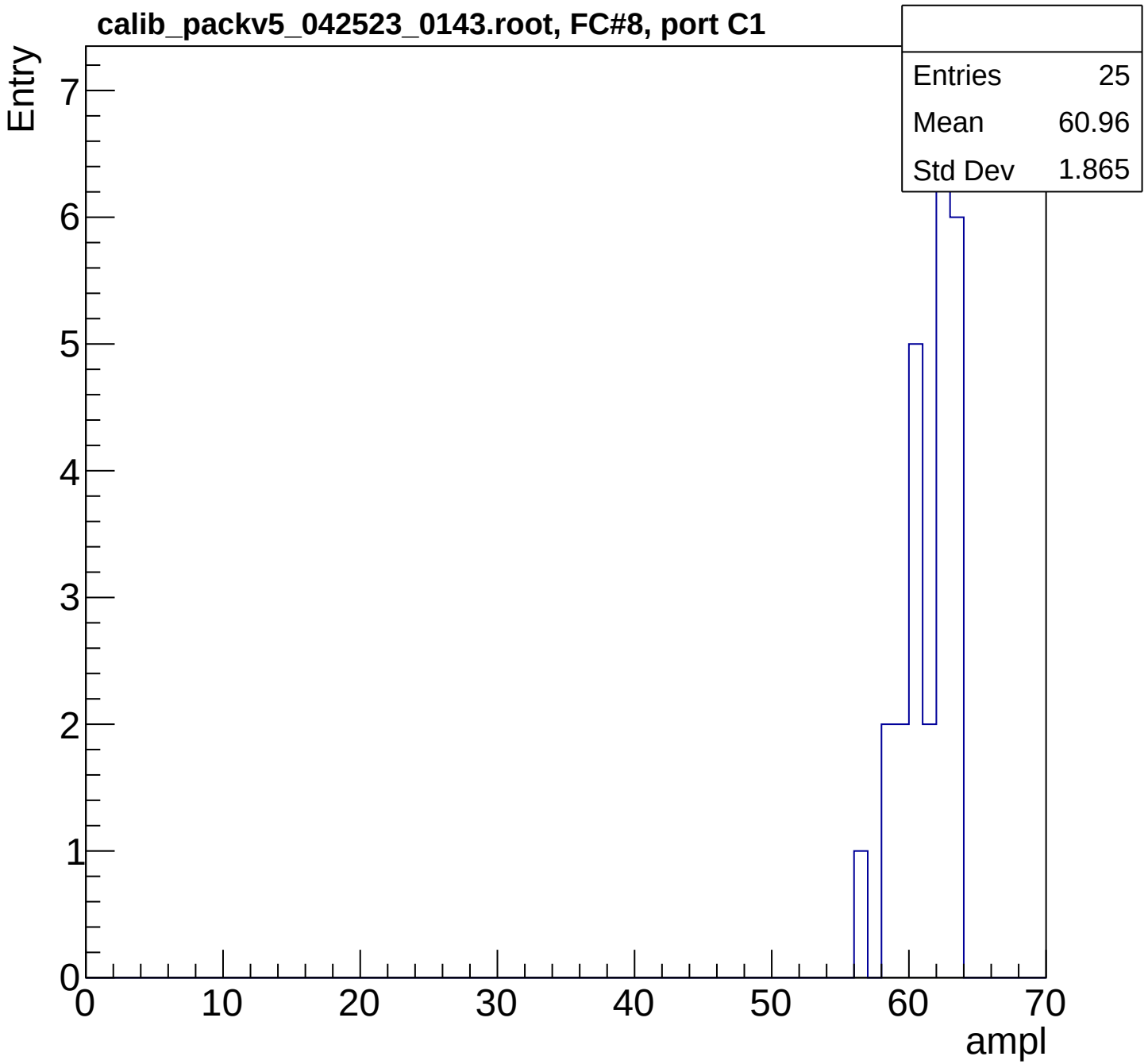

B0L002S, U19-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.9
Std Dev 3.125
Gaus mean : 31.4291
Gaus Width: 3.4166
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

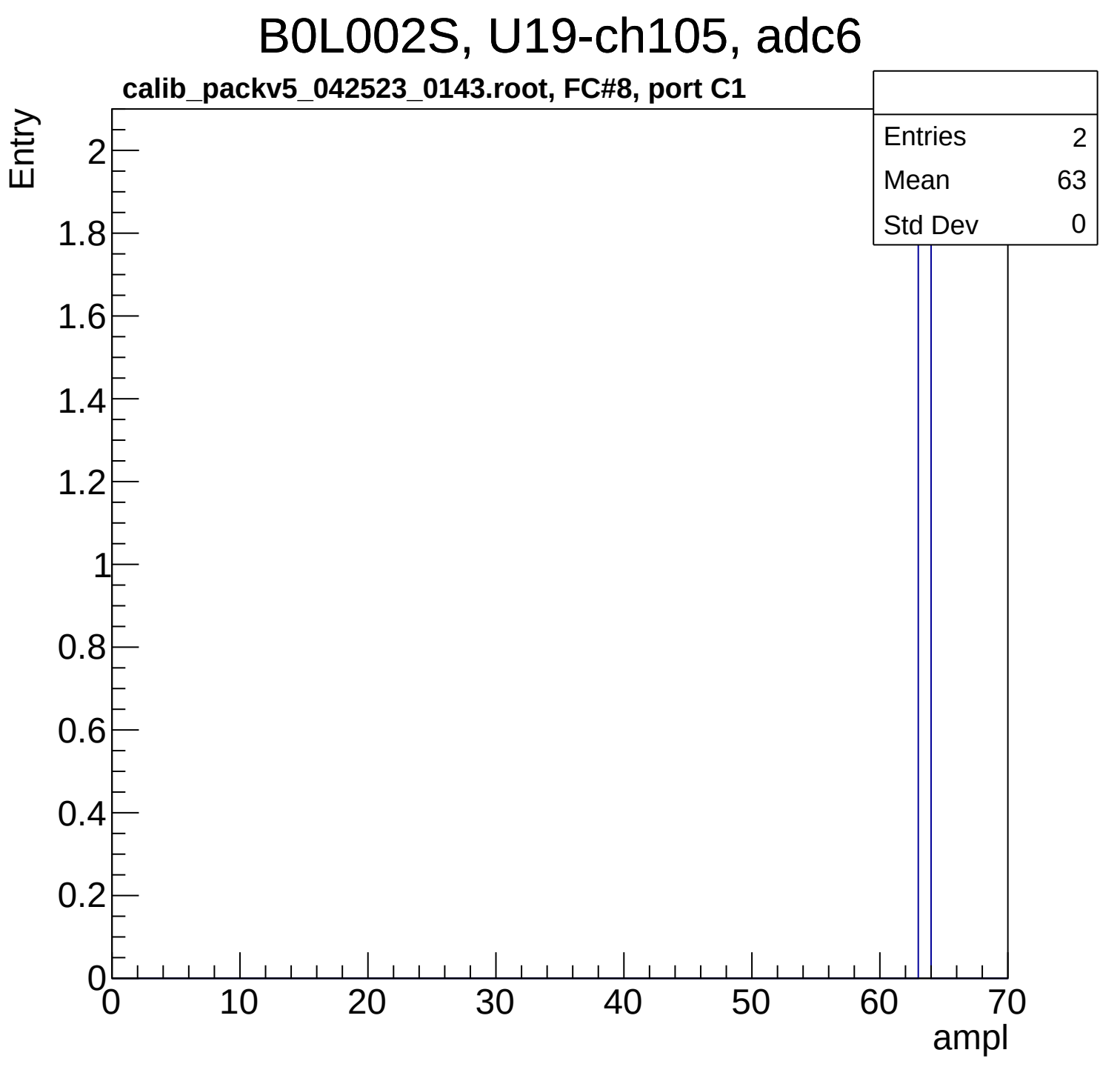

B0L002S, U19-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.87
Std Dev 3.791
Gaus mean : 38.7359
Gaus Width: 4.0803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

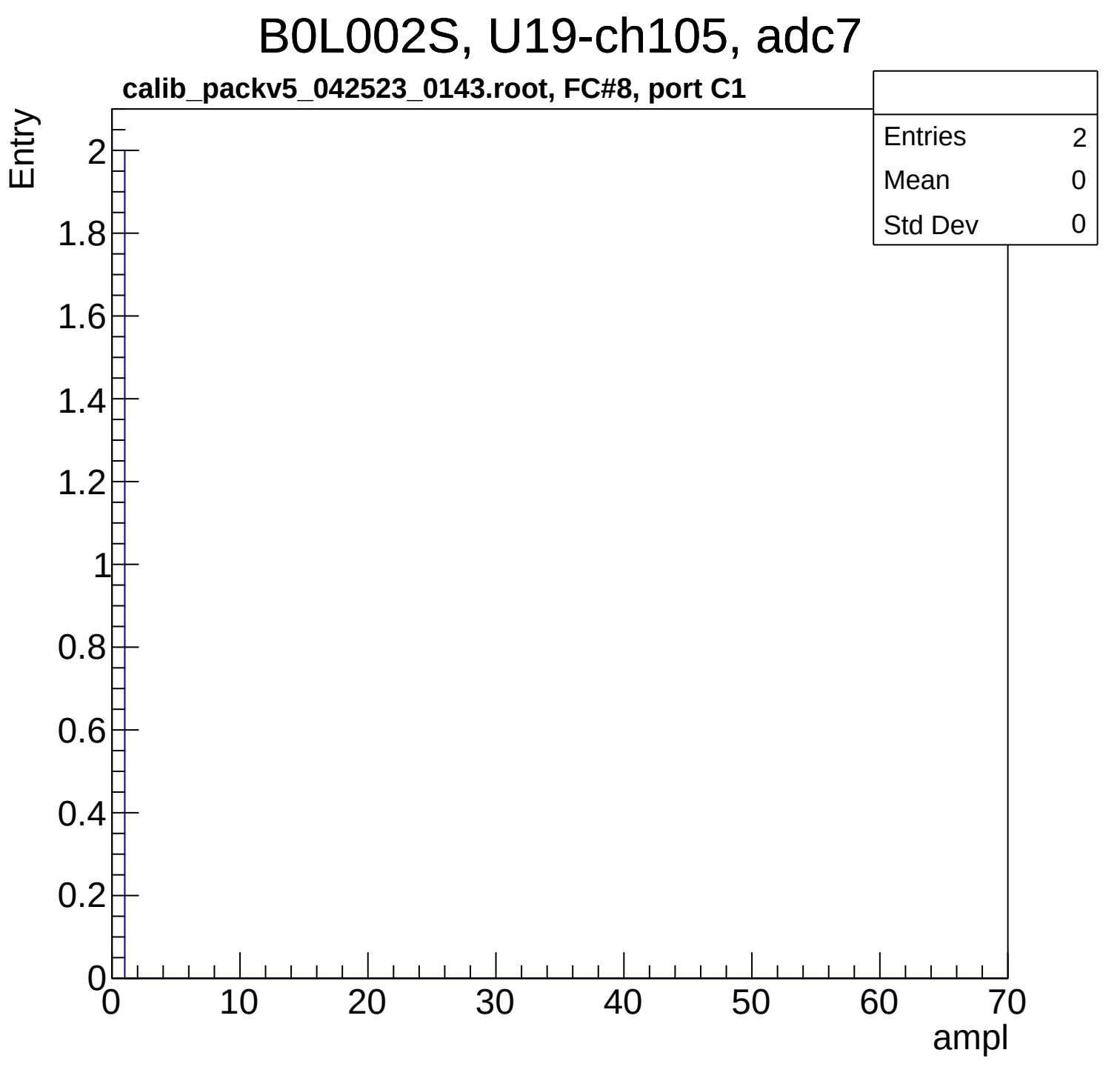

B0L002S, U19-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 44.55
Std Dev 3.405
Gaus mean : 45.1534
Gaus Width: 3.3831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

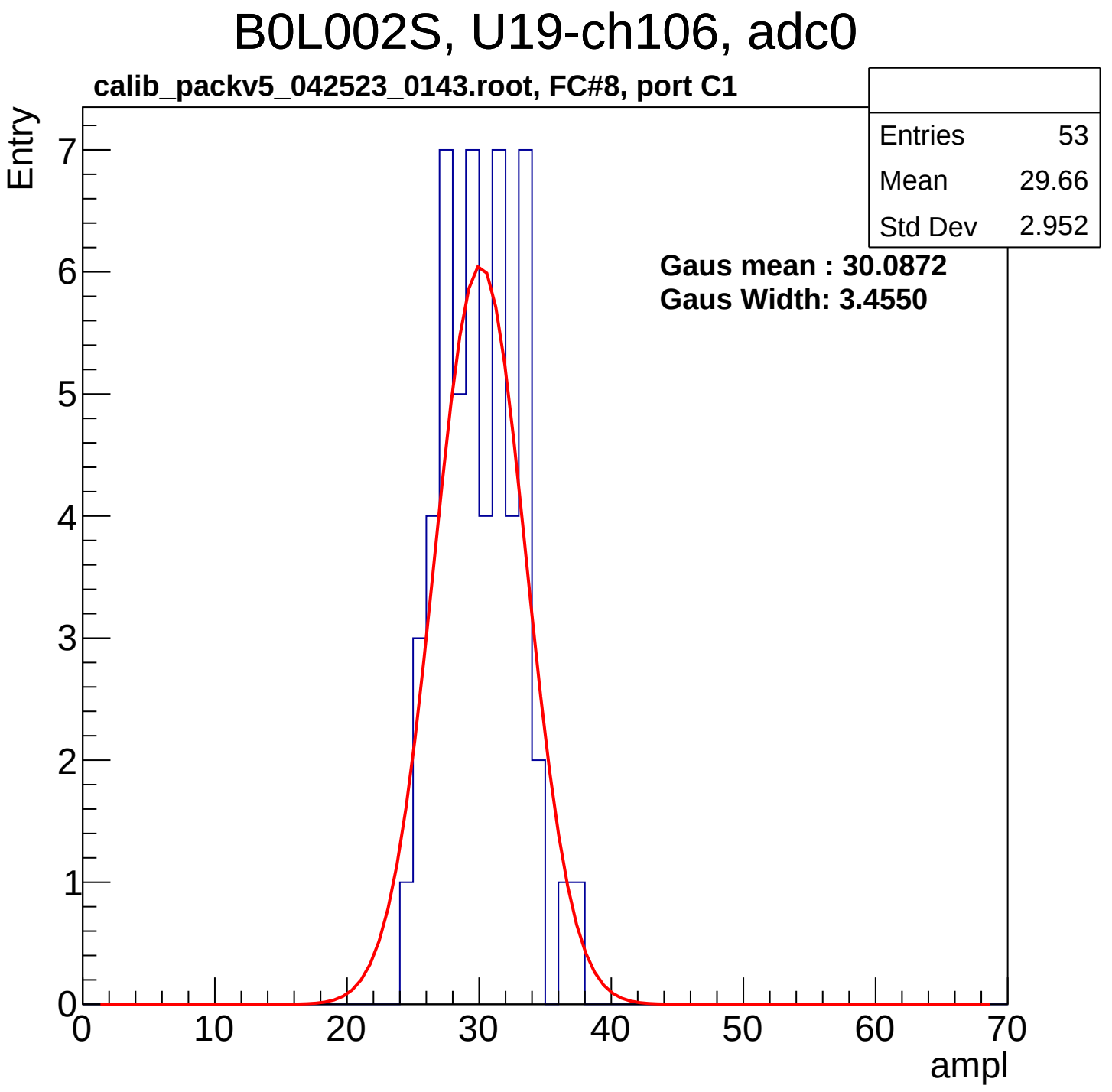

B0L002S, U19-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.01
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

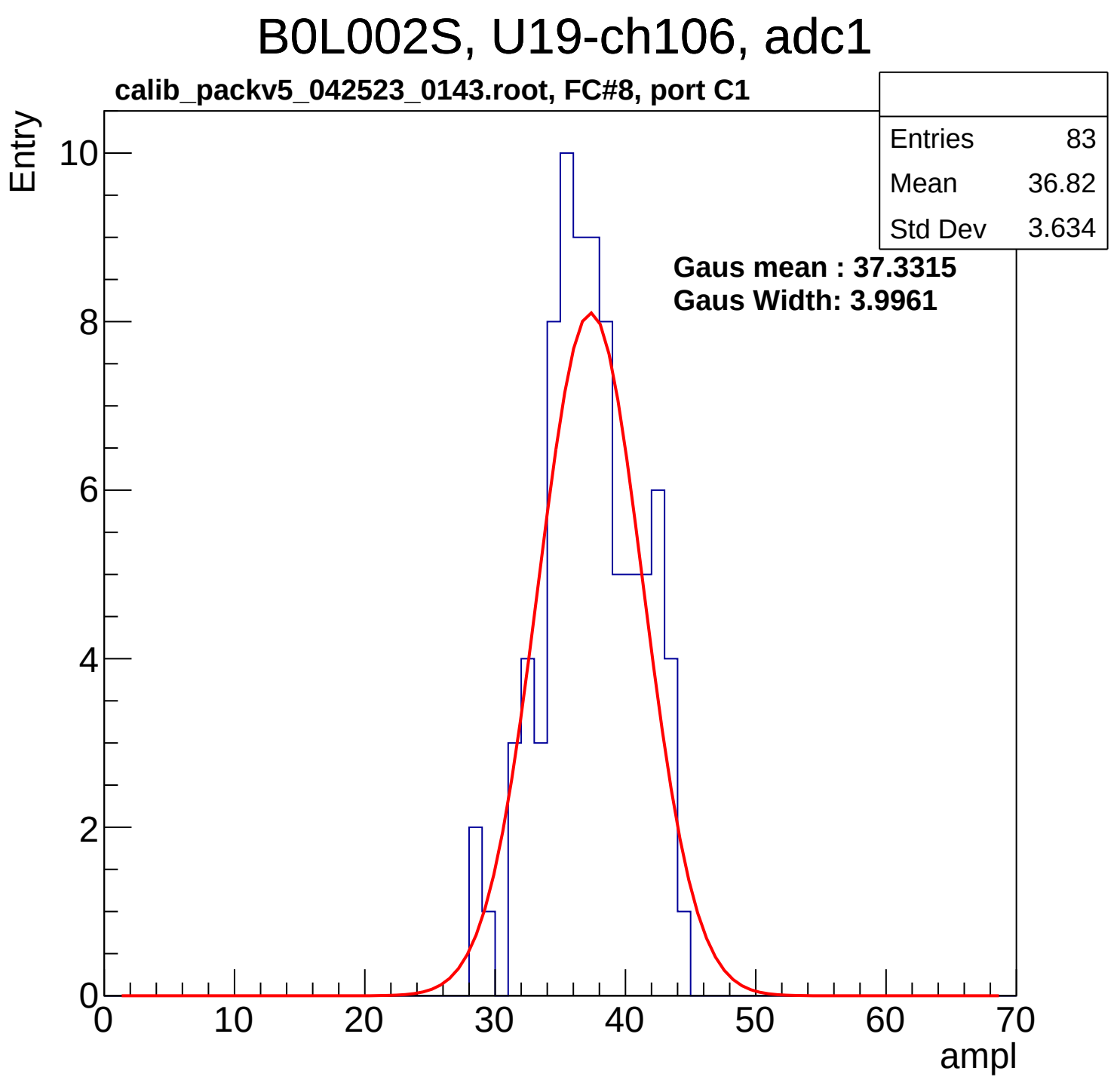

B0L002S, U19-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.79
Std Dev 7.837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

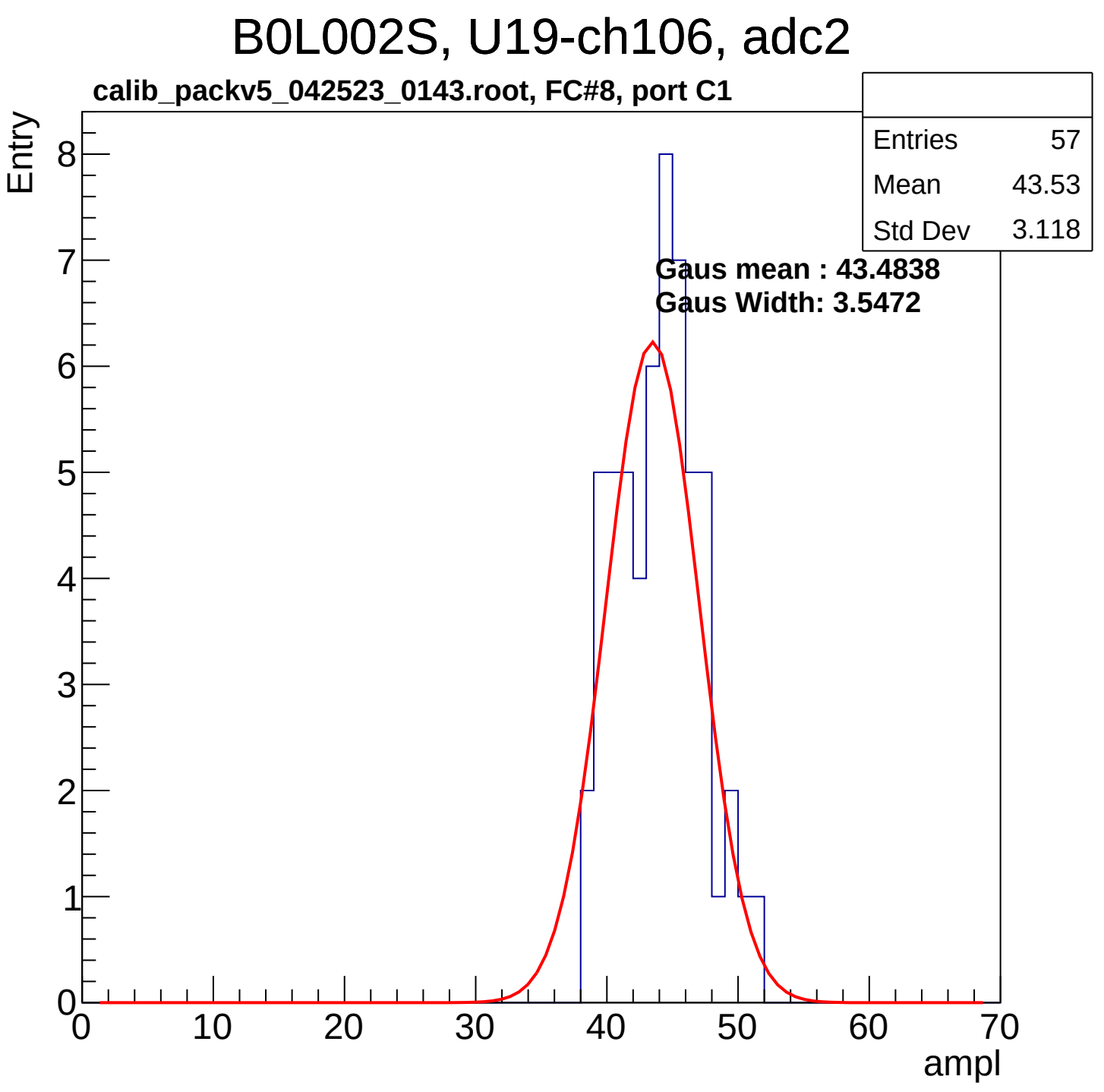

B0L002S, U19-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 60.96
Std Dev 1.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

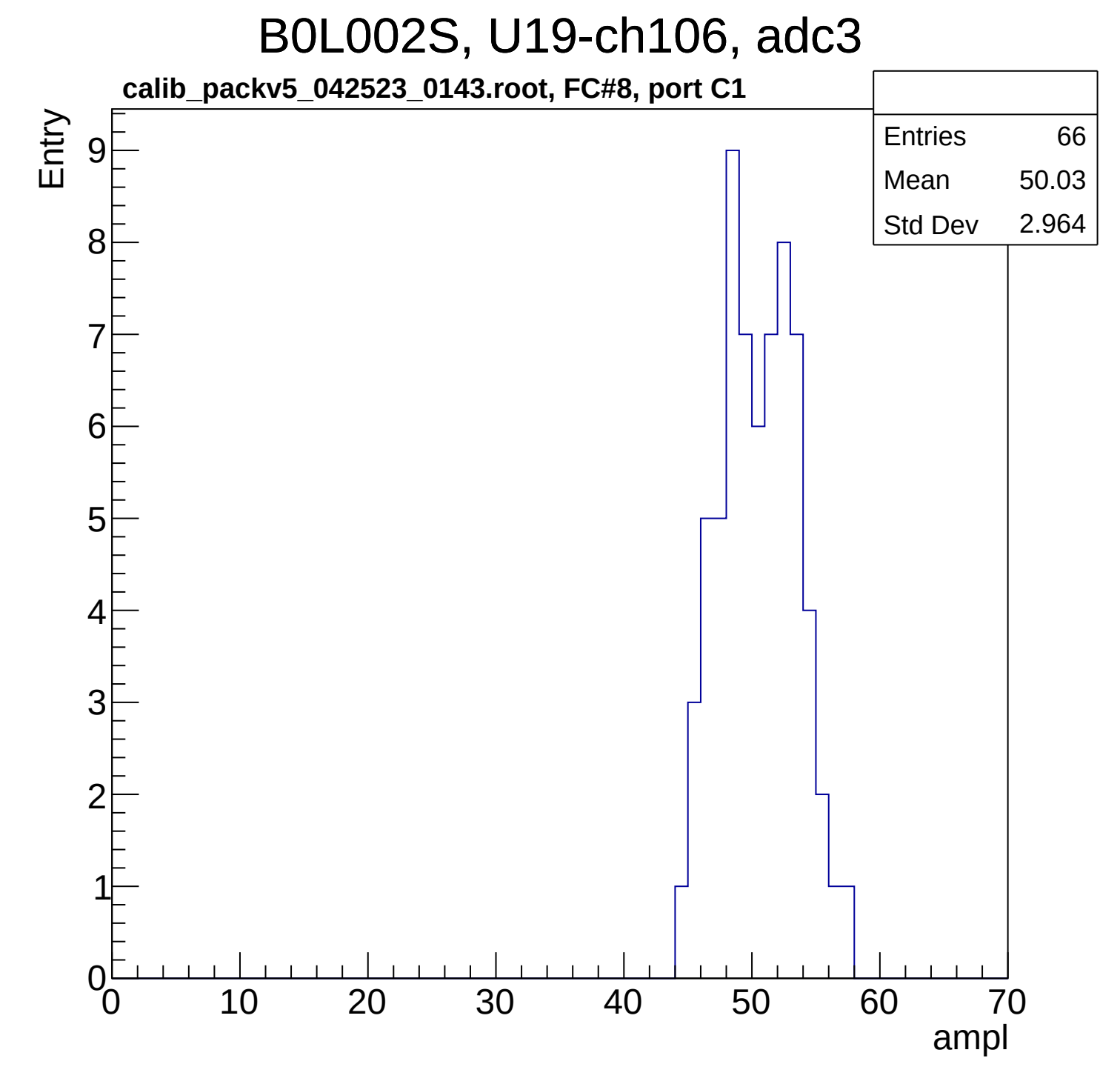

B0L002S, U19-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

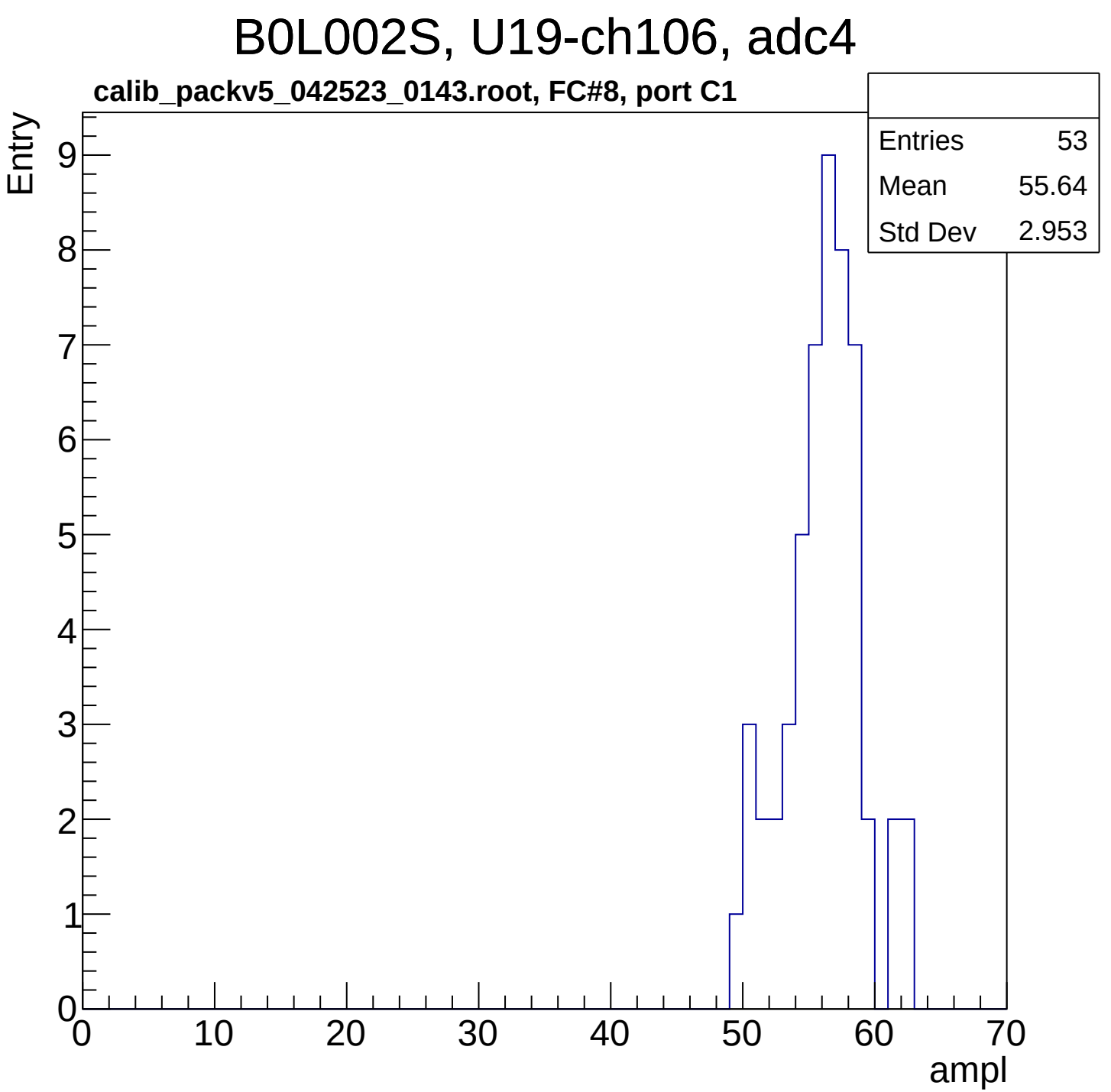

B0L002S, U19-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

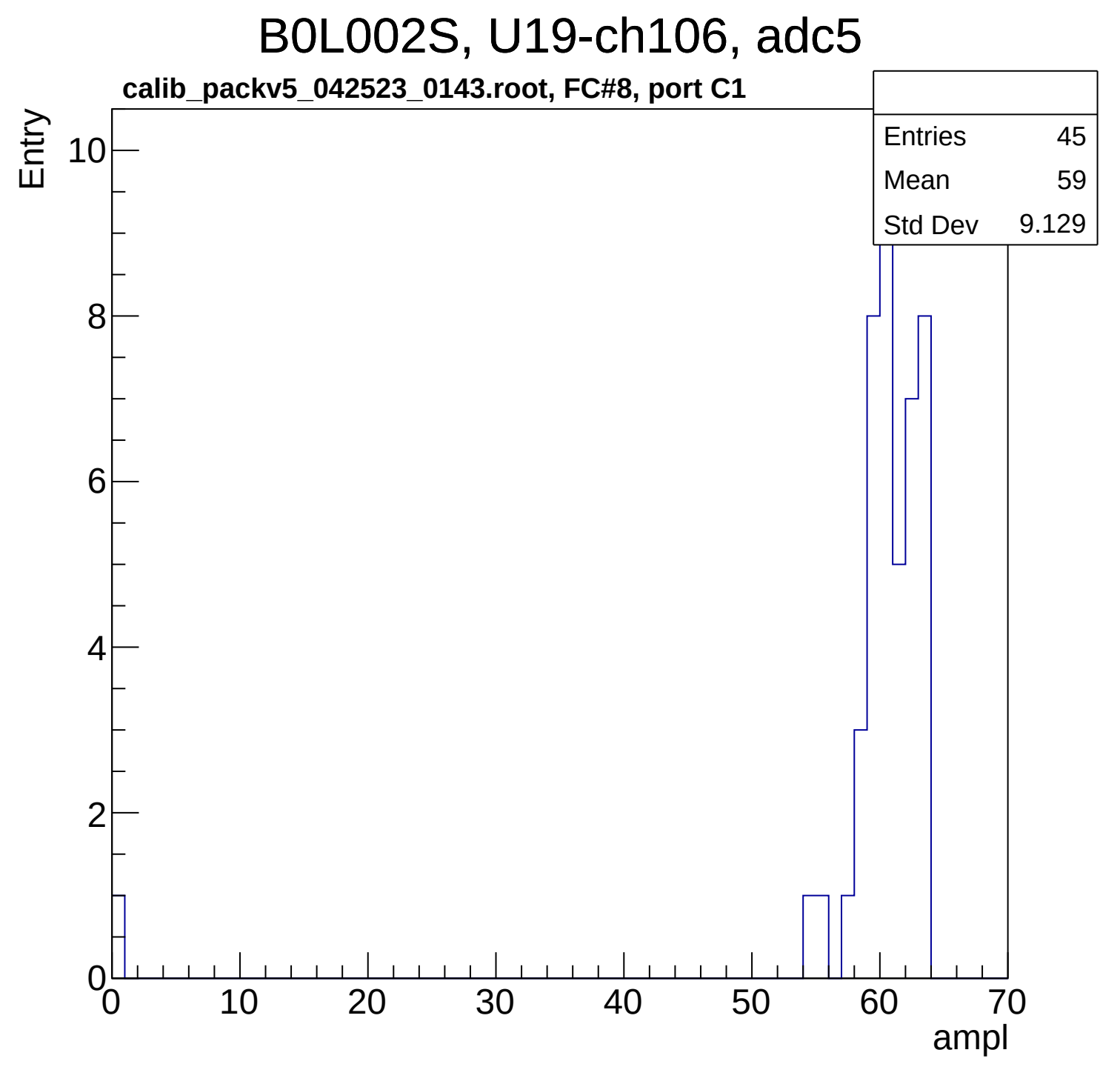

B0L002S, U19-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 29.66
Std Dev 2.952
Gaus mean : 30.0872
Gaus Width: 3.4550
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

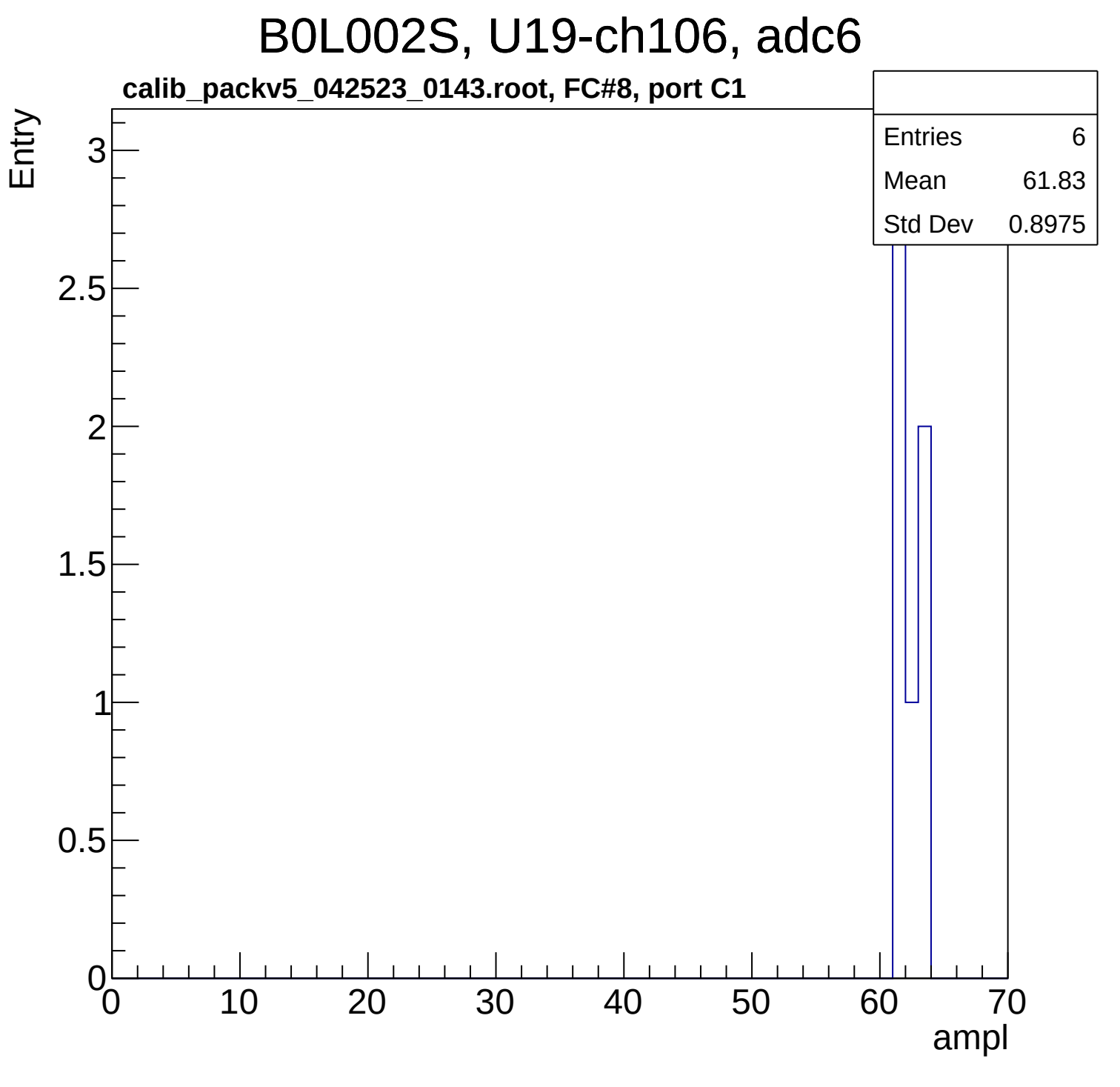

B0L002S, U19-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 36.82
Std Dev 3.634
Gaus mean : 37.3315
Gaus Width: 3.9961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

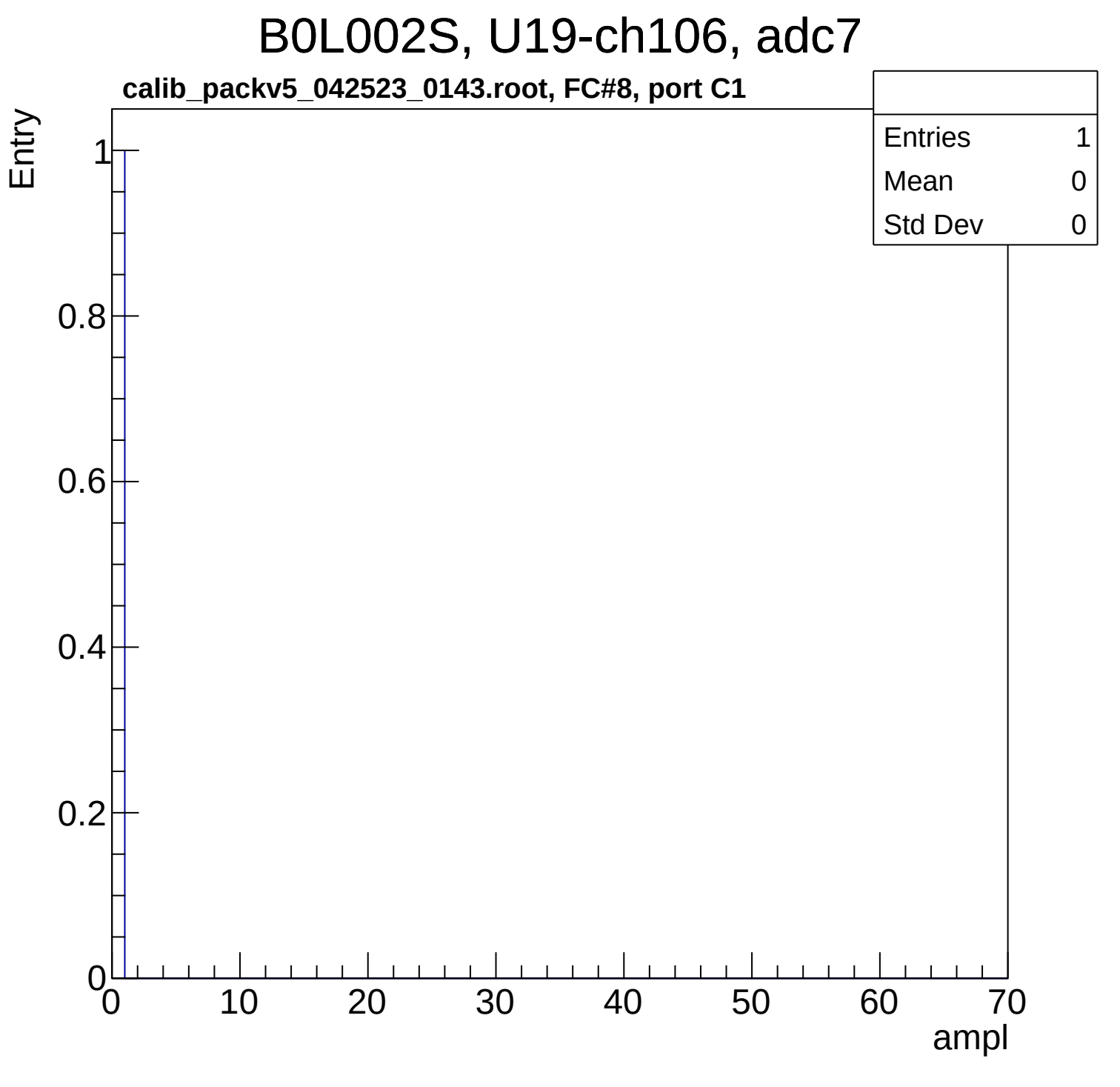

B0L002S, U19-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.53
Std Dev 3.118
Gaus mean : 43.4838
Gaus Width: 3.5472
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

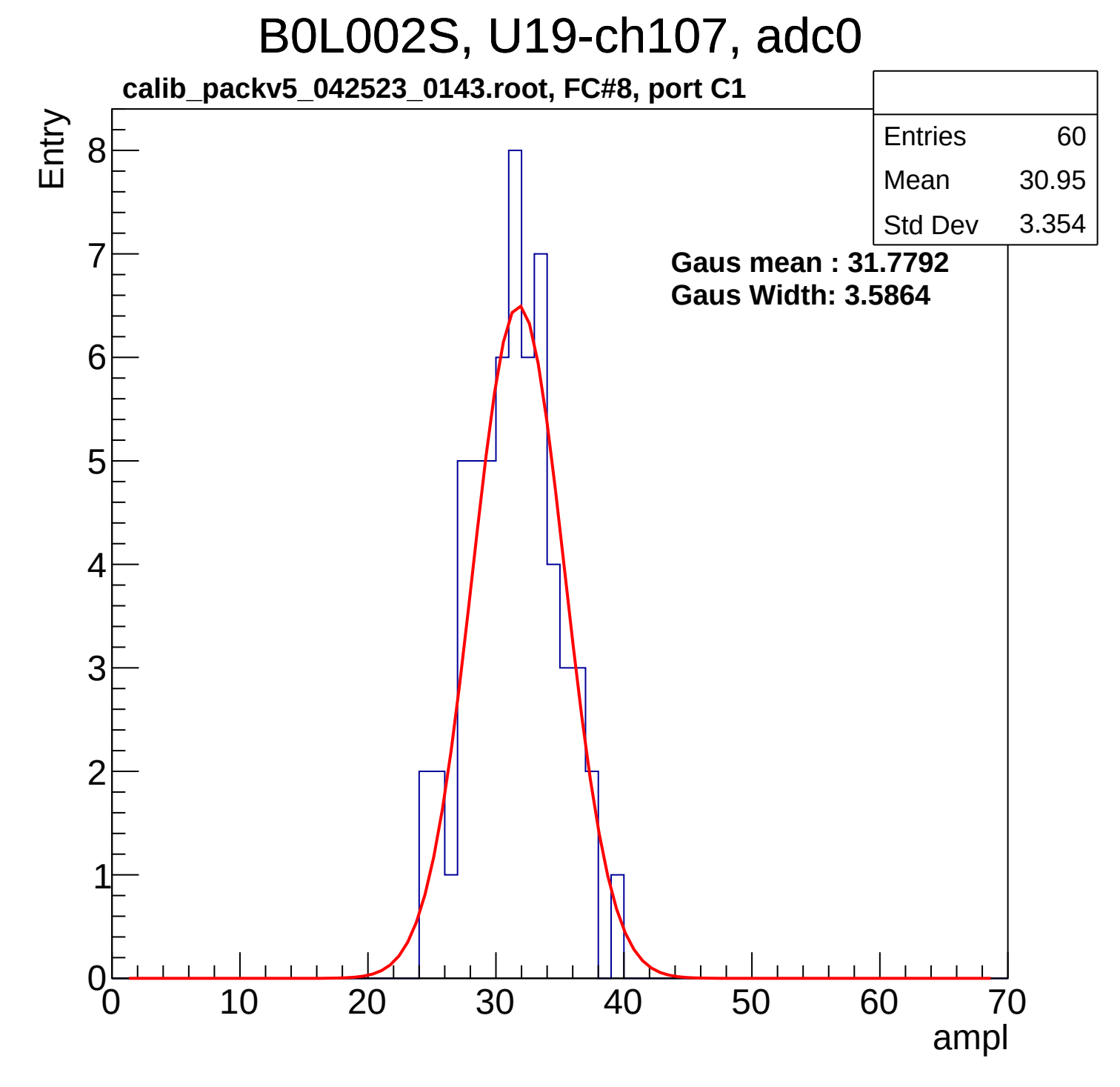

B0L002S, U19-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.03
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

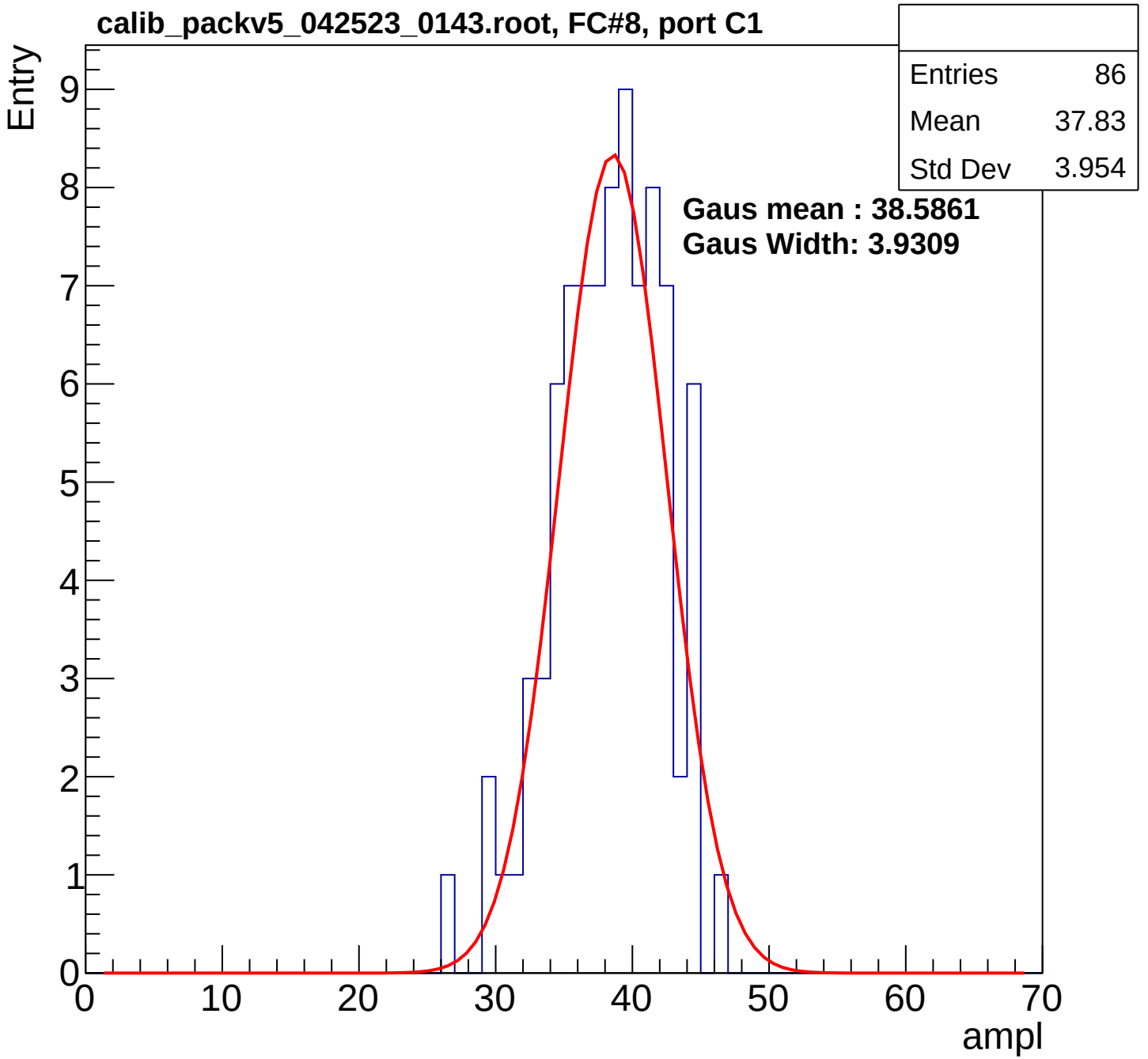

B0L002S, U19-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 55.64
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

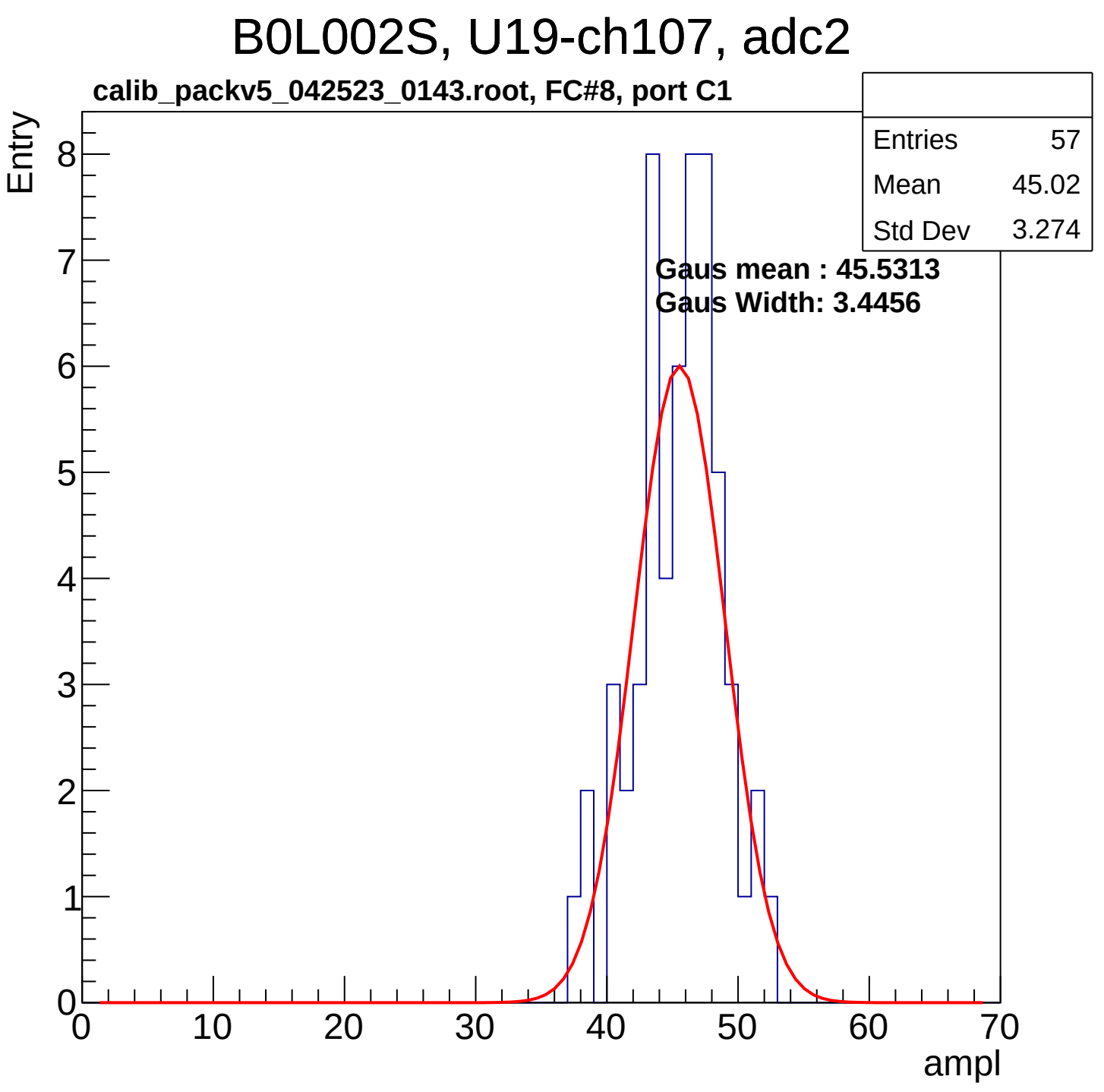

B0L002S, U19-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59
Std Dev 9.129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

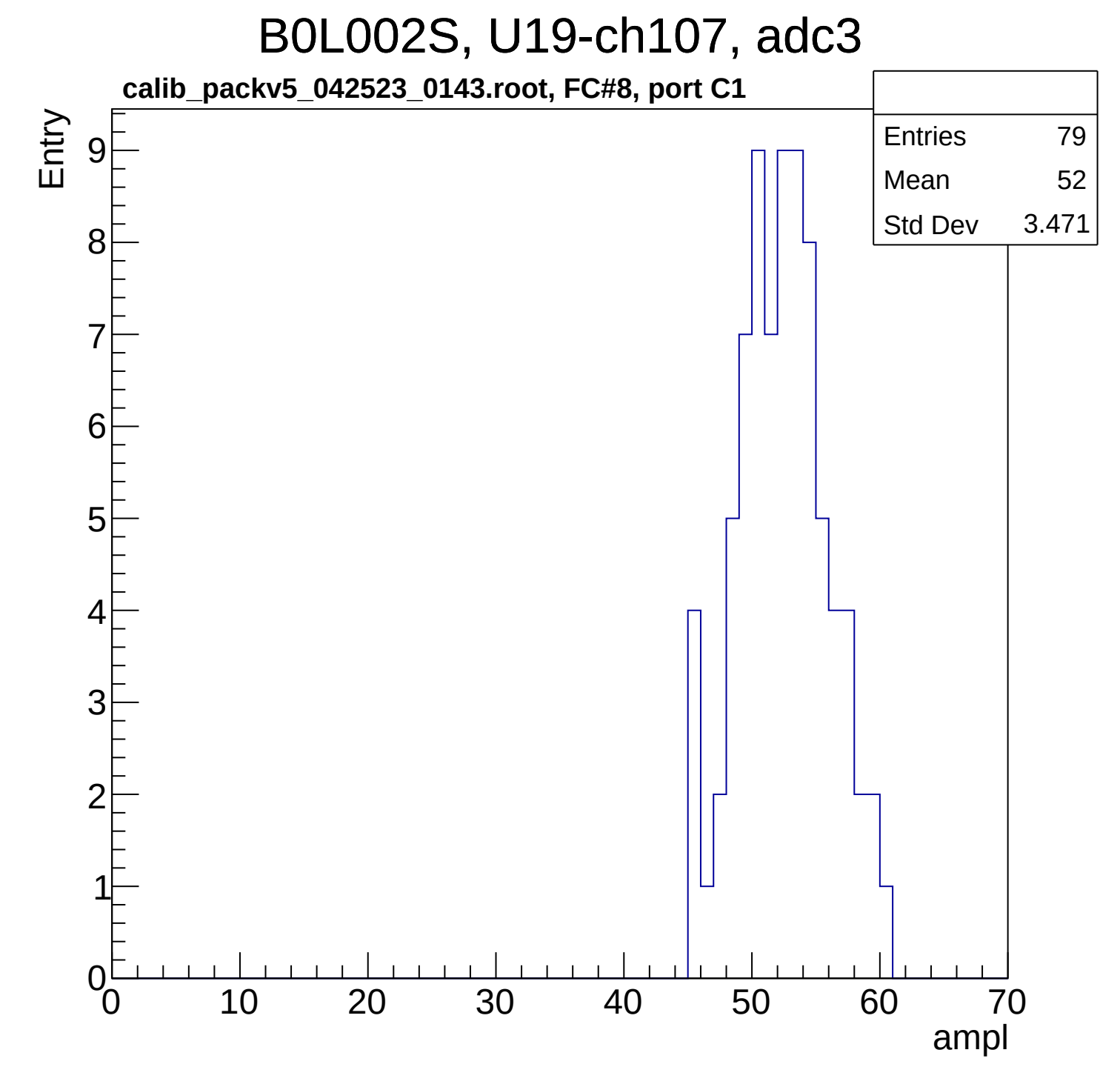

B0L002S, U19-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.83
Std Dev 0.8975
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

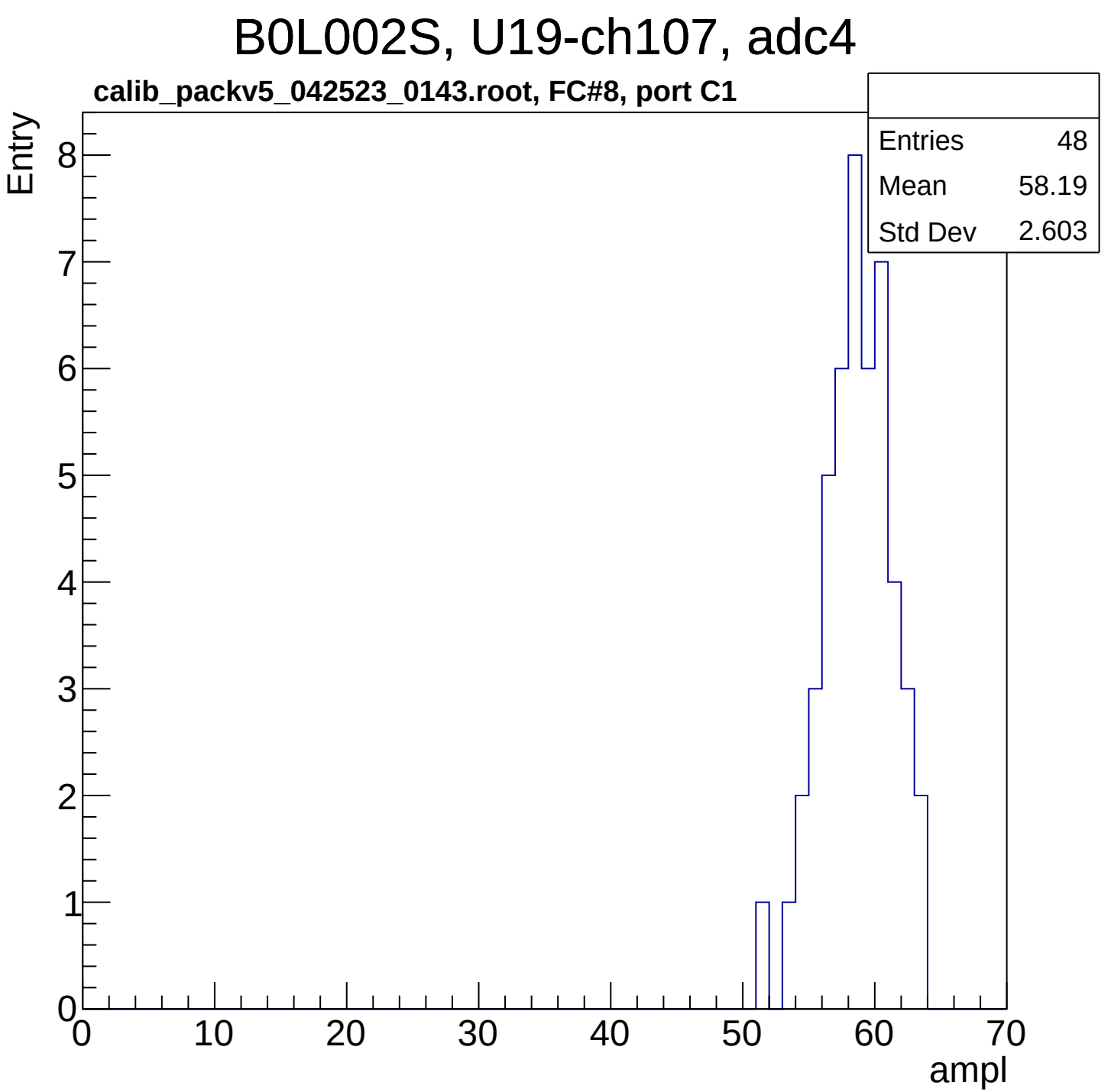

B0L002S, U19-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

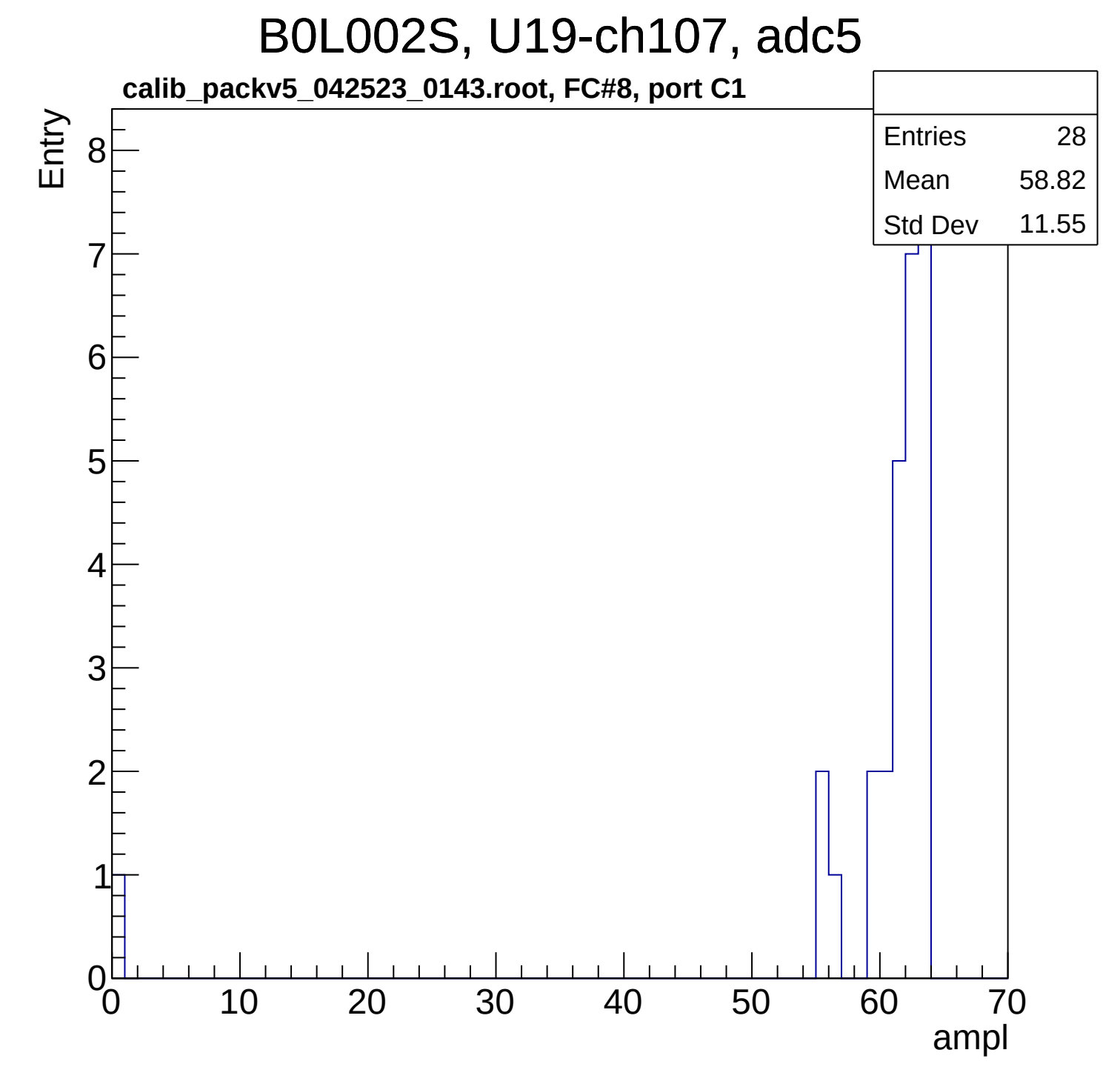

B0L002S, U19-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 30.95
Std Dev 3.354
Gaus mean : 31.7792
Gaus Width: 3.5864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

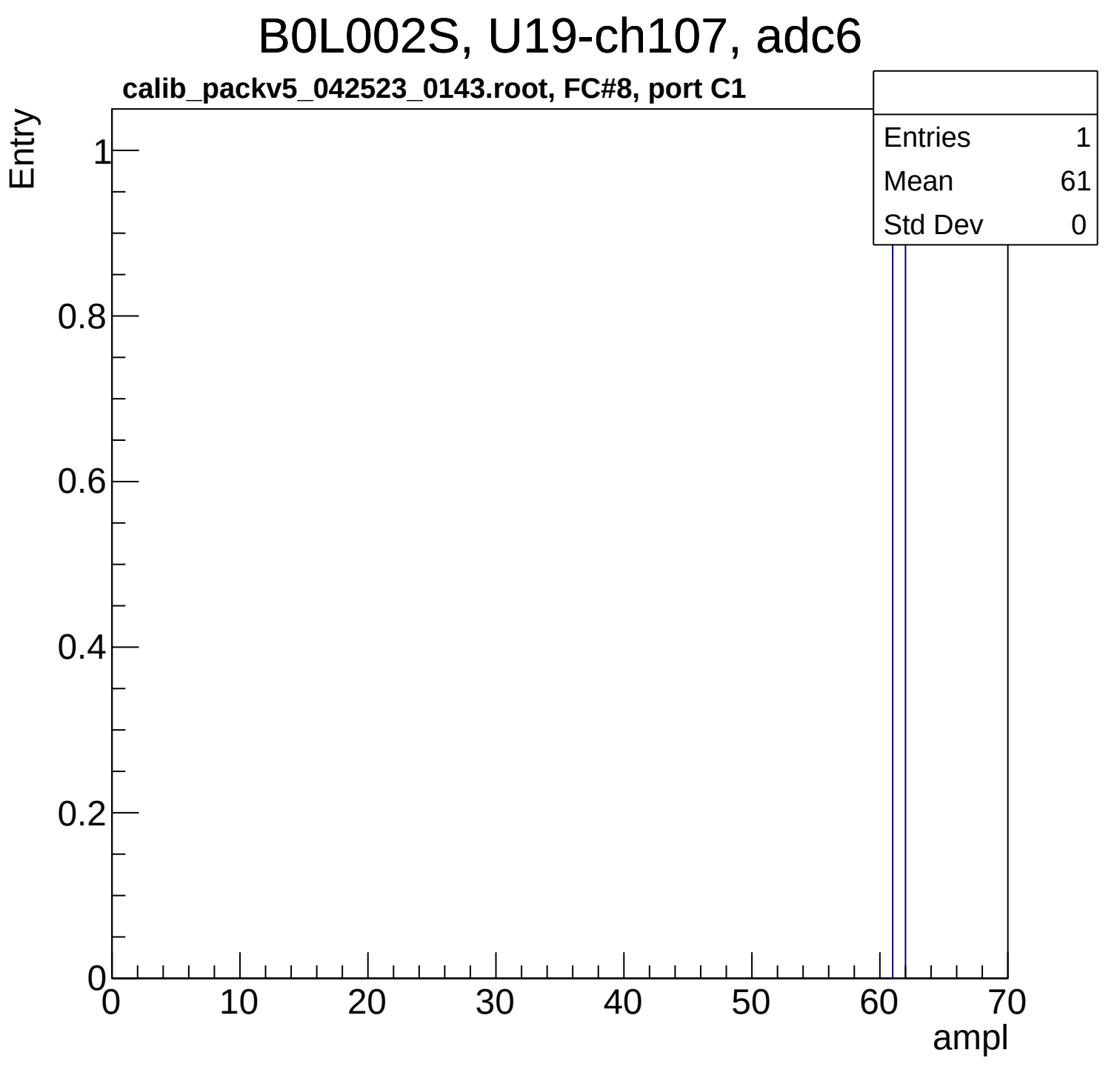

B0L002S, U19-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 37.83
Std Dev 3.954
Gaus mean : 38.5861
Gaus Width: 3.9309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

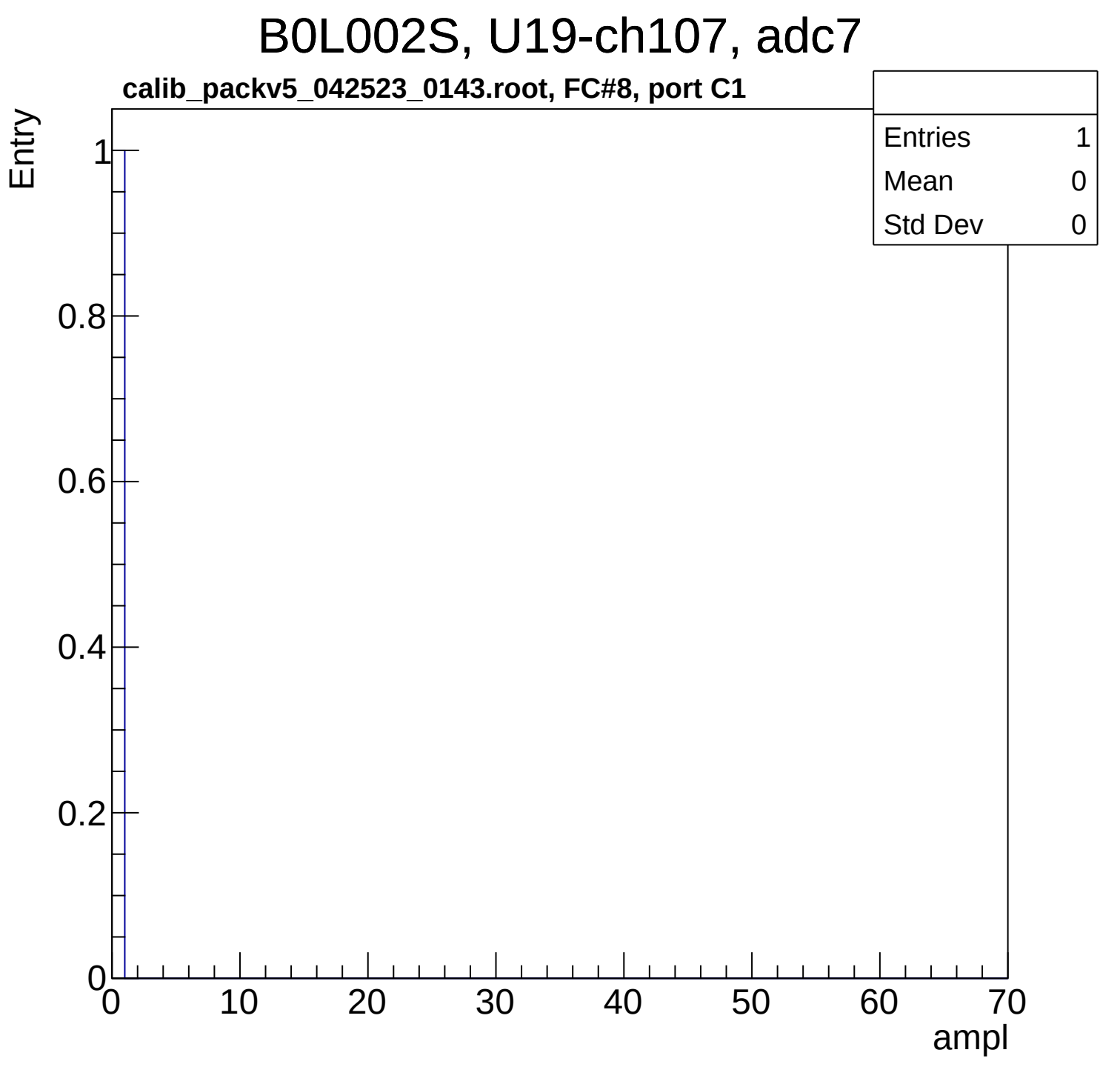

B0L002S, U19-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 45.02
Std Dev 3.274
Gaus mean : 45.5313
Gaus Width: 3.4456
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

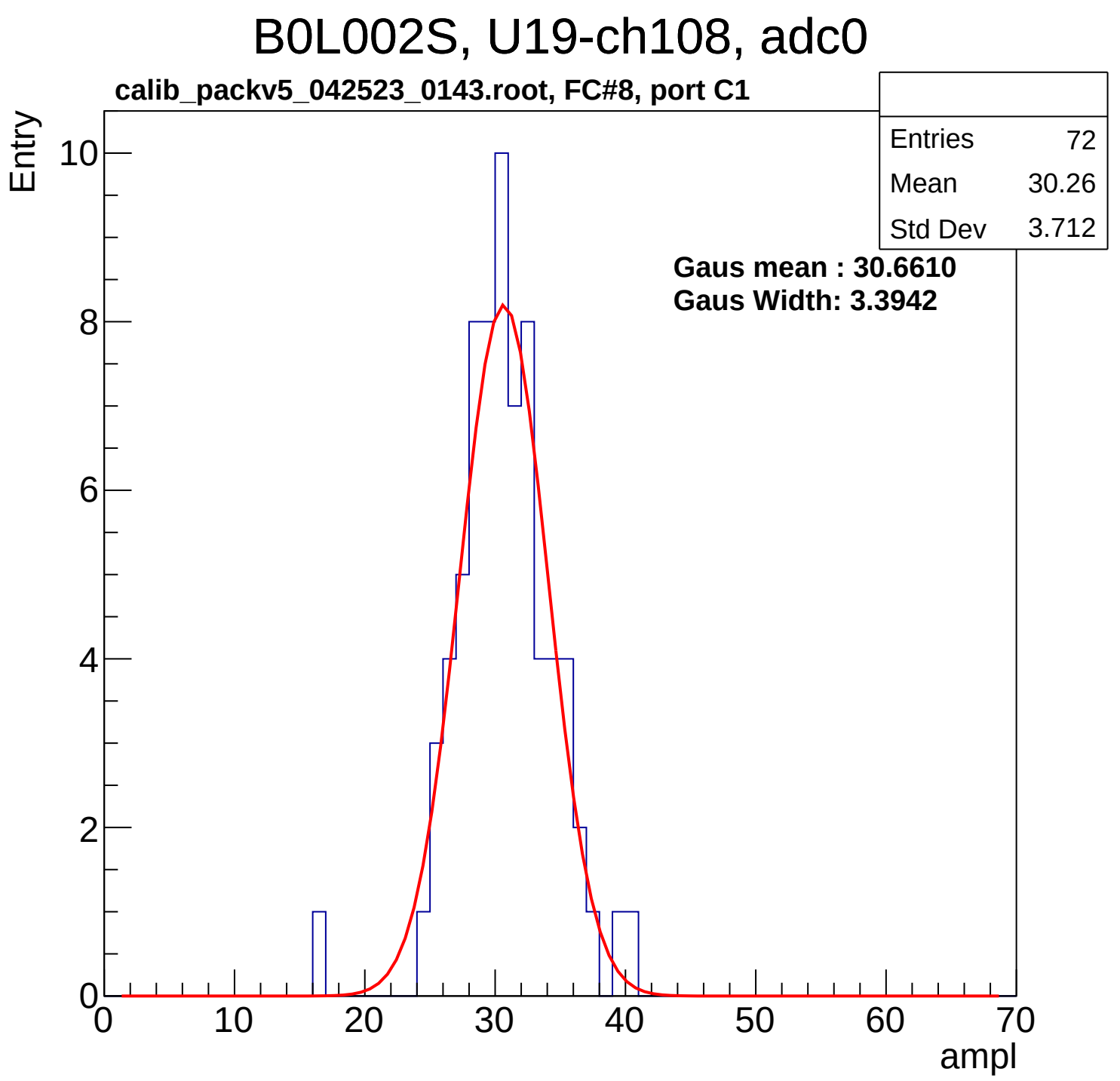

B0L002S, U19-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 52
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

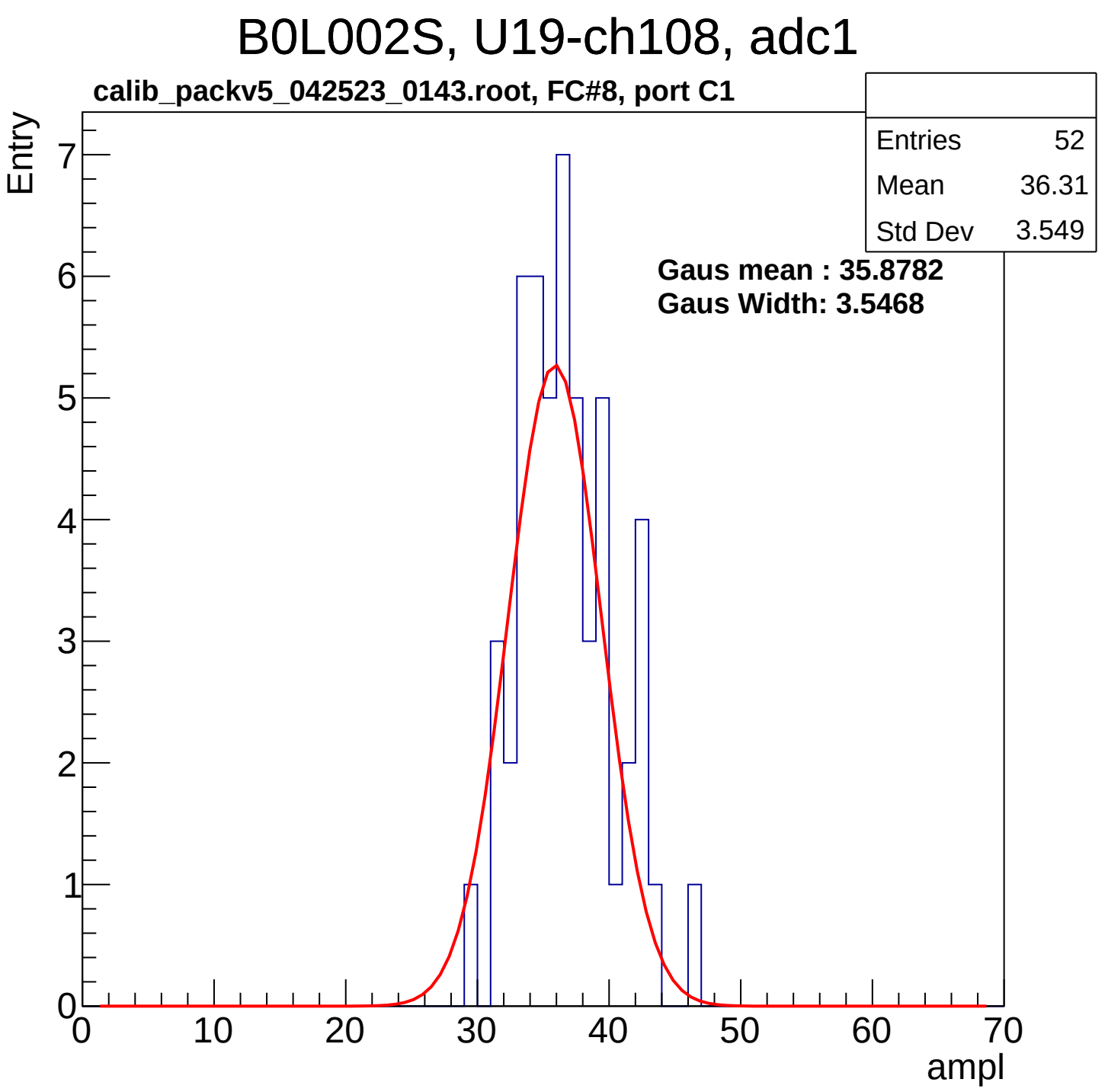

B0L002S, U19-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 58.19
Std Dev 2.603
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

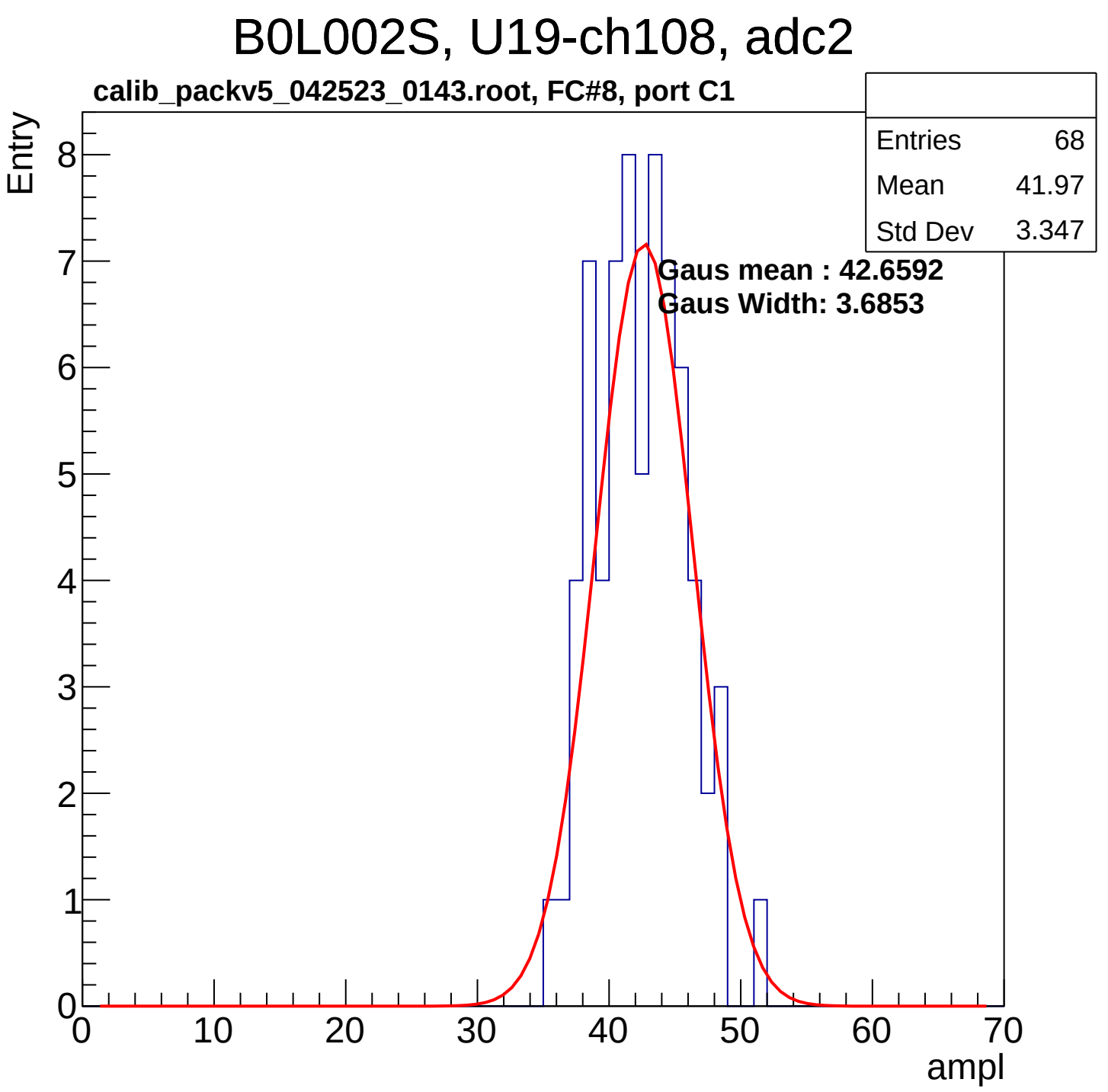

B0L002S, U19-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.82
Std Dev 11.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

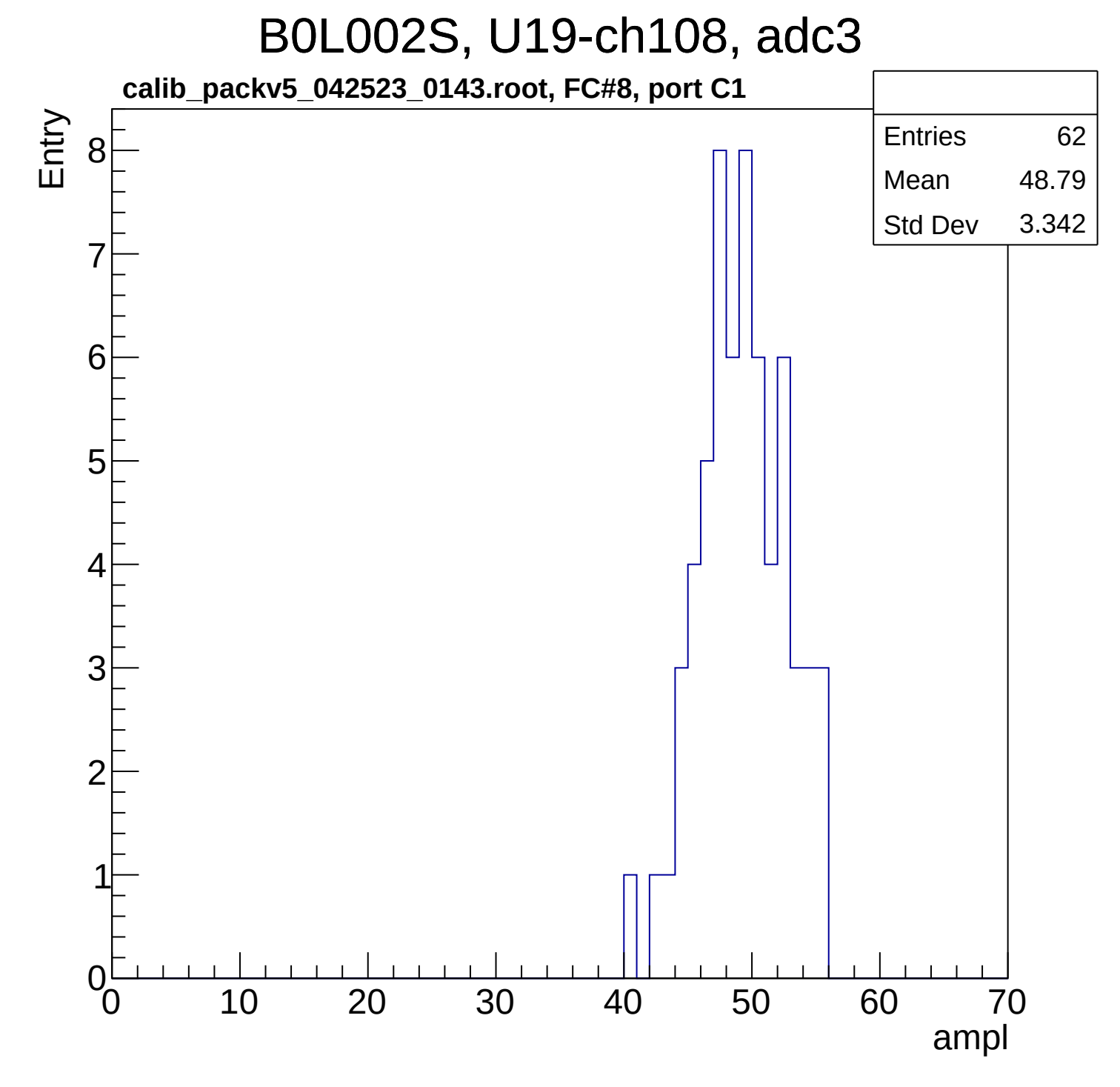

B0L002S, U19-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

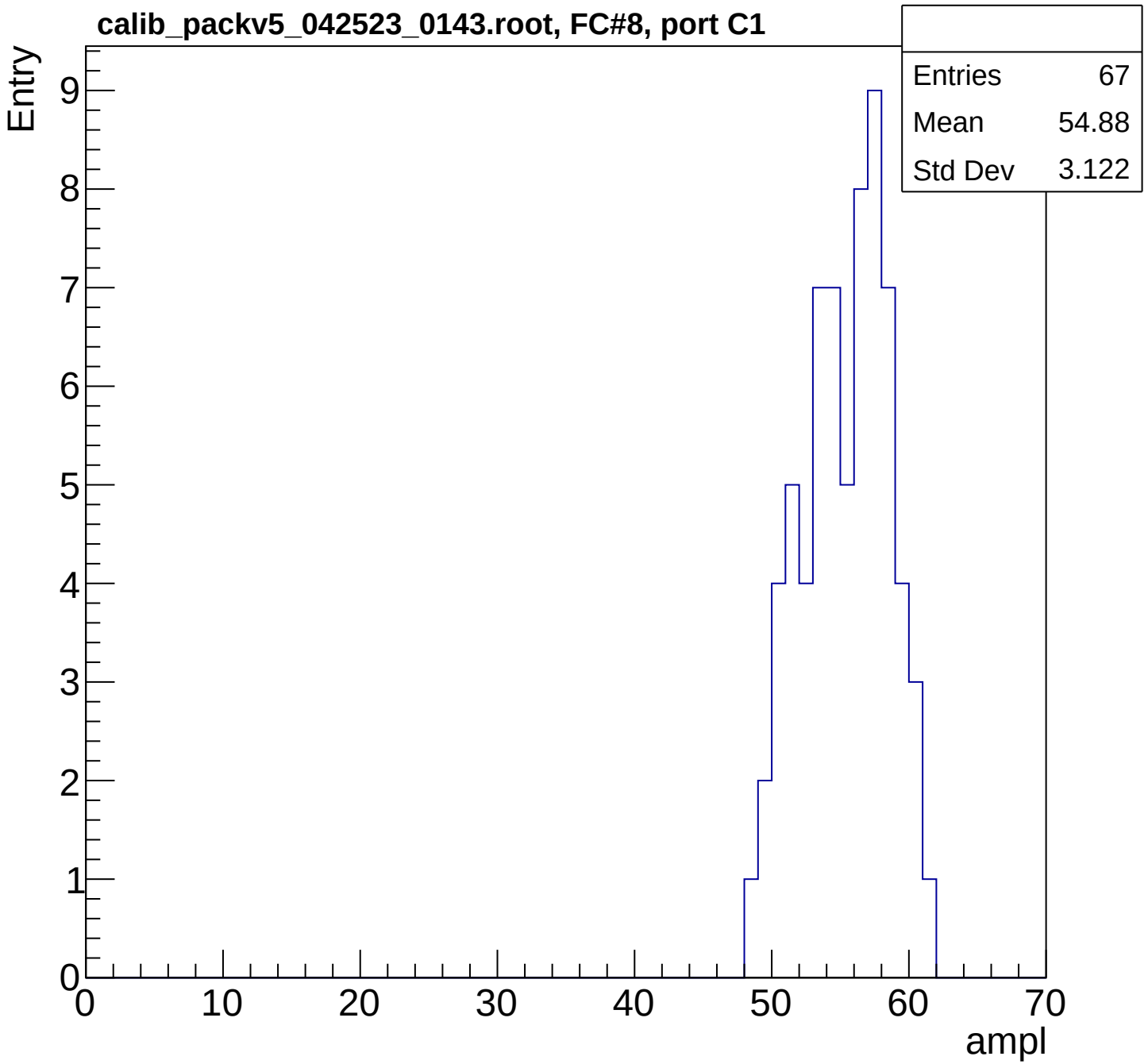

B0L002S, U19-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

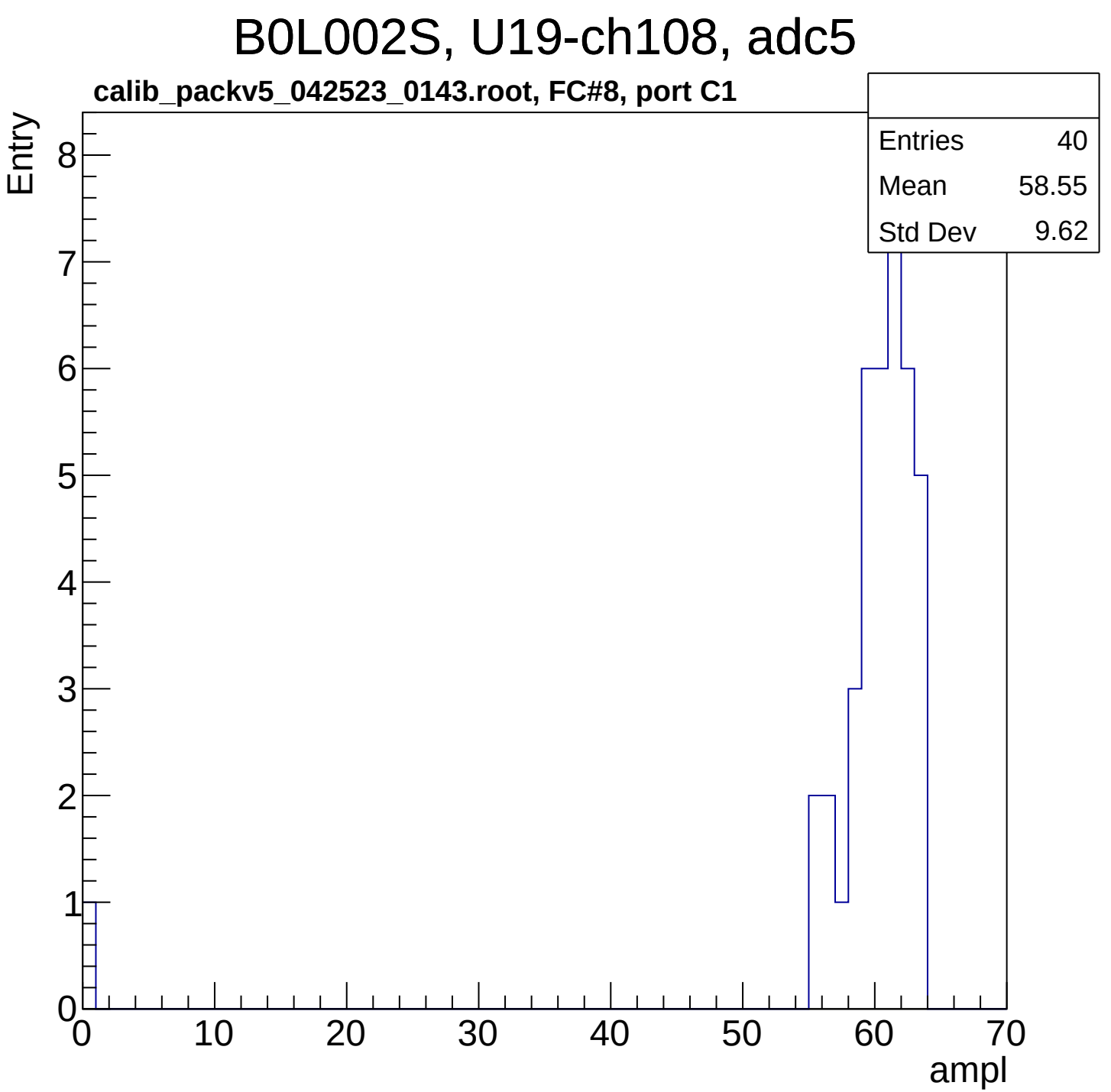

B0L002S, U19-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.26
Std Dev 3.712
Gaus mean : 30.6610
Gaus Width: 3.3942
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

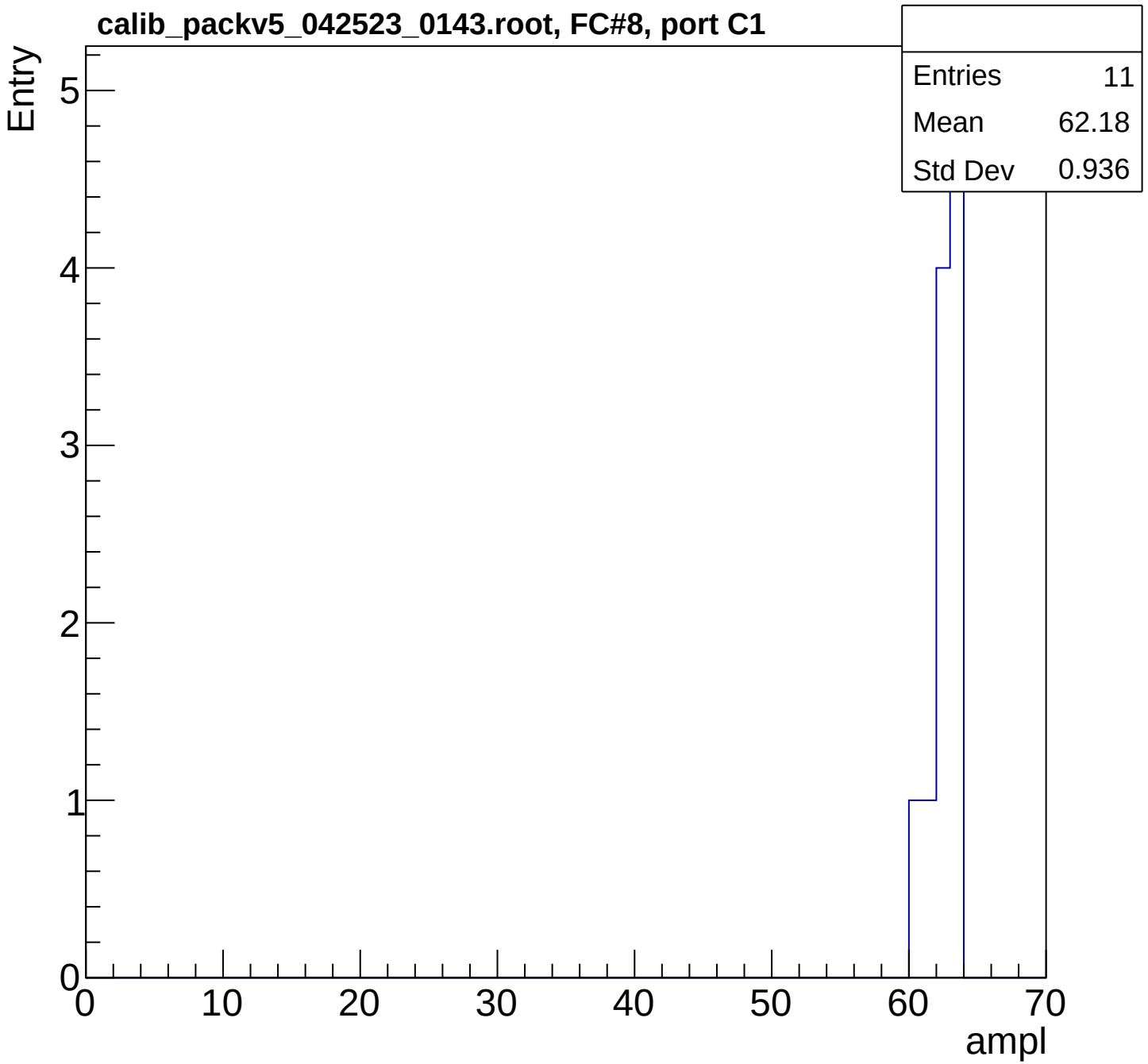

B0L002S, U19-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 36.31
Std Dev 3.549
Gaus mean : 35.8782
Gaus Width: 3.5468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

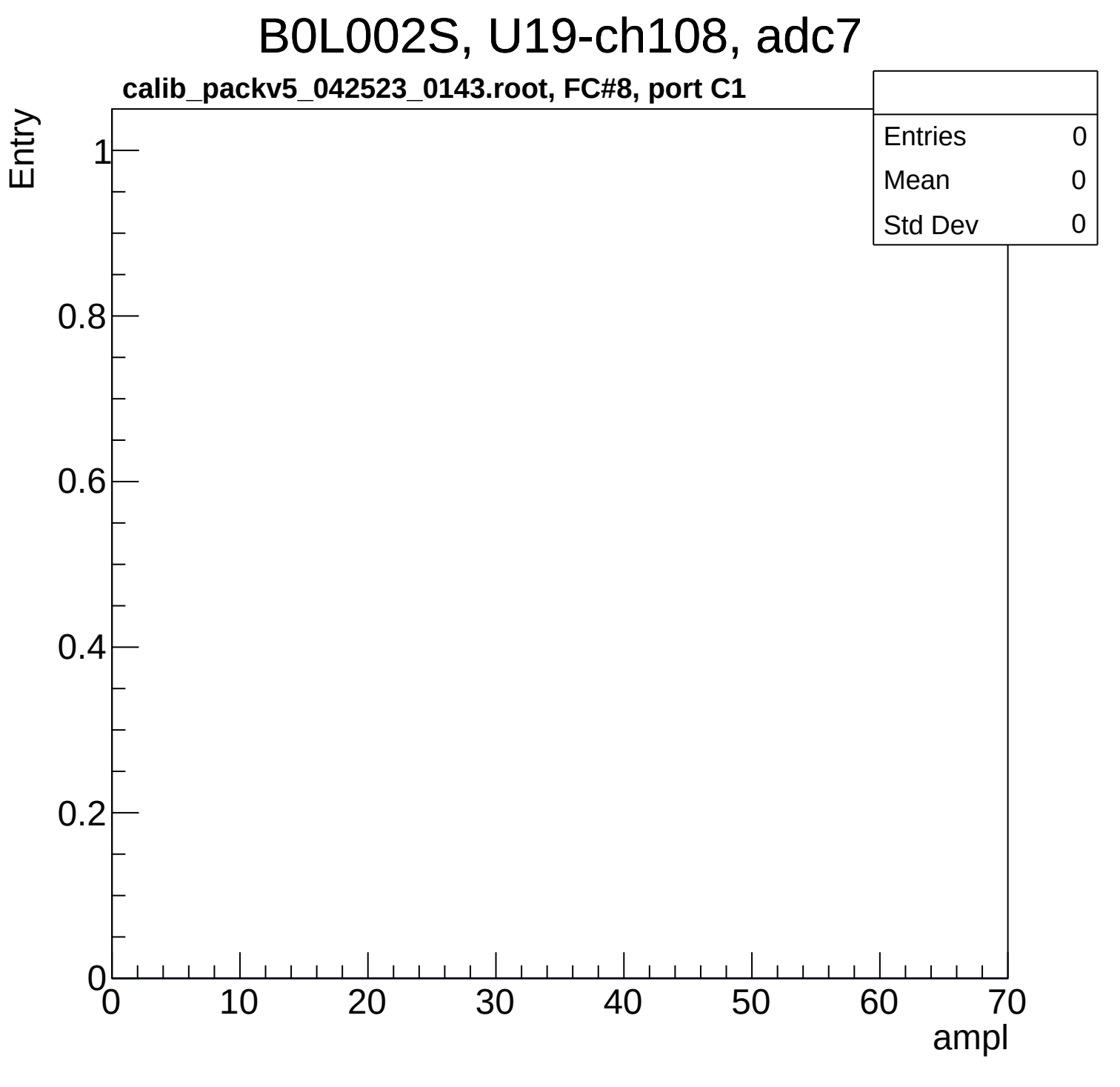

B0L002S, U19-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 41.97
Std Dev 3.347
Gaus mean : 42.6592
Gaus Width: 3.6853
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

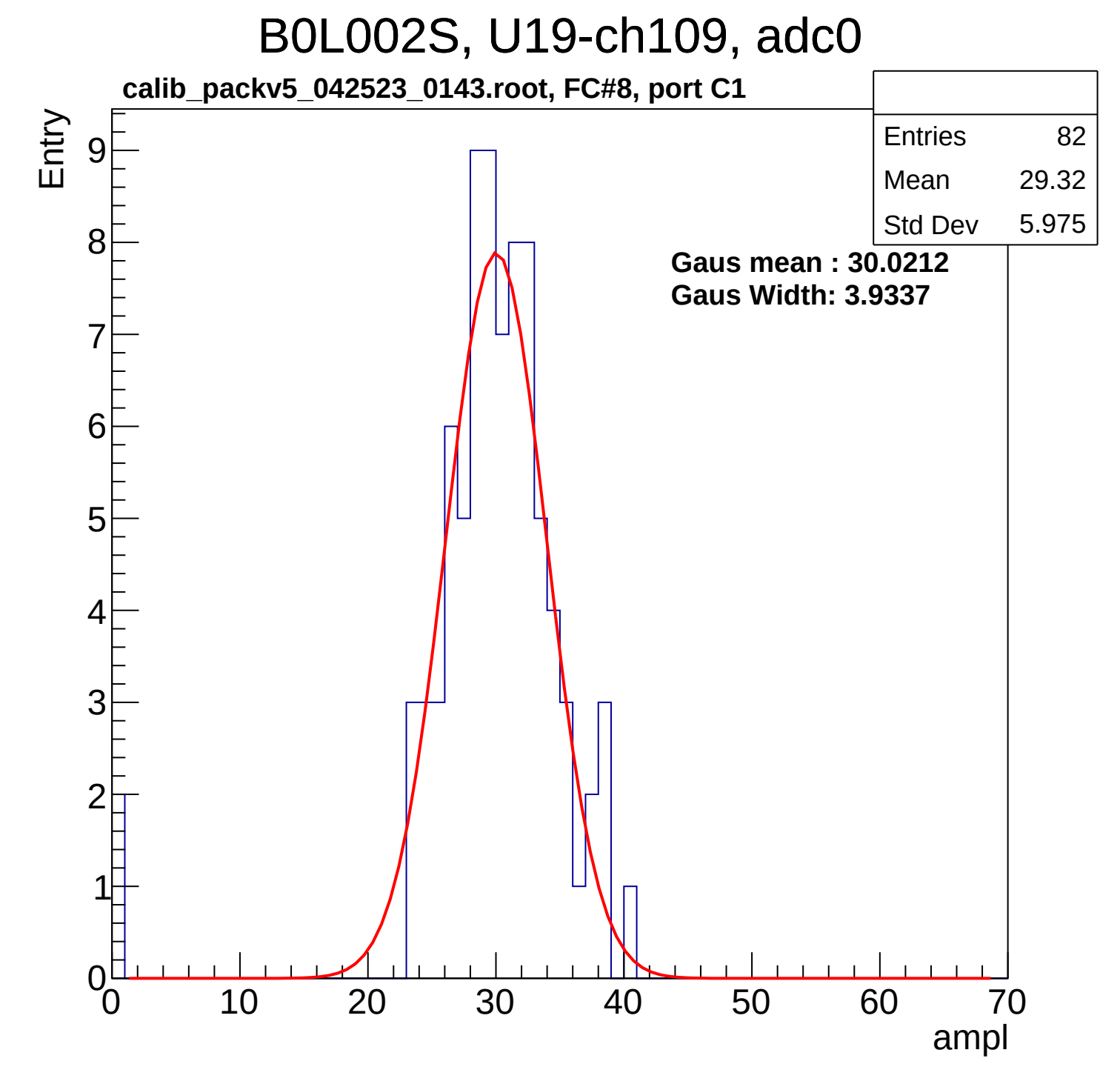

B0L002S, U19-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 48.79
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

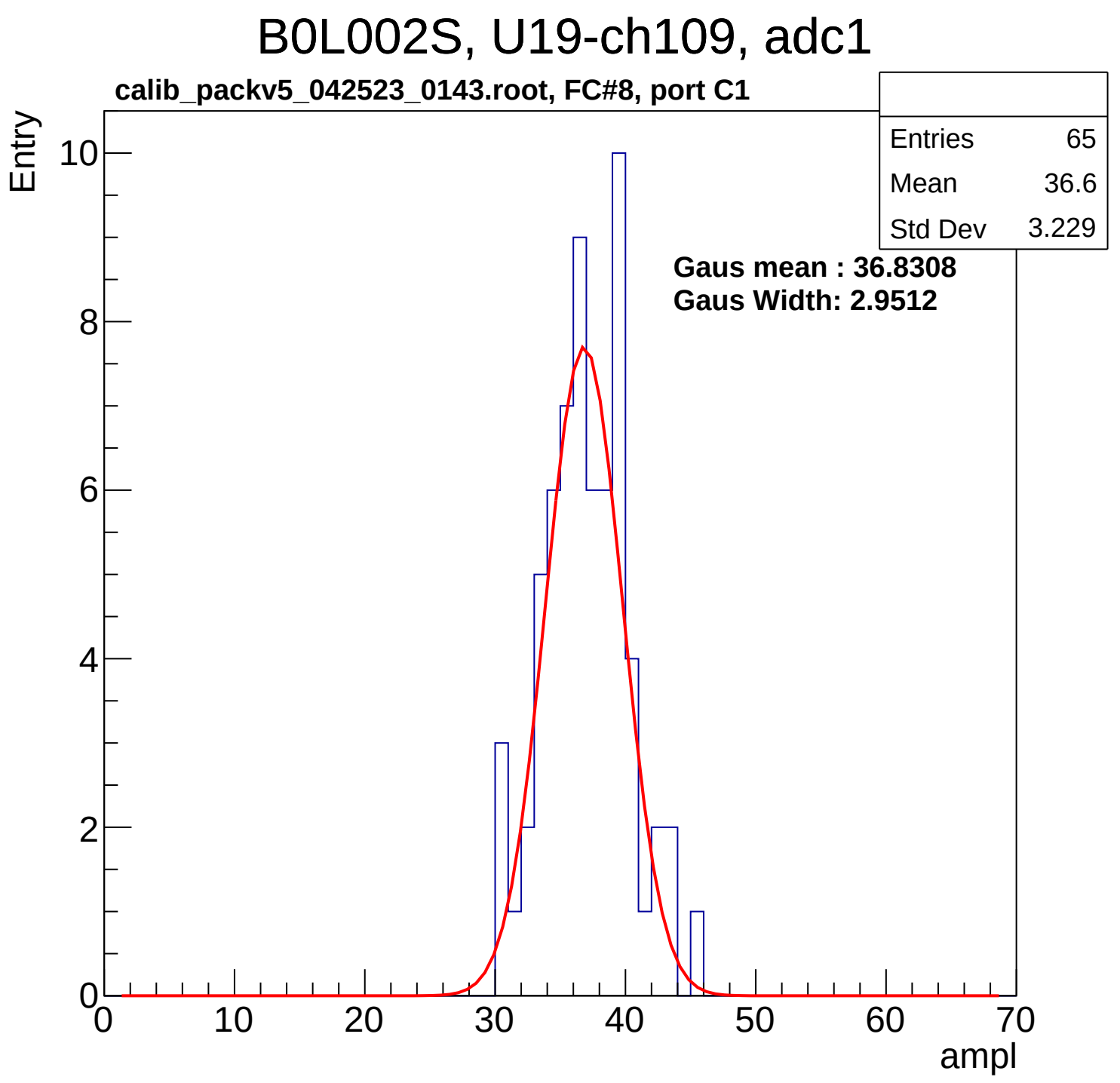

B0L002S, U19-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 54.88
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

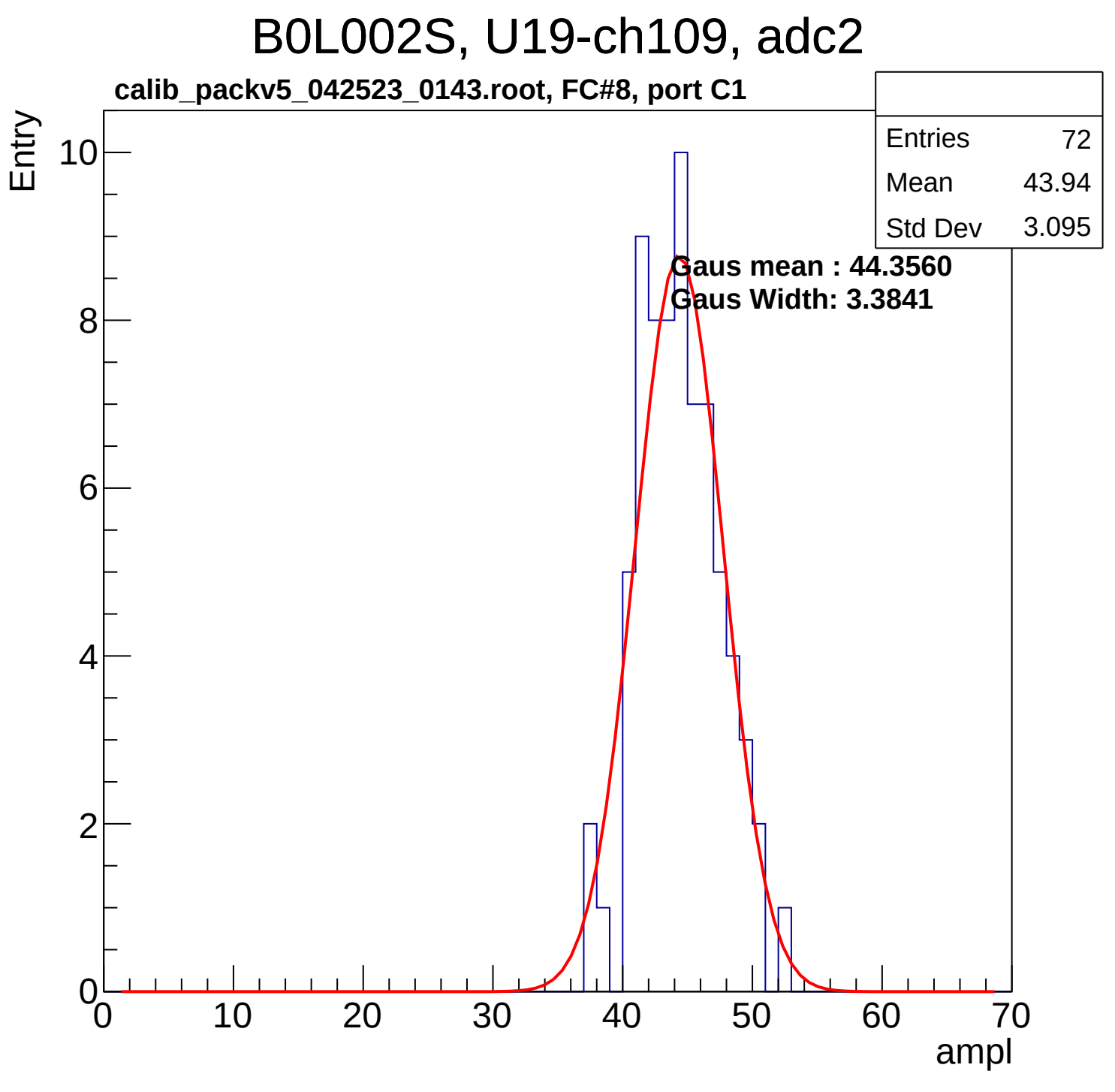

B0L002S, U19-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.55
Std Dev 9.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

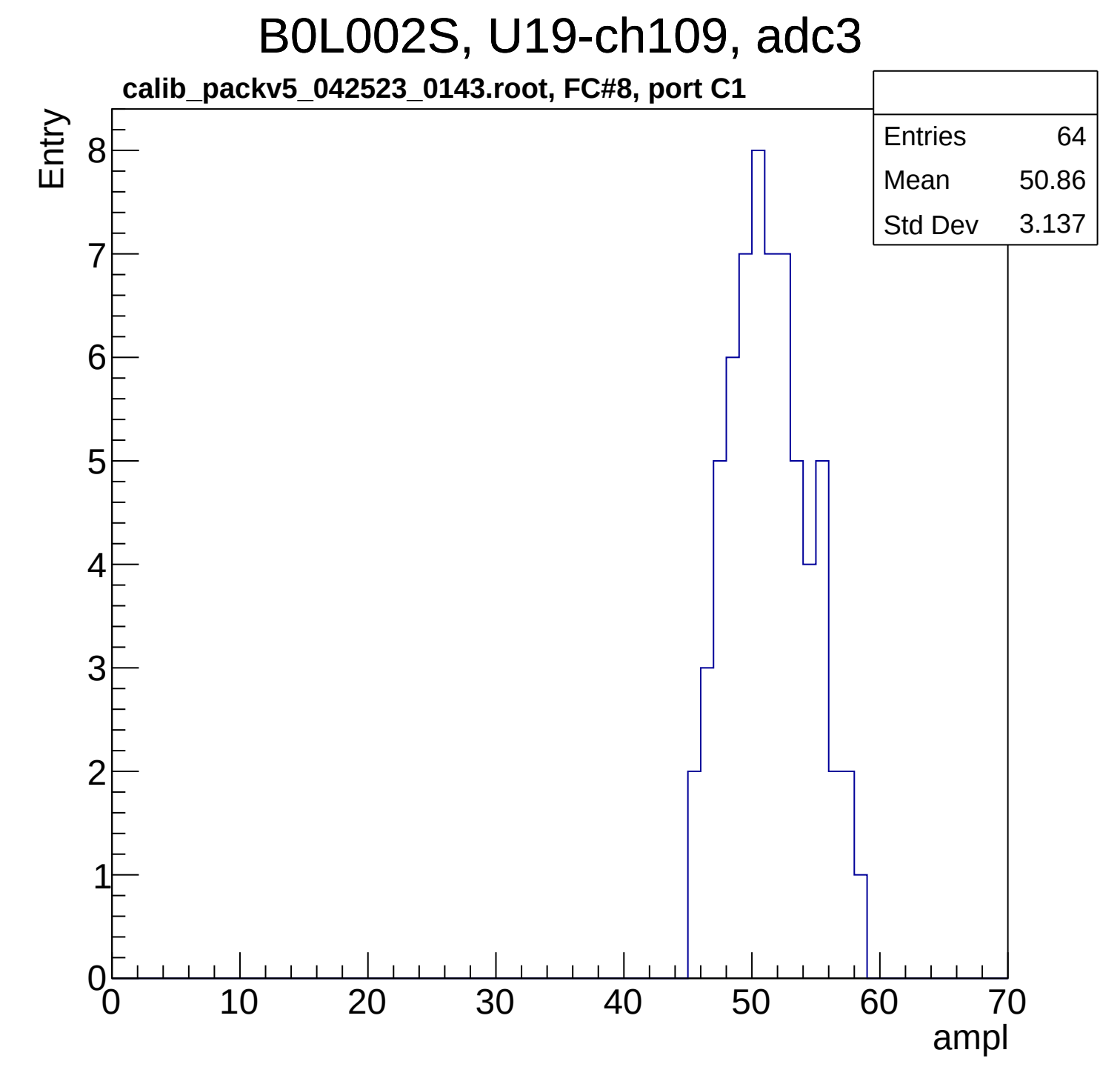

B0L002S, U19-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 11
Mean 62.18
Std Dev 0.936
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

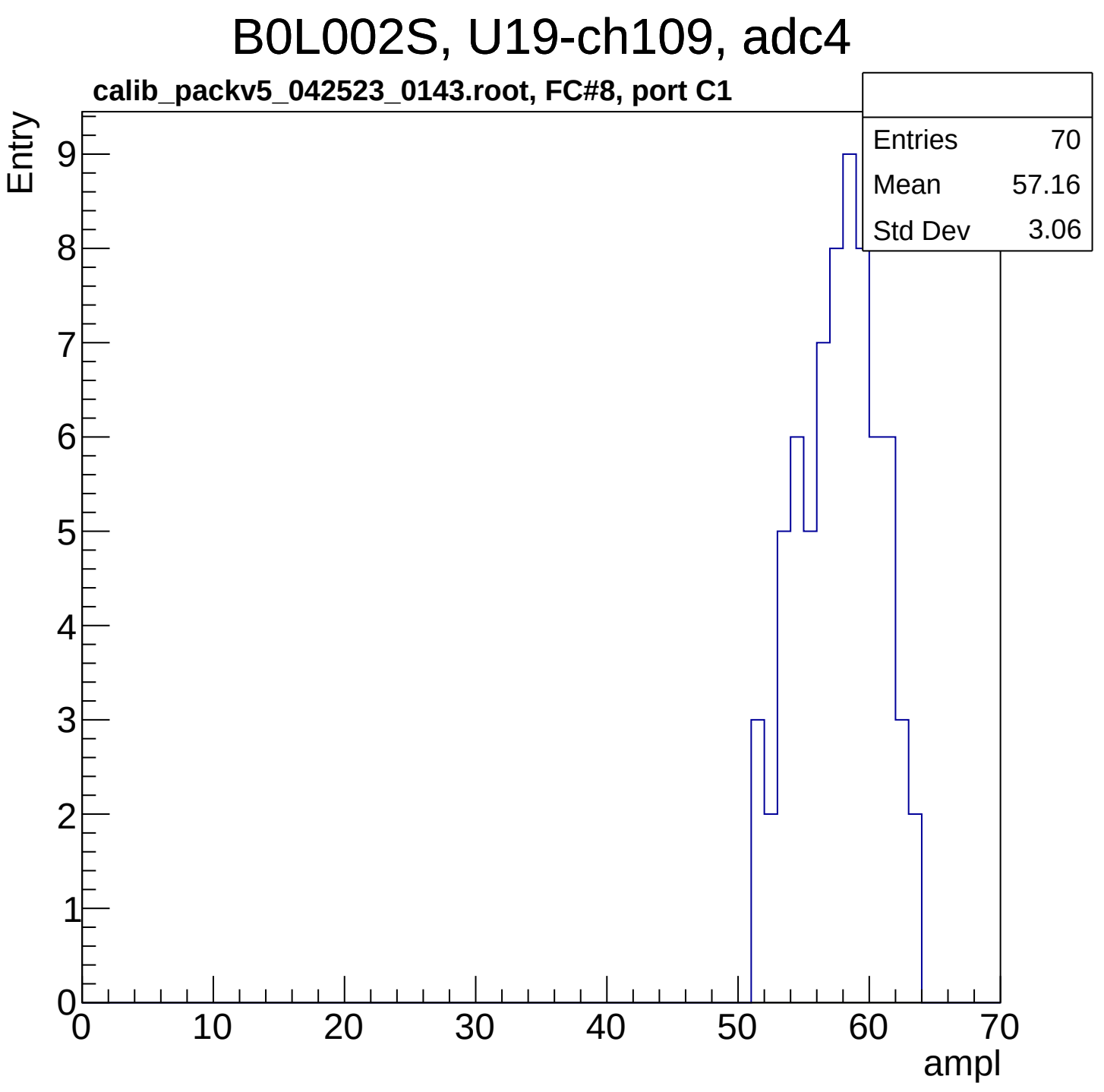

B0L002S, U19-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

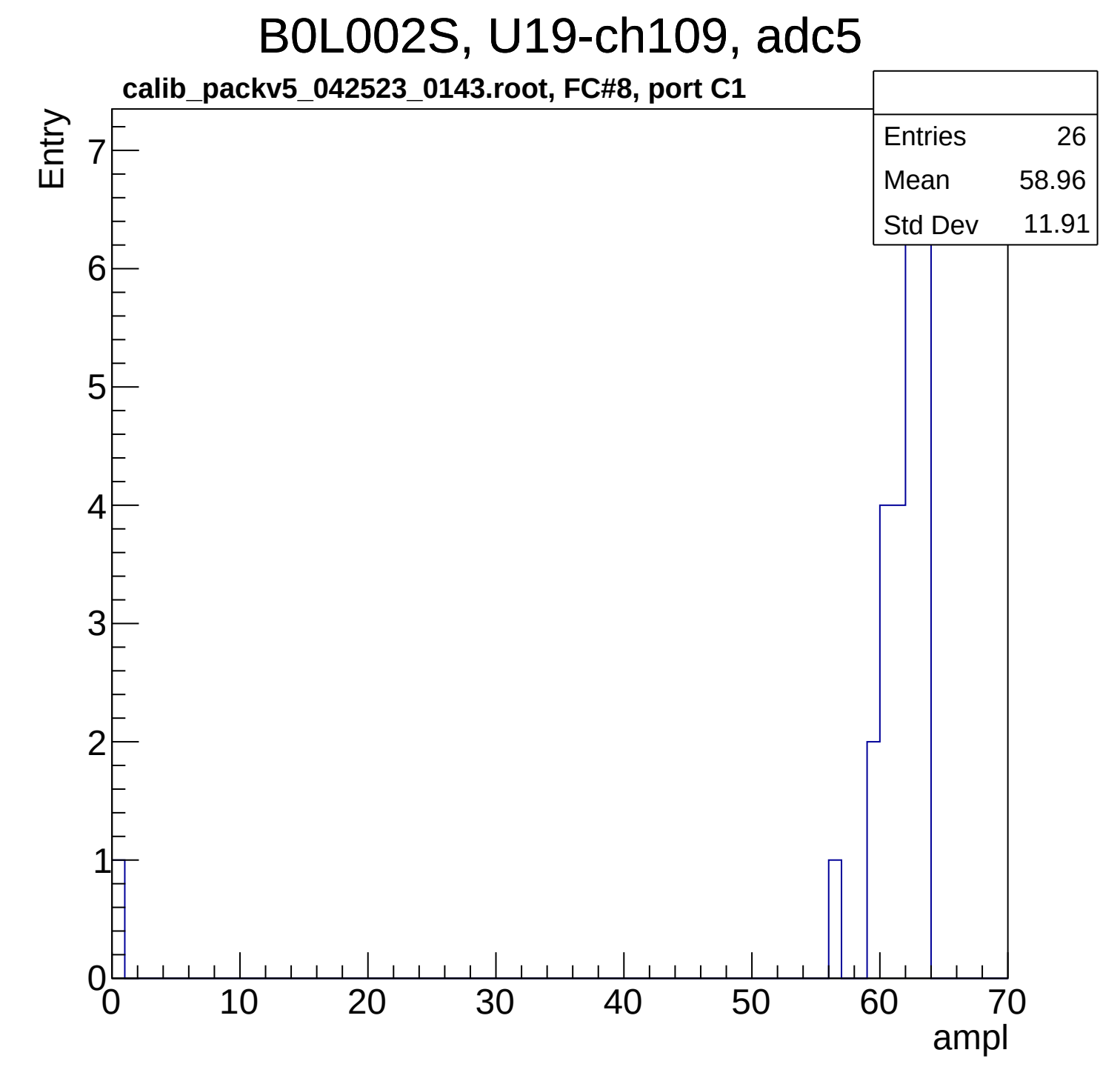

B0L002S, U19-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 29.32
Std Dev 5.975
Gaus mean : 30.0212
Gaus Width: 3.9337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

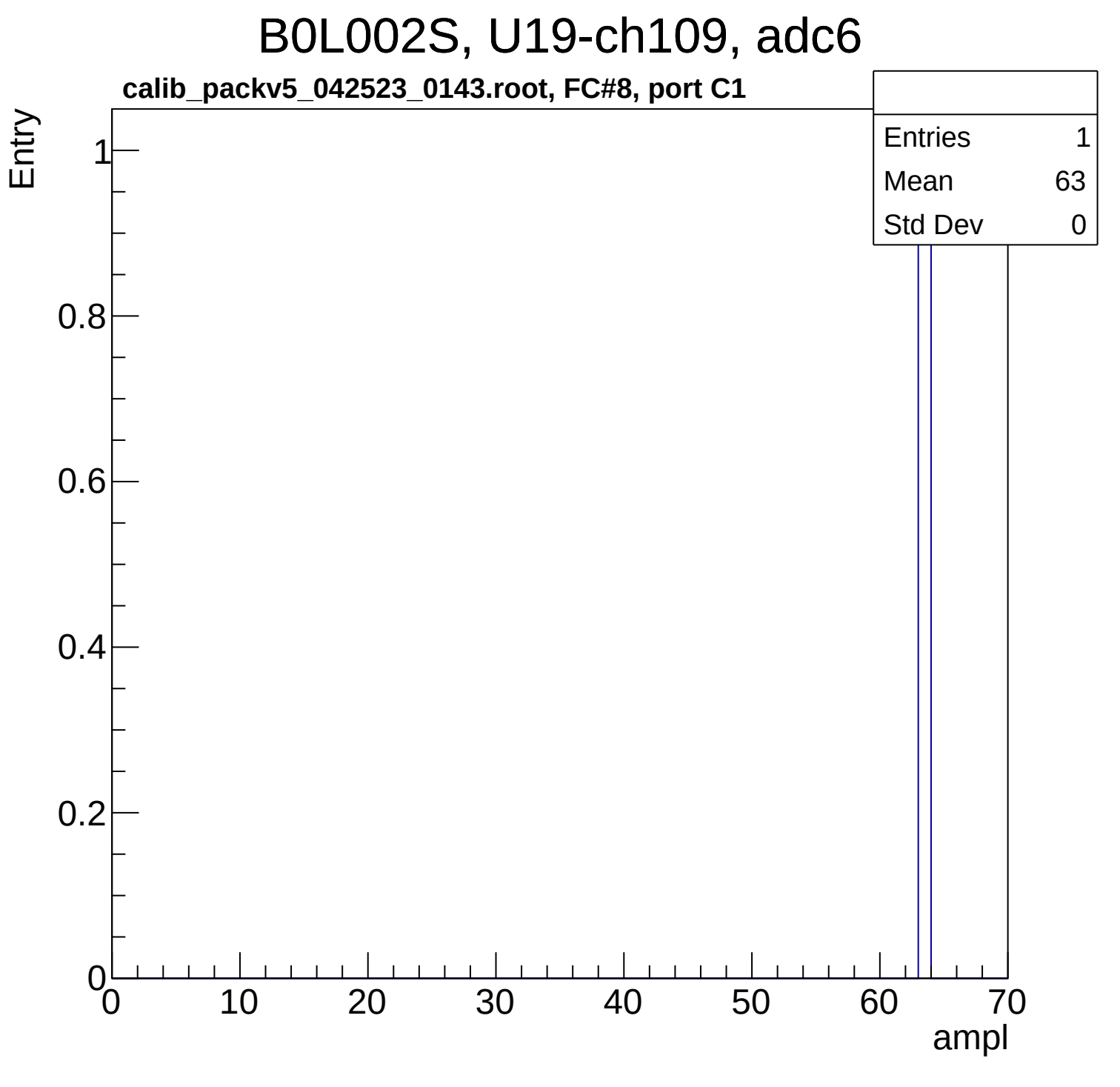

B0L002S, U19-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.6
Std Dev 3.229
Gaus mean : 36.8308
Gaus Width: 2.9512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

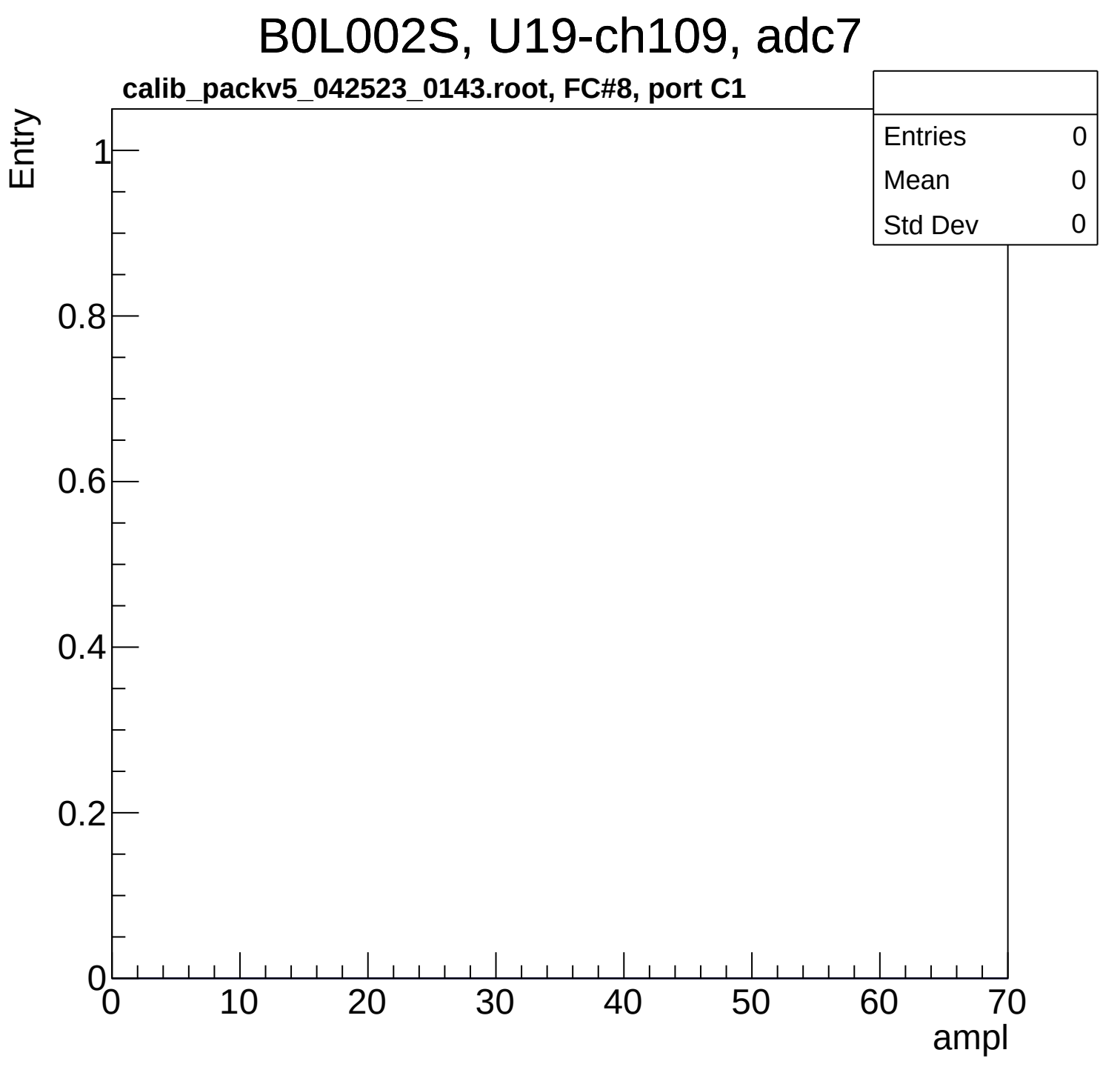

B0L002S, U19-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.94
Std Dev 3.095
Gaus mean : 44.3560
Gaus Width: 3.3841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

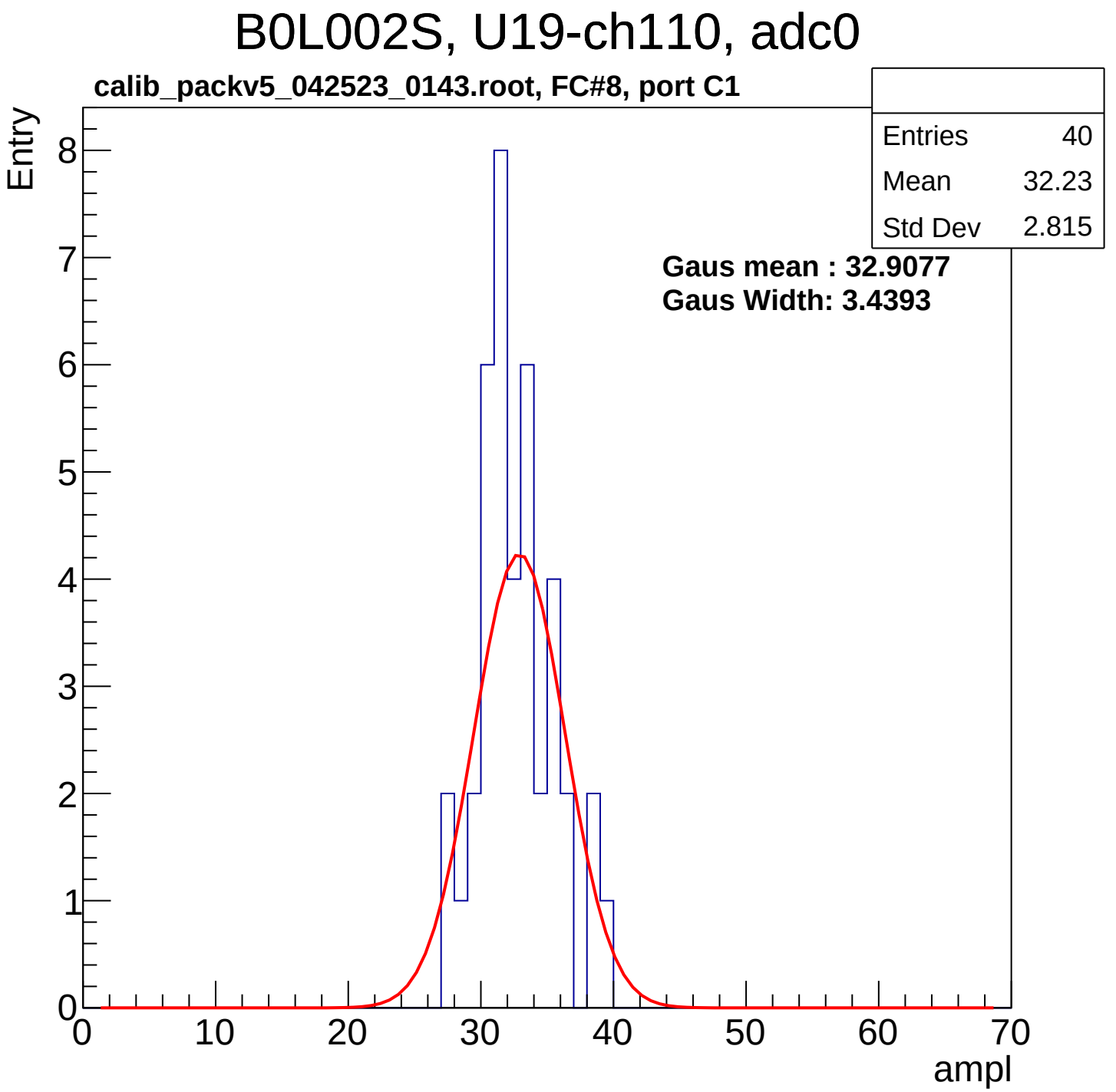

B0L002S, U19-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.86
Std Dev 3.137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

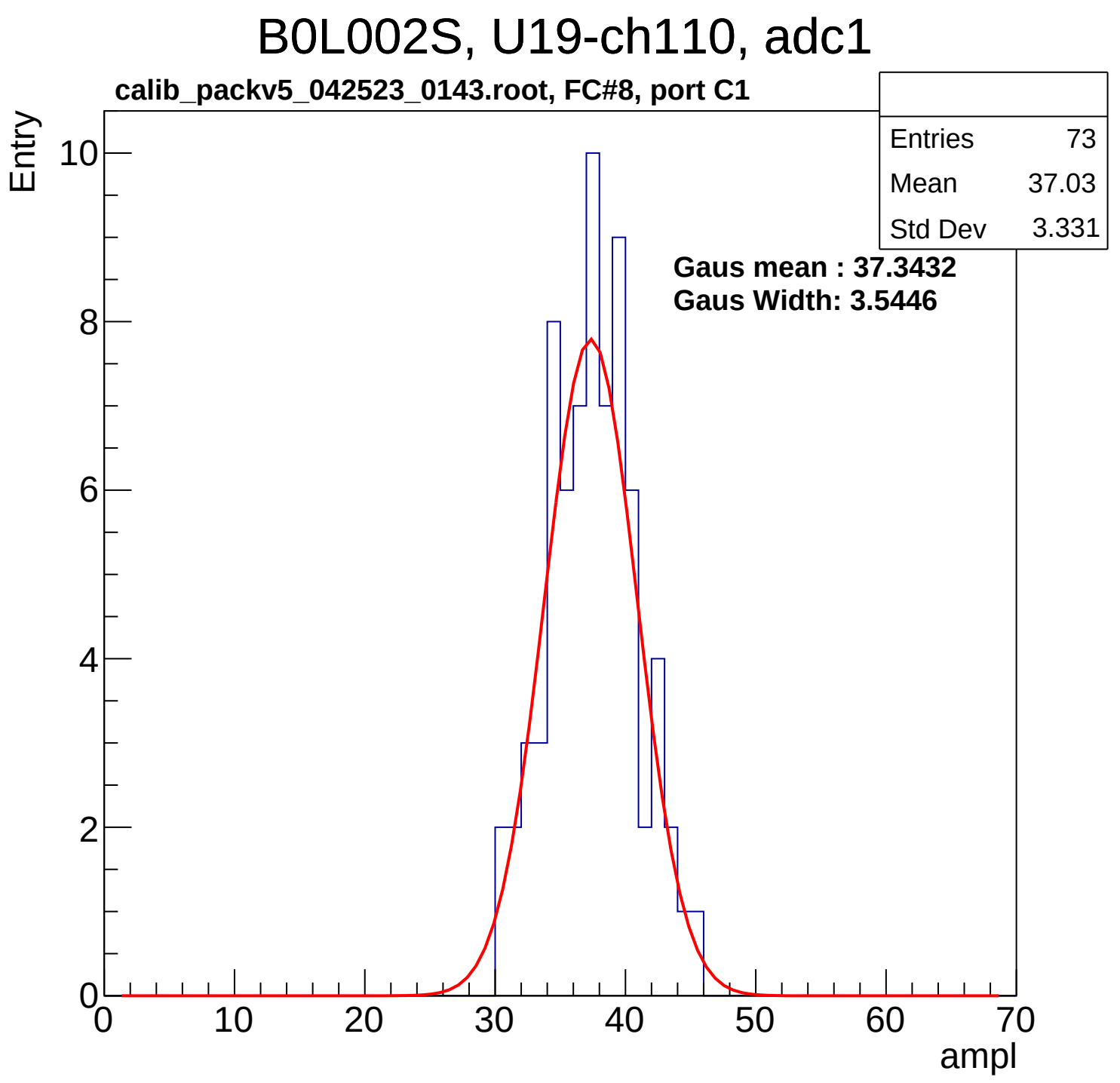

B0L002S, U19-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 57.16
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

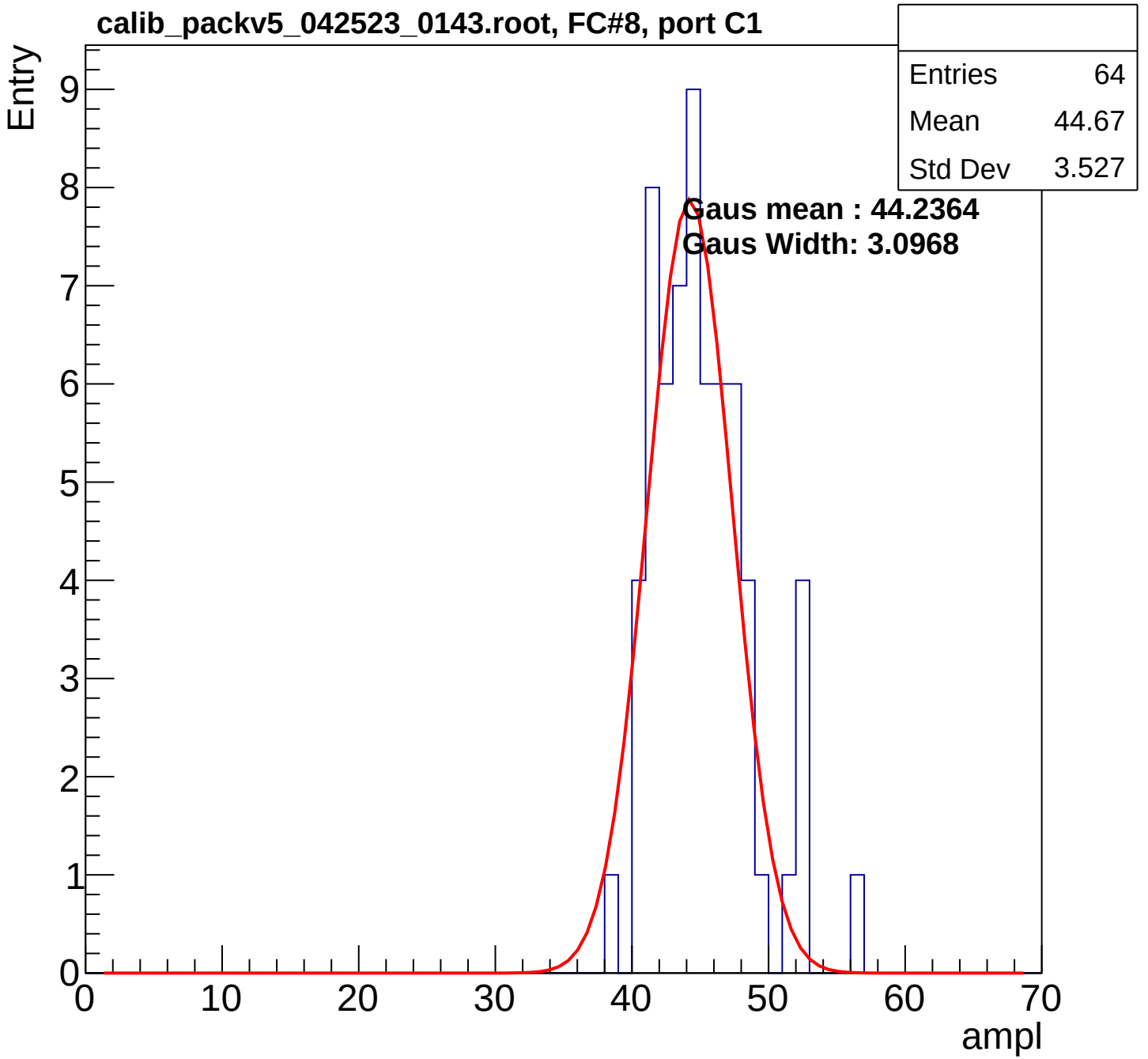

B0L002S, U19-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.96
Std Dev 11.91
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

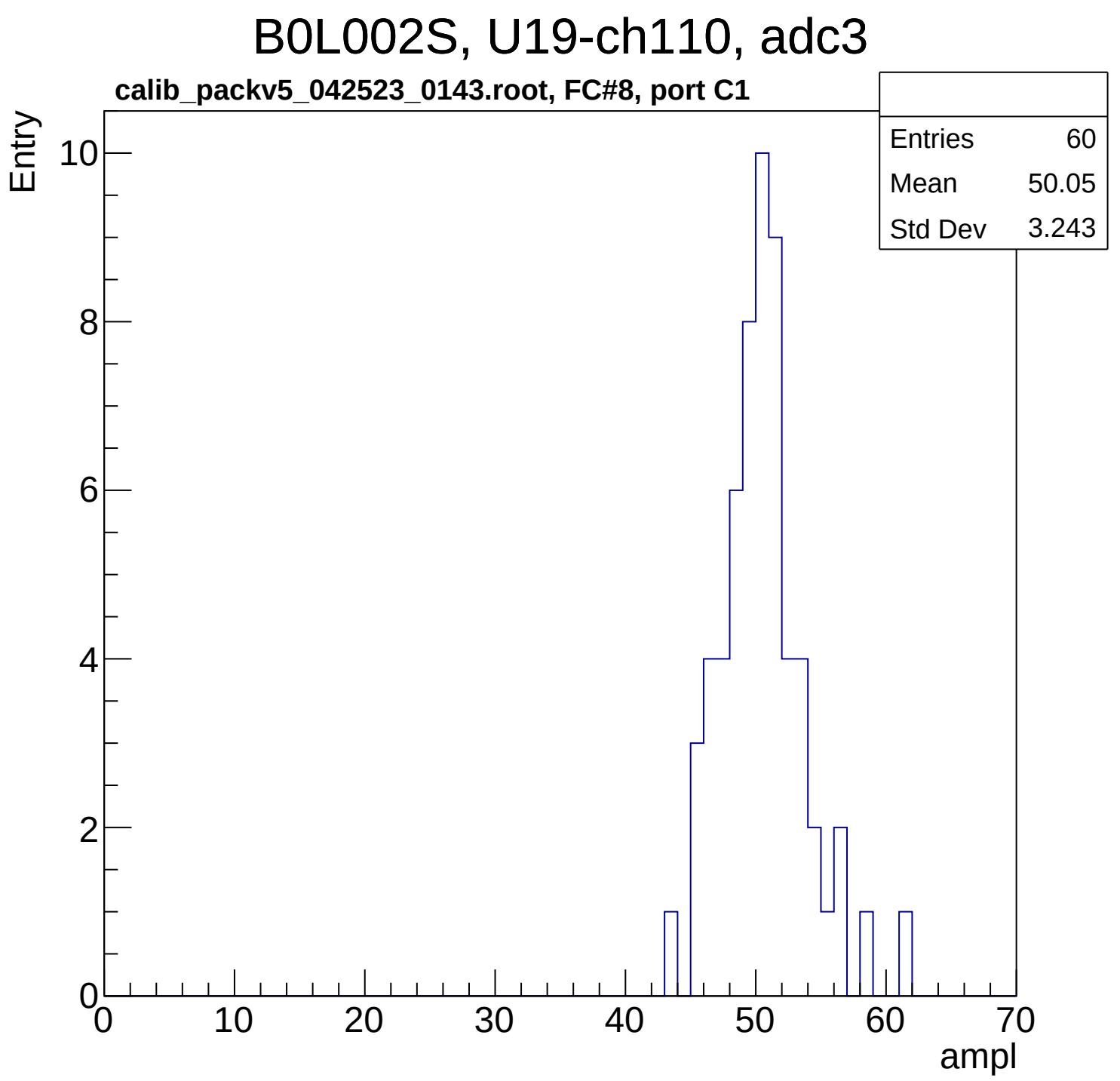

B0L002S, U19-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

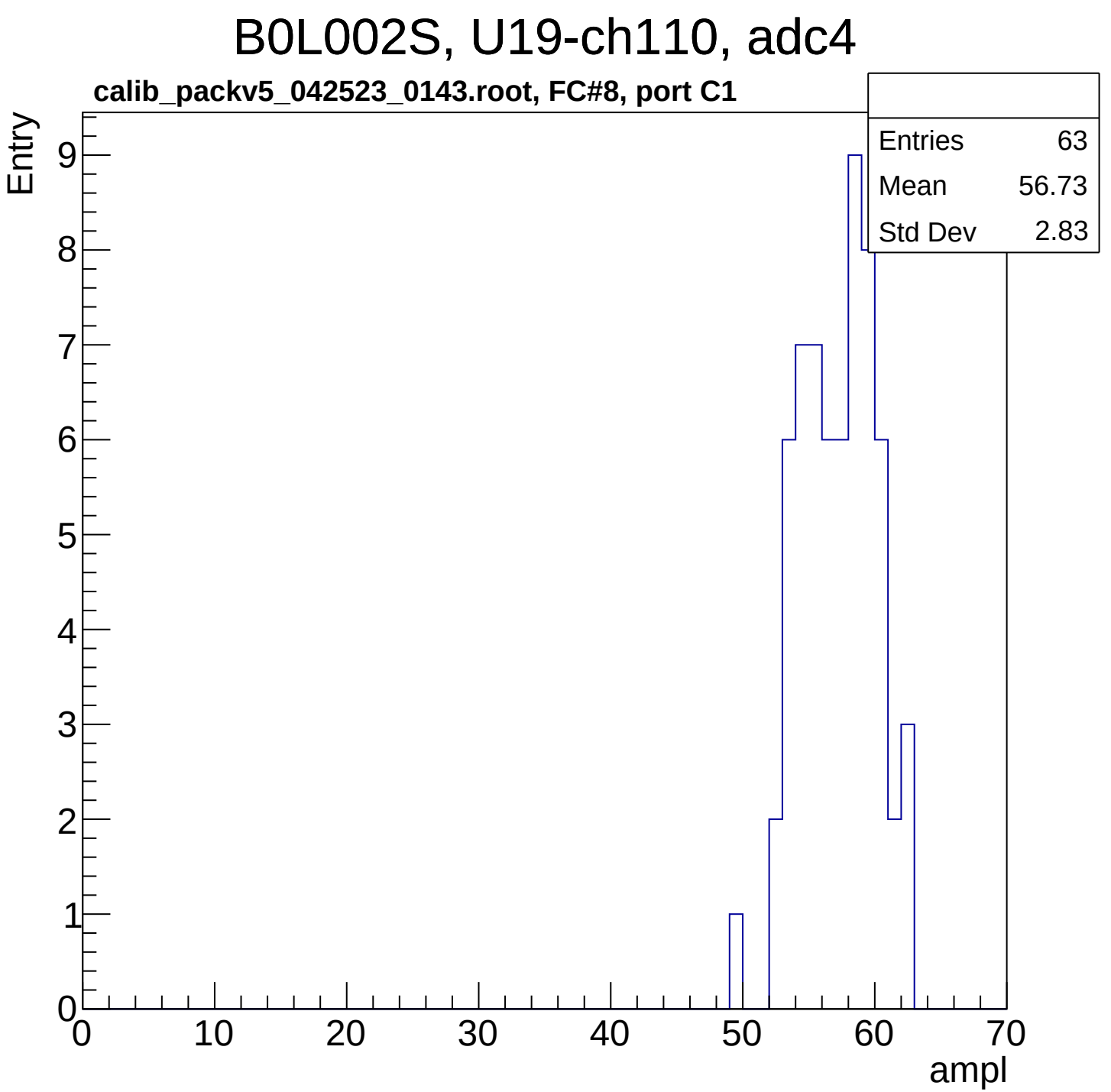

B0L002S, U19-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

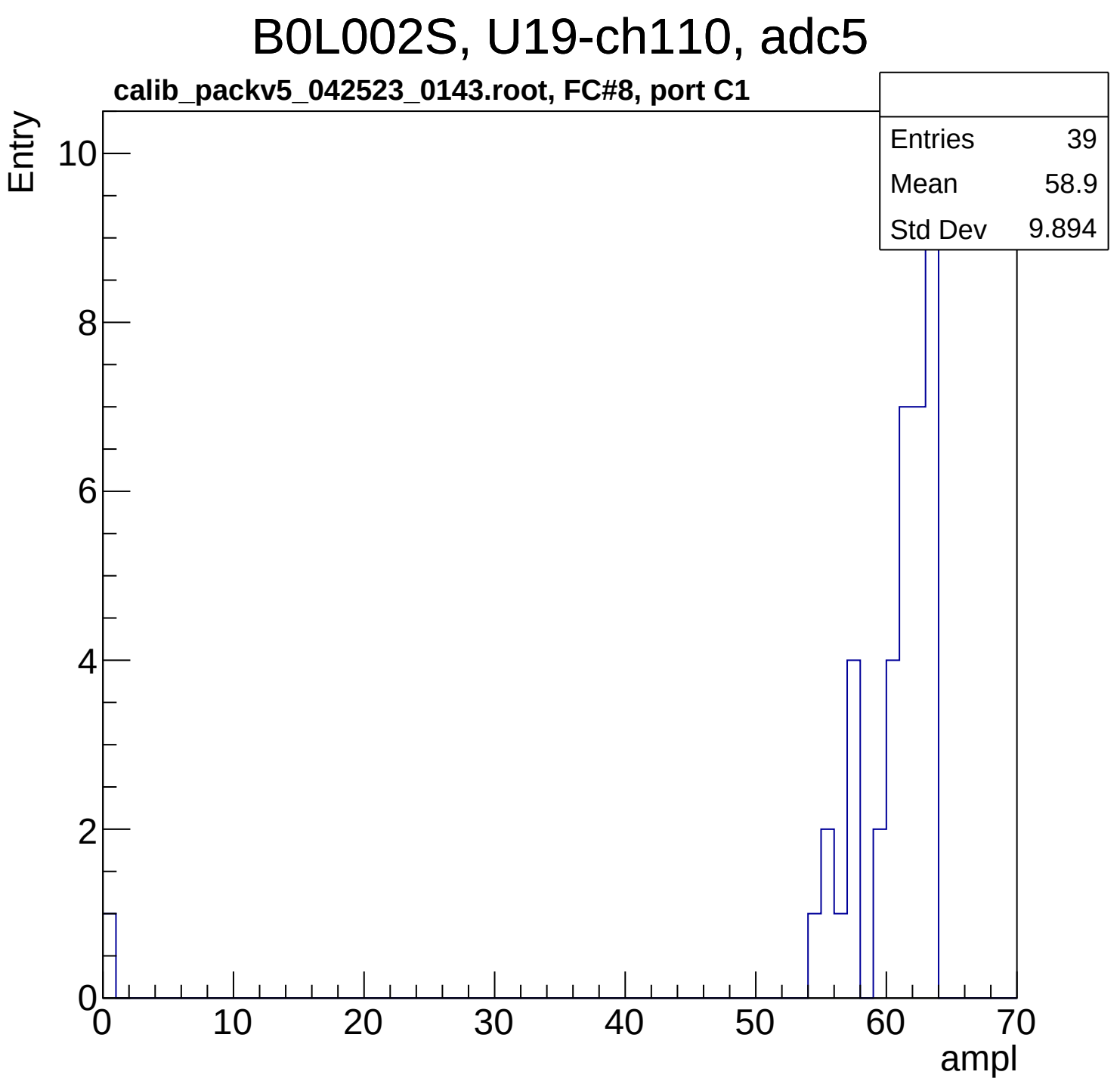

B0L002S, U19-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 32.23
Std Dev 2.815
Gaus mean : 32.9077
Gaus Width: 3.4393
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

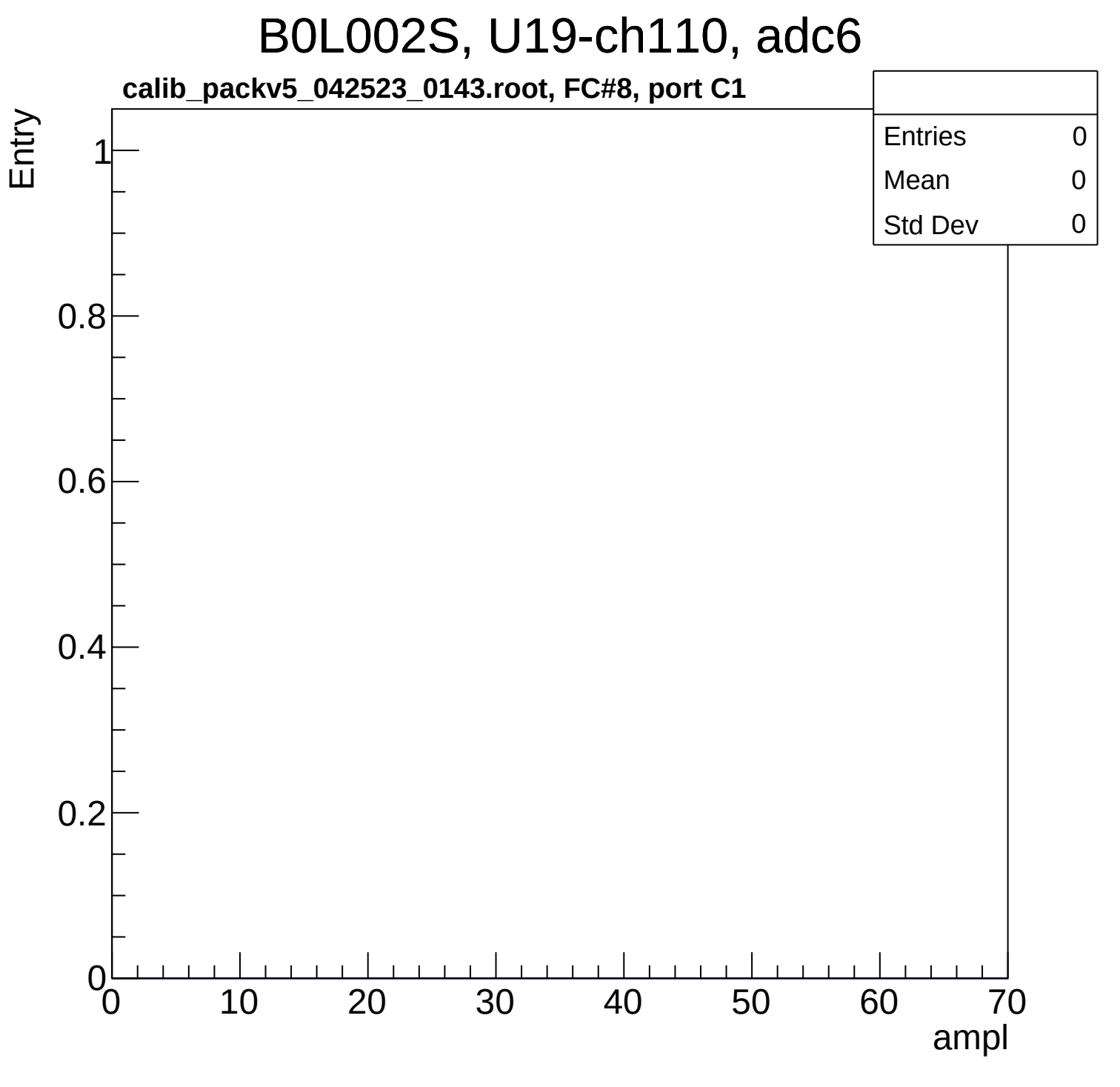

B0L002S, U19-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.03
Std Dev 3.331
Gaus mean : 37.3432
Gaus Width: 3.5446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

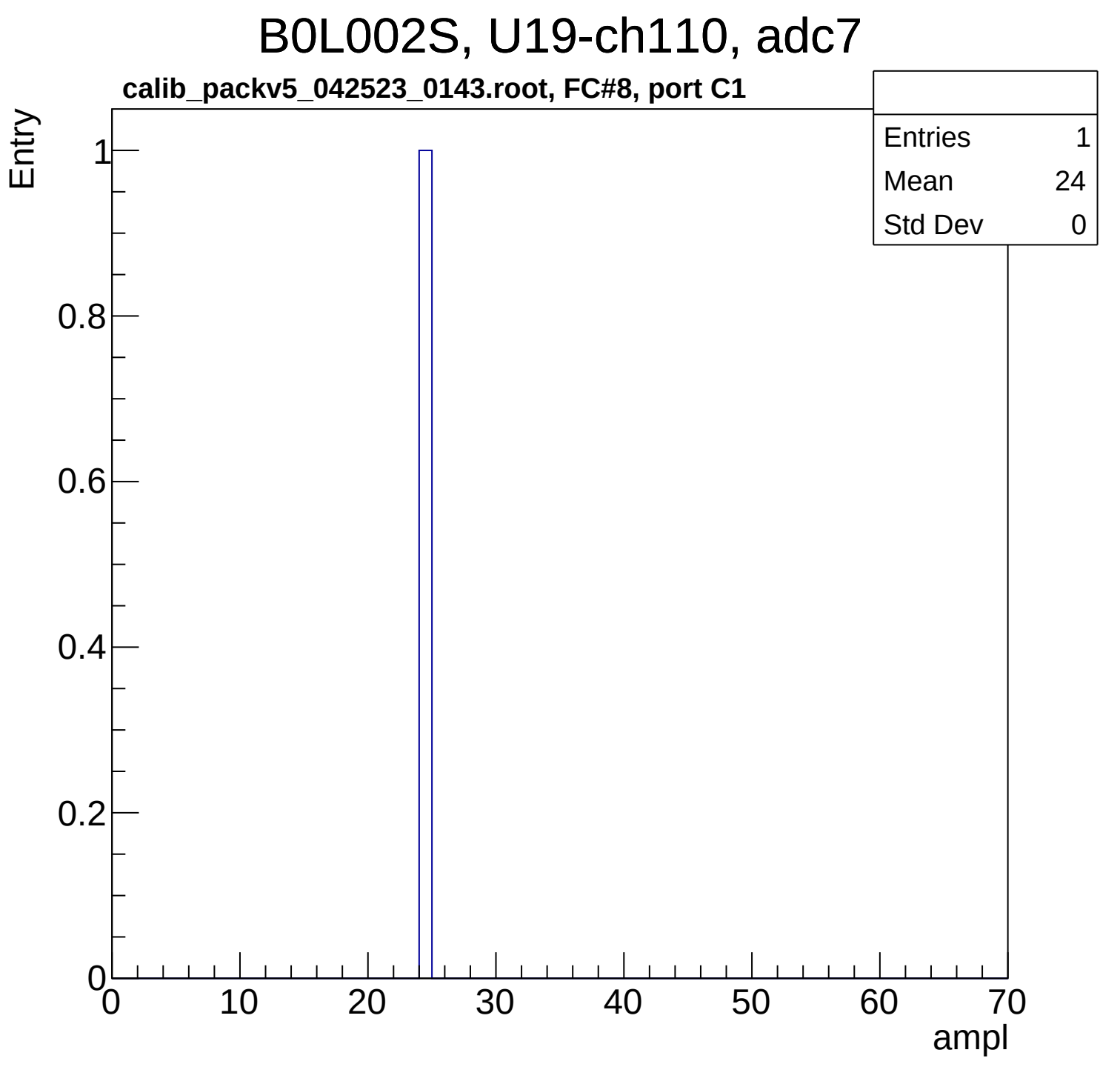

B0L002S, U19-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.67
Std Dev 3.527
Gaus mean : 44.2364
Gaus Width: 3.0968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

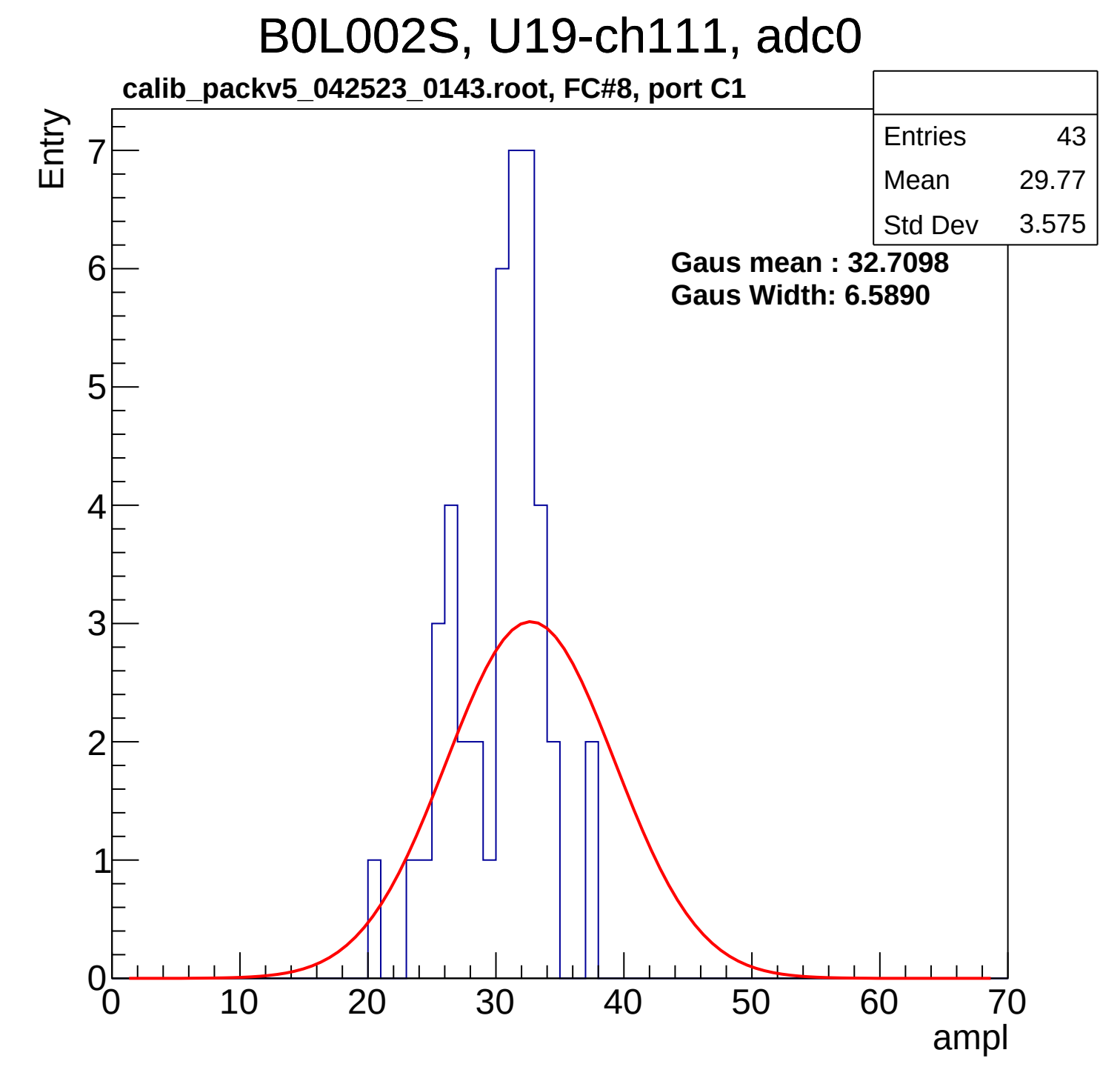

B0L002S, U19-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.05
Std Dev 3.243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

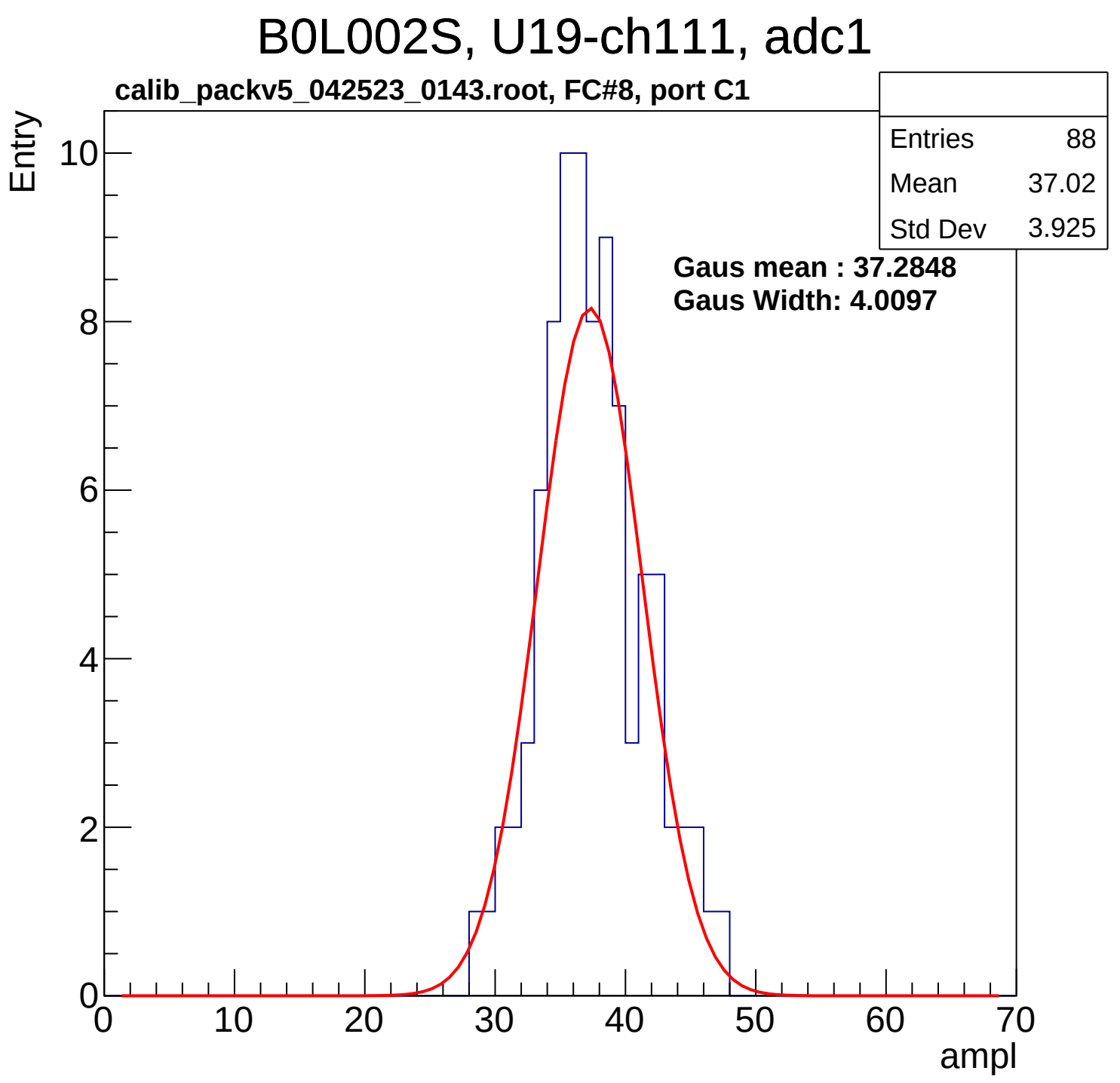

B0L002S, U19-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.73
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

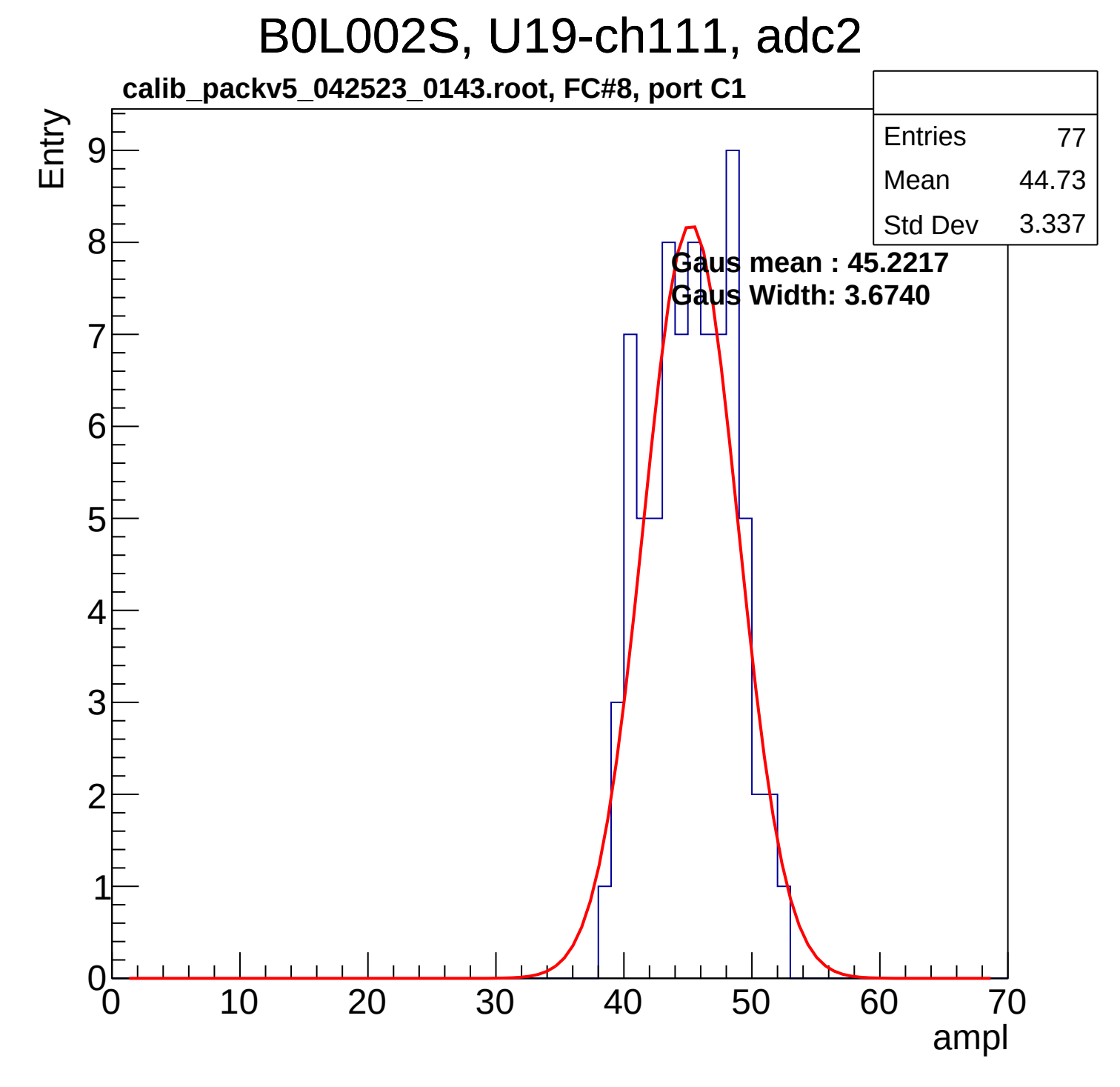

B0L002S, U19-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.9
Std Dev 9.894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

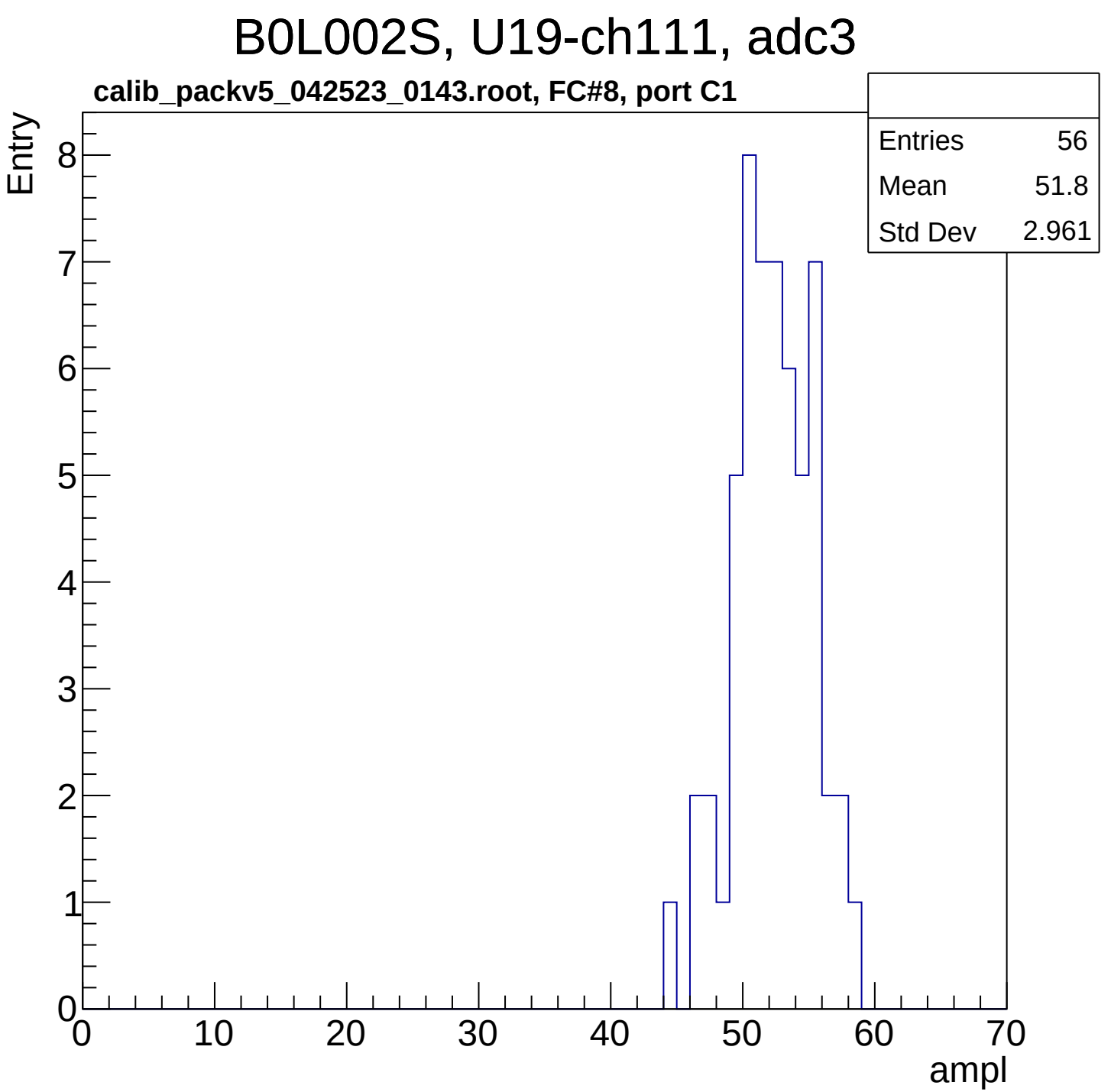

B0L002S, U19-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

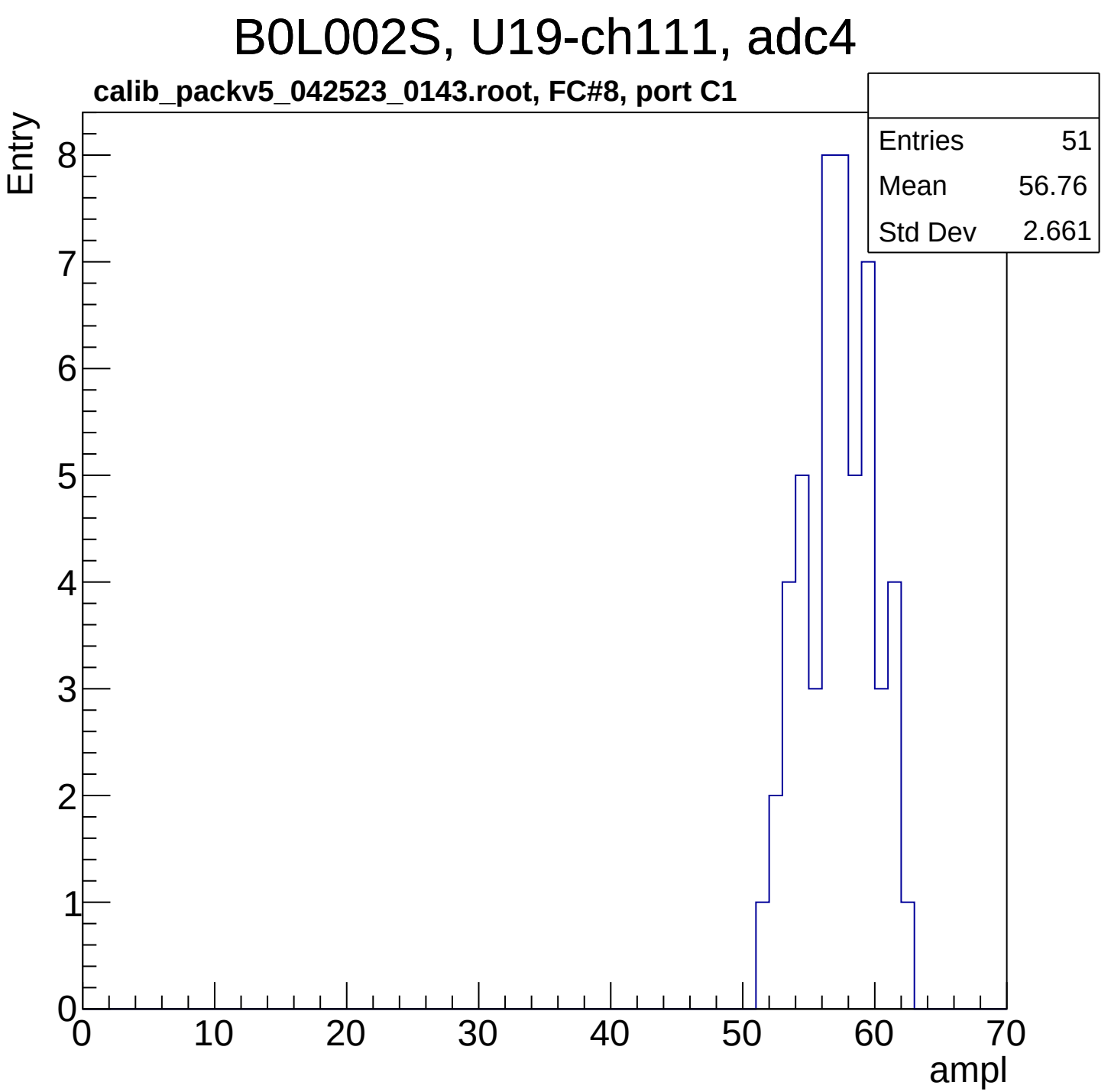

B0L002S, U19-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

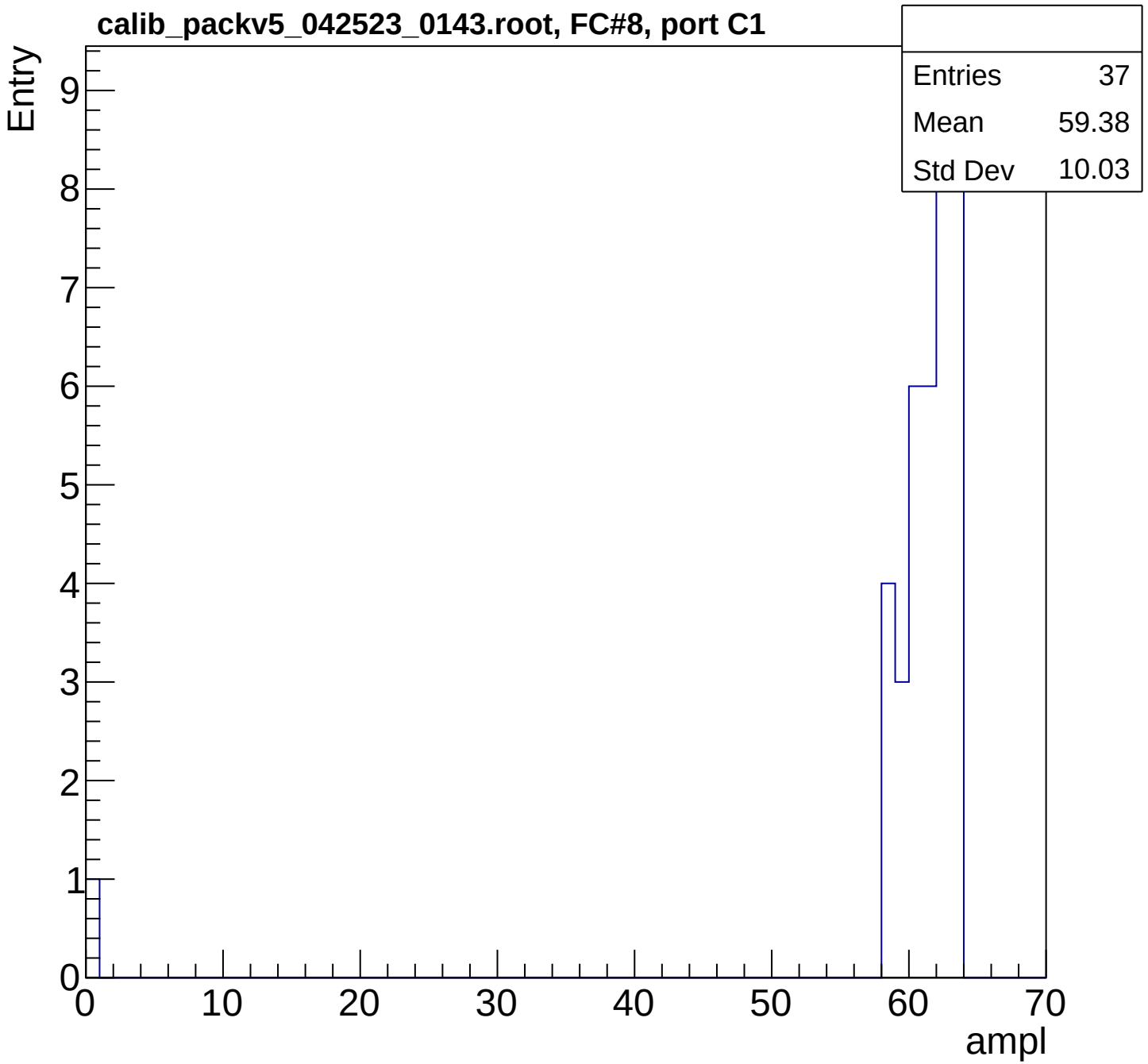

B0L002S, U19-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 29.77
Std Dev 3.575
Gaus mean : 32.7098
Gaus Width: 6.5890
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

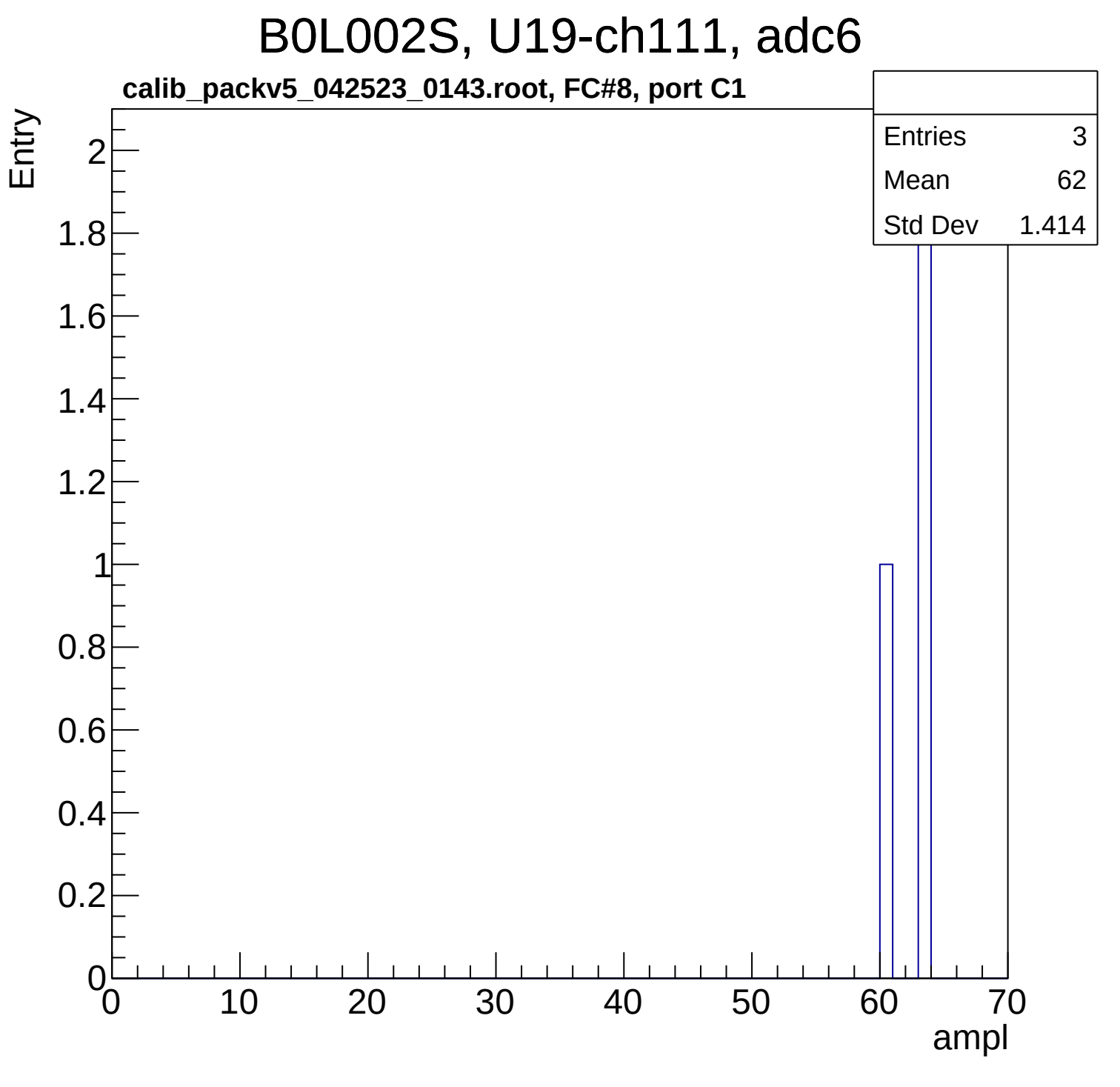

B0L002S, U19-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 37.02
Std Dev 3.925
Gaus mean : 37.2848
Gaus Width: 4.0097
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

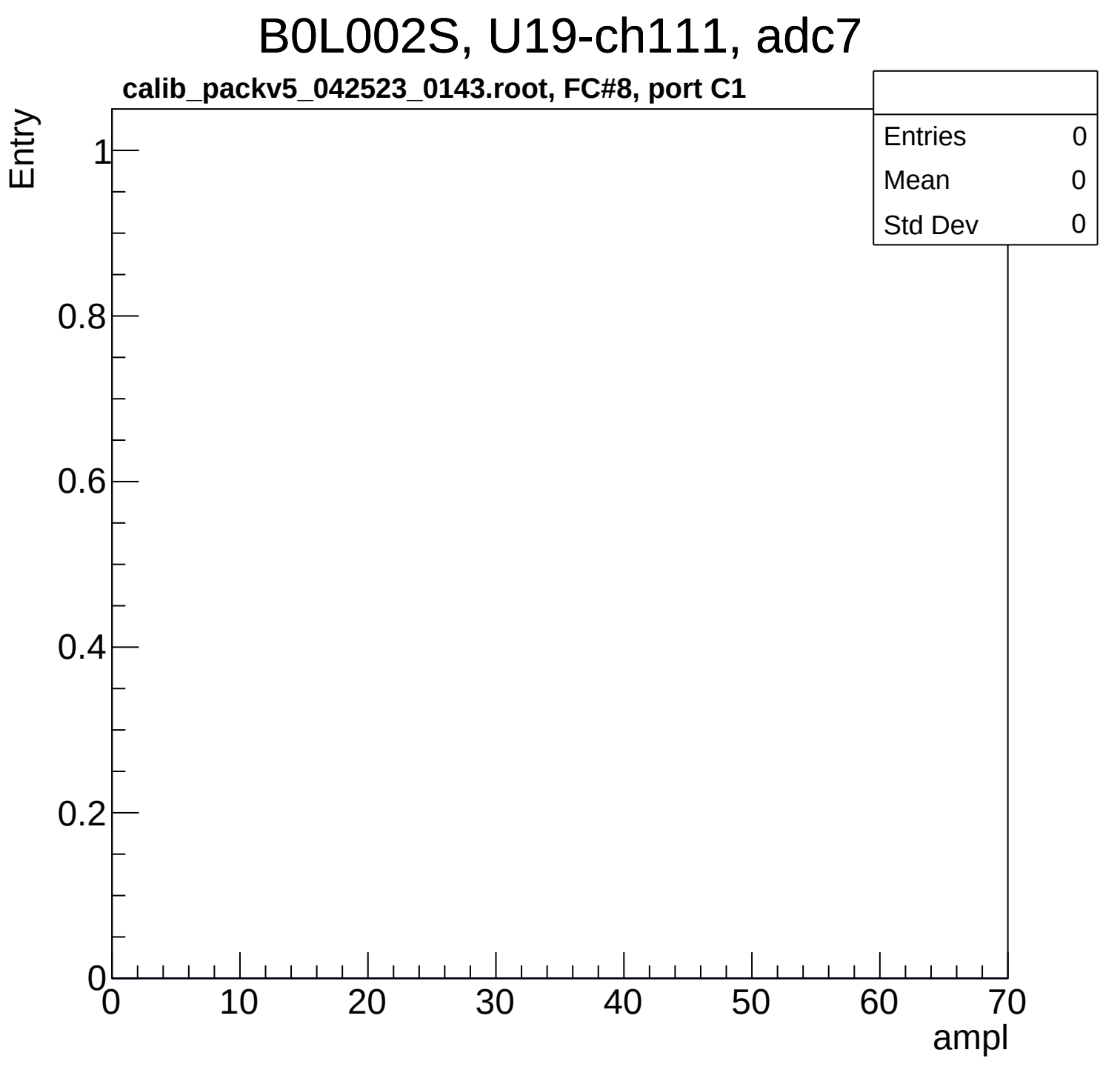

B0L002S, U19-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 44.73
Std Dev 3.337
Gaus mean : 45.2217
Gaus Width: 3.6740
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

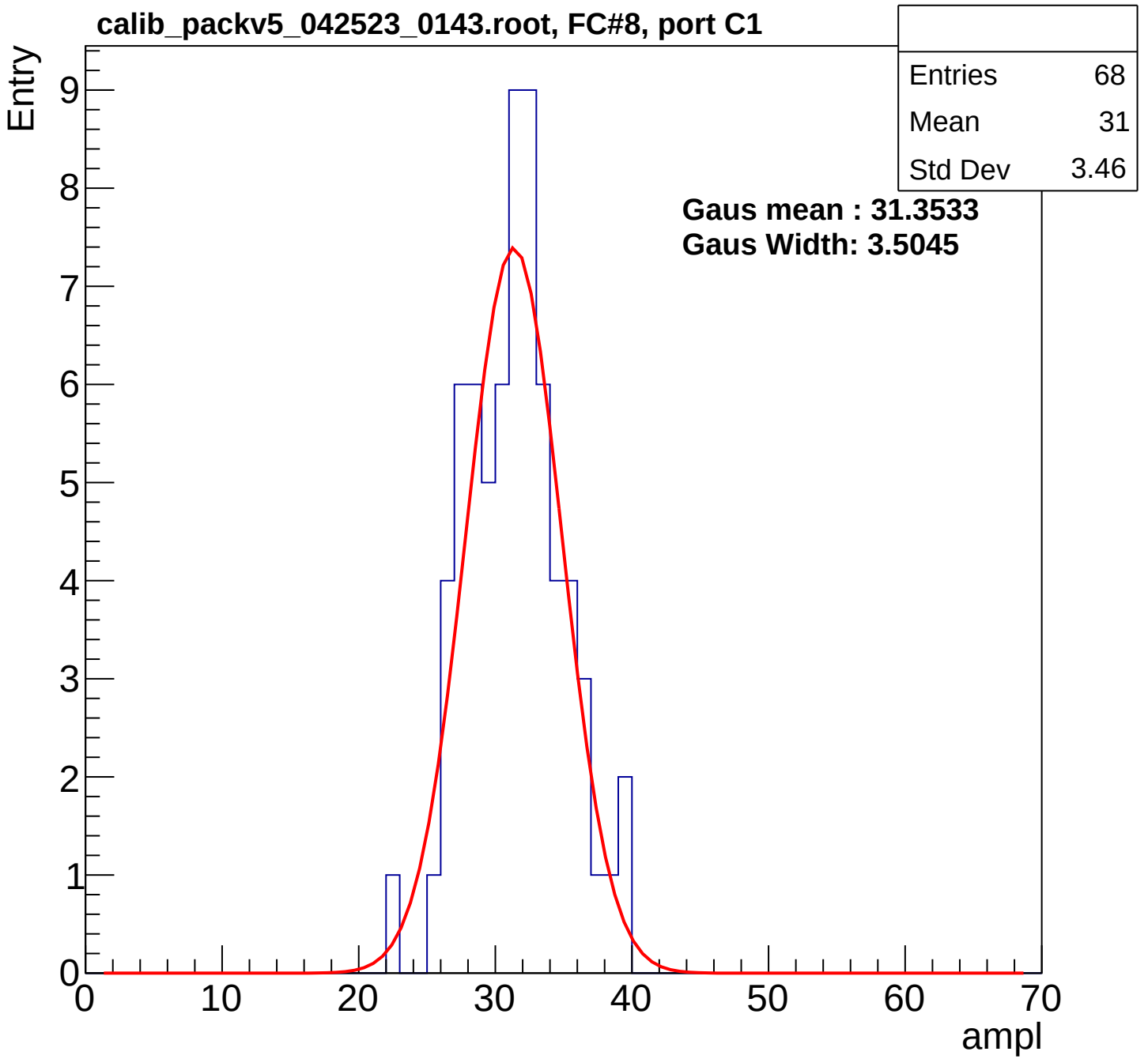

B0L002S, U19-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 51.8
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

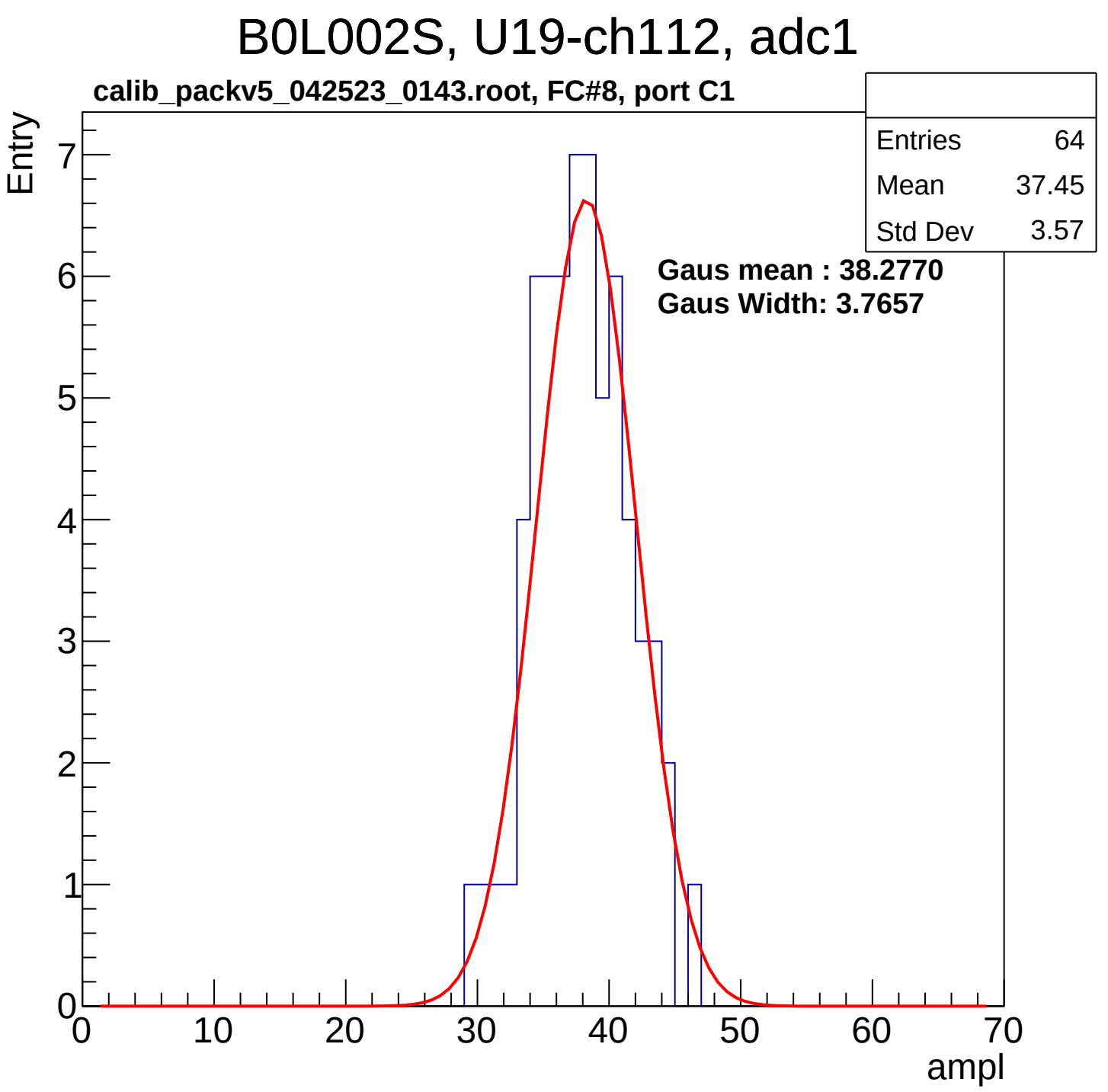

B0L002S, U19-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.76
Std Dev 2.661
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

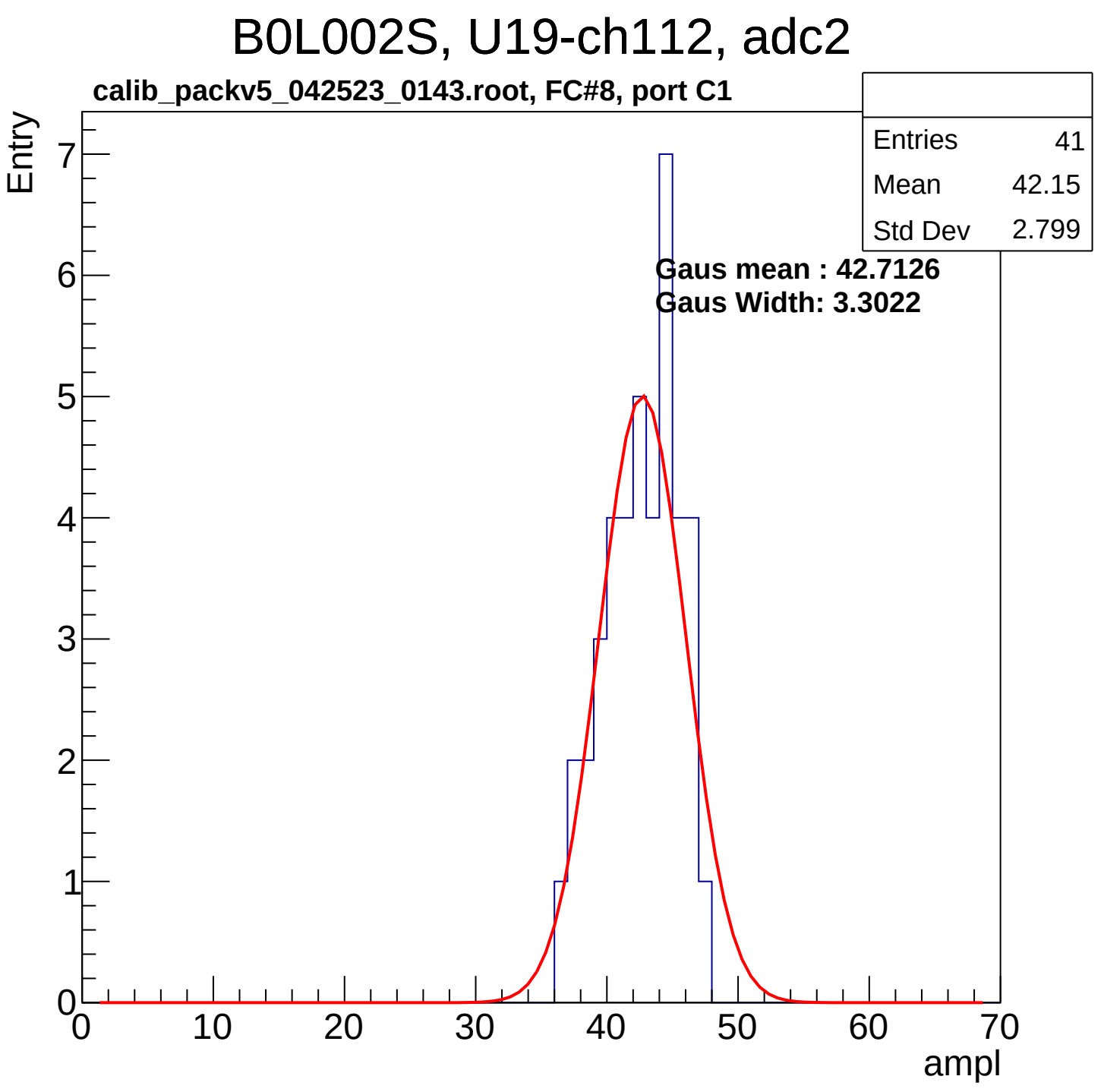

B0L002S, U19-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.38
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

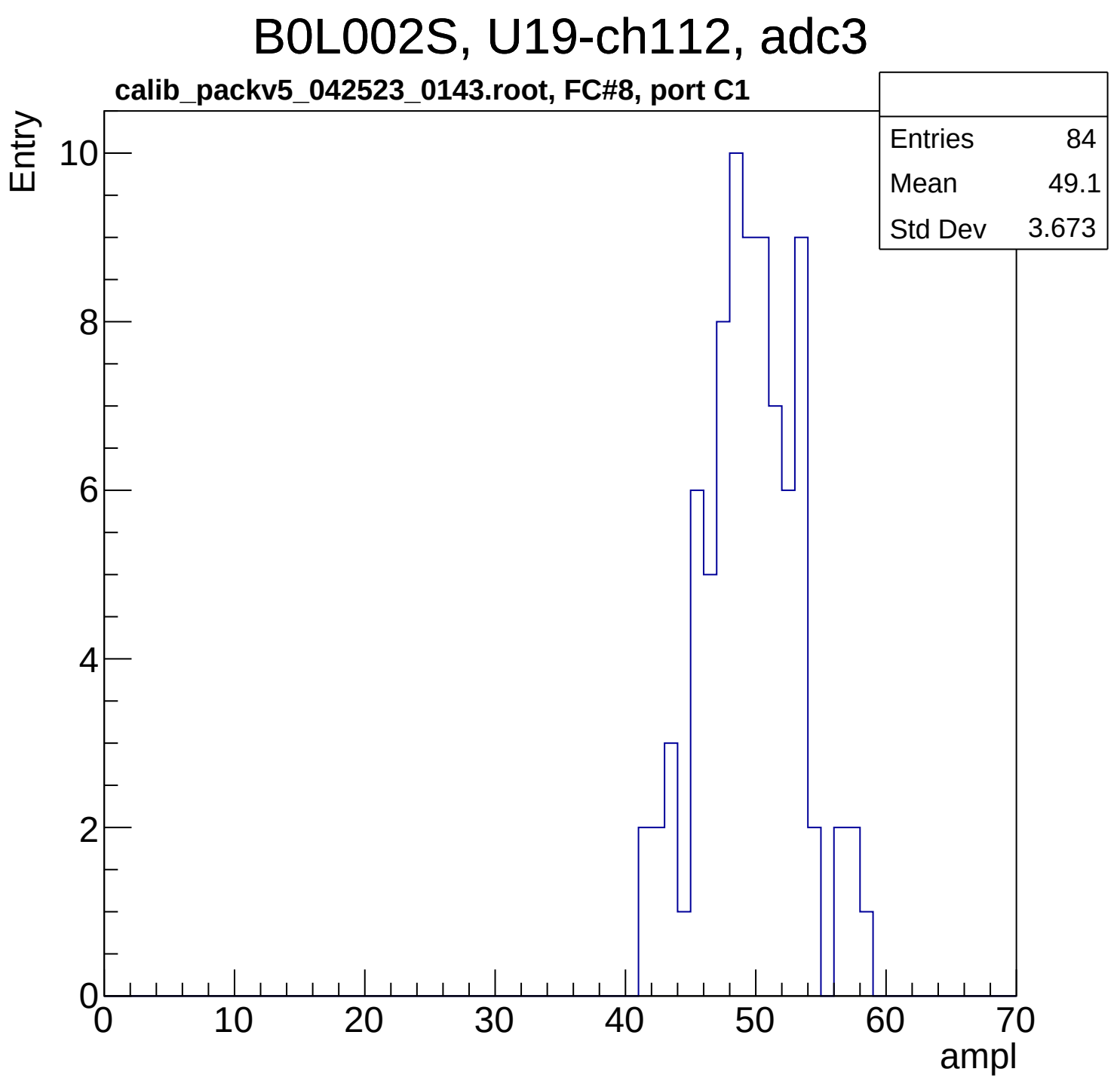

B0L002S, U19-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

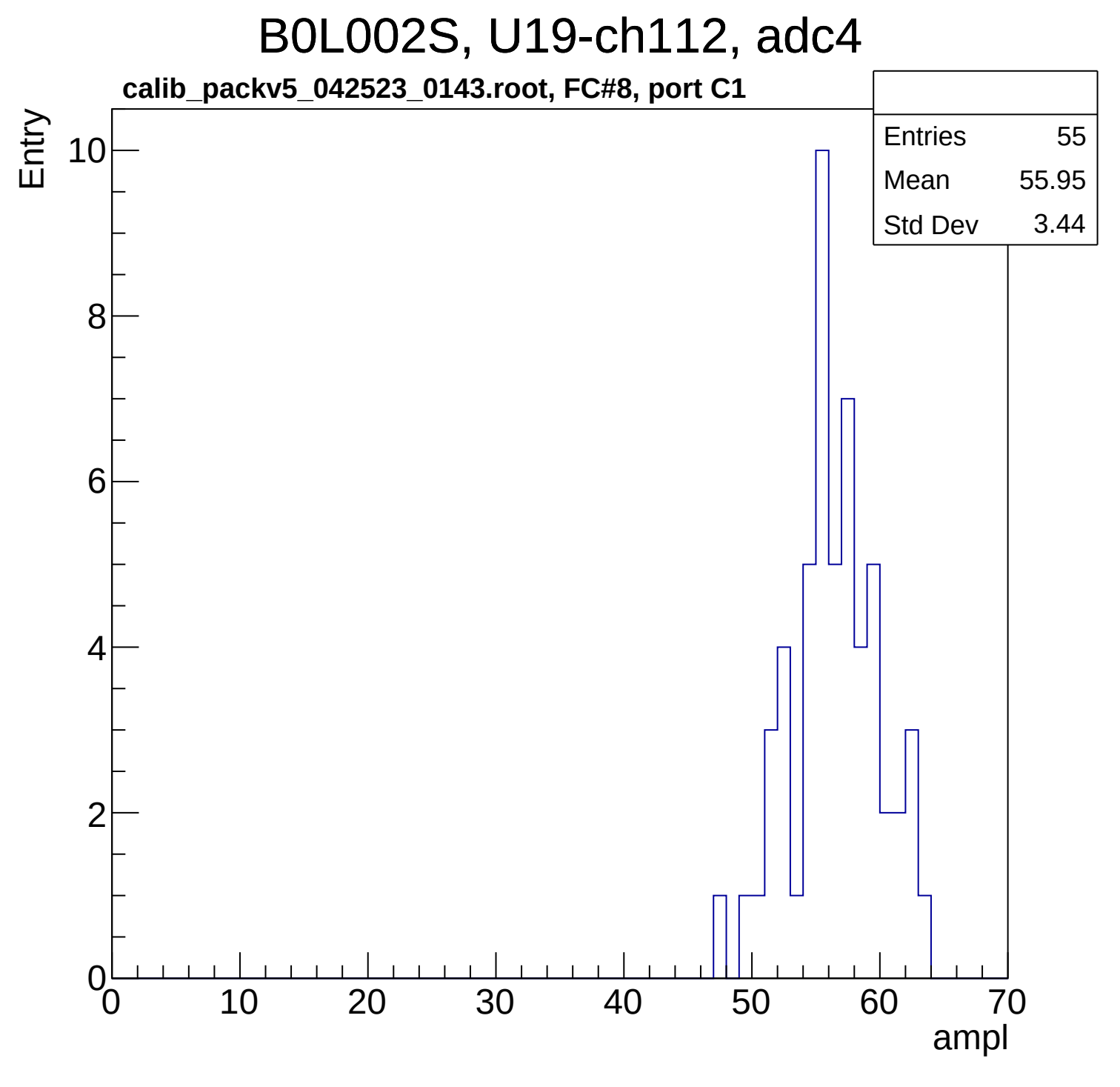

B0L002S, U19-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

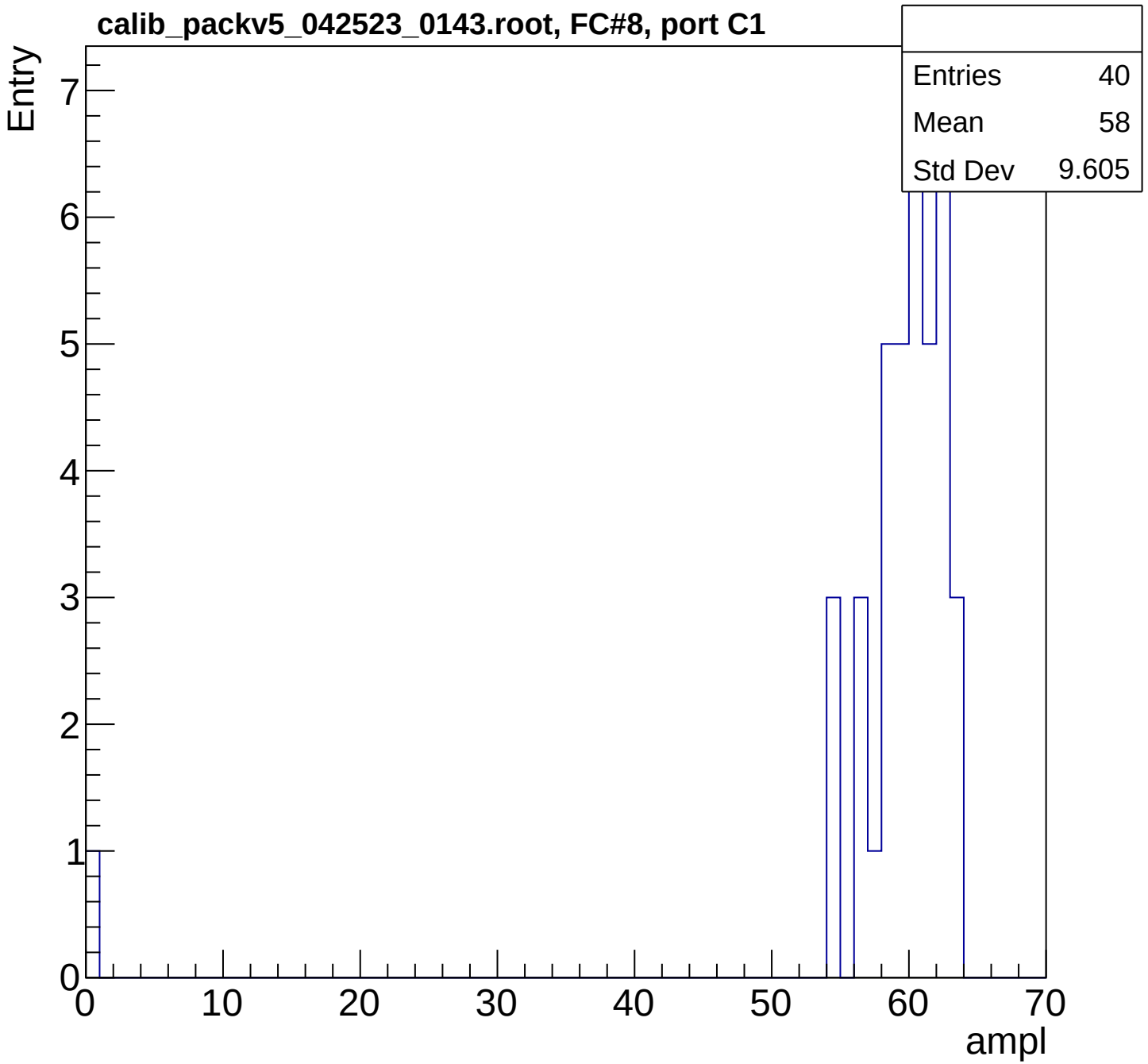

B0L002S, U19-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31
Std Dev 3.46
Gaus mean : 31.3533
Gaus Width: 3.5045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

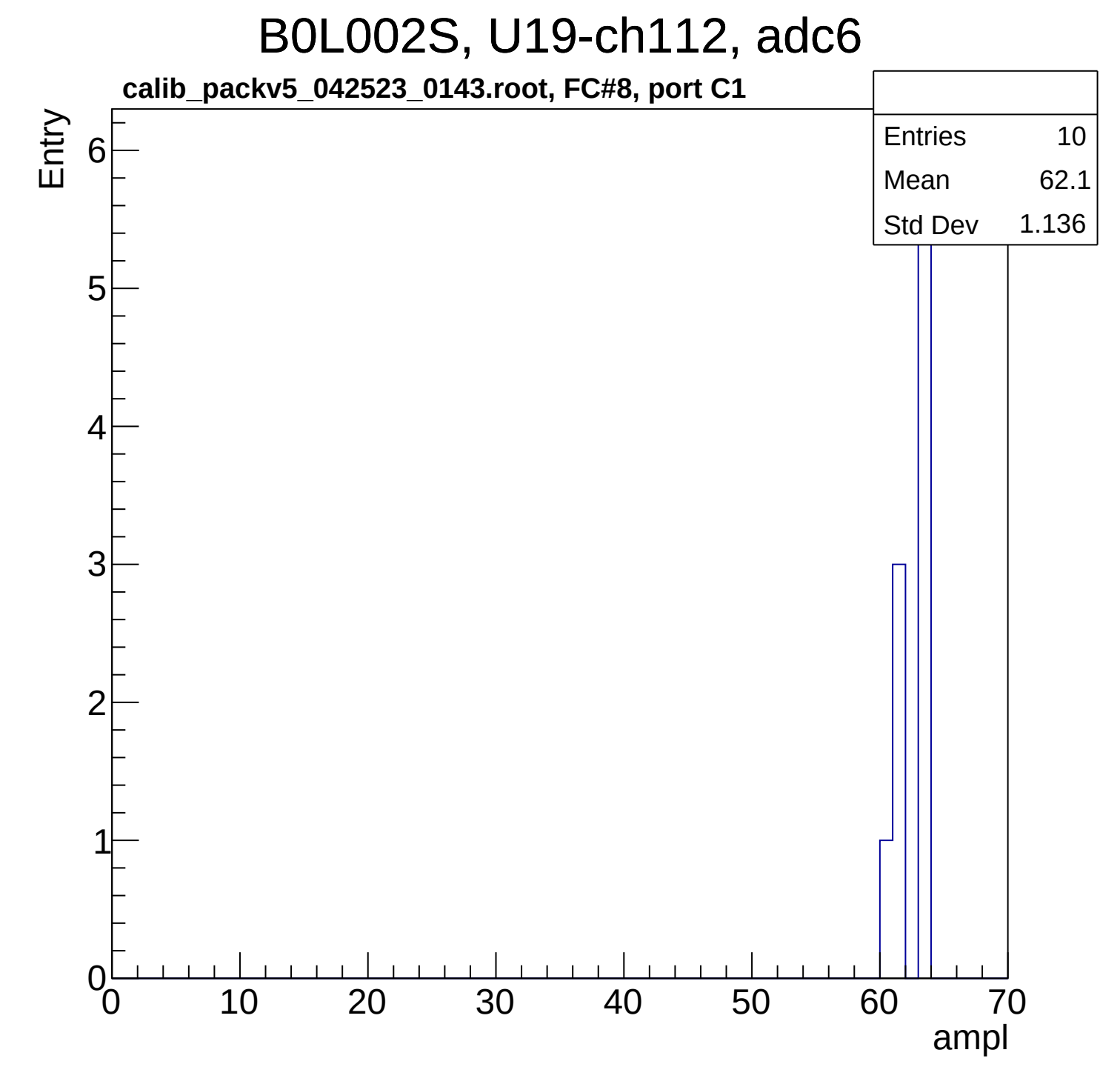

B0L002S, U19-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.45
Std Dev 3.57
Gaus mean : 38.2770
Gaus Width: 3.7657
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

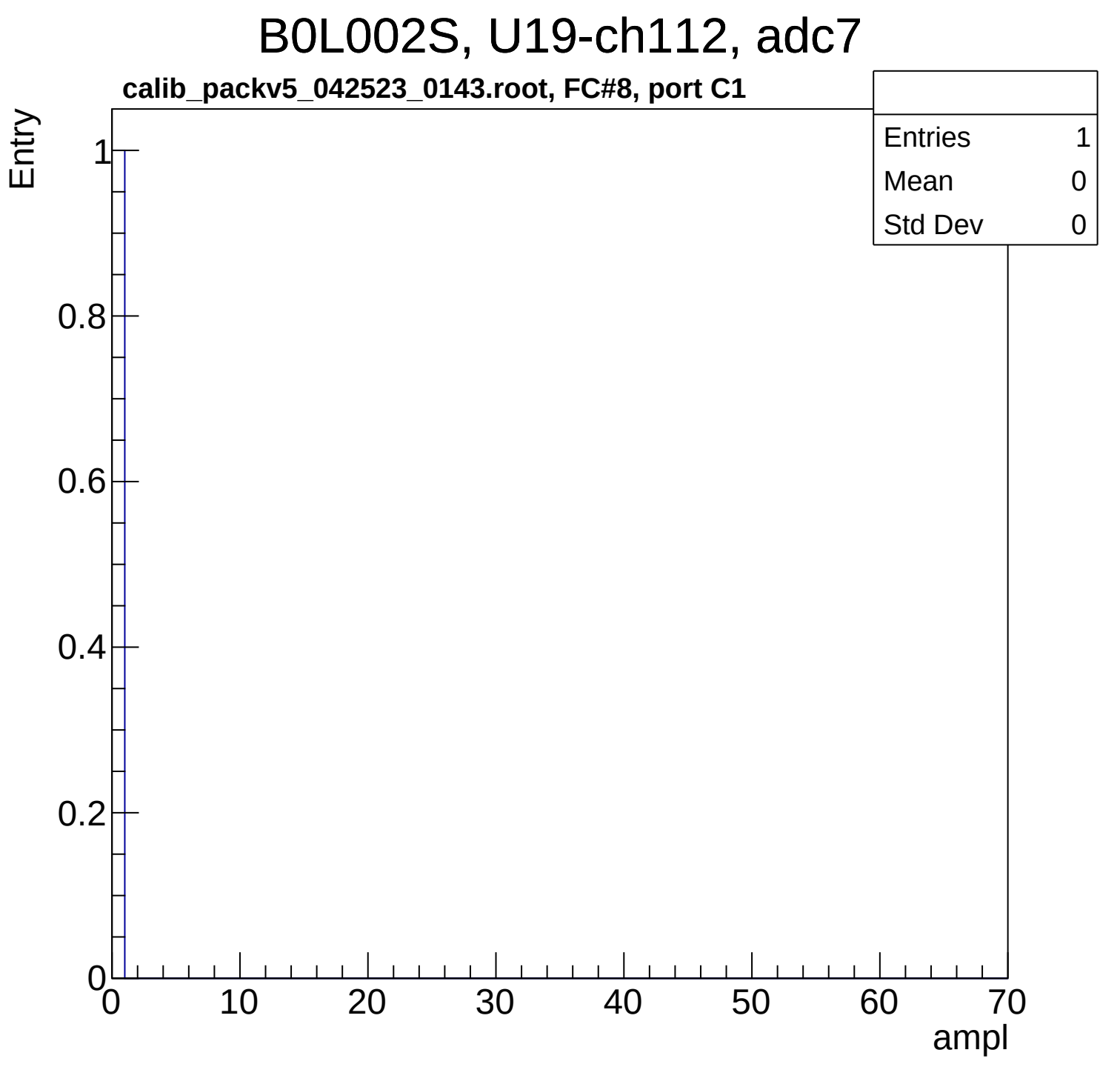

B0L002S, U19-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 42.15
Std Dev 2.799
Gaus mean : 42.7126
Gaus Width: 3.3022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

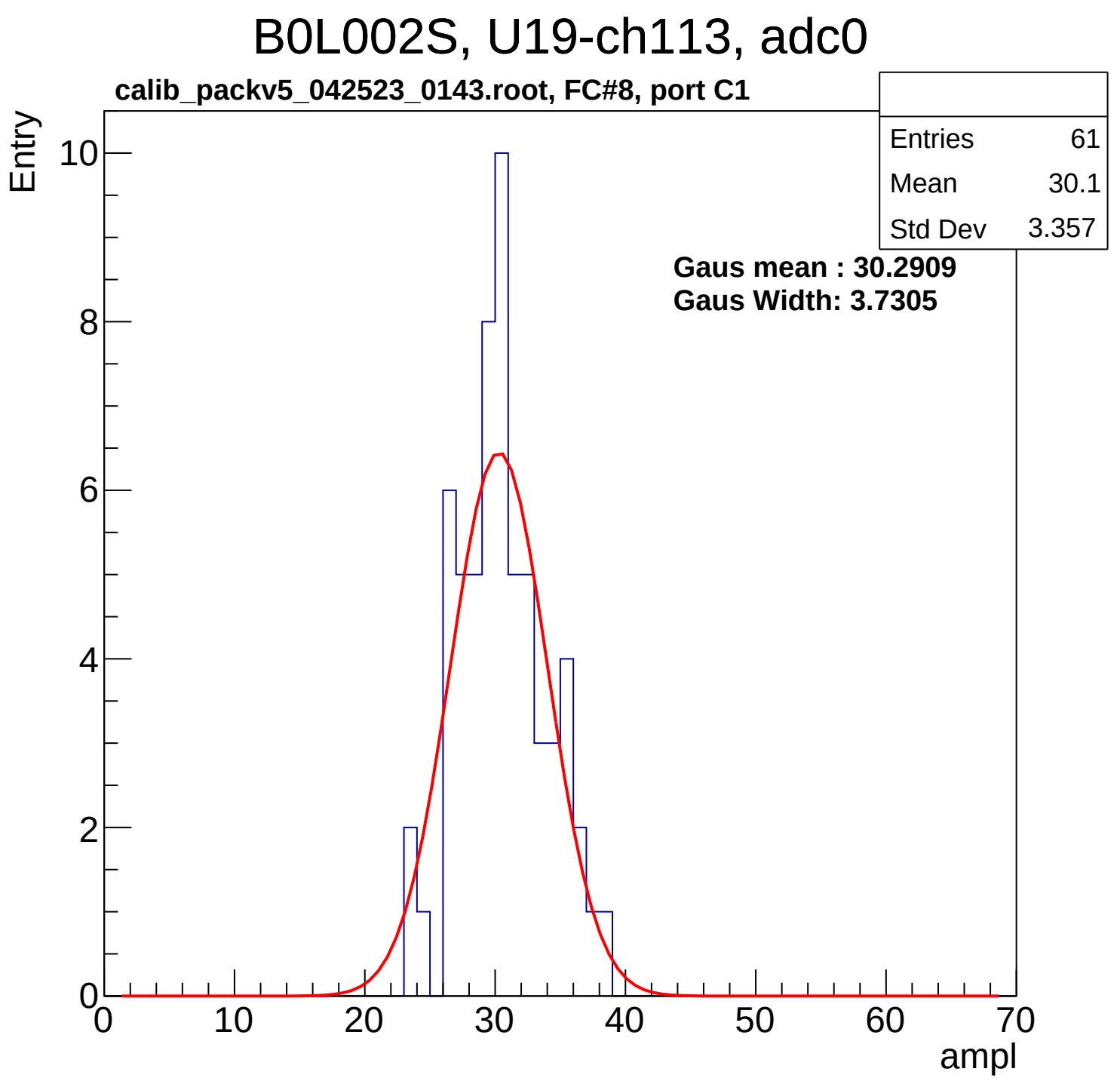

B0L002S, U19-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 49.1
Std Dev 3.673
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

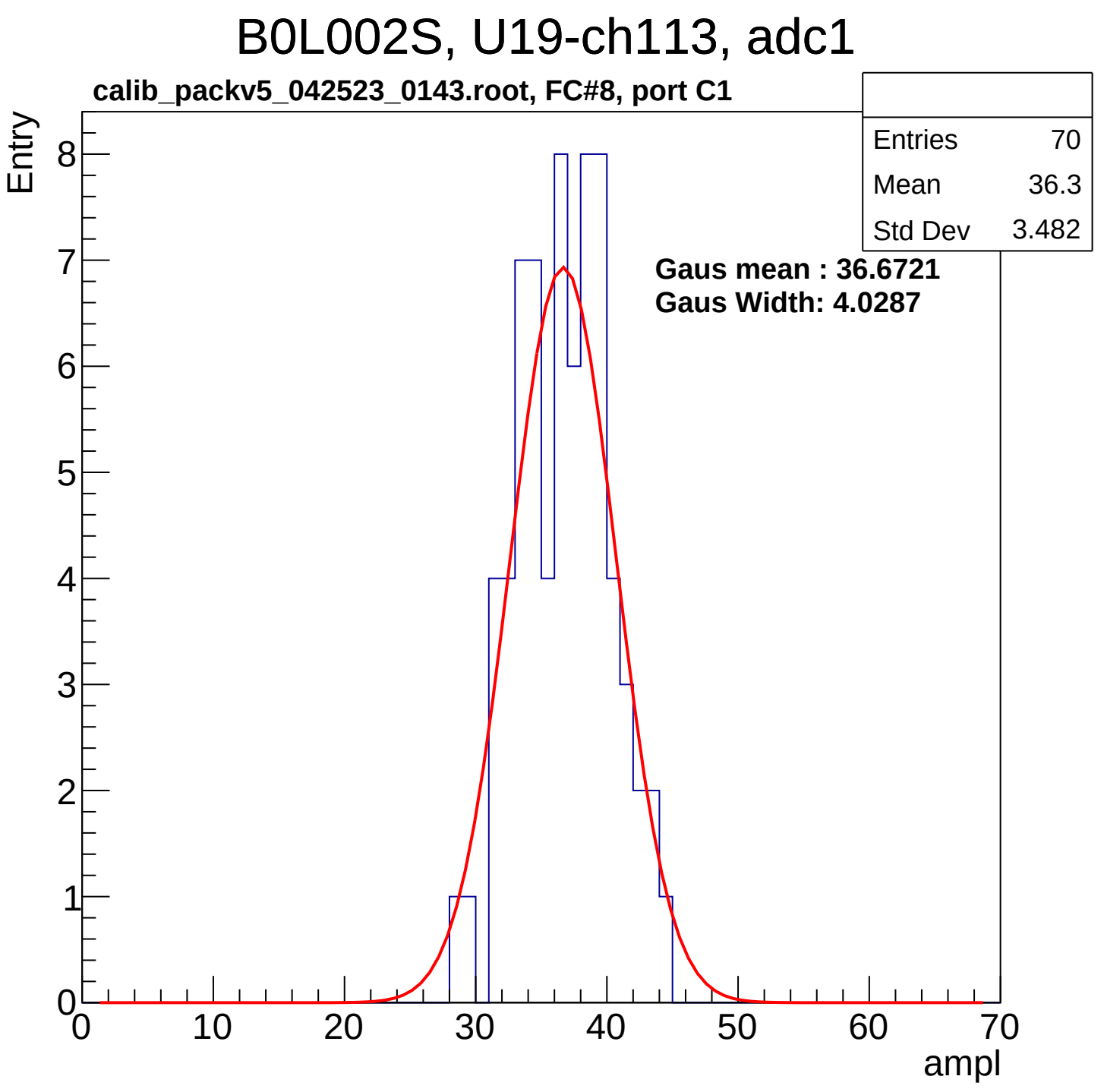

B0L002S, U19-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 55.95
Std Dev 3.44
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

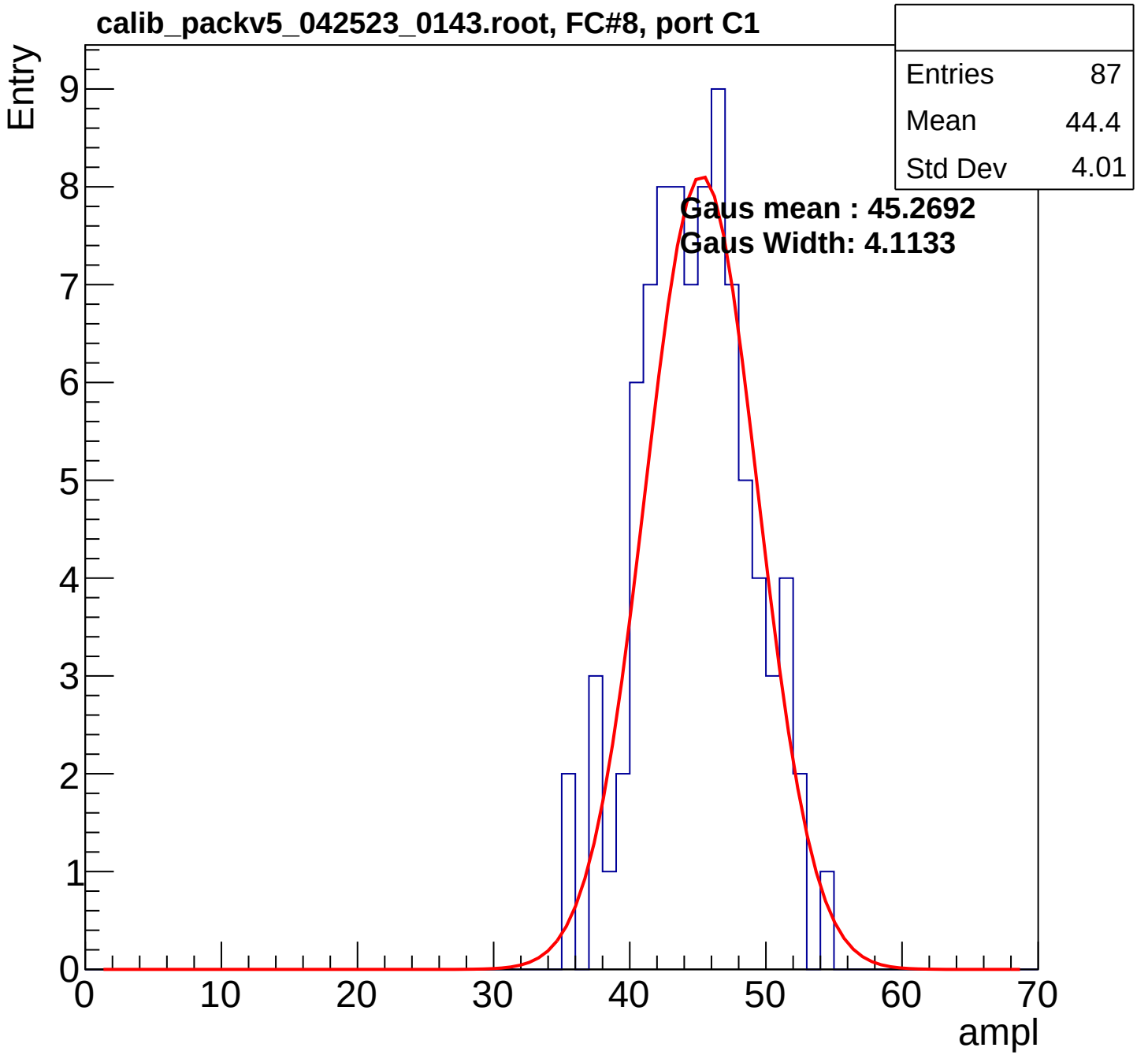

B0L002S, U19-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58
Std Dev 9.605
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

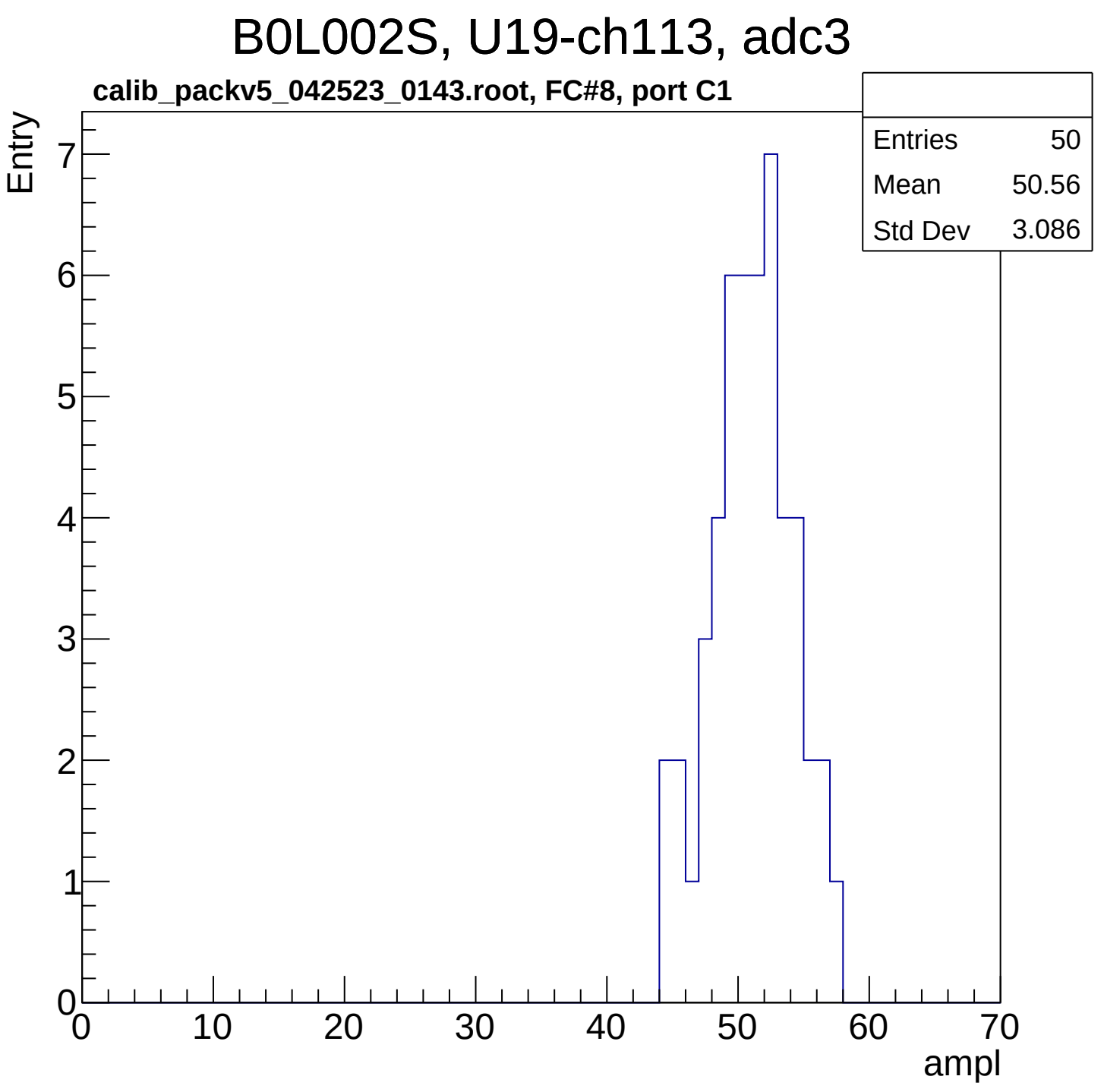

B0L002S, U19-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 62.1
Std Dev 1.136
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

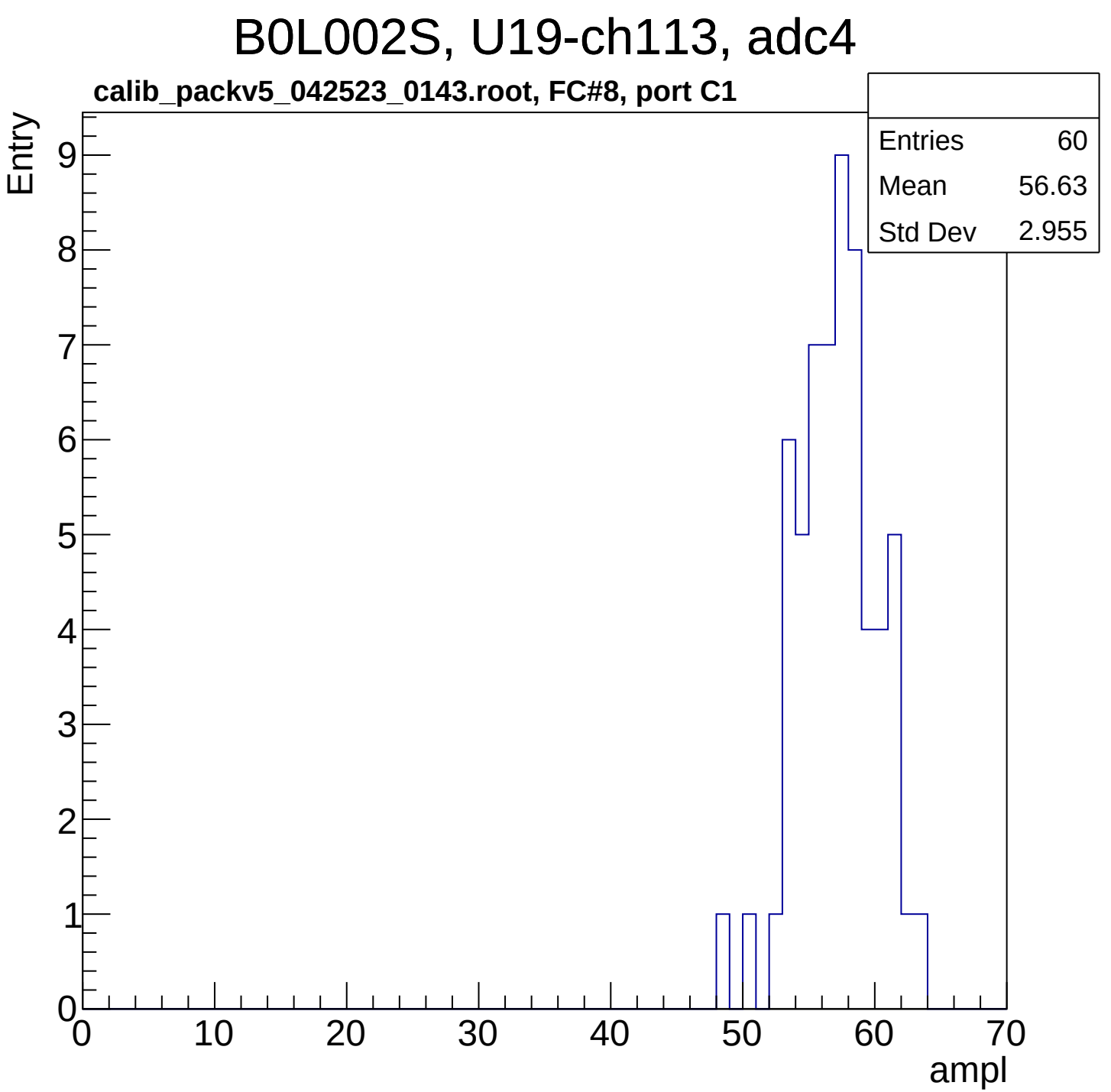

B0L002S, U19-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

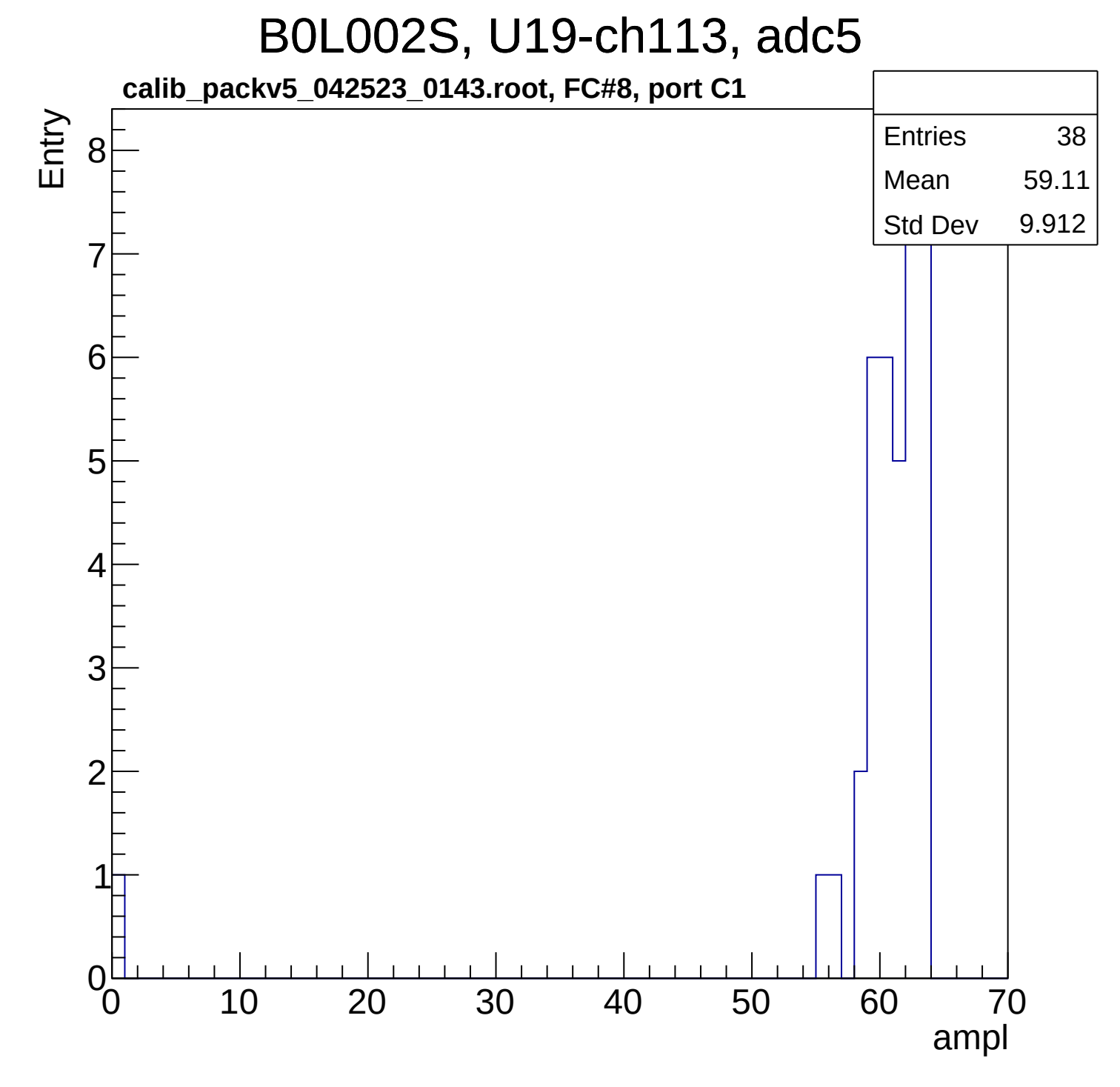

B0L002S, U19-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.1
Std Dev 3.357
Gaus mean : 30.2909
Gaus Width: 3.7305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

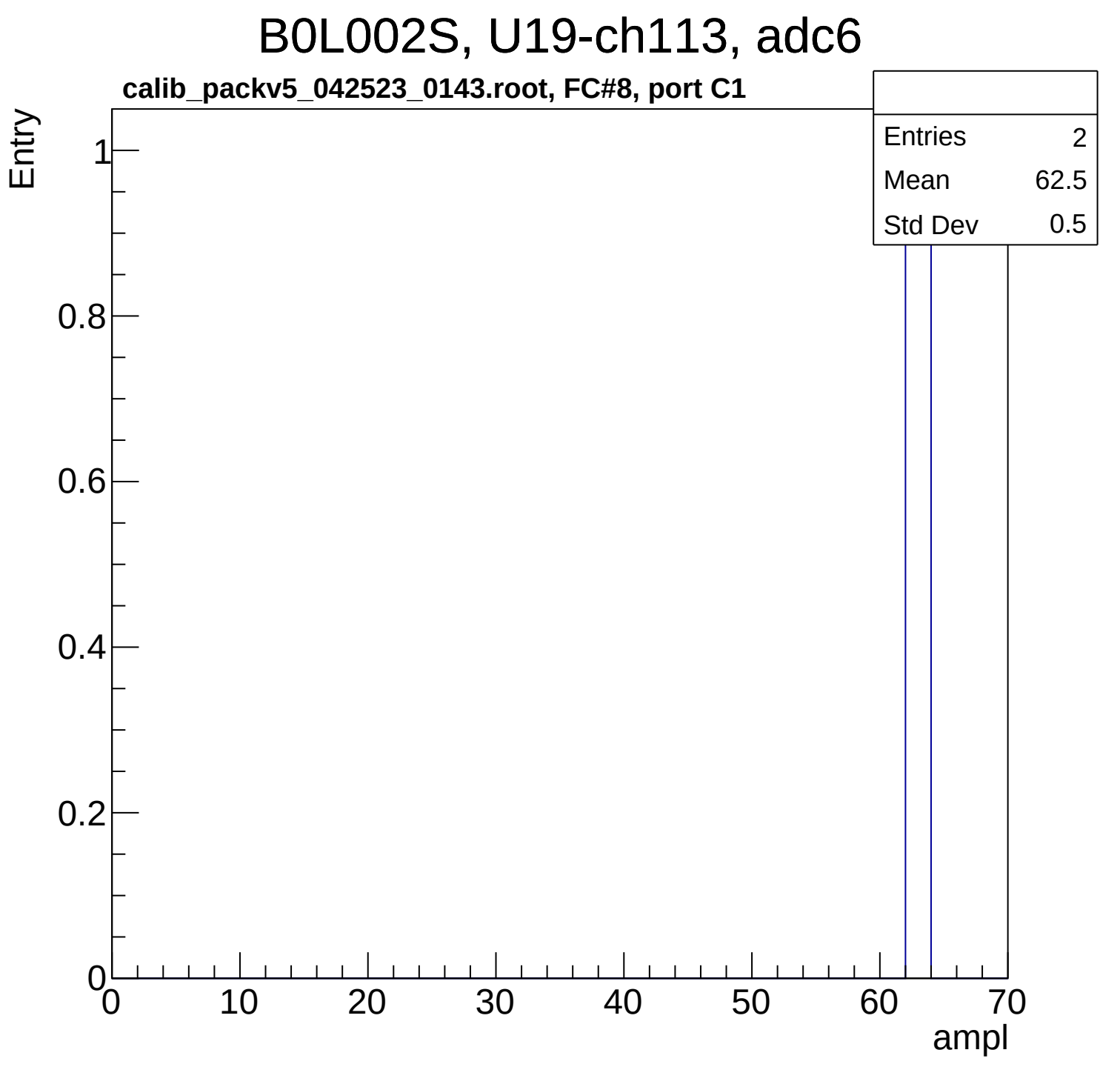

B0L002S, U19-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.3
Std Dev 3.482
Gaus mean : 36.6721
Gaus Width: 4.0287
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

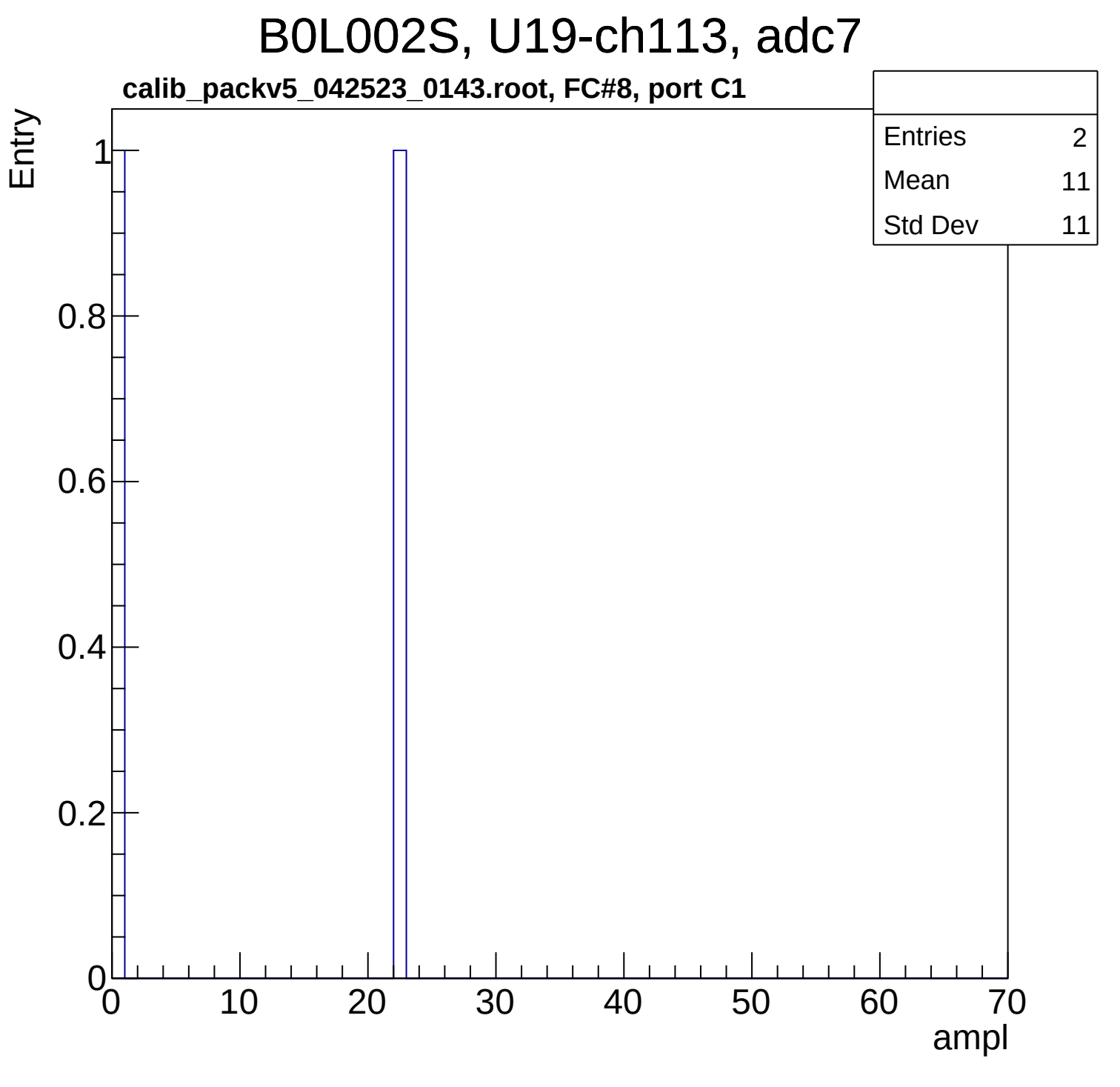

B0L002S, U19-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 44.4
Std Dev 4.01
Gaus mean : 45.2692
Gaus Width: 4.1133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

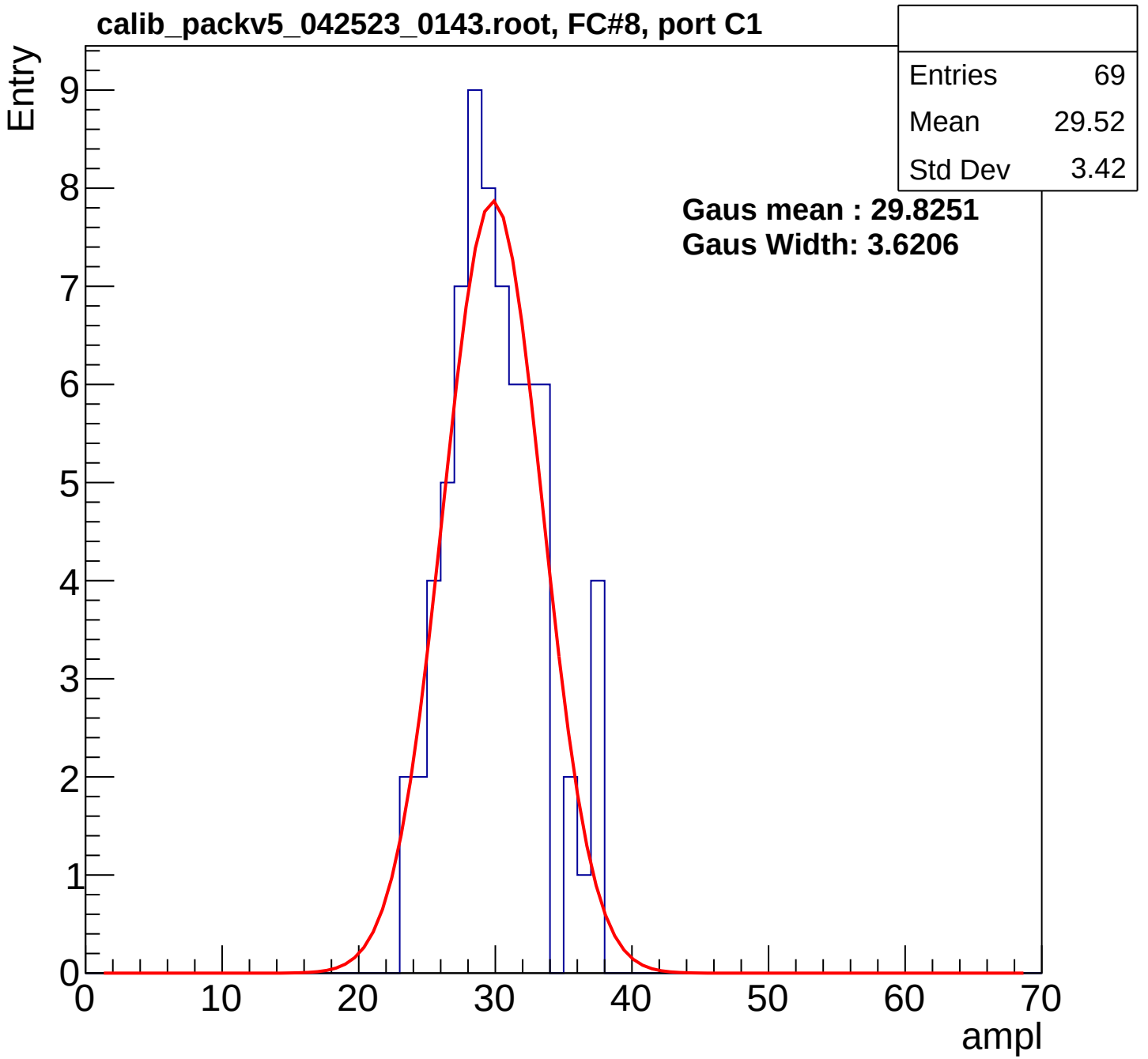

B0L002S, U19-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 50.56
Std Dev 3.086
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

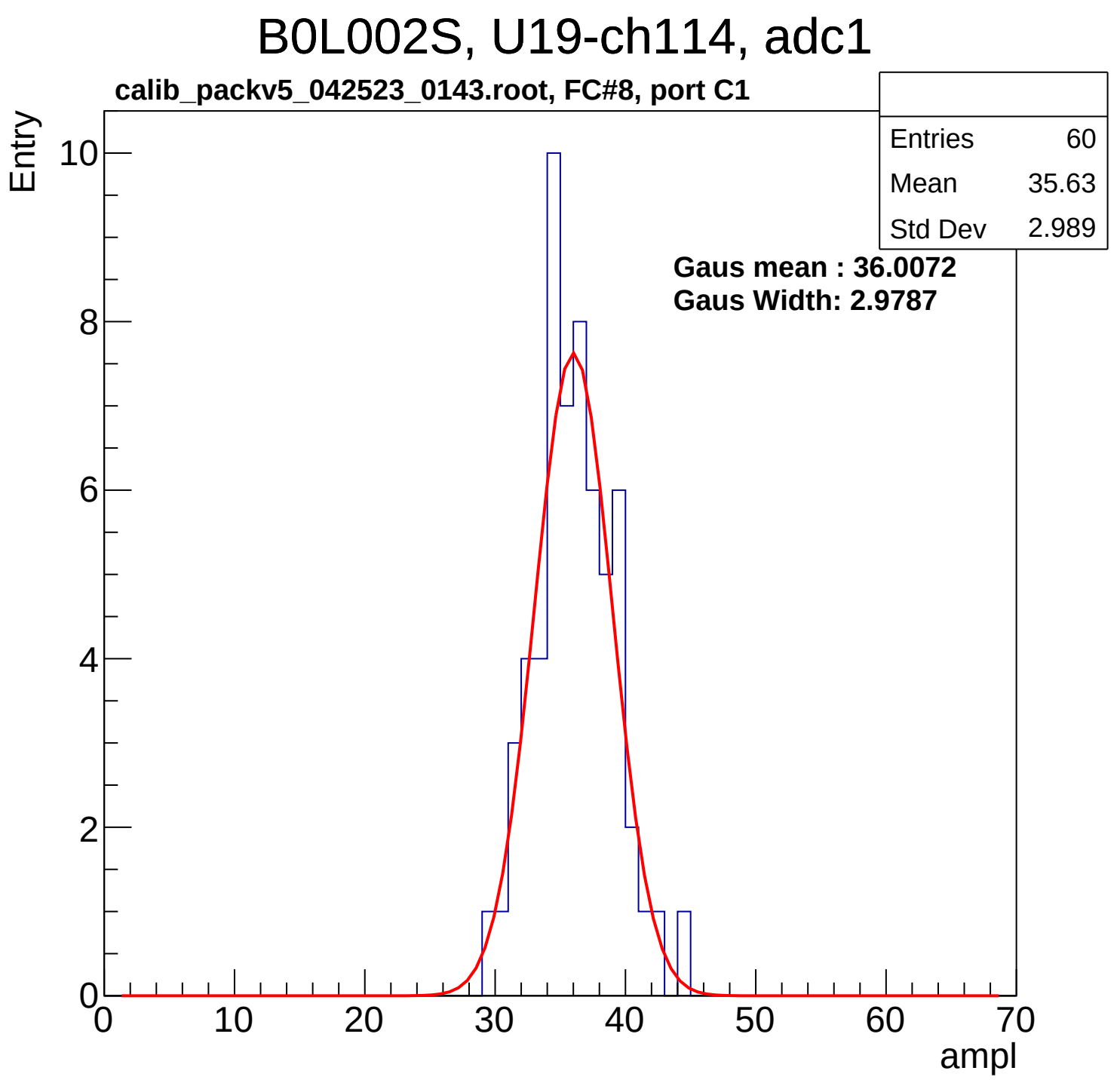

B0L002S, U19-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.63
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

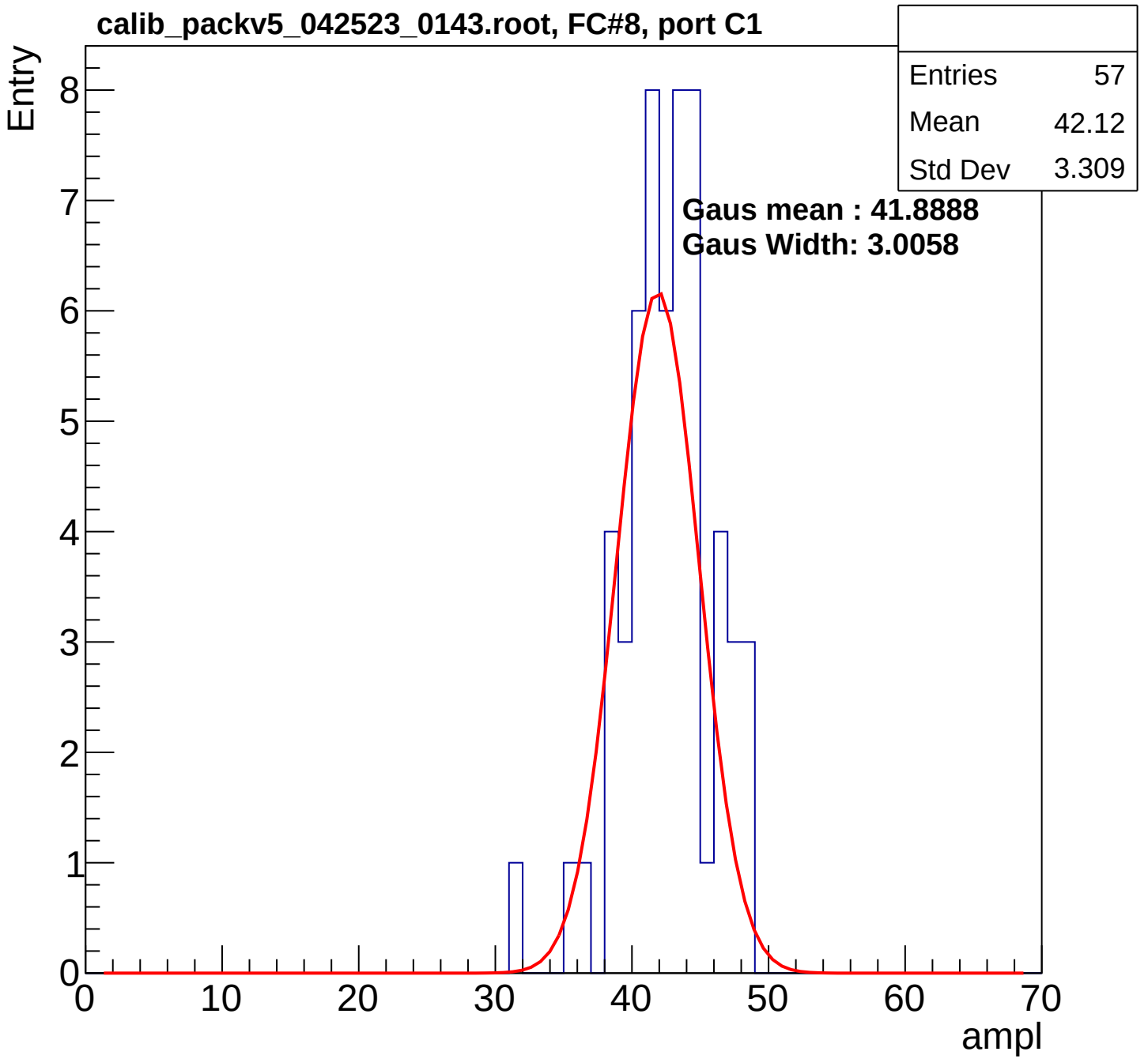

B0L002S, U19-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.11
Std Dev 9.912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

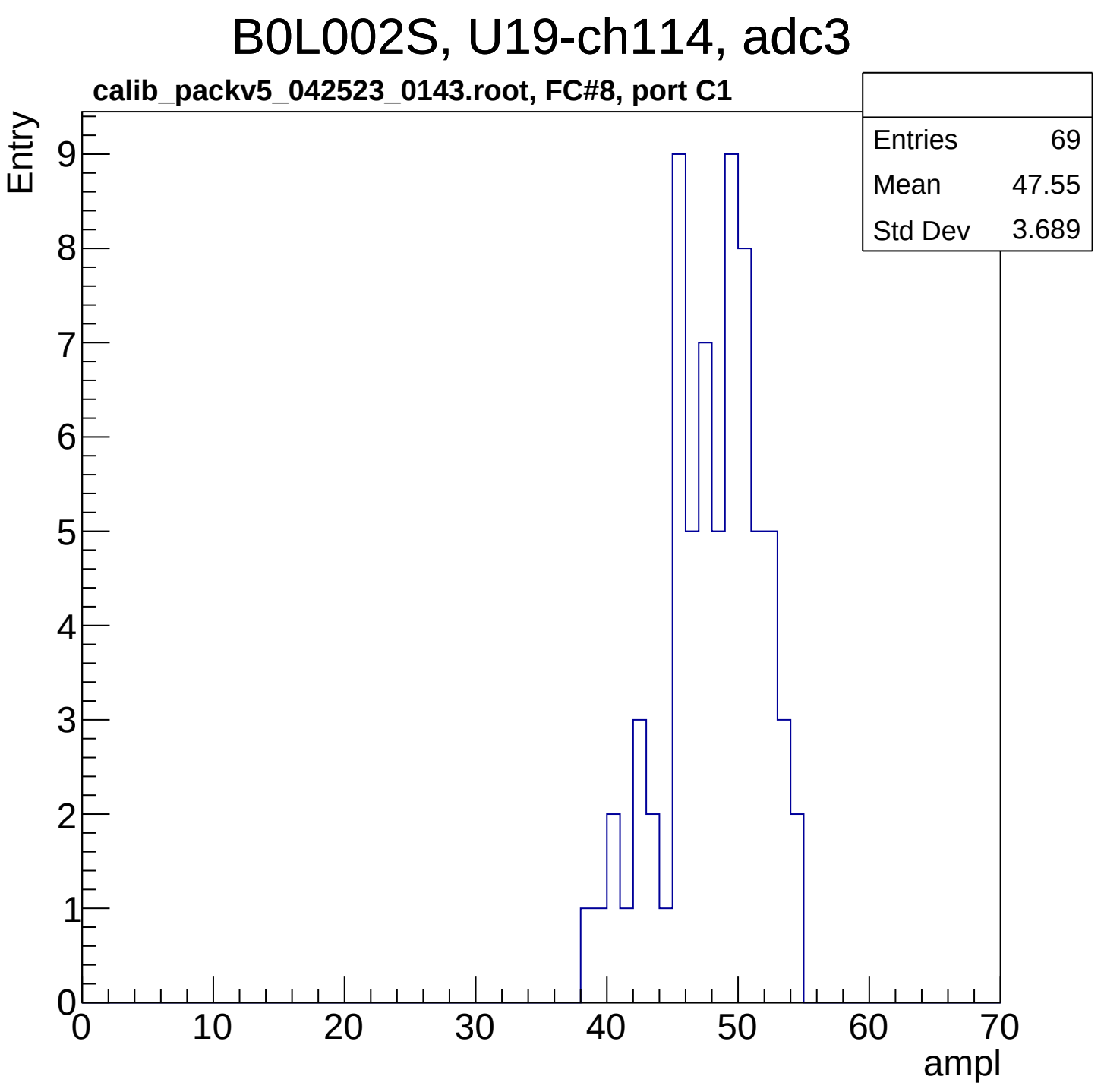

B0L002S, U19-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

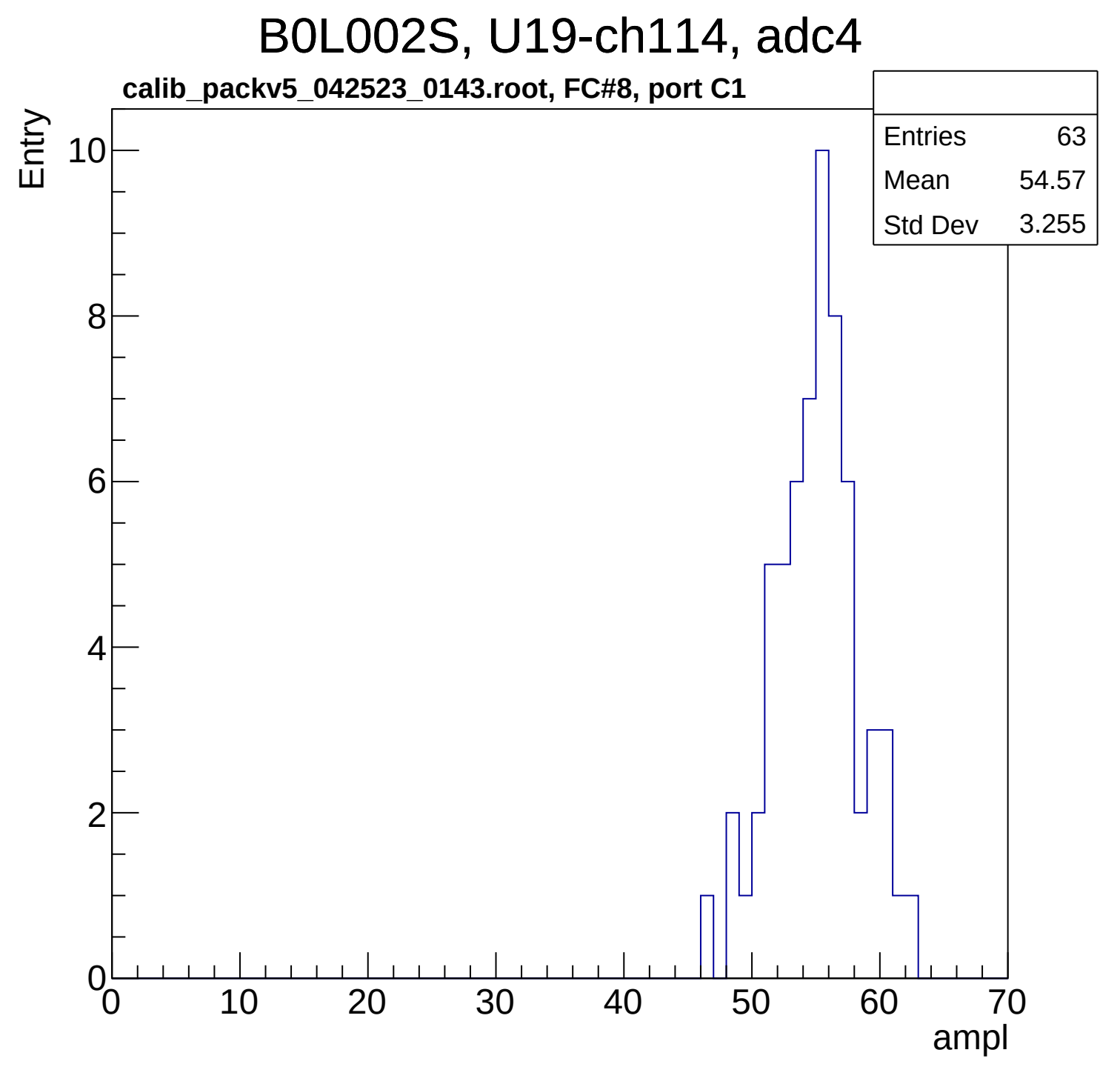

B0L002S, U19-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

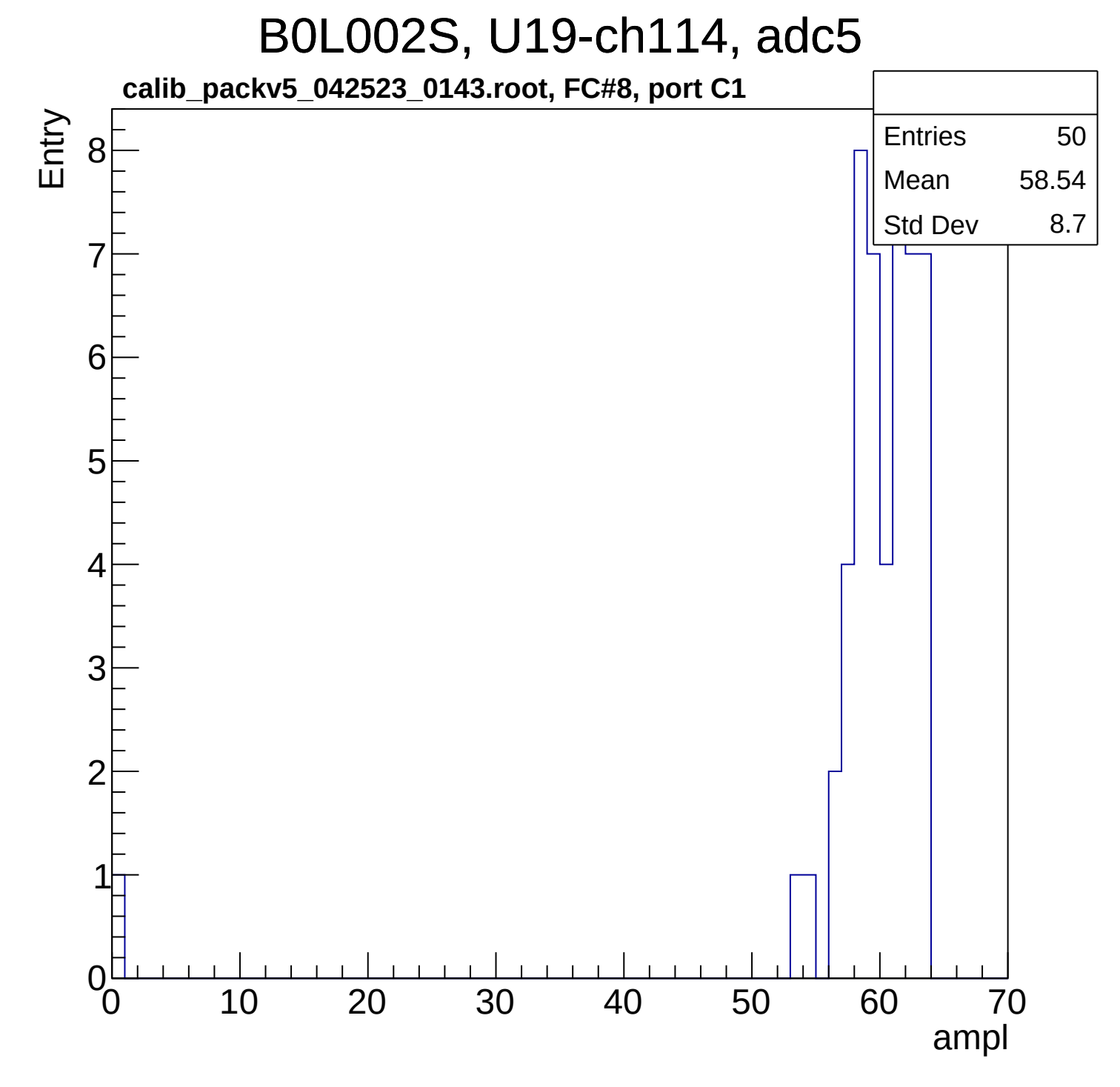

B0L002S, U19-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.52
Std Dev 3.42
Gaus mean : 29.8251
Gaus Width: 3.6206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

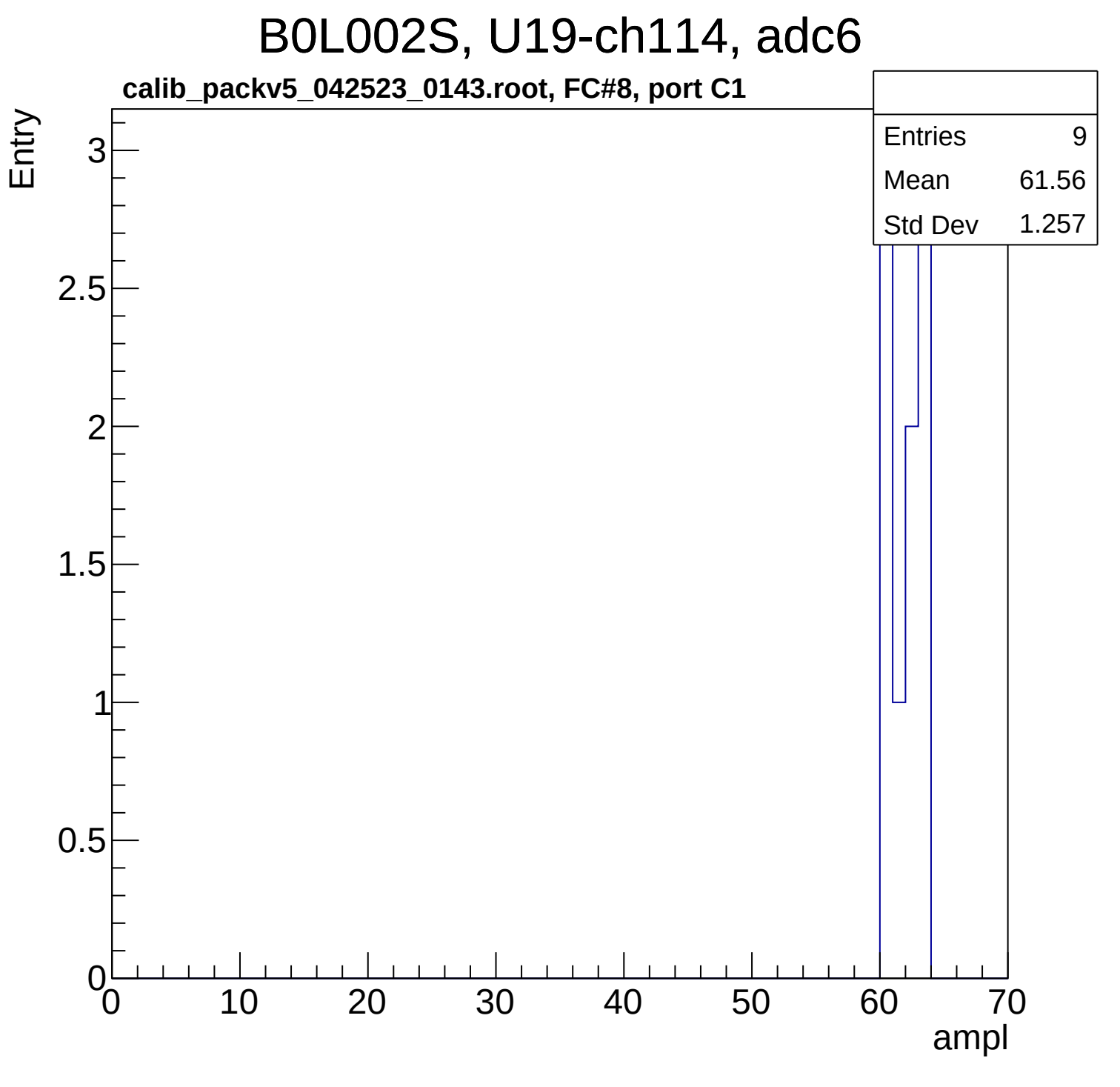

B0L002S, U19-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 35.63
Std Dev 2.989
Gaus mean : 36.0072
Gaus Width: 2.9787
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

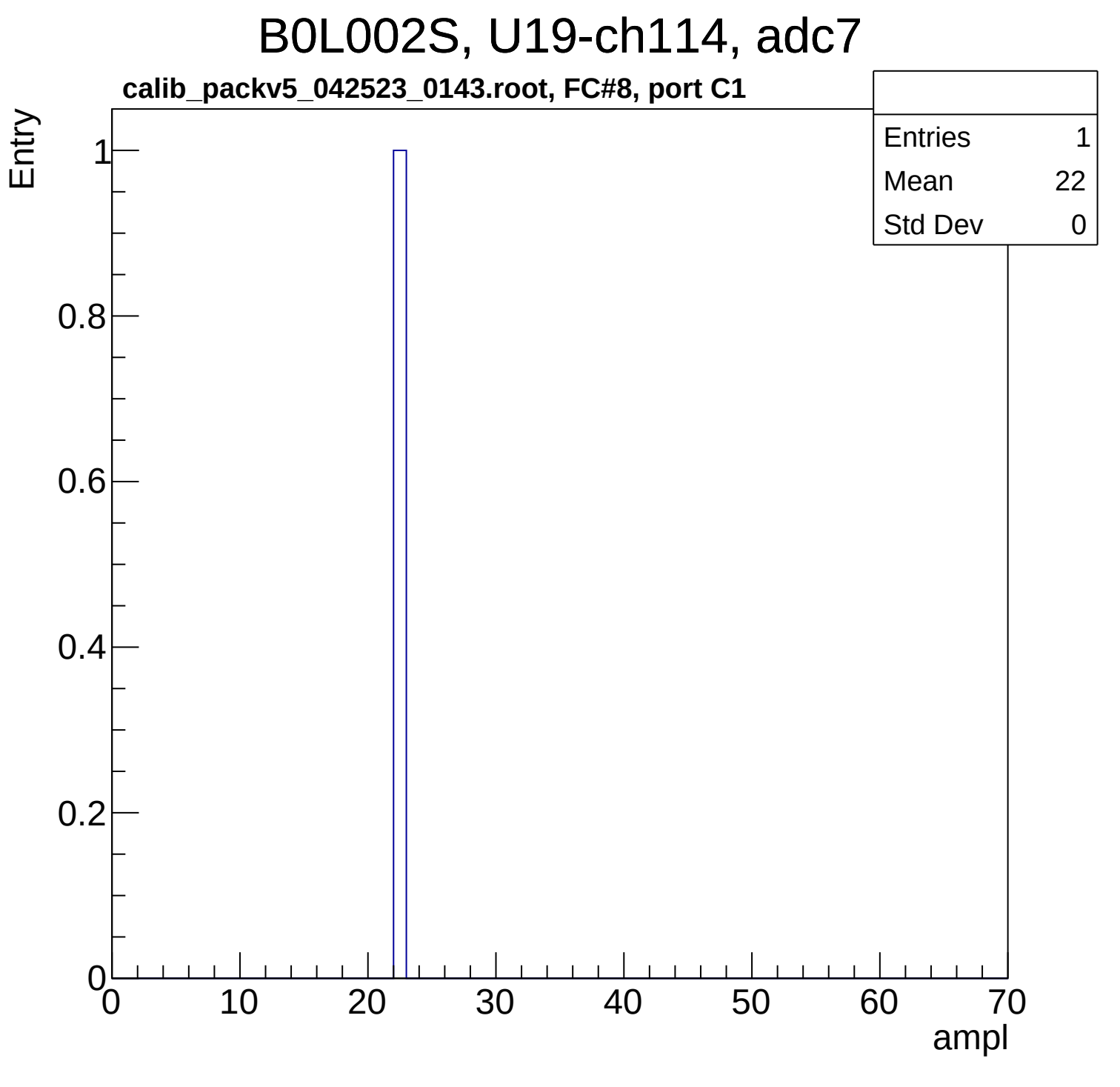

B0L002S, U19-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 42.12
Std Dev 3.309
Gaus mean : 41.8888
Gaus Width: 3.0058
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

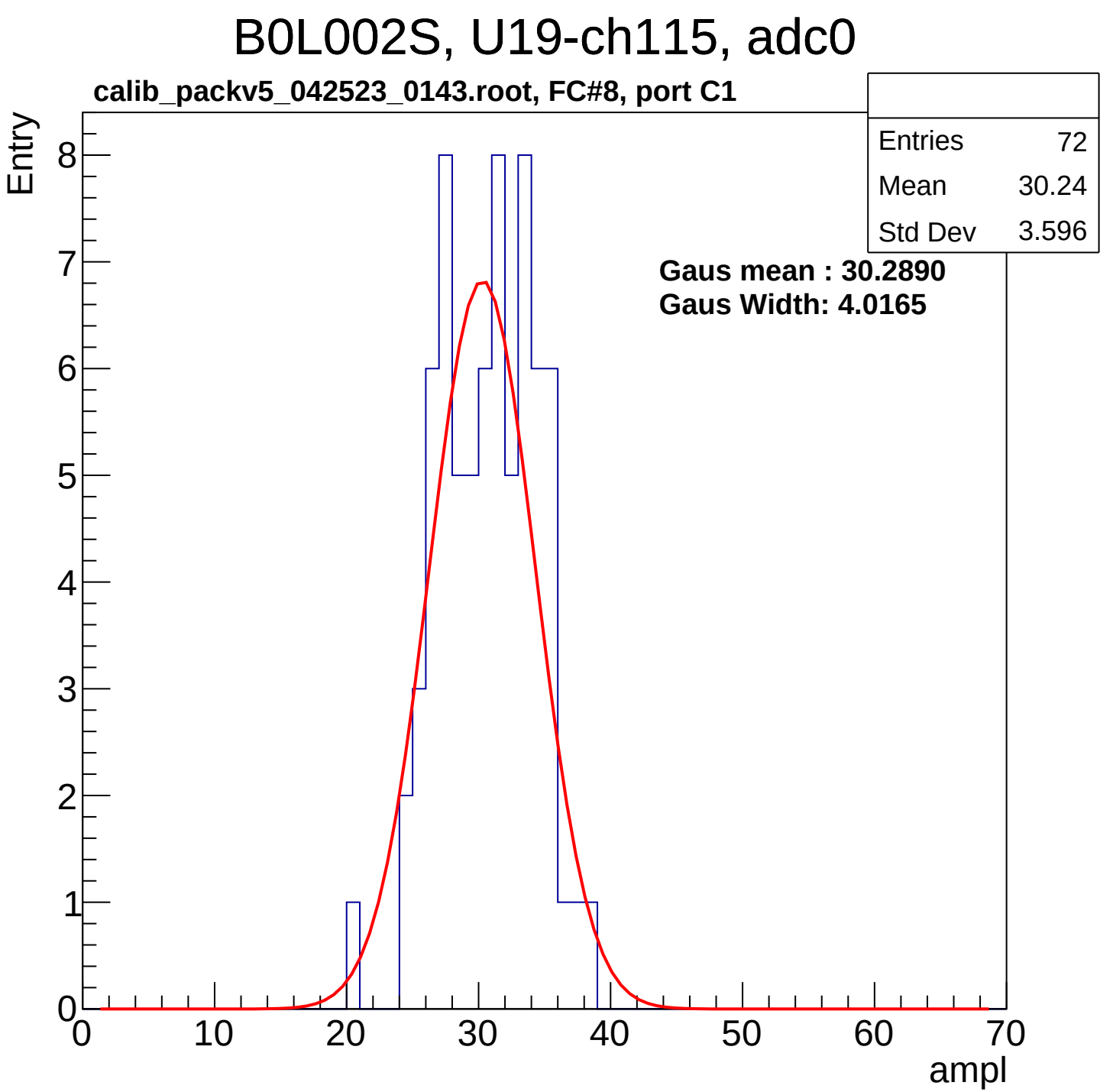

B0L002S, U19-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 47.55
Std Dev 3.689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

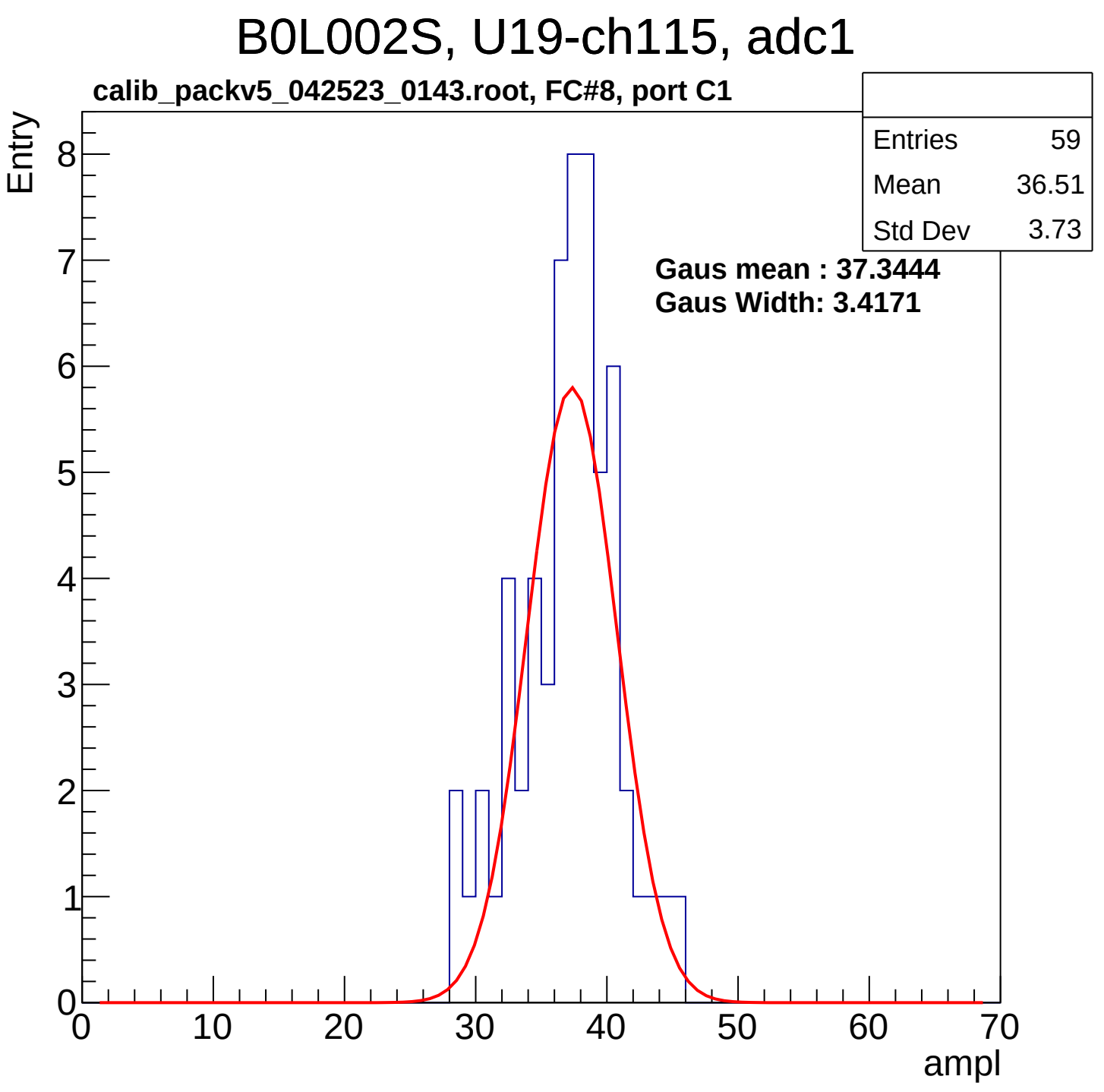

B0L002S, U19-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 54.57
Std Dev 3.255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

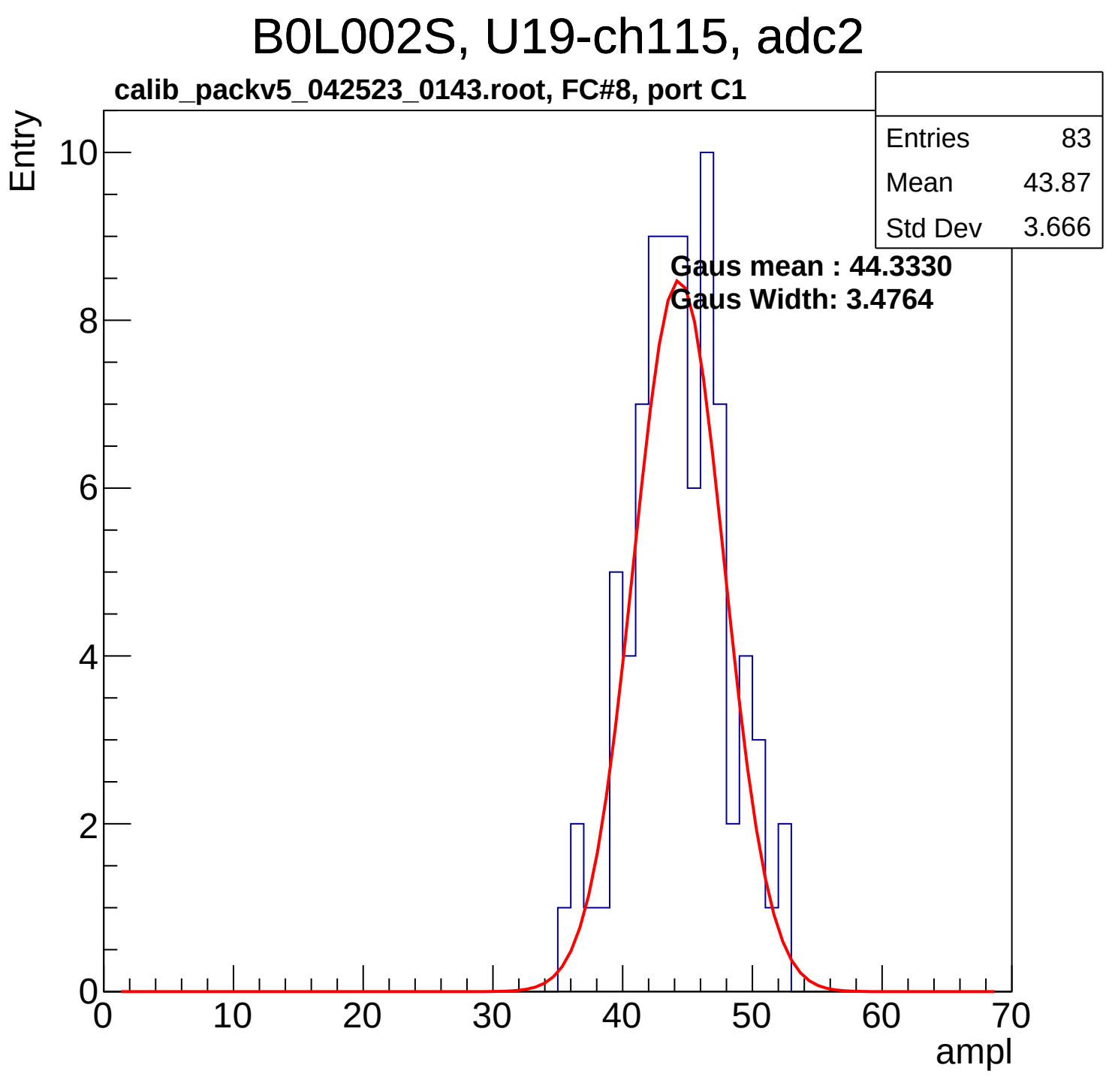

B0L002S, U19-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.54
Std Dev 8.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

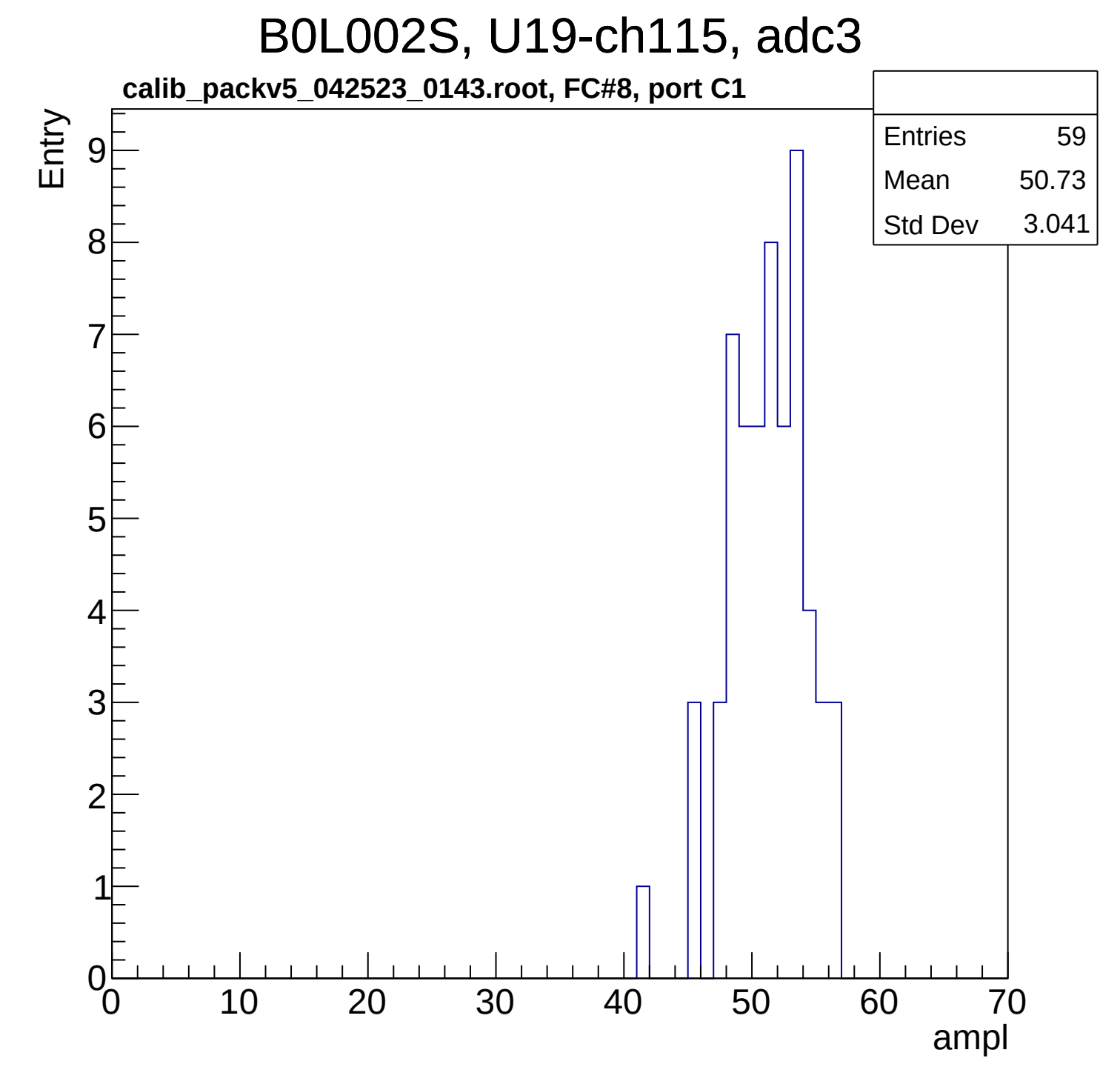

B0L002S, U19-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.56
Std Dev 1.257
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

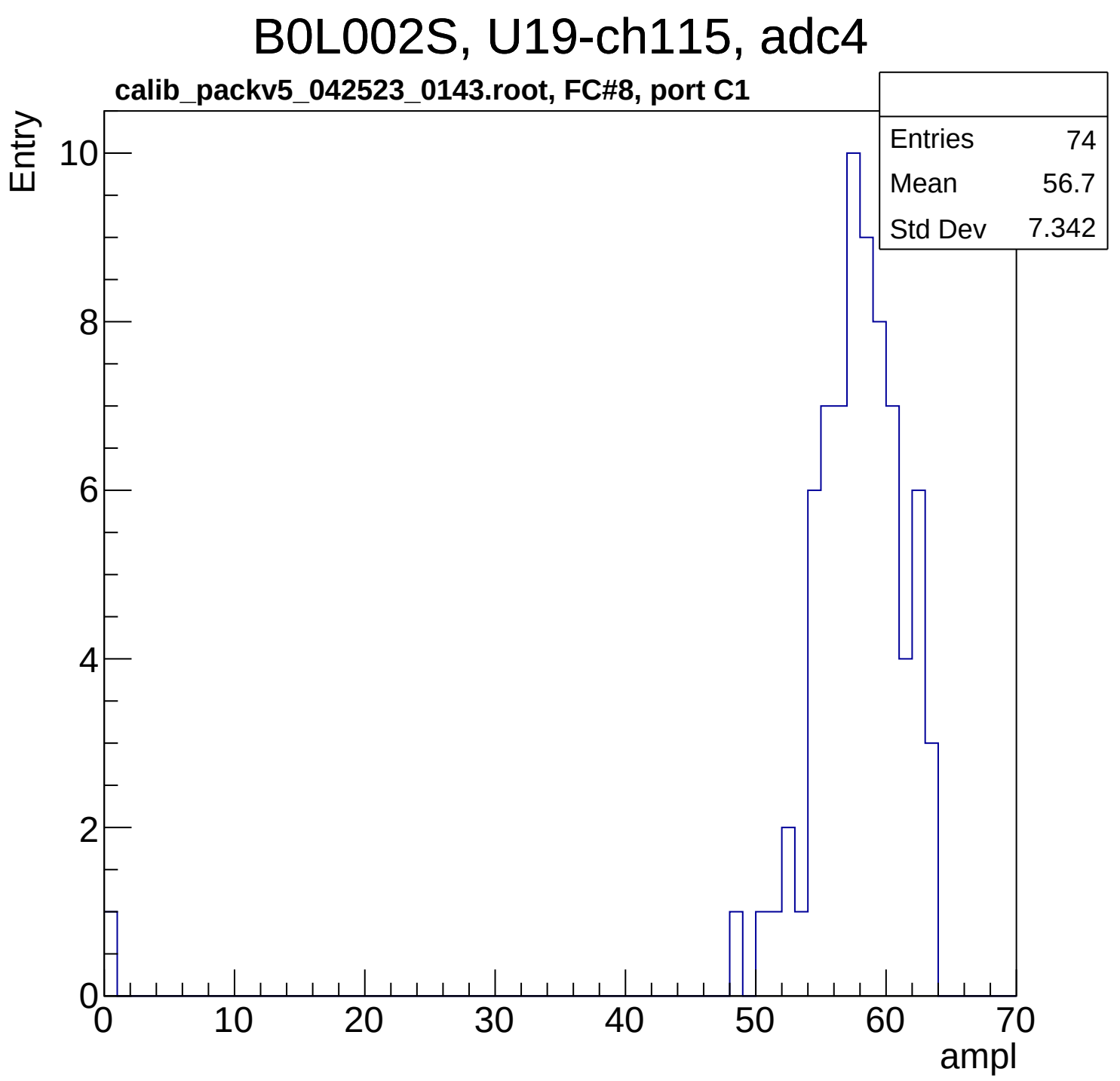

B0L002S, U19-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

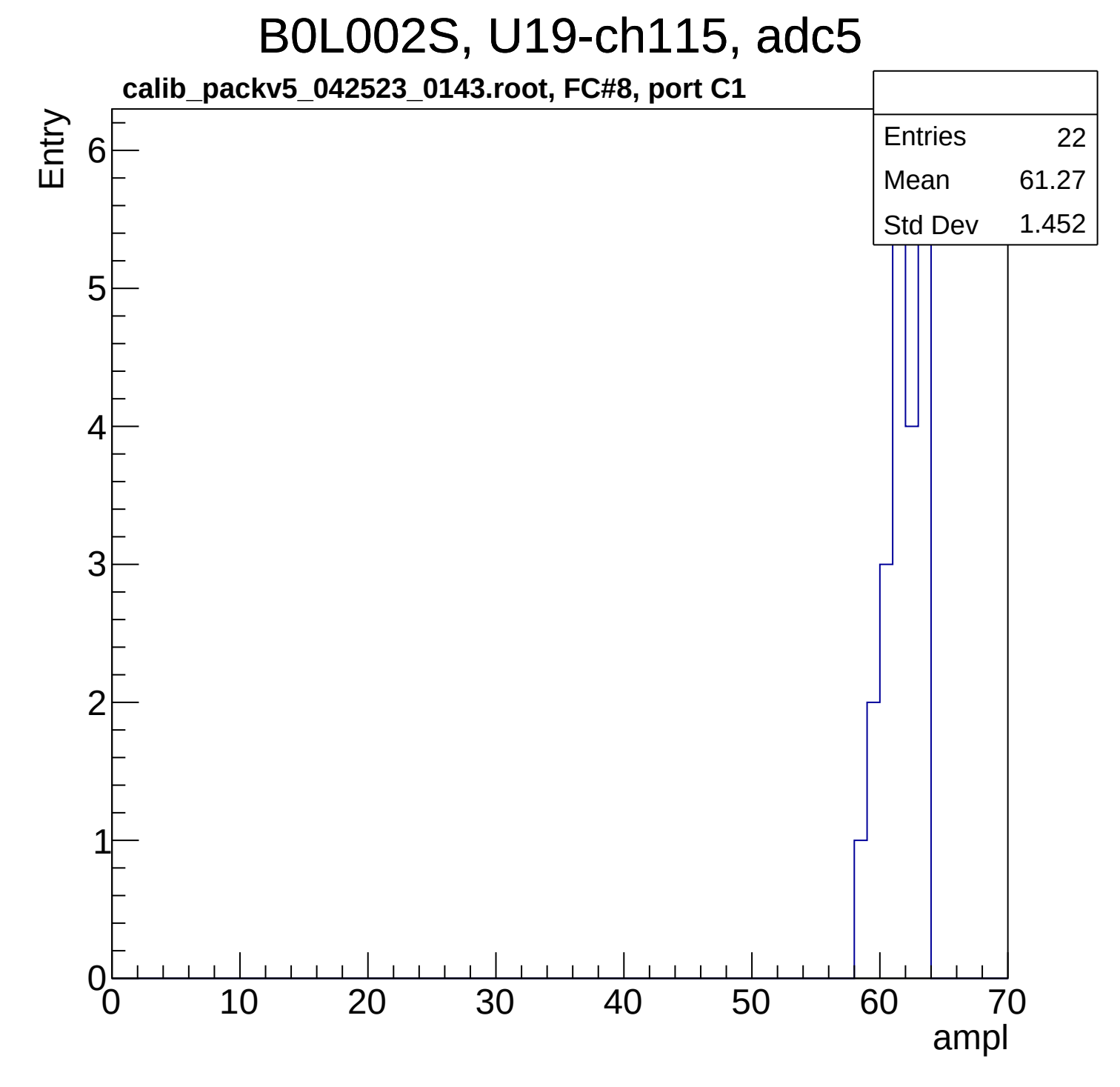

B0L002S, U19-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.24
Std Dev 3.596
Gaus mean : 30.2890
Gaus Width: 4.0165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

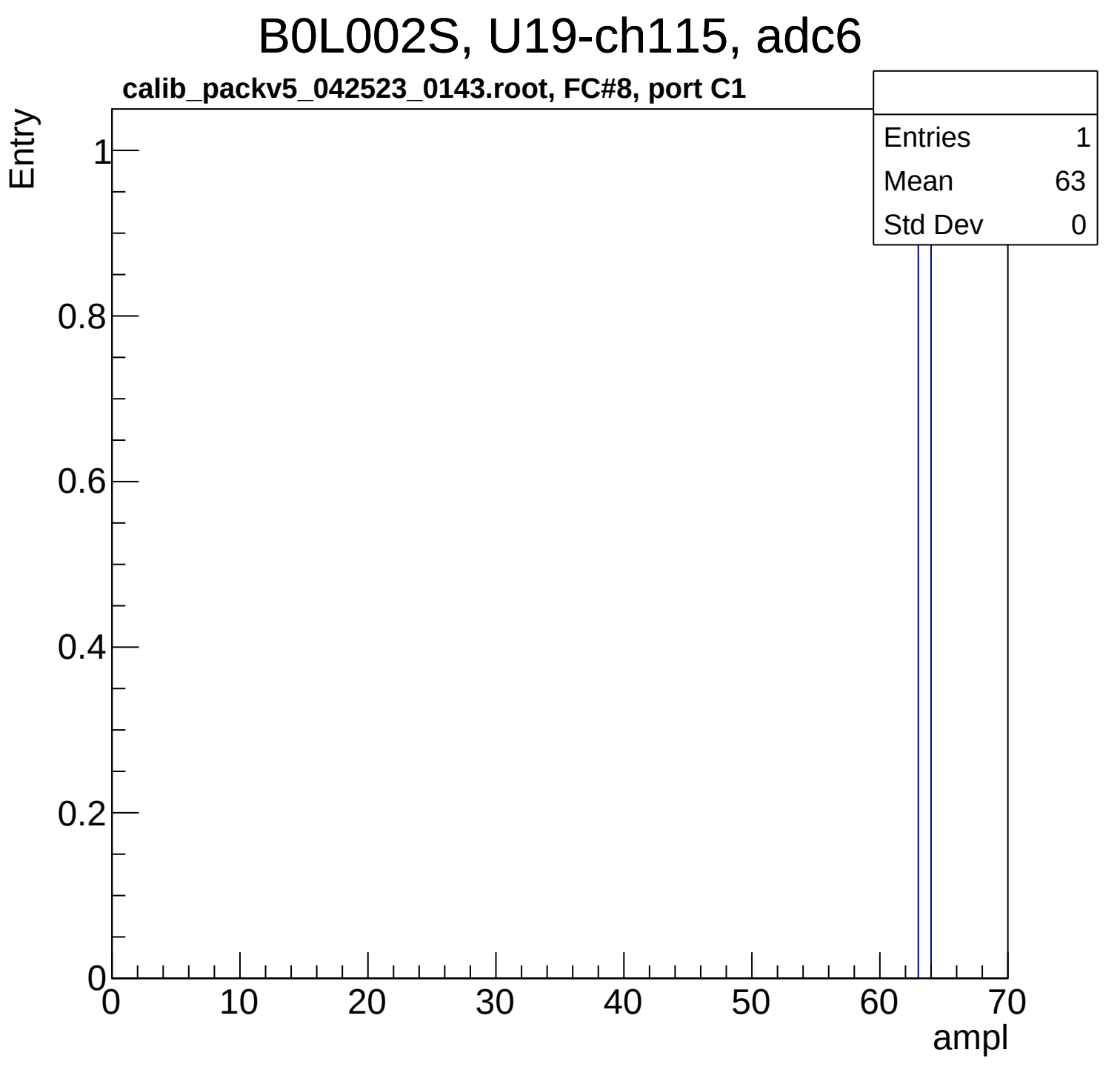

B0L002S, U19-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 36.51
Std Dev 3.73
Gaus mean : 37.3444
Gaus Width: 3.4171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

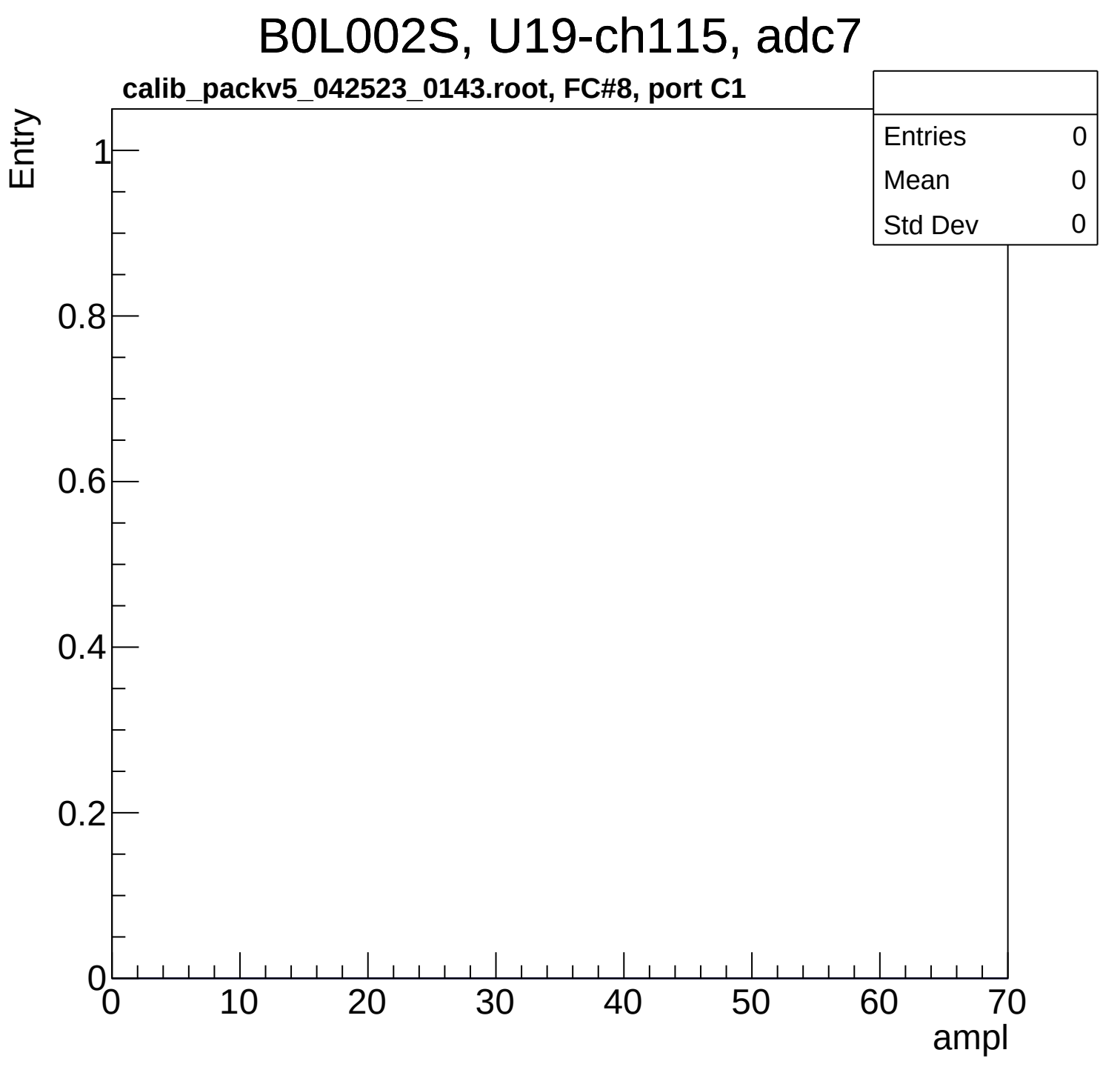

B0L002S, U19-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 43.87
Std Dev 3.666
Gaus mean : 44.3330
Gaus Width: 3.4764
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

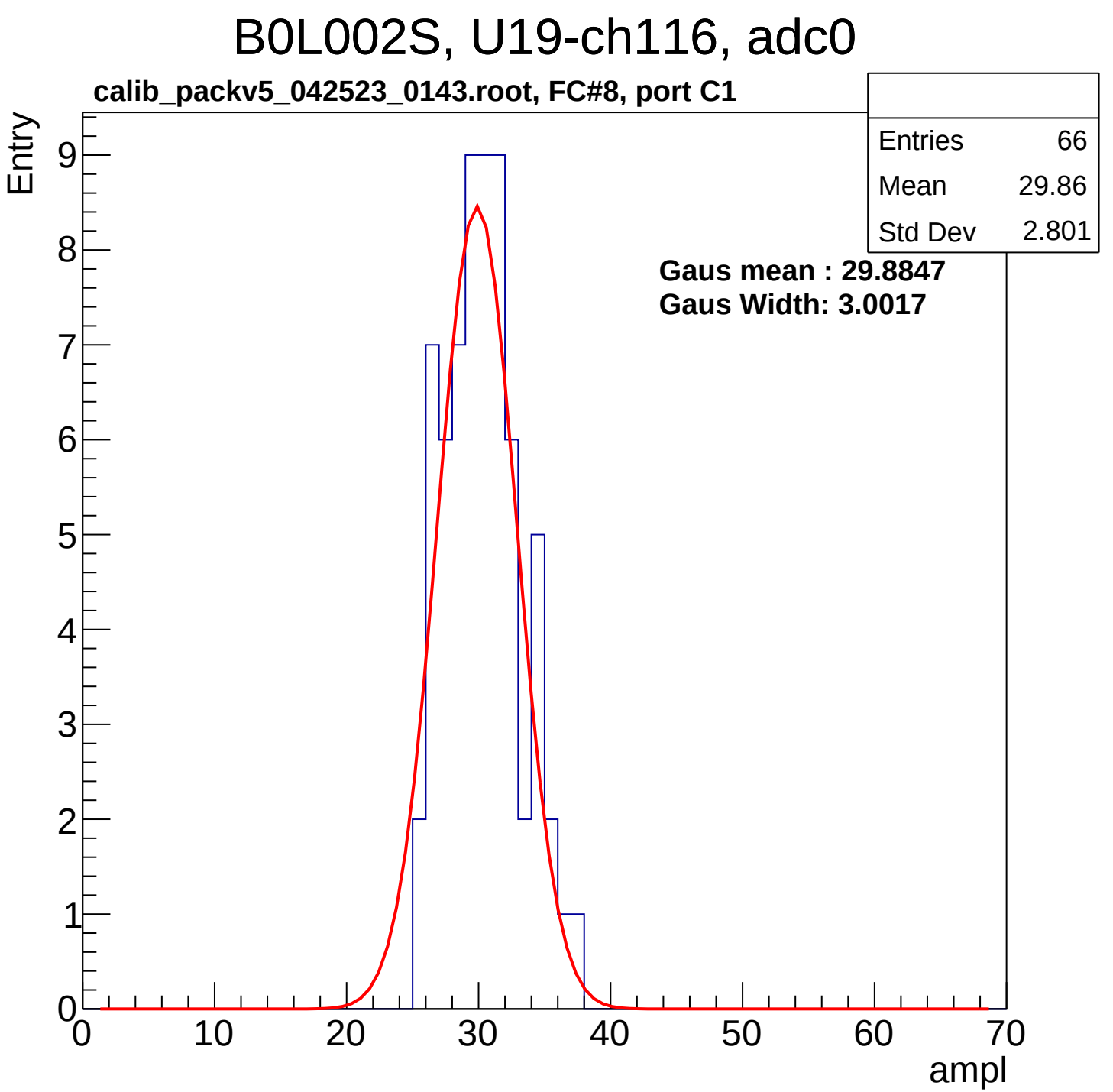

B0L002S, U19-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.73
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

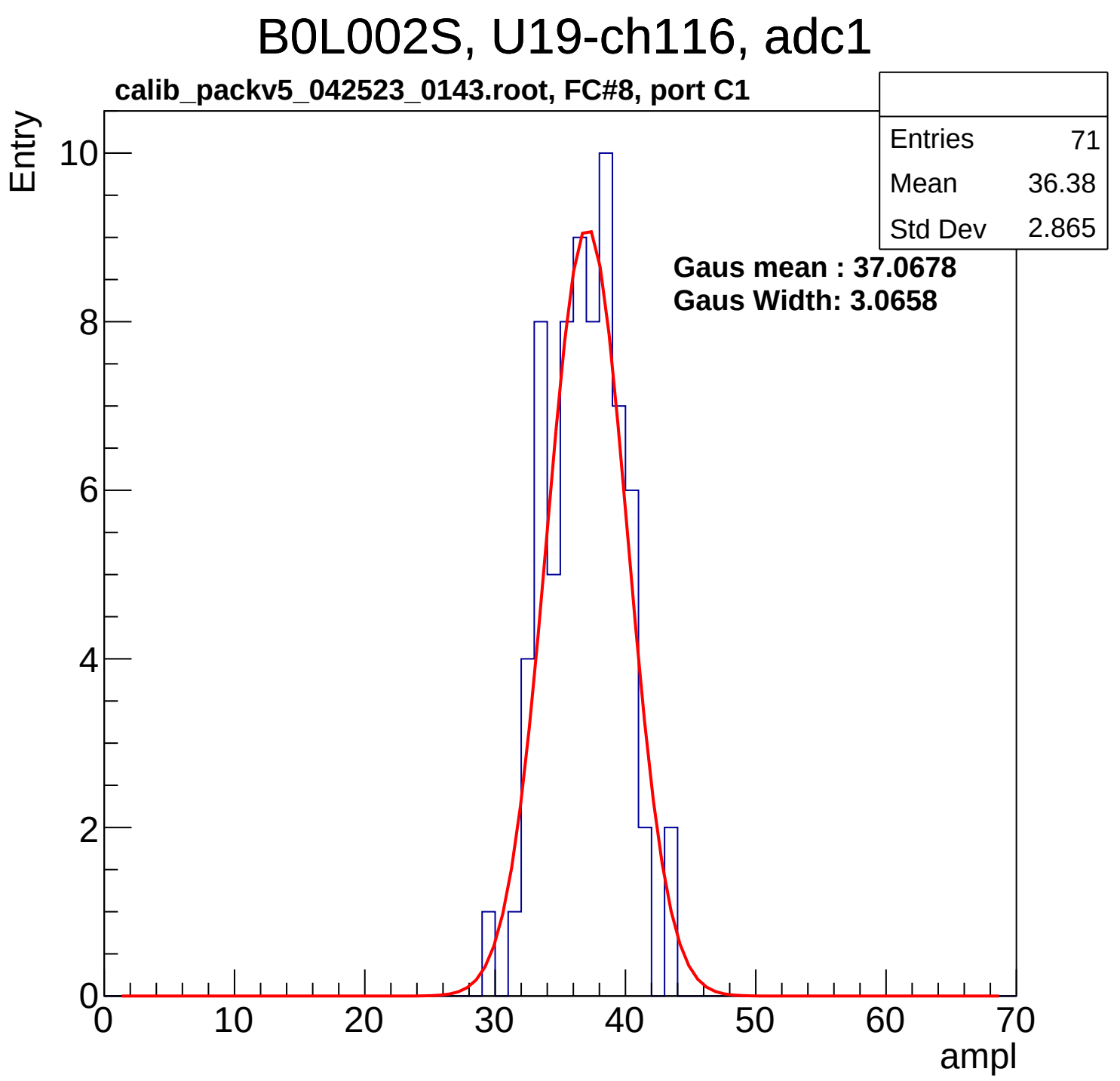

B0L002S, U19-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 56.7
Std Dev 7.342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

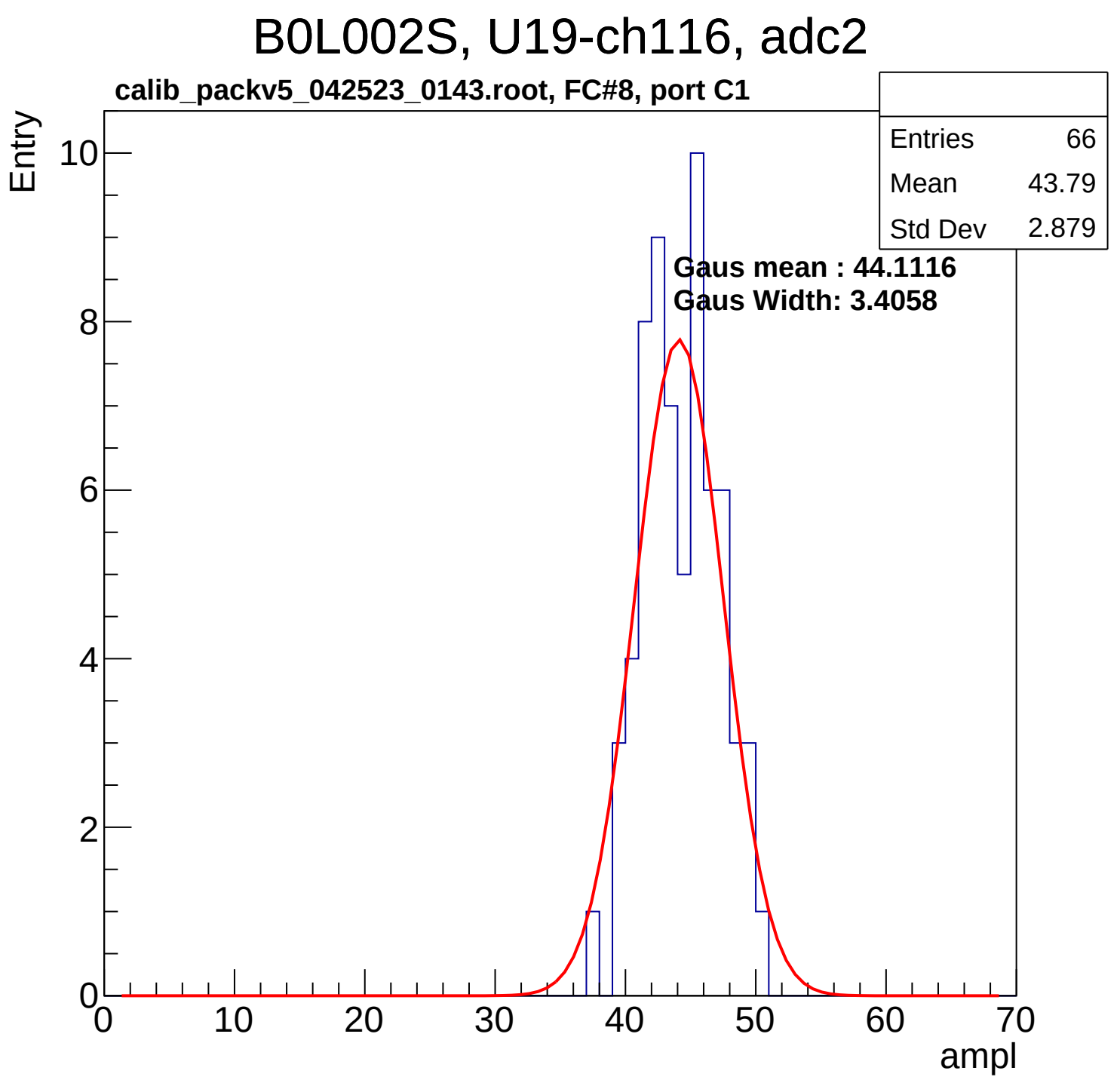

B0L002S, U19-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.27
Std Dev 1.452
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

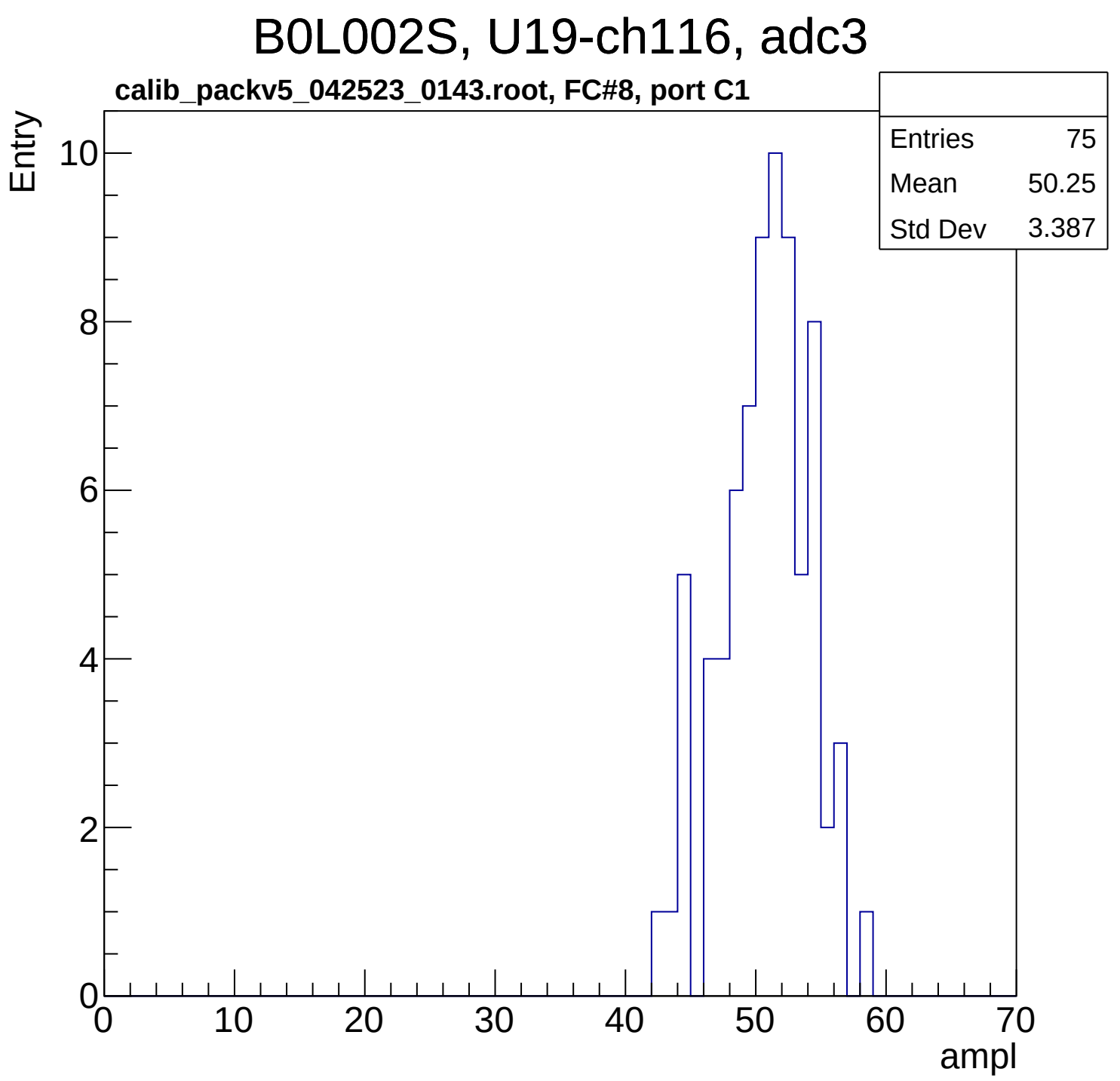

B0L002S, U19-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

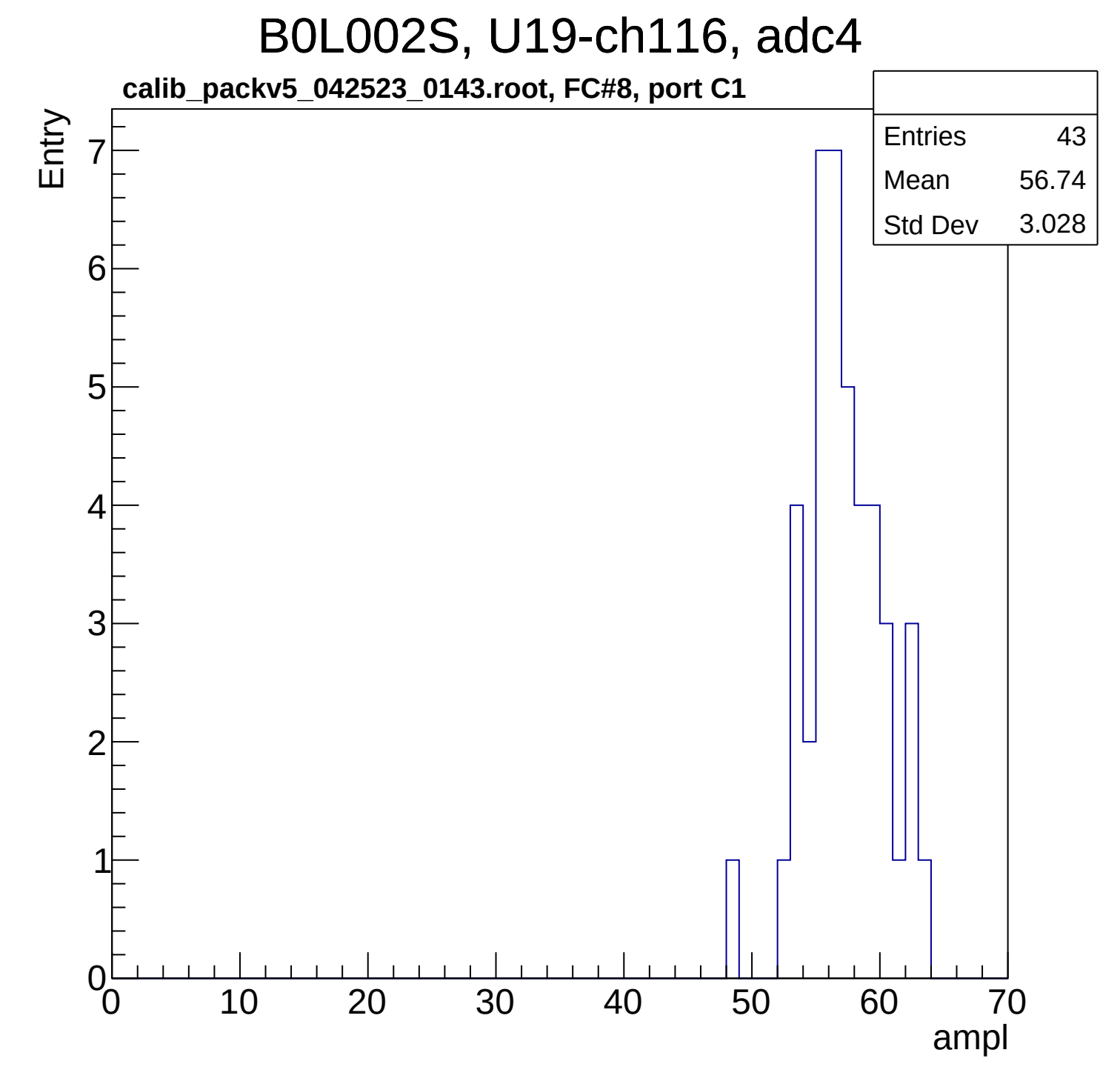

B0L002S, U19-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

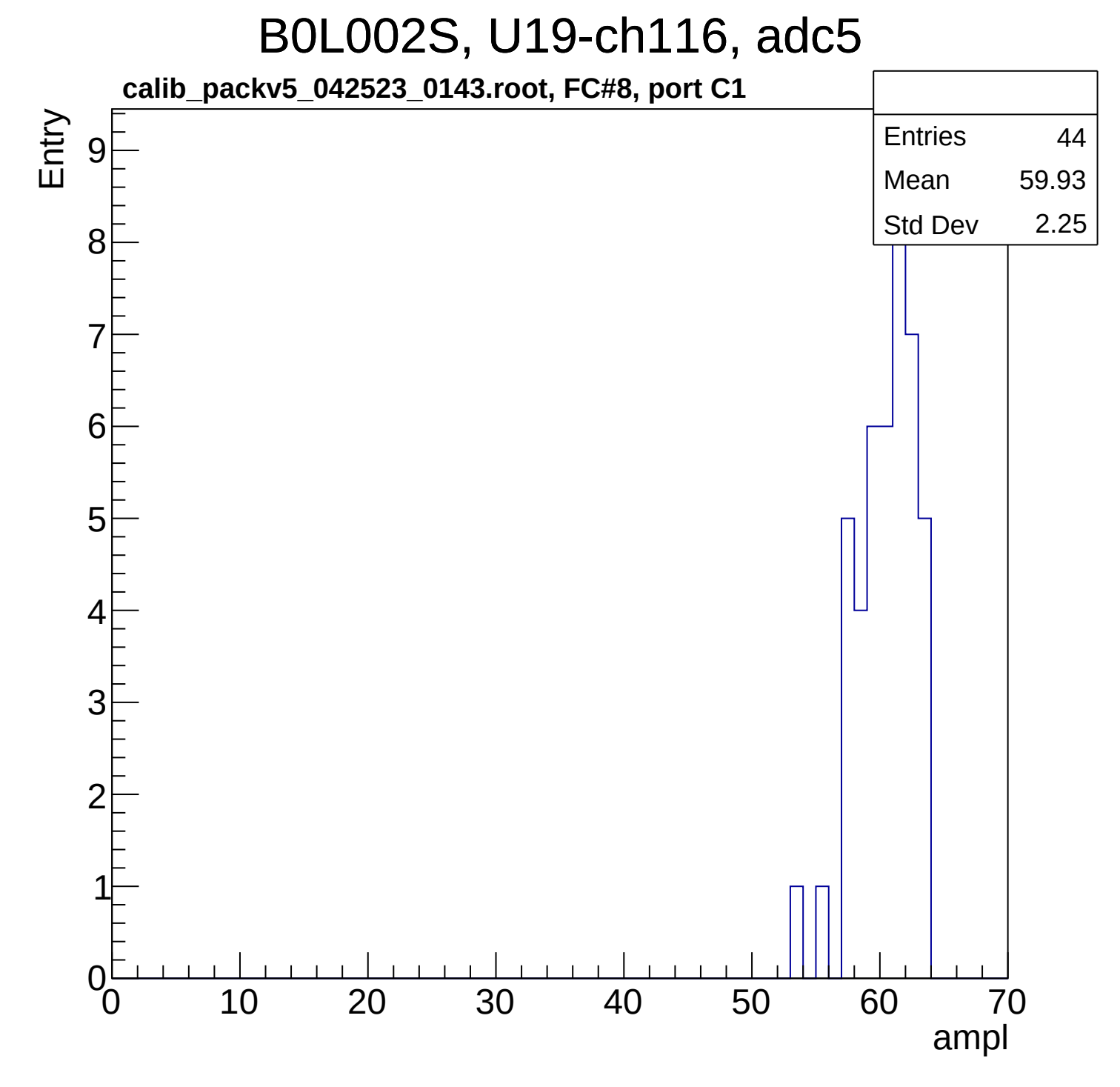

B0L002S, U19-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.86
Std Dev 2.801
Gaus mean : 29.8847
Gaus Width: 3.0017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

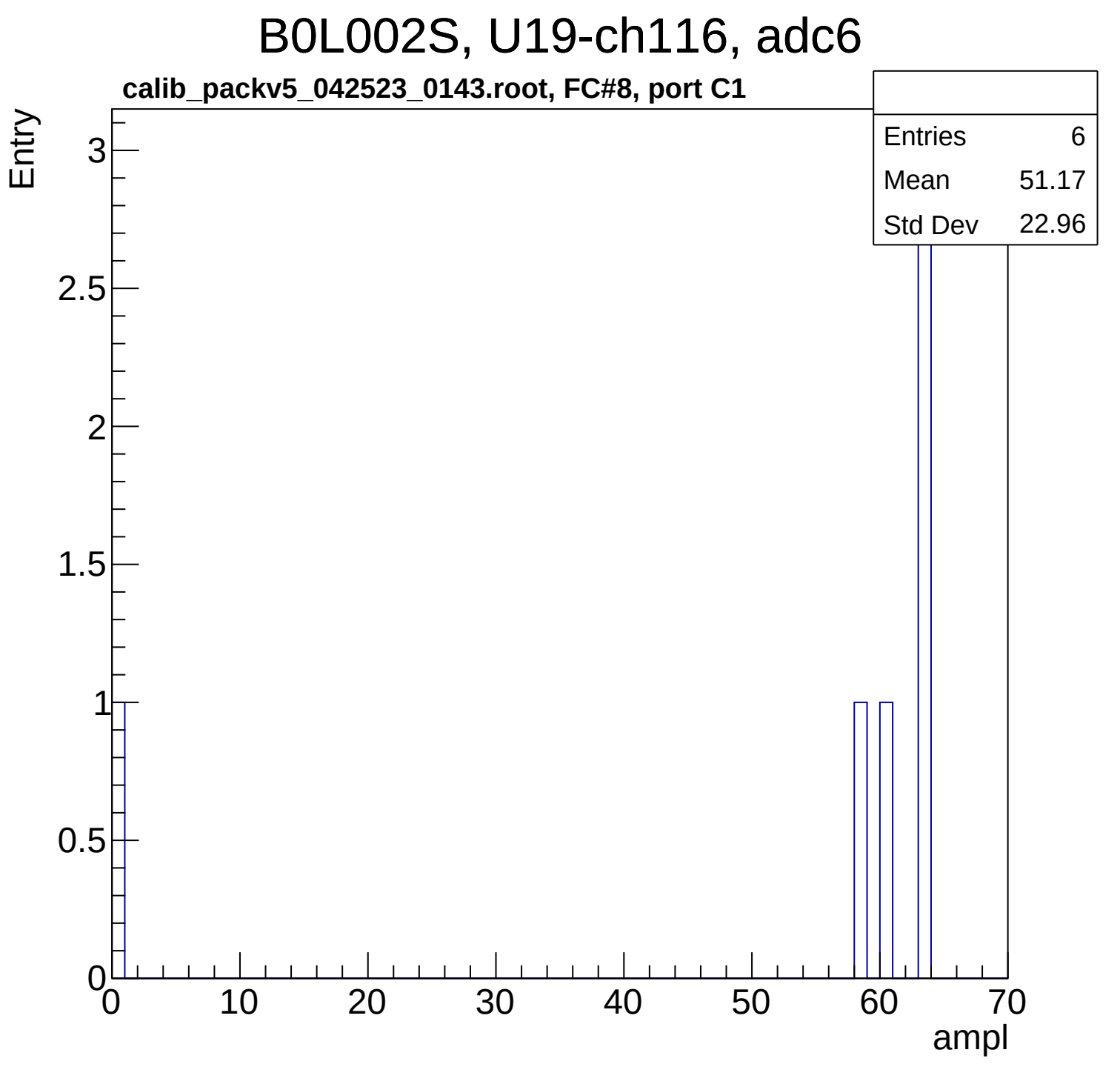

B0L002S, U19-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.38
Std Dev 2.865
Gaus mean : 37.0678
Gaus Width: 3.0658
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

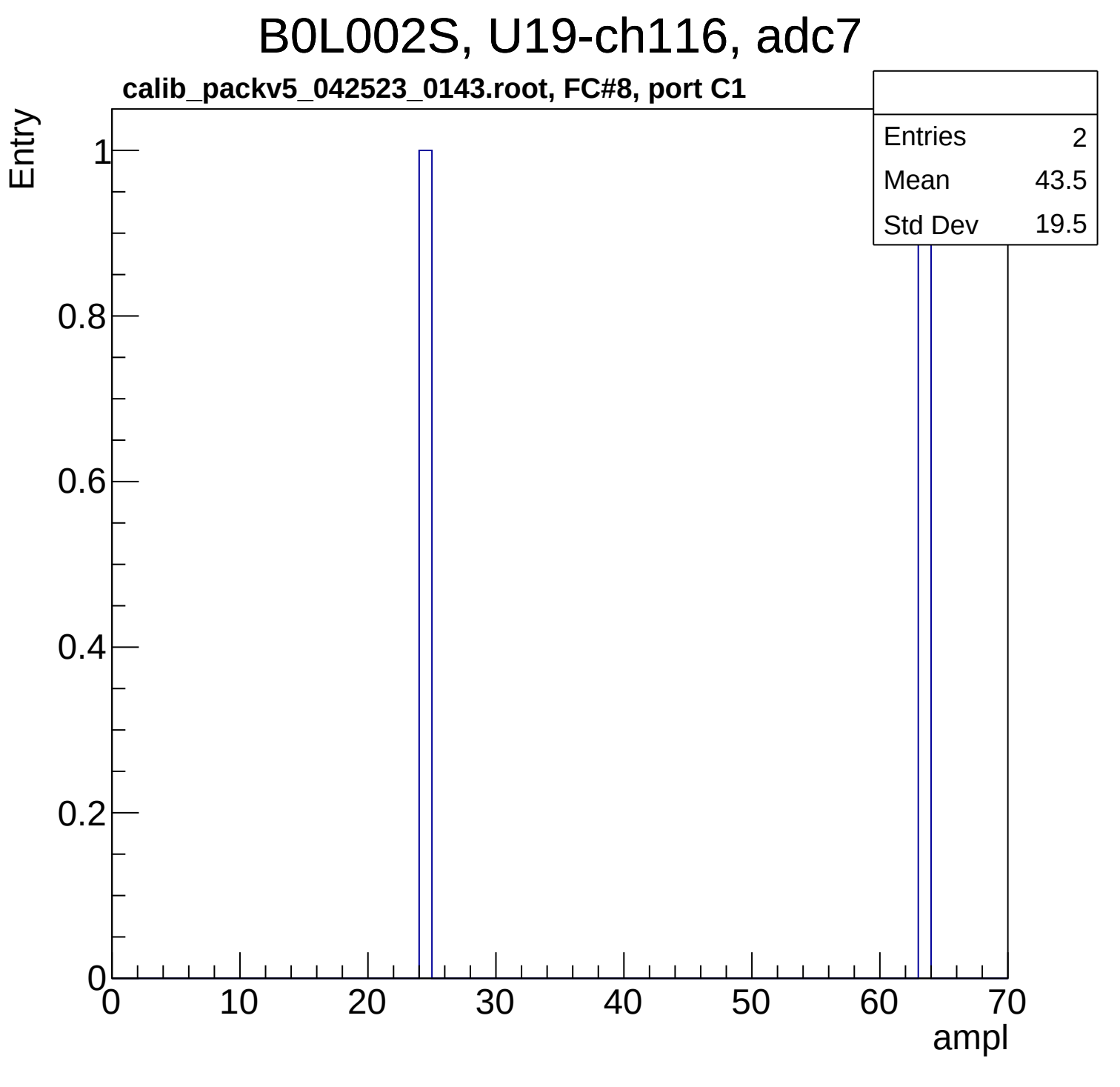

B0L002S, U19-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.79
Std Dev 2.879
Gaus mean : 44.1116
Gaus Width: 3.4058
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

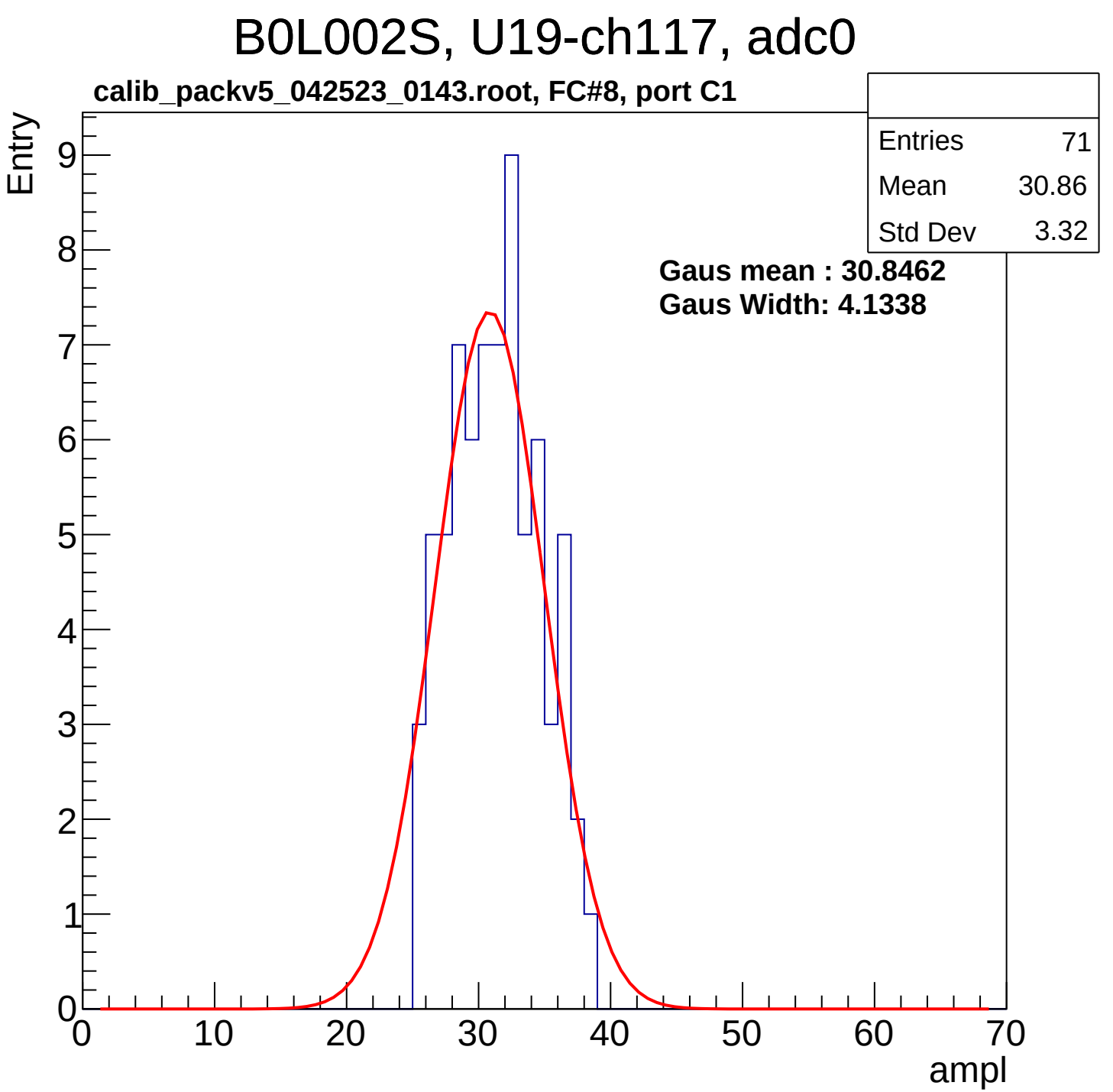

B0L002S, U19-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.25
Std Dev 3.387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

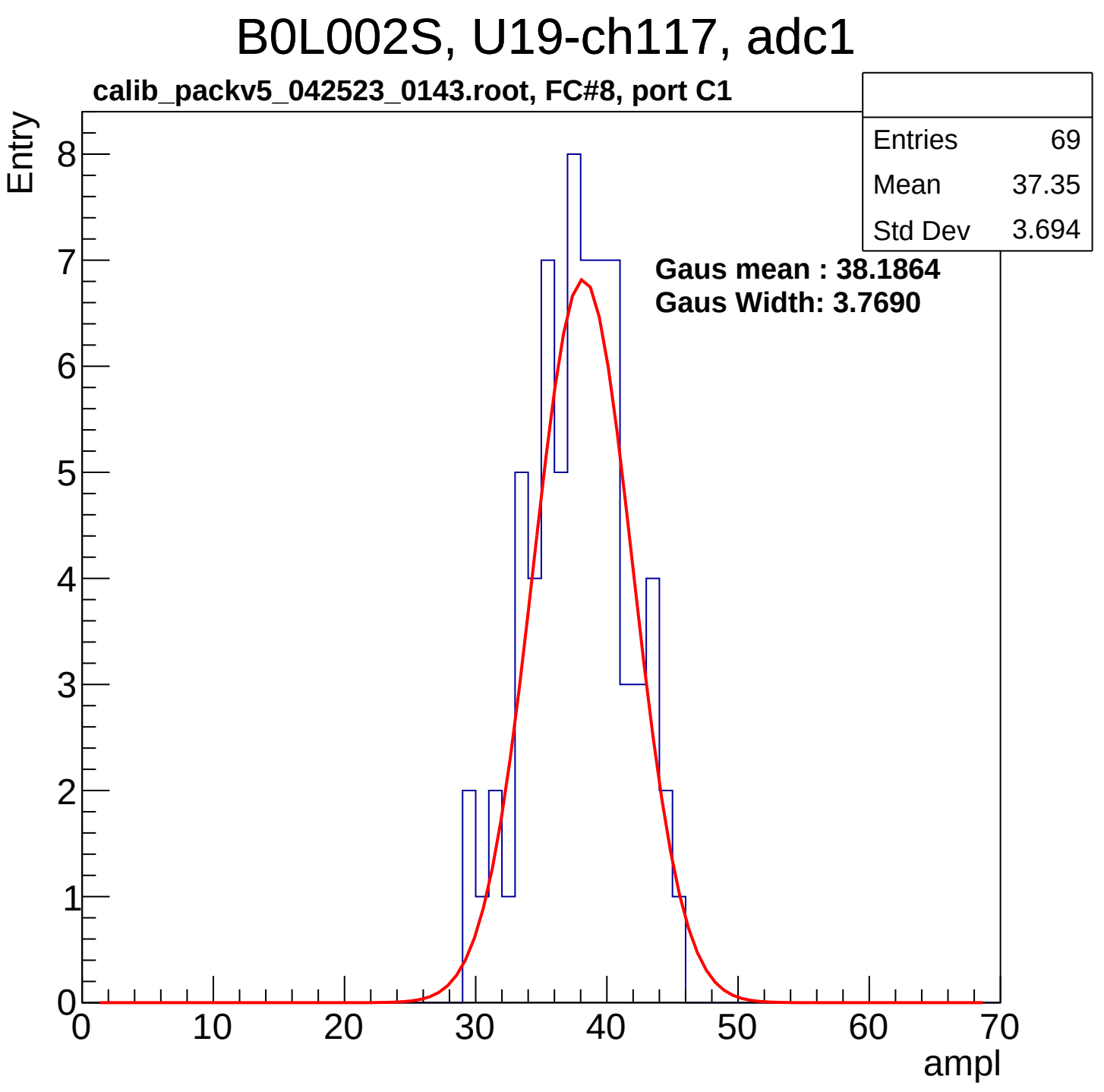

B0L002S, U19-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 56.74
Std Dev 3.028
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

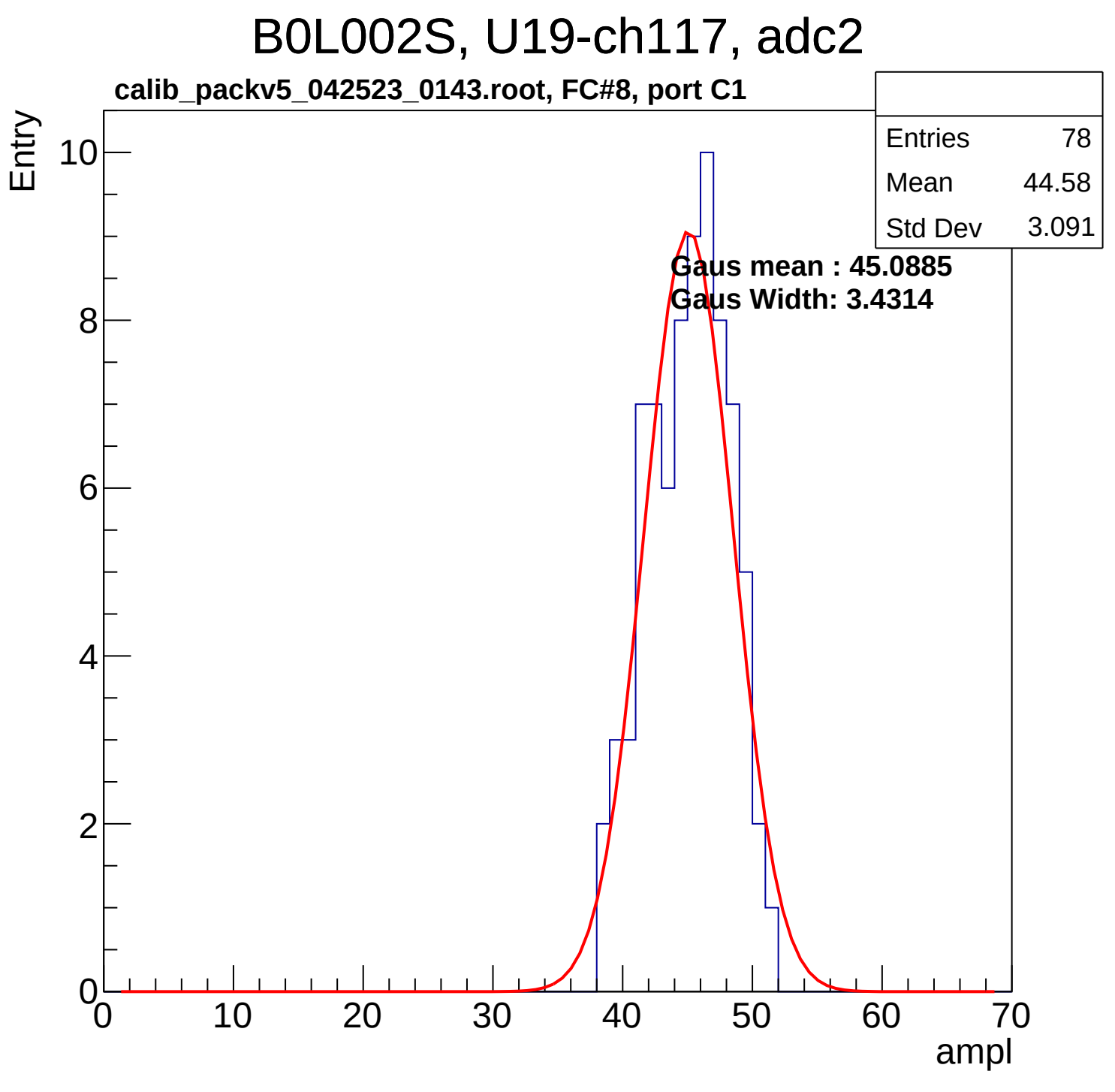

B0L002S, U19-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.93
Std Dev 2.25
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

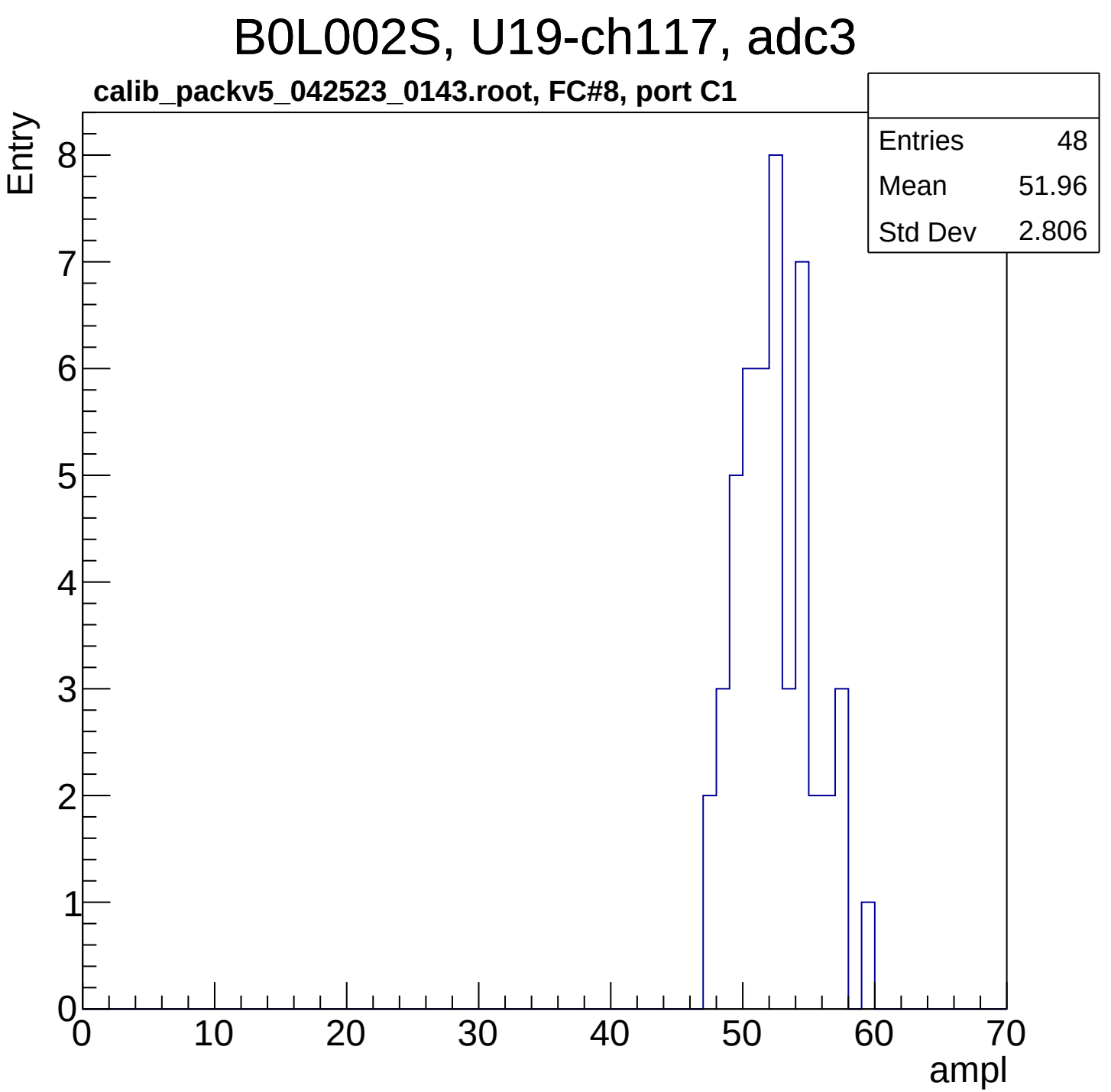

B0L002S, U19-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.17
Std Dev 22.96
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

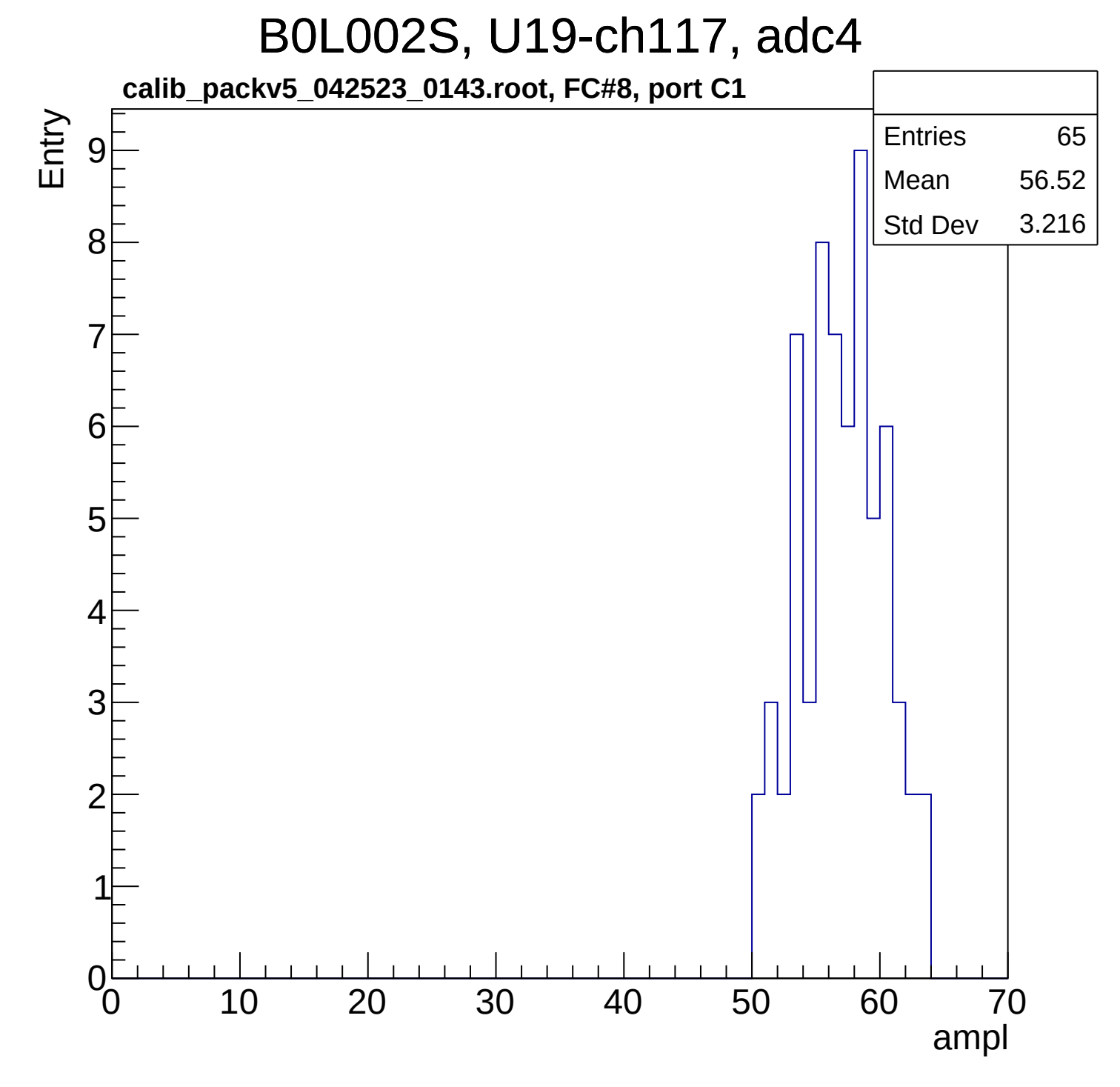

B0L002S, U19-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 43.5
Std Dev 19.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

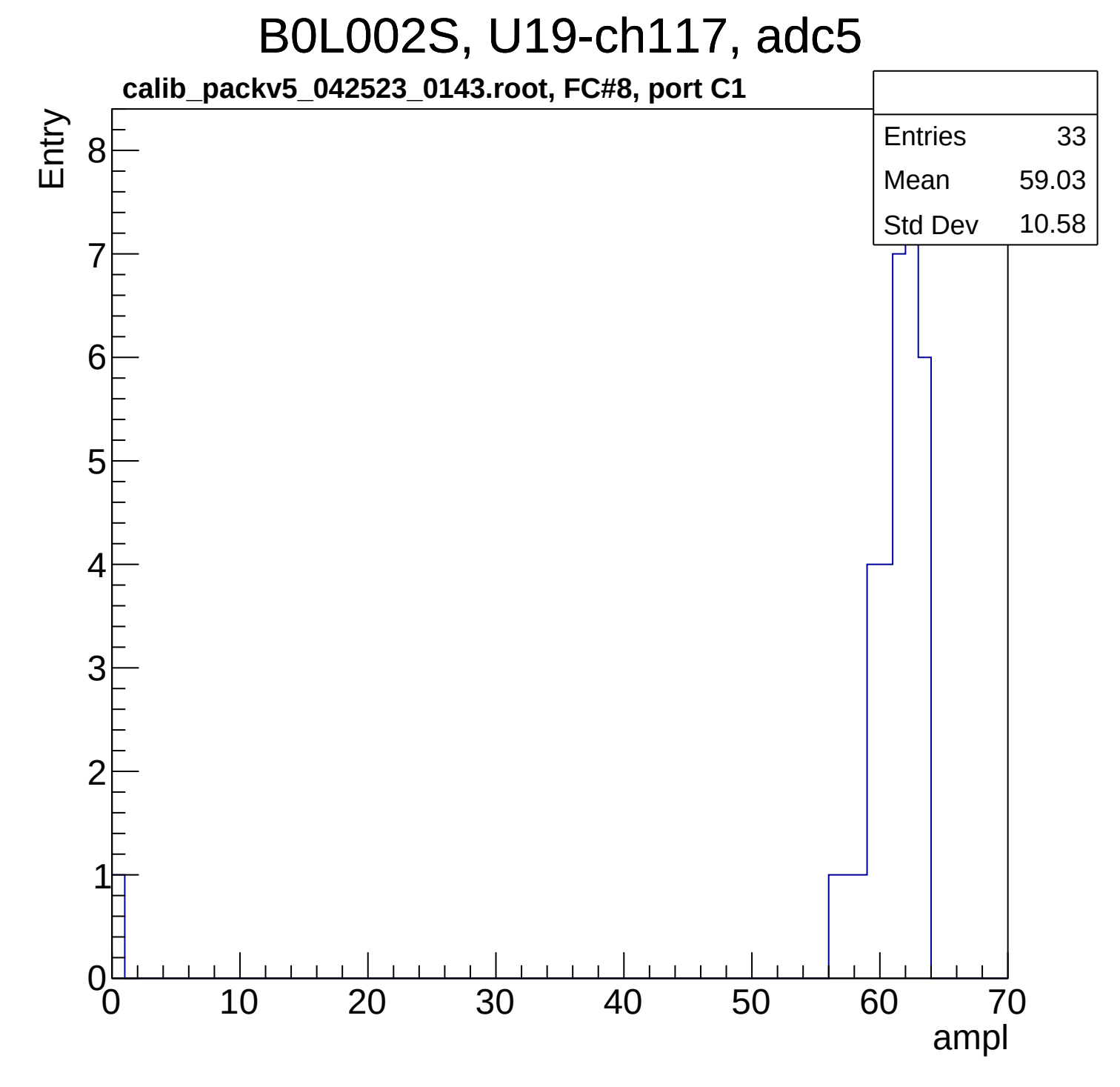

B0L002S, U19-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.86
Std Dev 3.32
Gaus mean : 30.8462
Gaus Width: 4.1338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

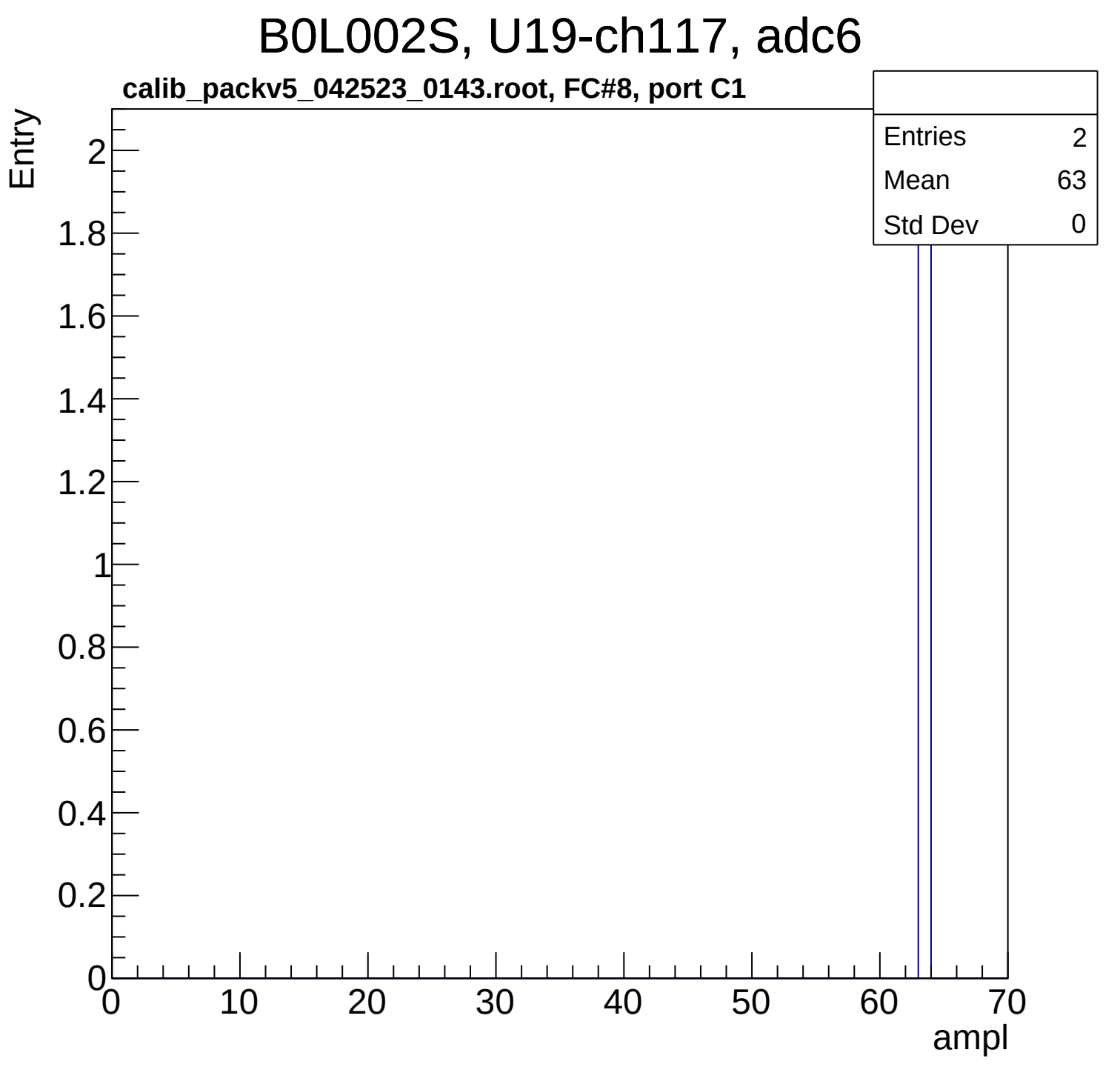

B0L002S, U19-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.35
Std Dev 3.694
Gaus mean : 38.1864
Gaus Width: 3.7690
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

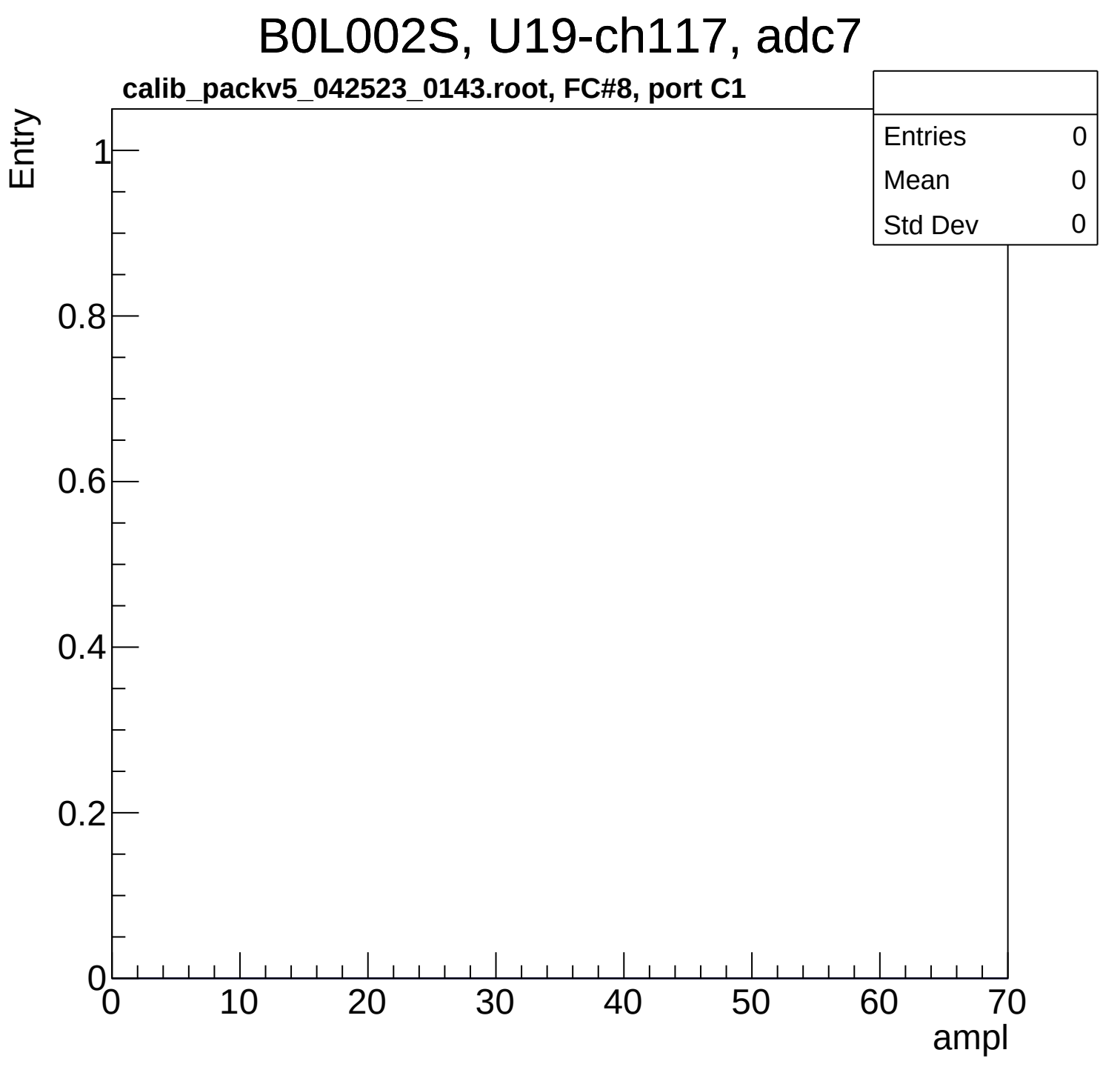

B0L002S, U19-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 44.58
Std Dev 3.091
Gaus mean : 45.0885
Gaus Width: 3.4314
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

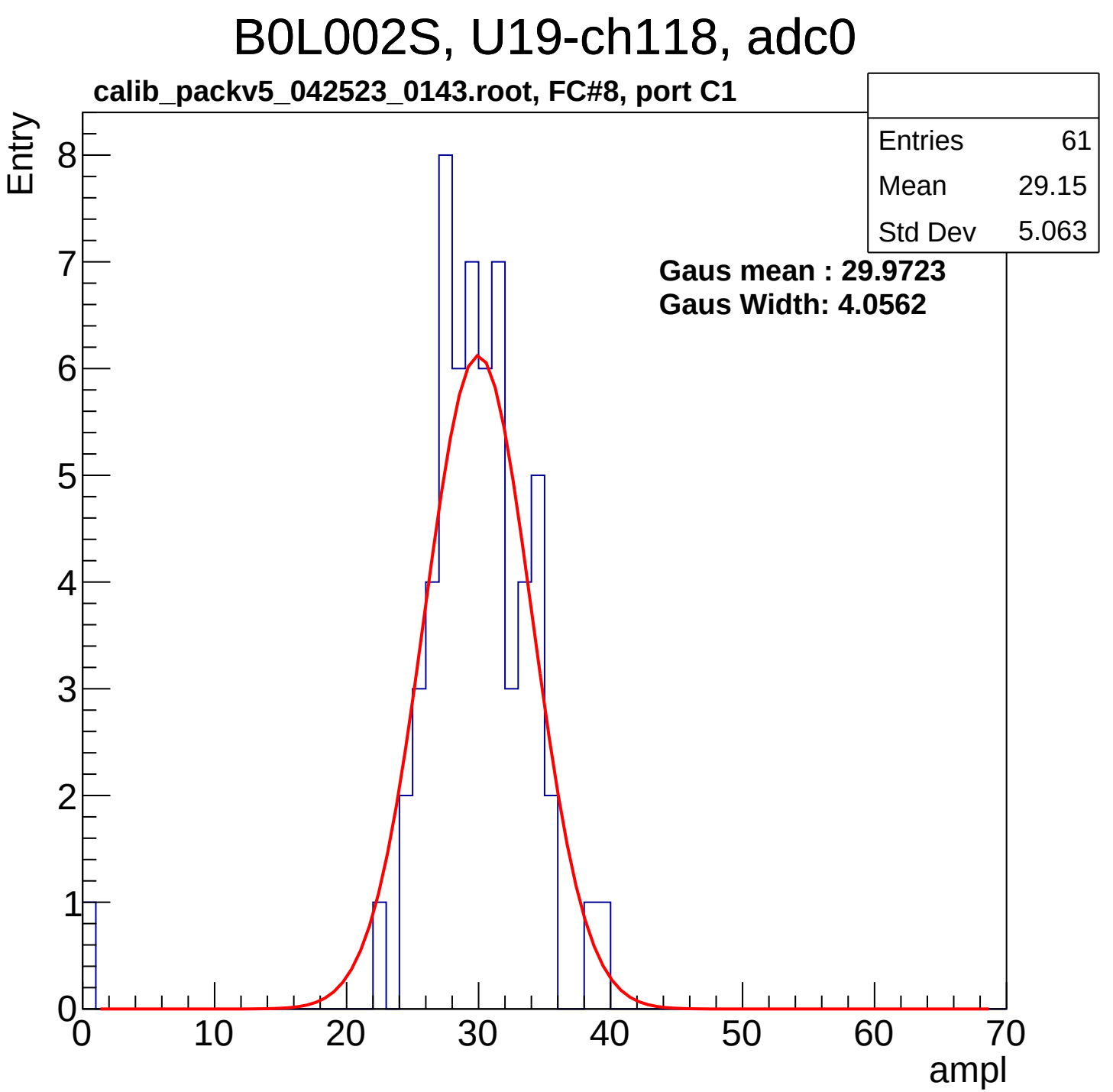

B0L002S, U19-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 51.96
Std Dev 2.806
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

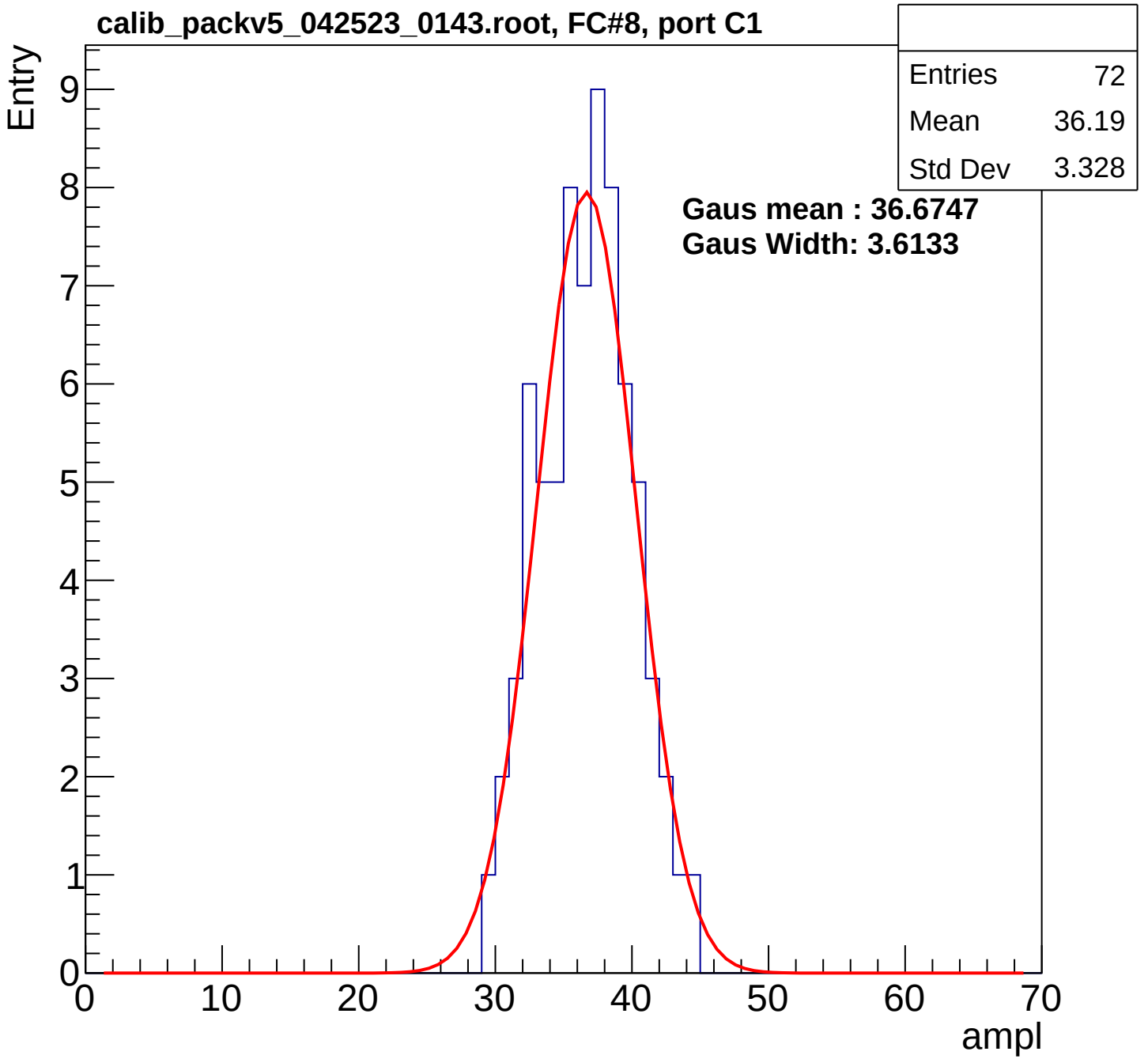

B0L002S, U19-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.52
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

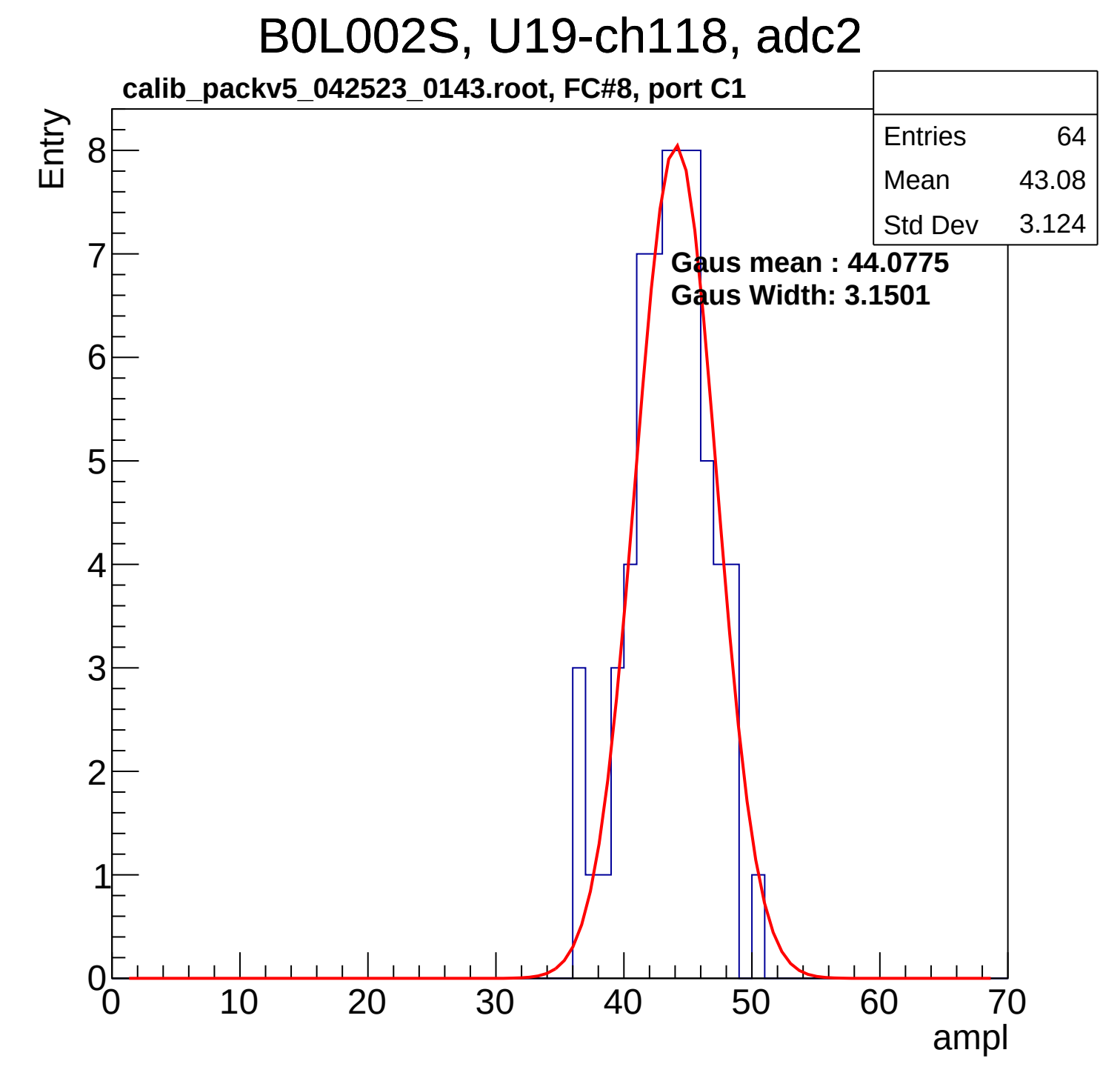

B0L002S, U19-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.03
Std Dev 10.58
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

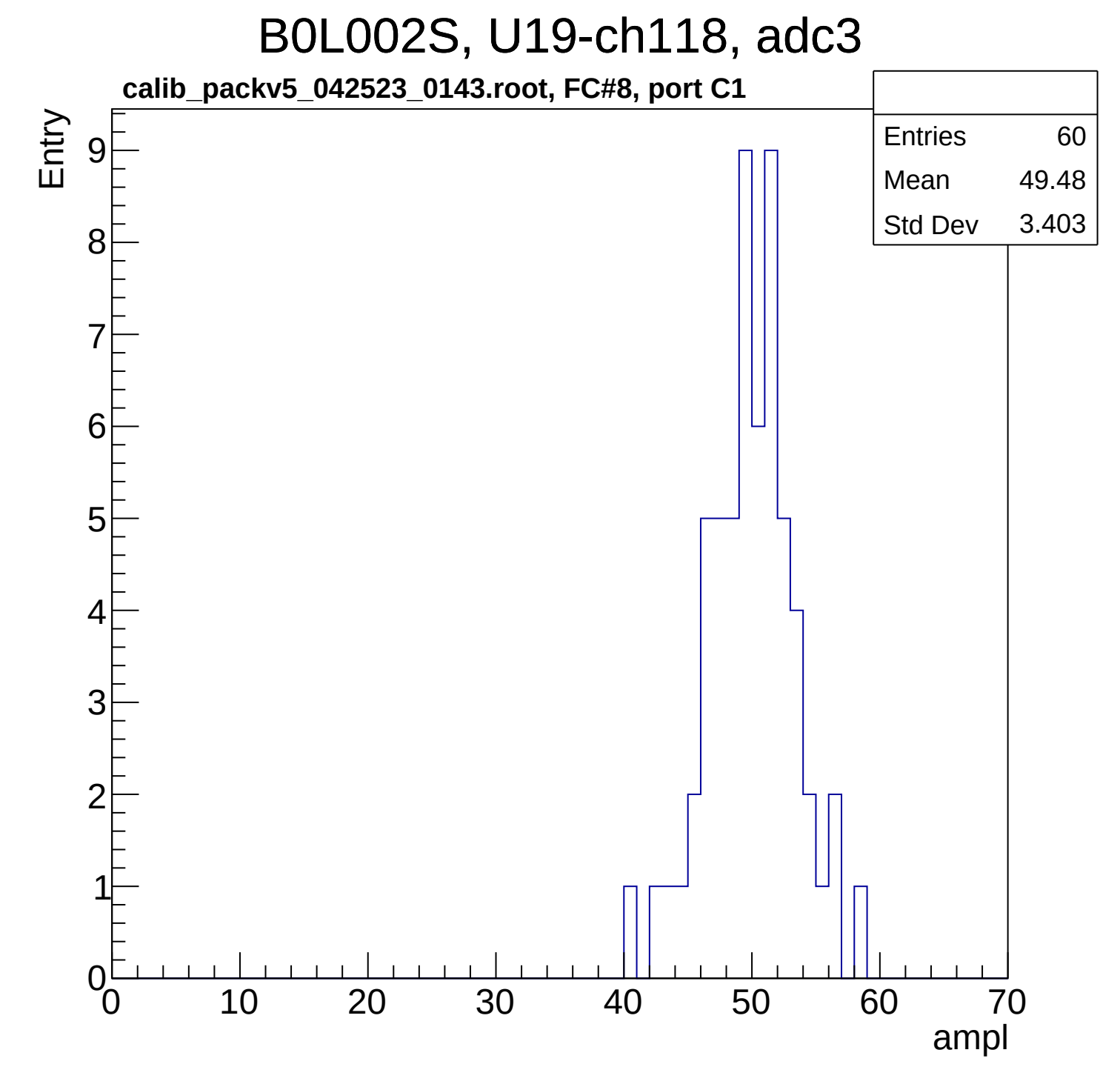

B0L002S, U19-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

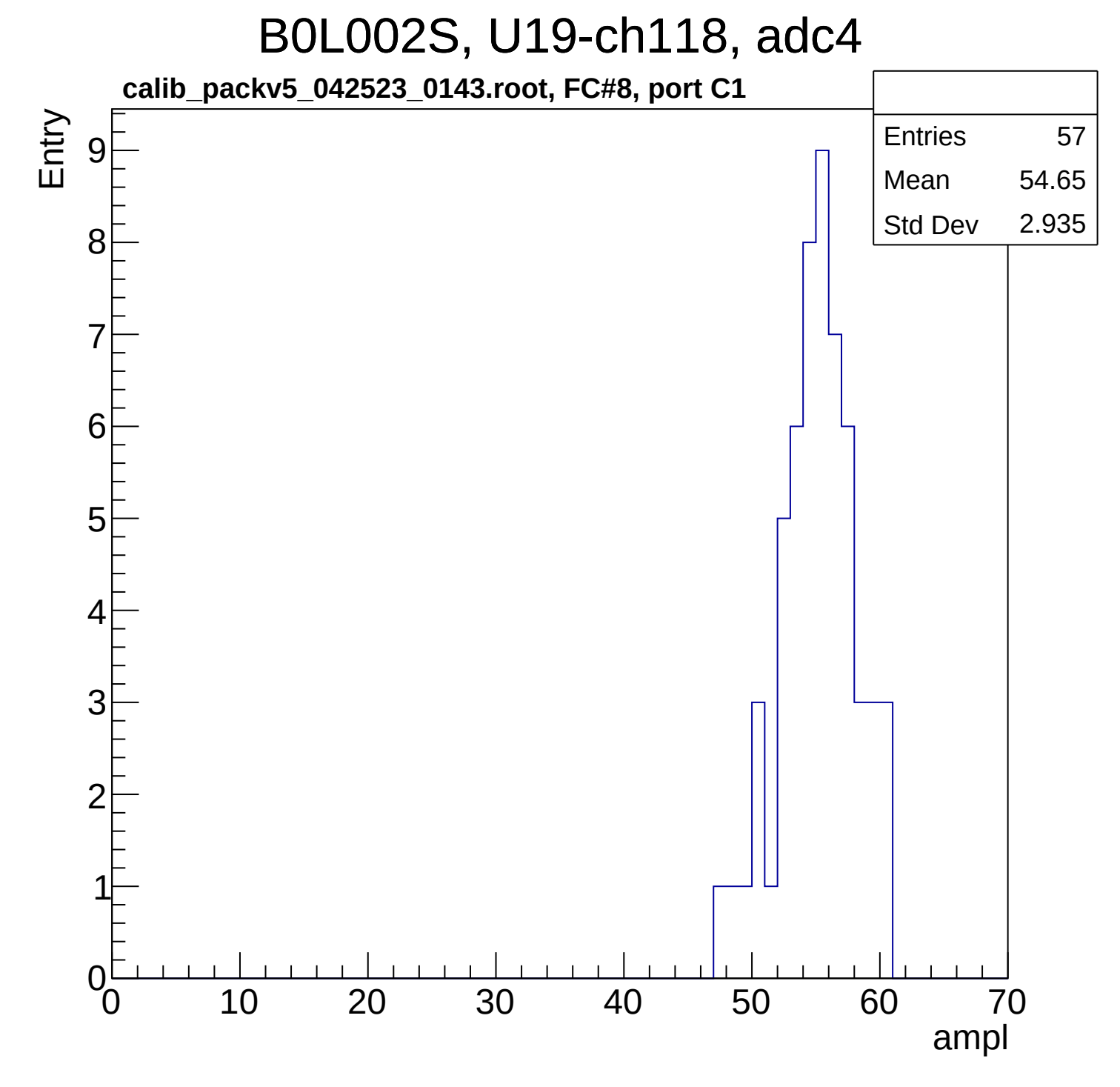

B0L002S, U19-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

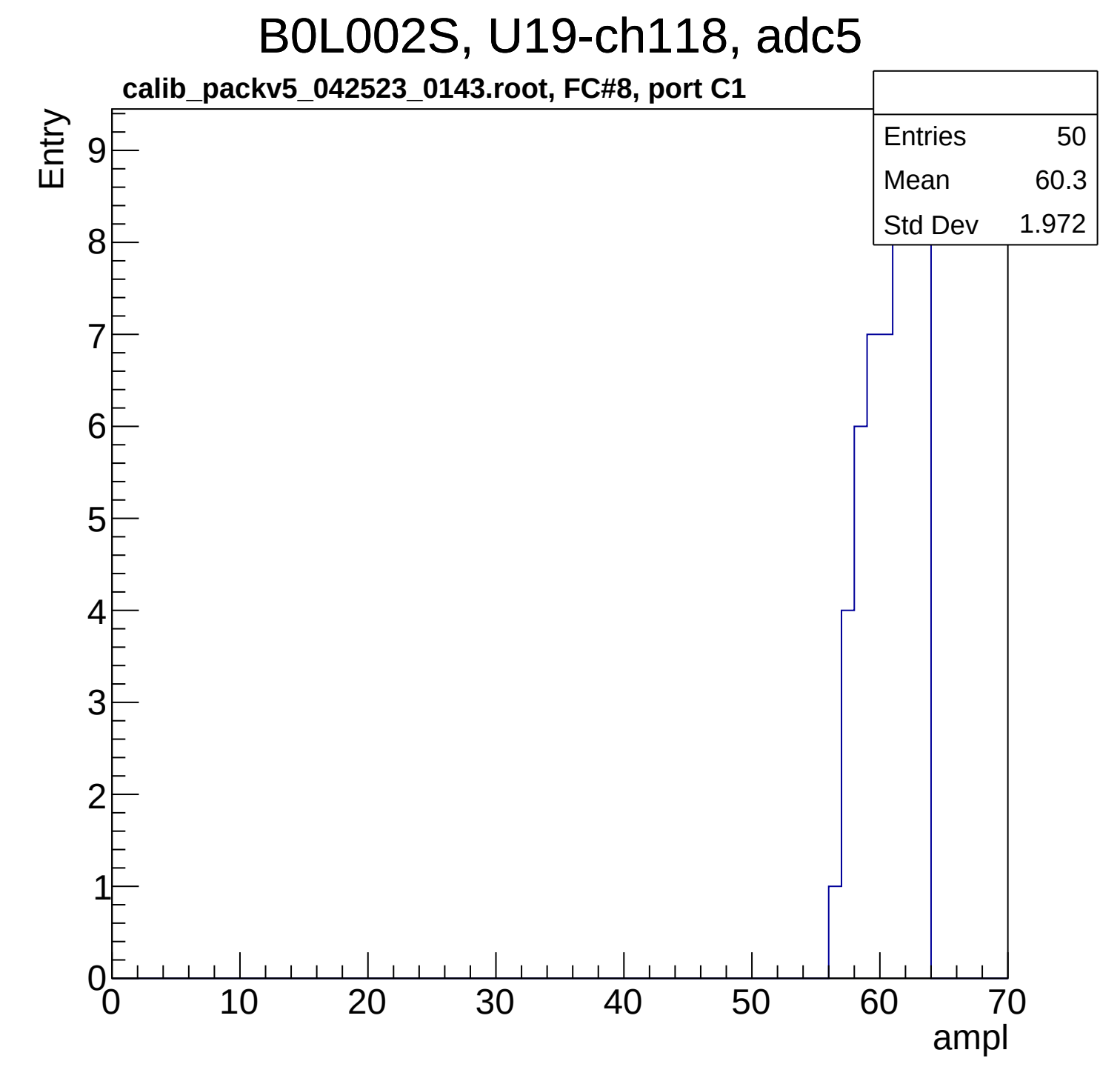

B0L002S, U19-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.15
Std Dev 5.063
Gaus mean : 29.9723
Gaus Width: 4.0562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

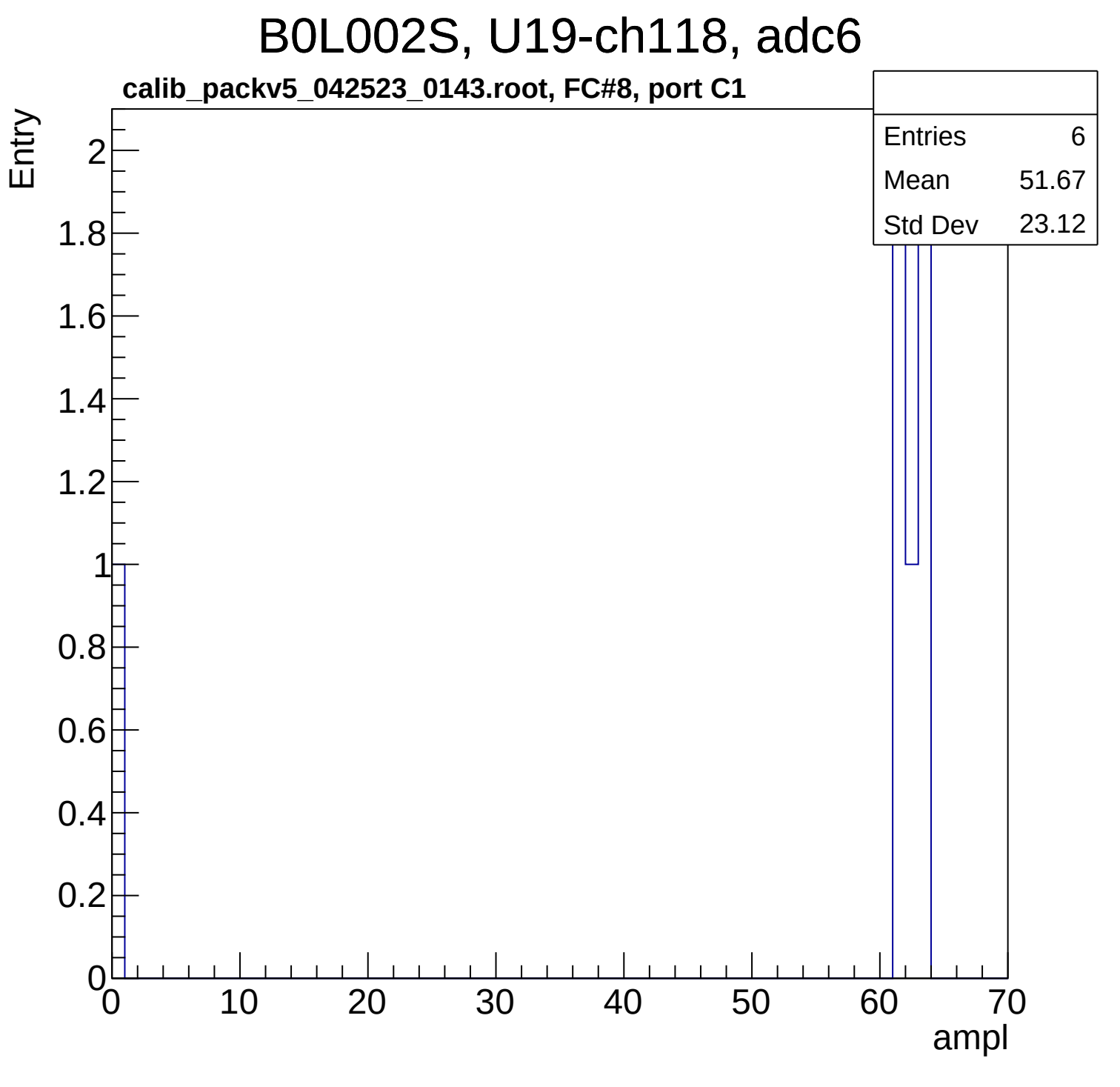

B0L002S, U19-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.19
Std Dev 3.328
Gaus mean : 36.6747
Gaus Width: 3.6133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

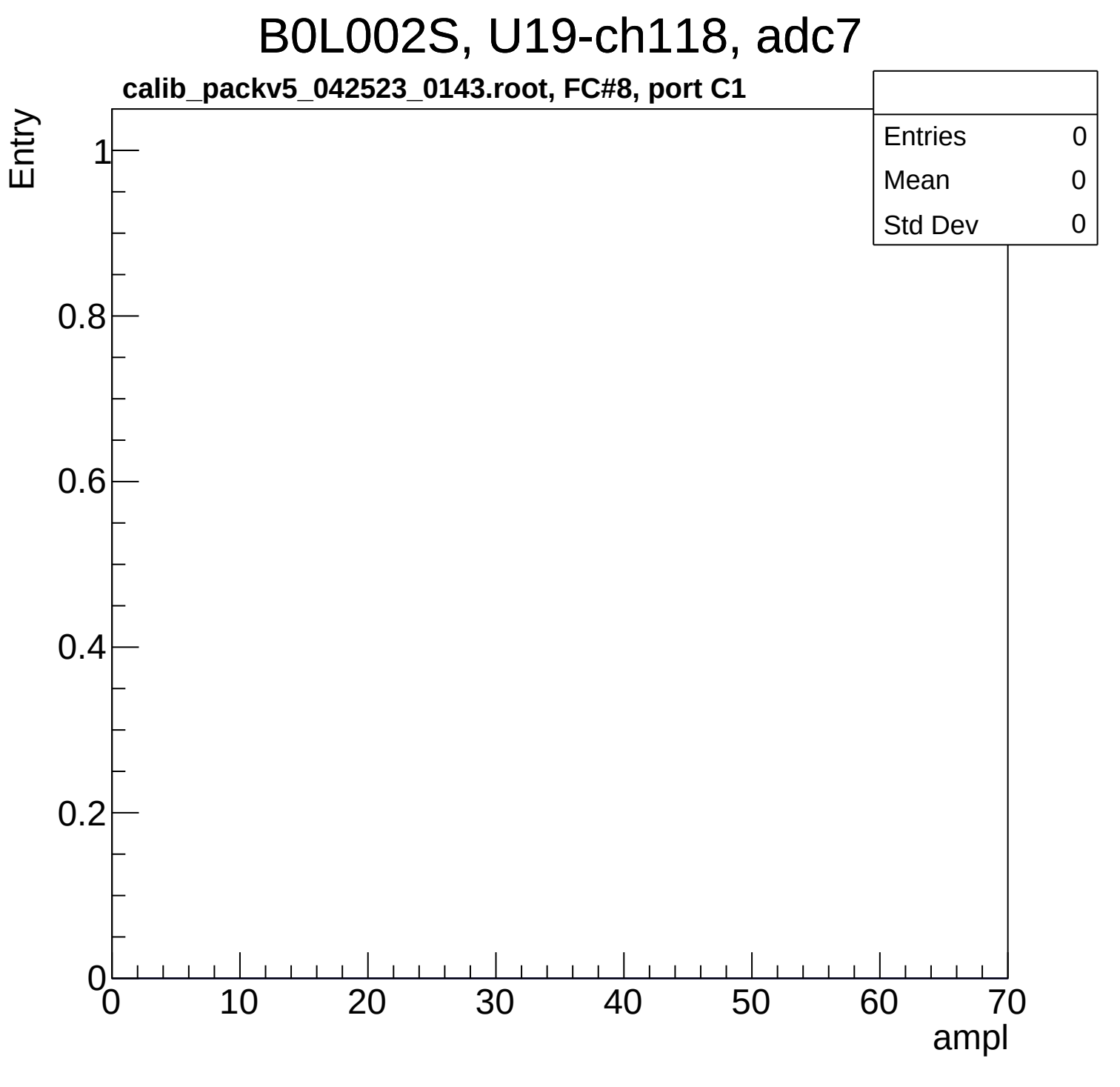

B0L002S, U19-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.08
Std Dev 3.124
Gaus mean : 44.0775
Gaus Width: 3.1501
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

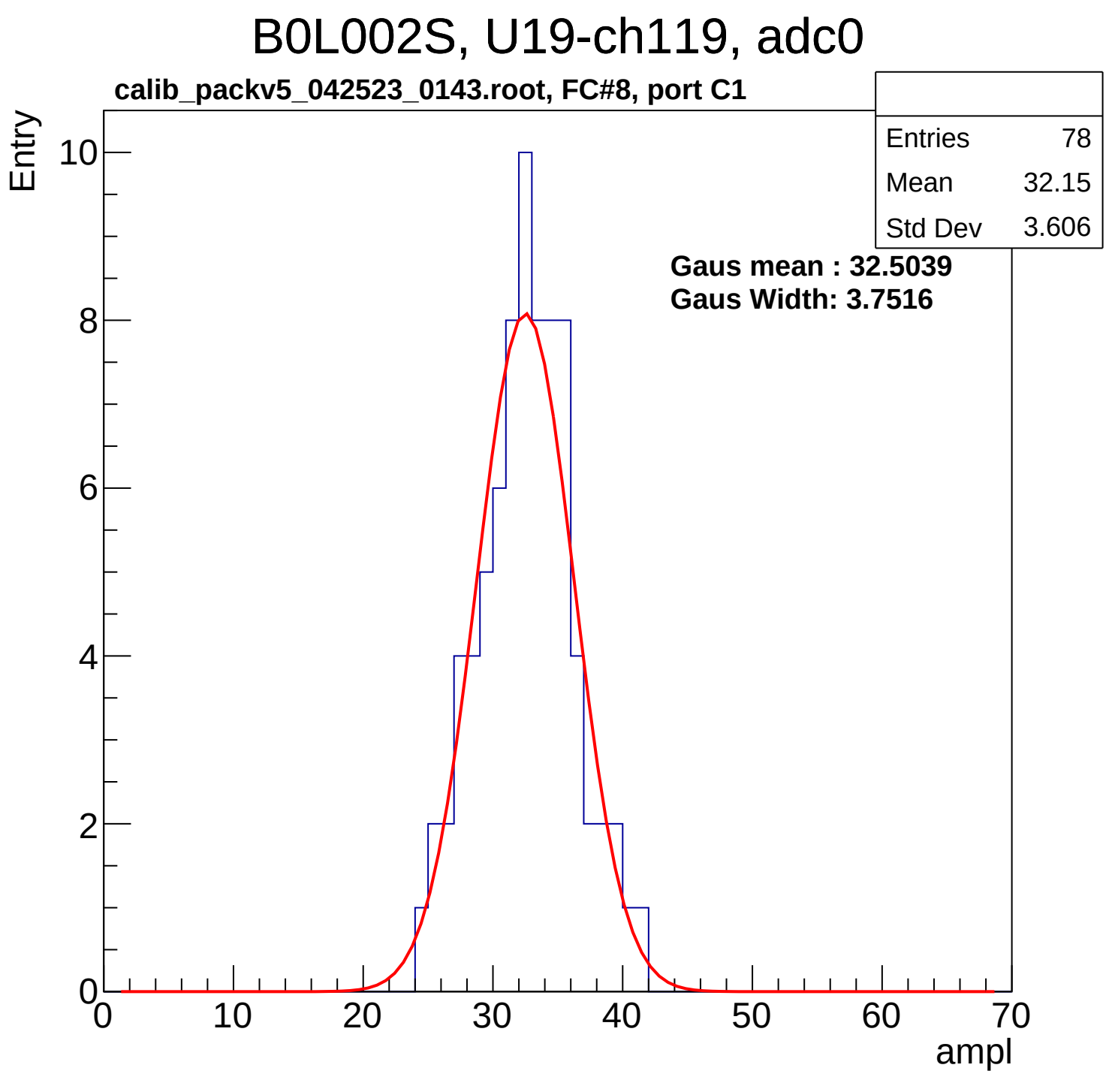

B0L002S, U19-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 49.48
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

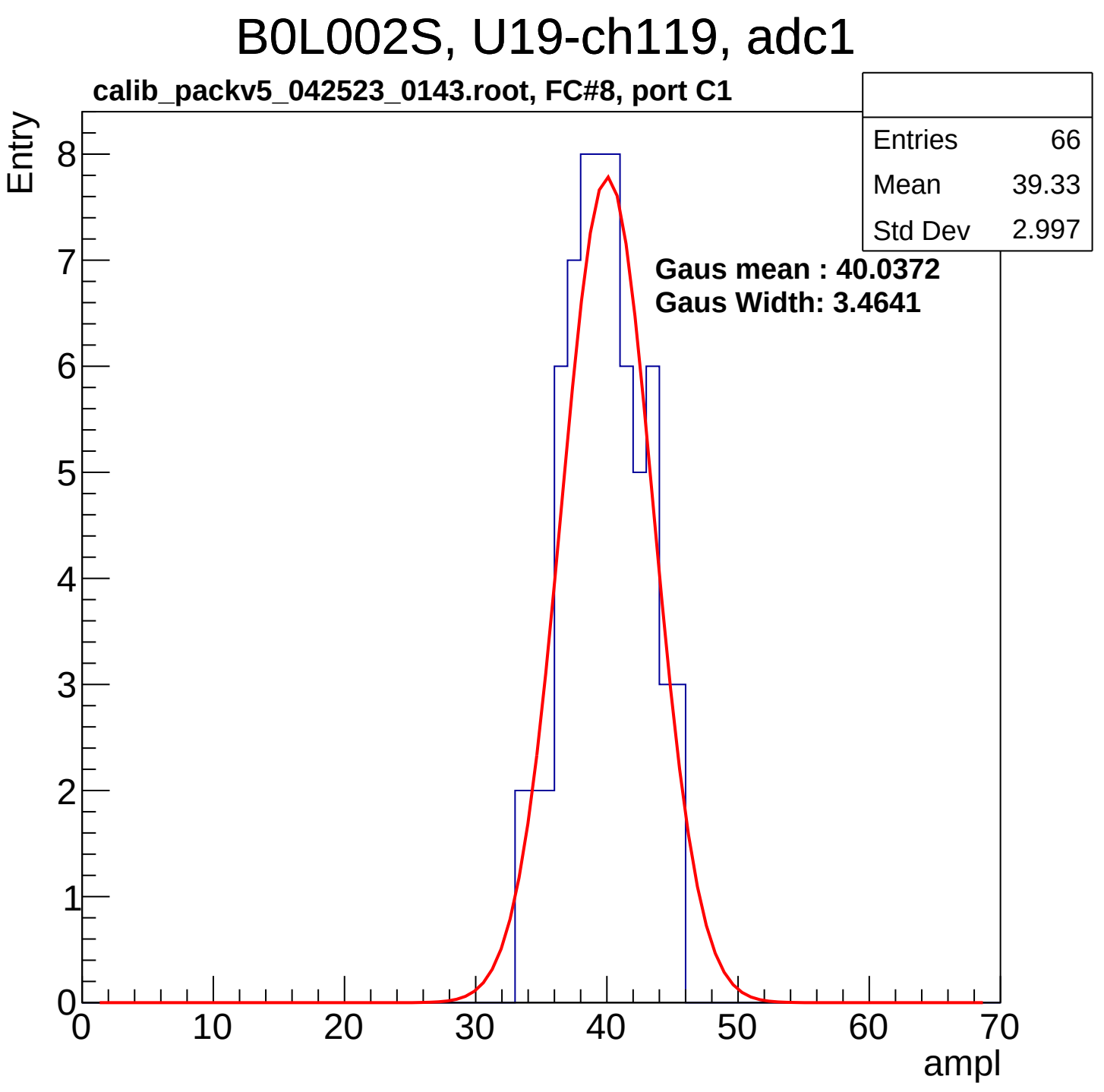

B0L002S, U19-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 54.65
Std Dev 2.935
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

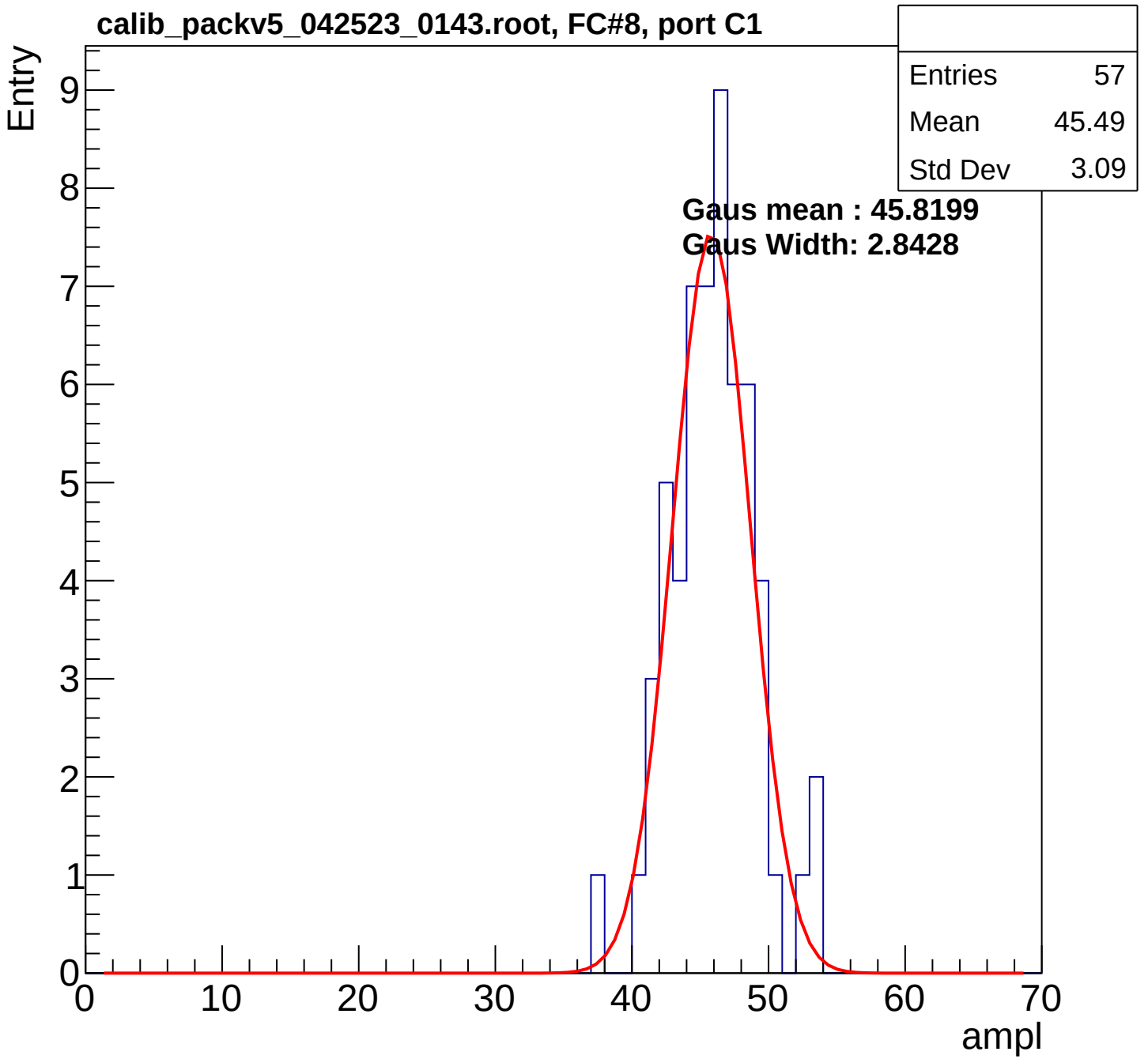

B0L002S, U19-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 60.3
Std Dev 1.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

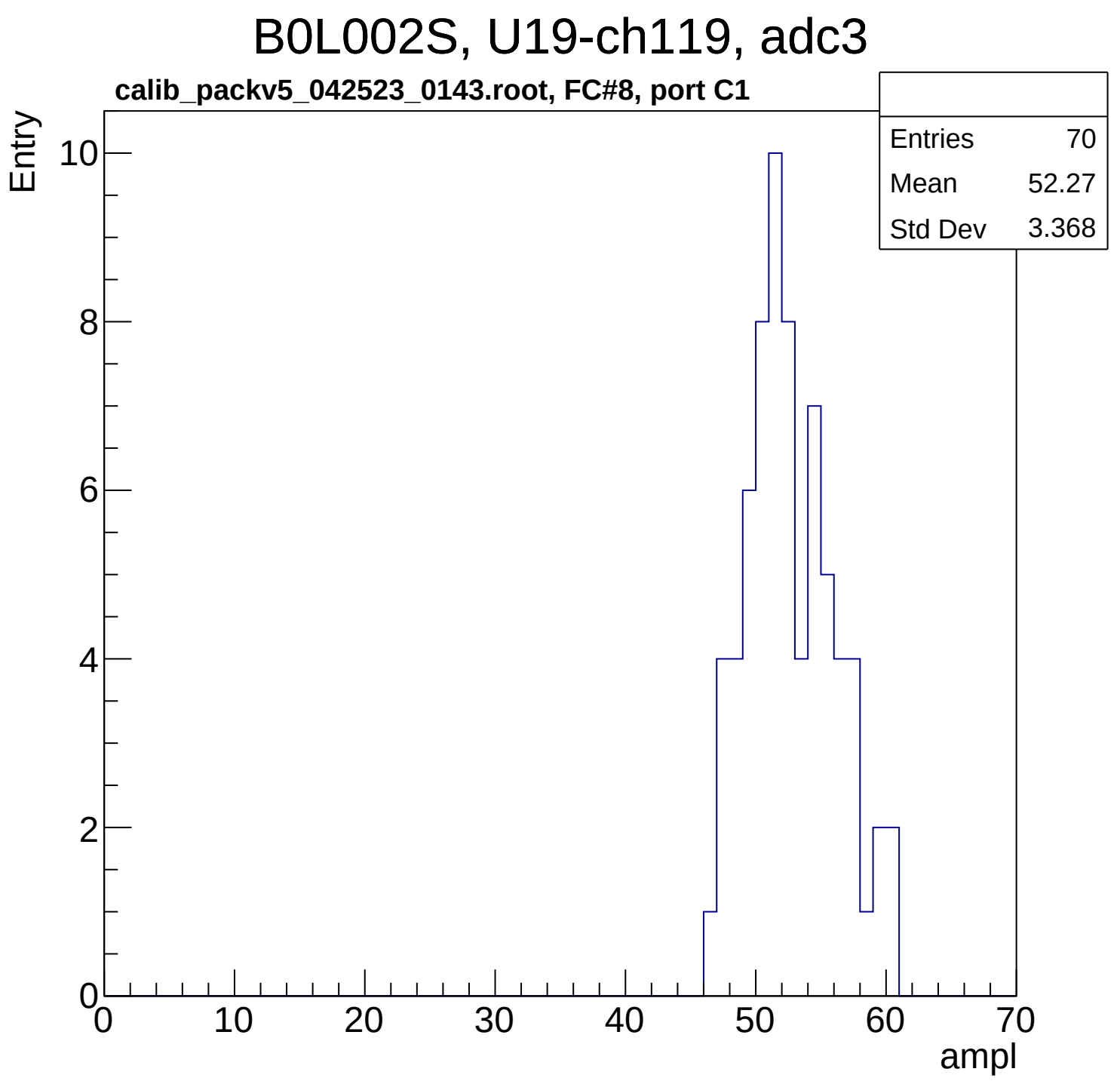

B0L002S, U19-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.67
Std Dev 23.12
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

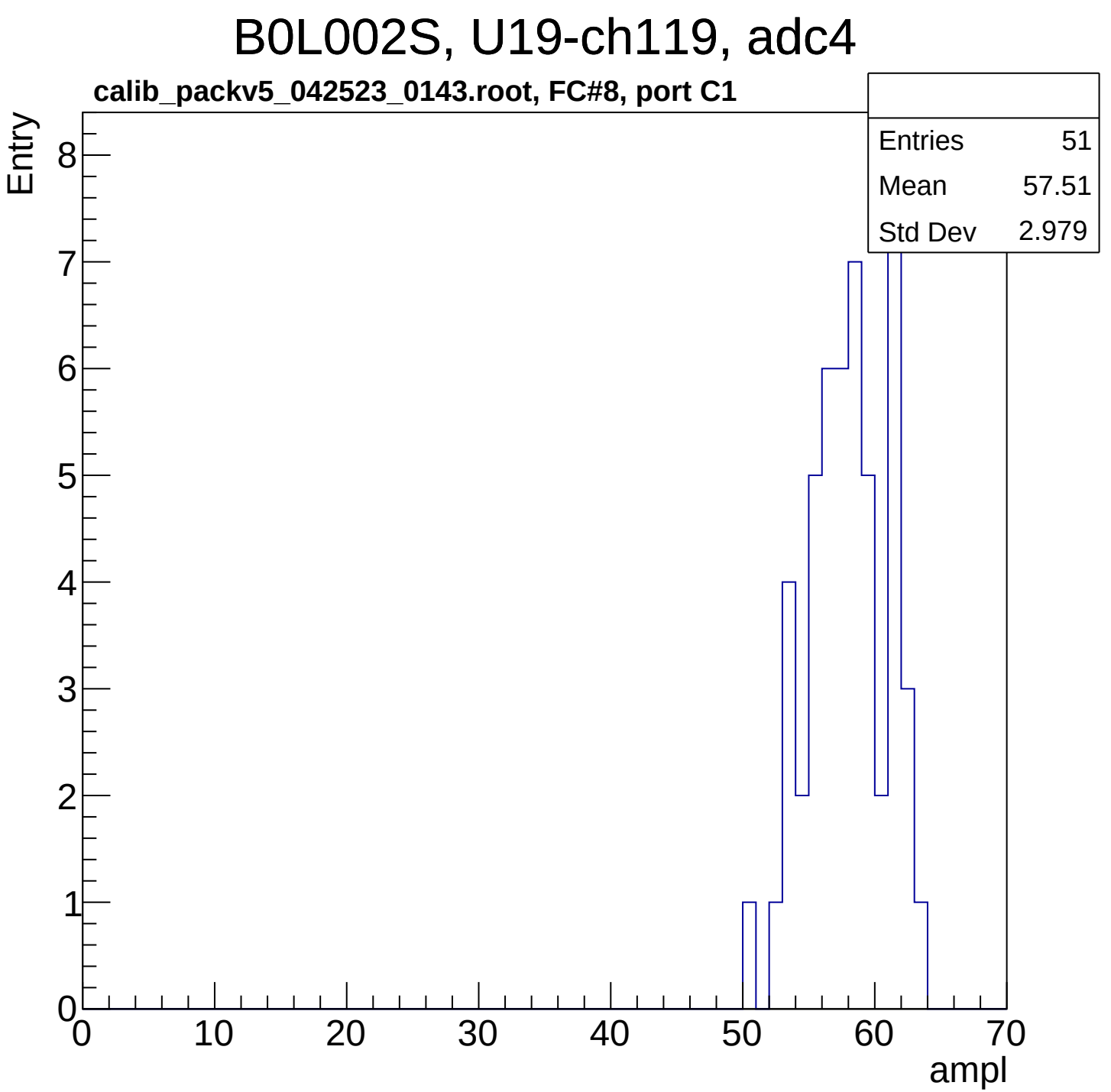

B0L002S, U19-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

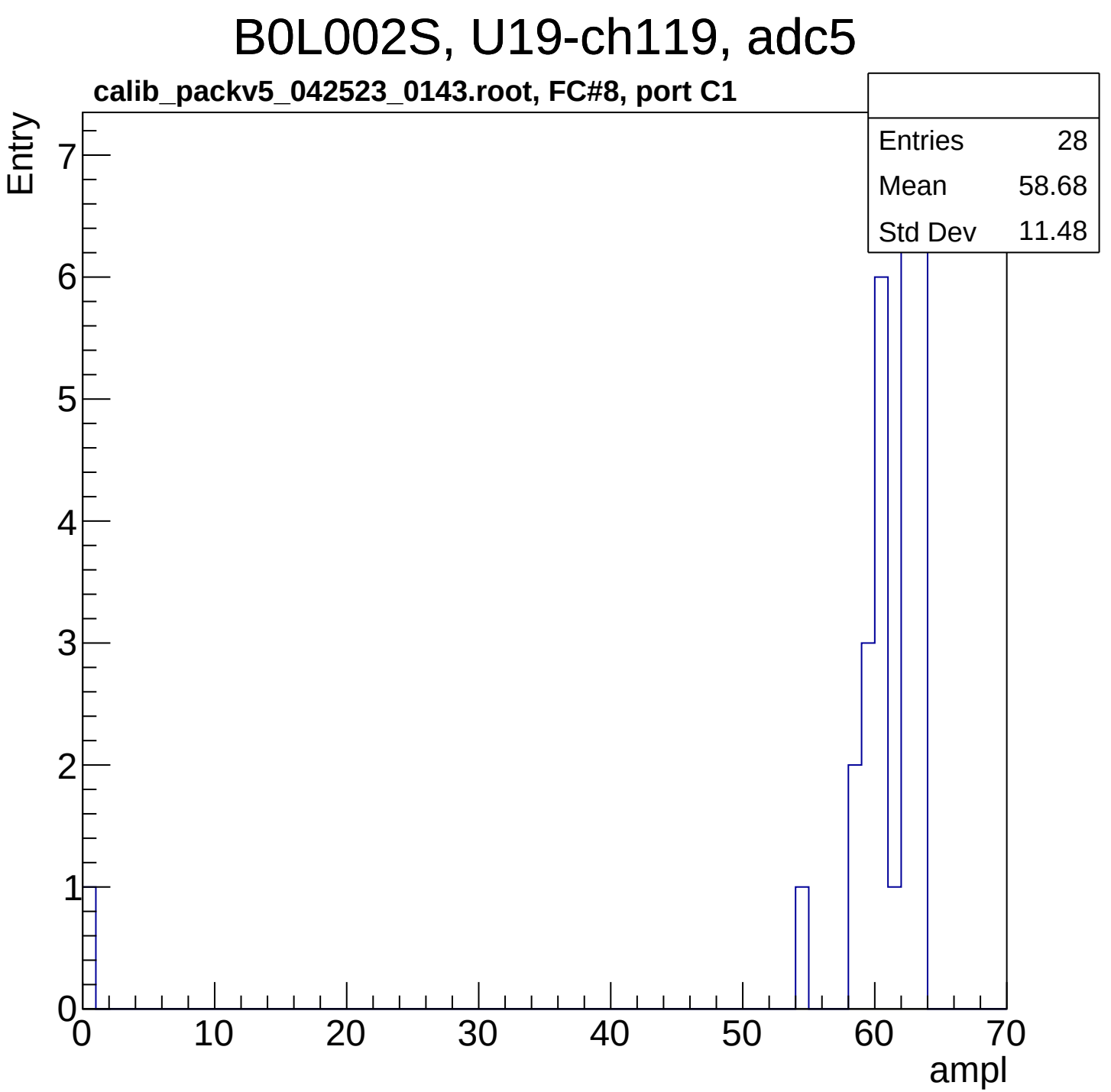

B0L002S, U19-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 32.15
Std Dev 3.606
Gaus mean : 32.5039
Gaus Width: 3.7516
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

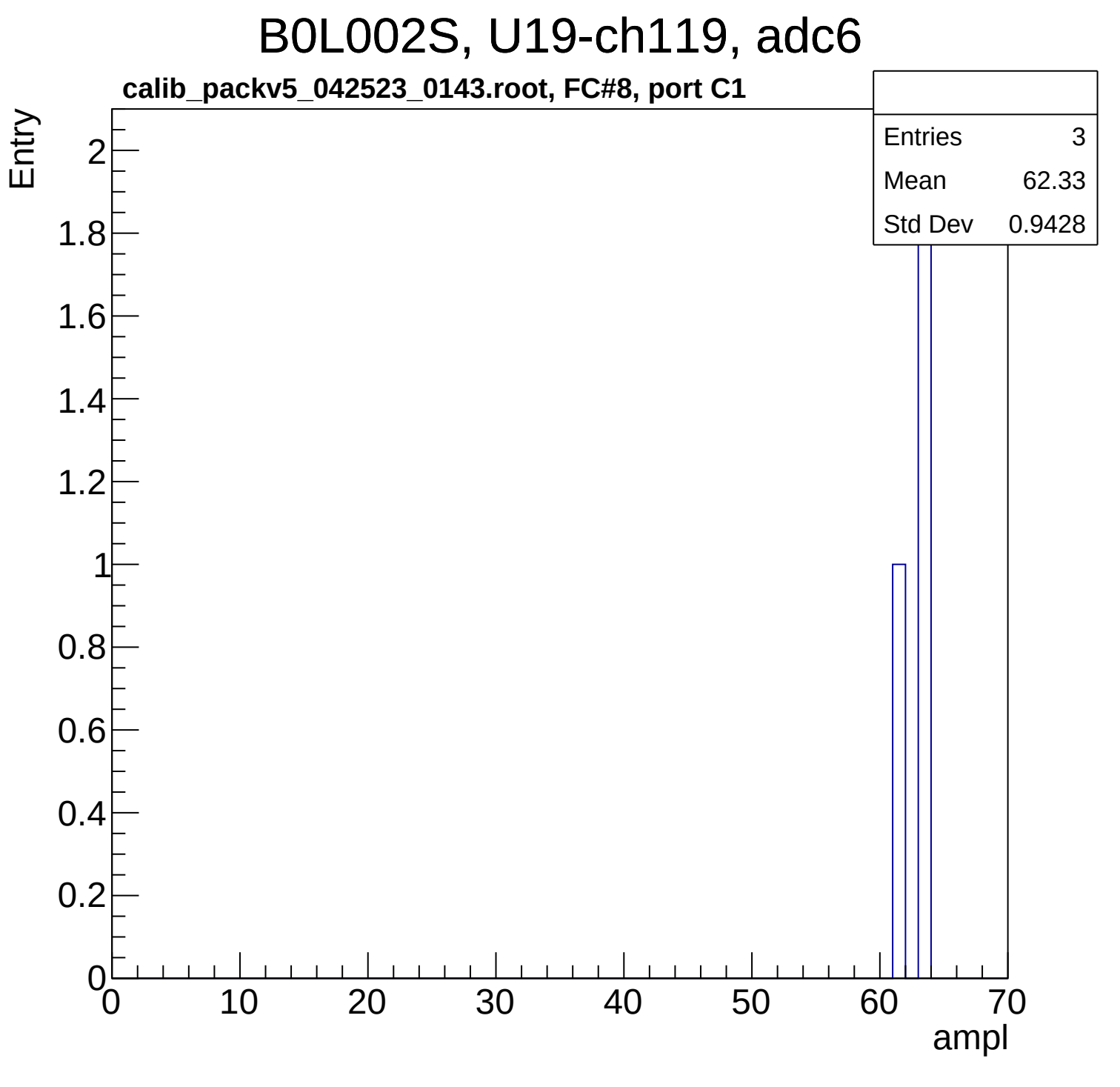

B0L002S, U19-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 39.33
Std Dev 2.997
Gaus mean : 40.0372
Gaus Width: 3.4641
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

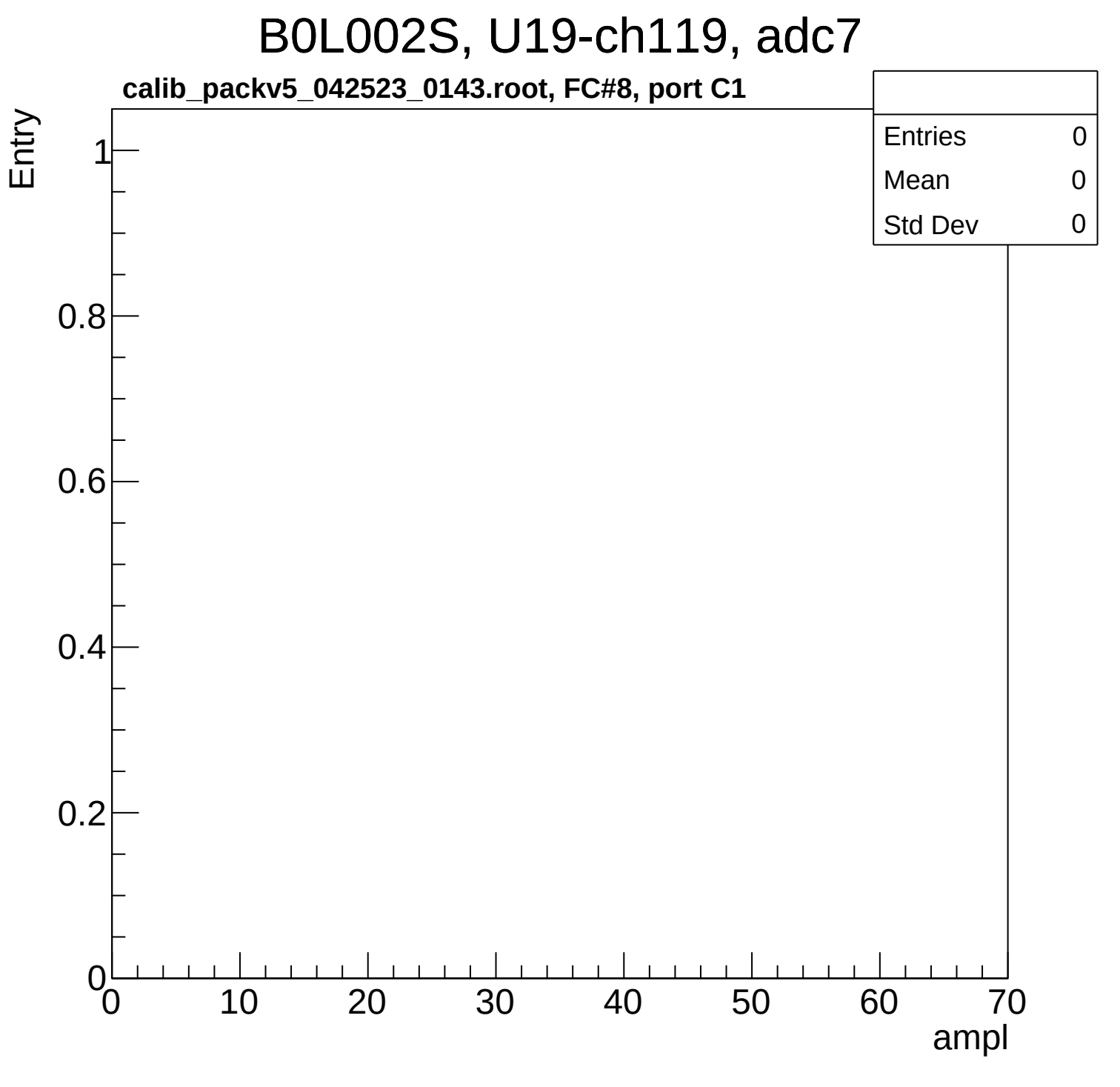

B0L002S, U19-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 45.49
Std Dev 3.09
Gaus mean : 45.8199
Gaus Width: 2.8428
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

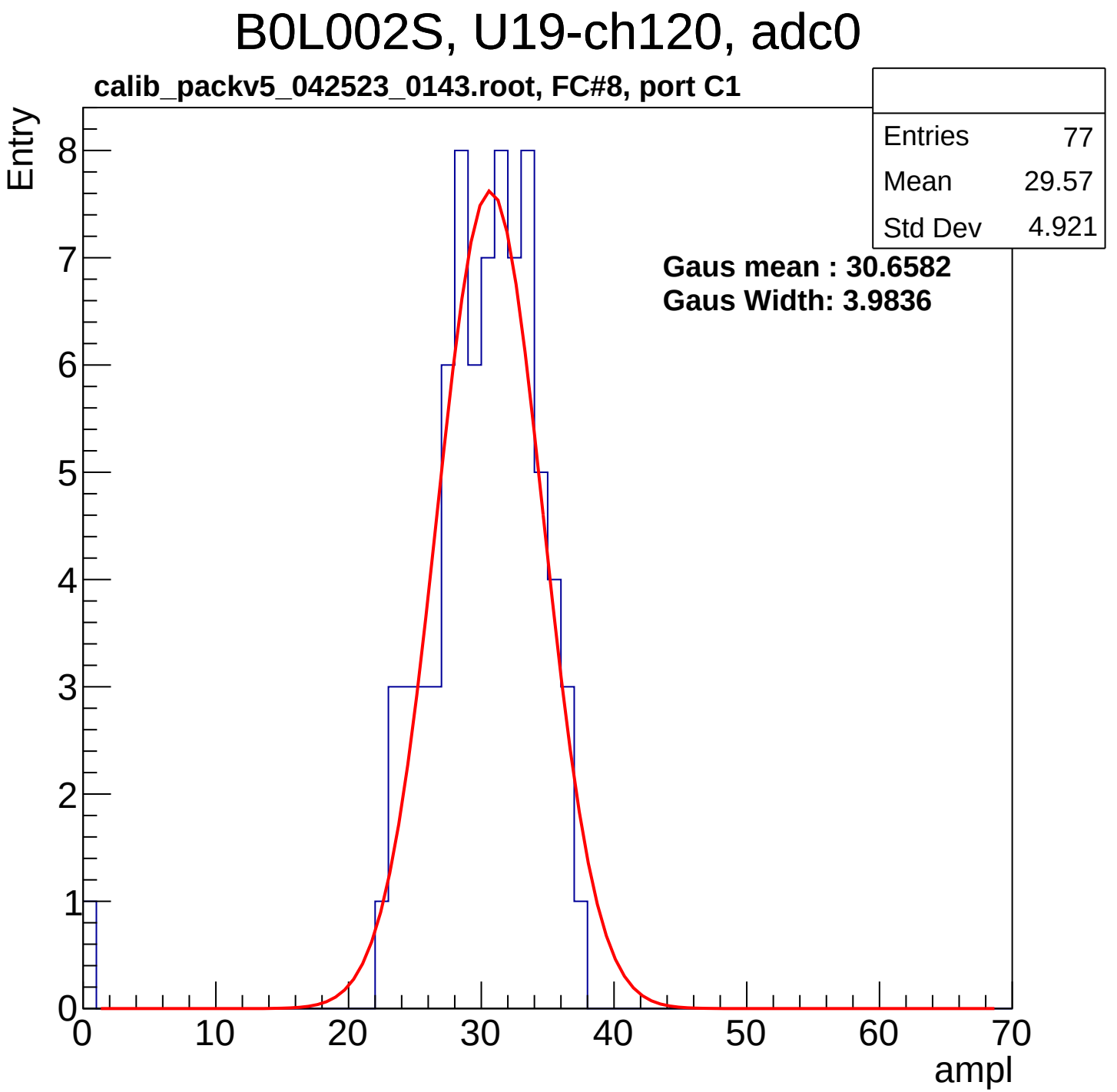

B0L002S, U19-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 52.27
Std Dev 3.368
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

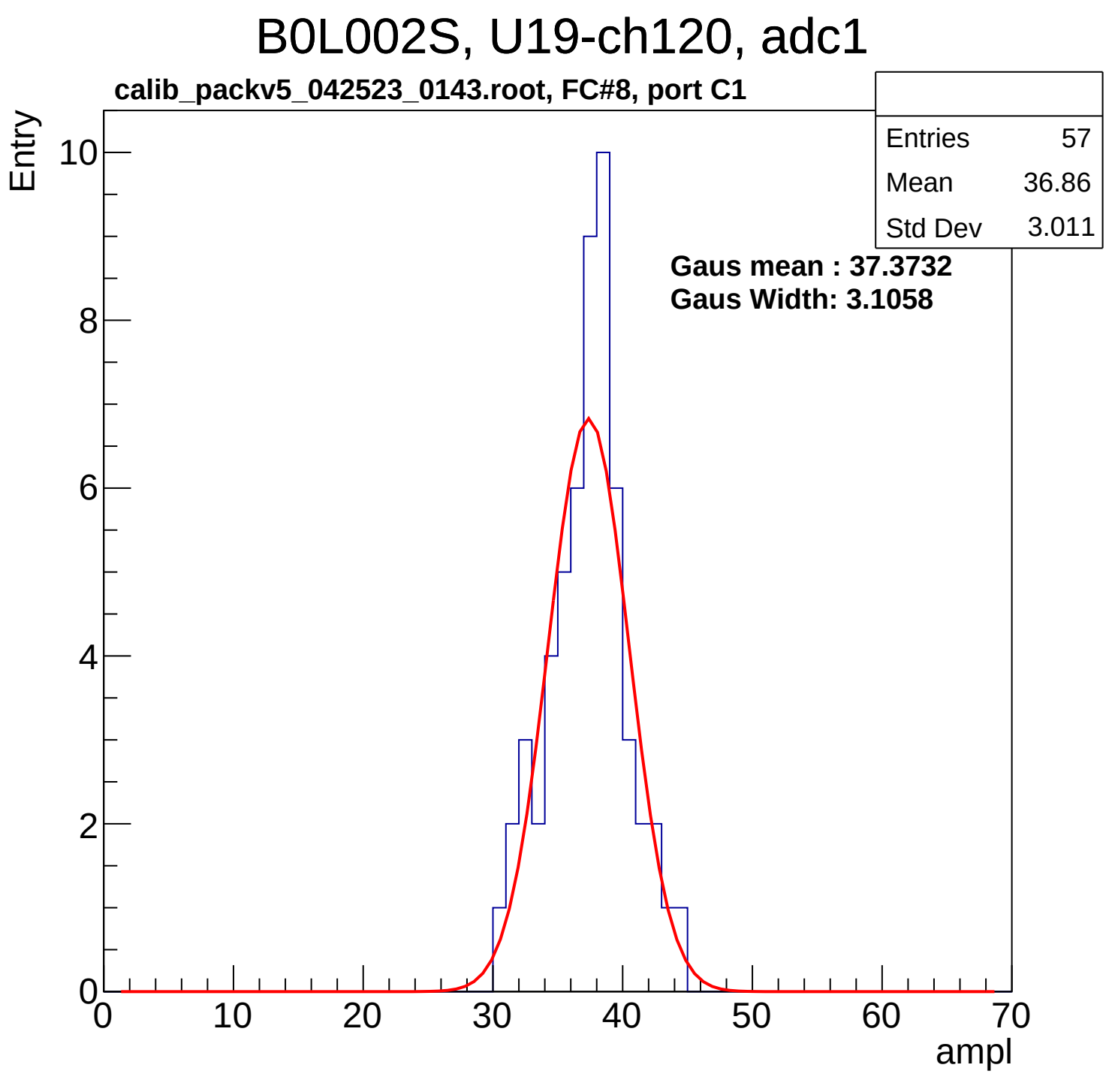

B0L002S, U19-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.51
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

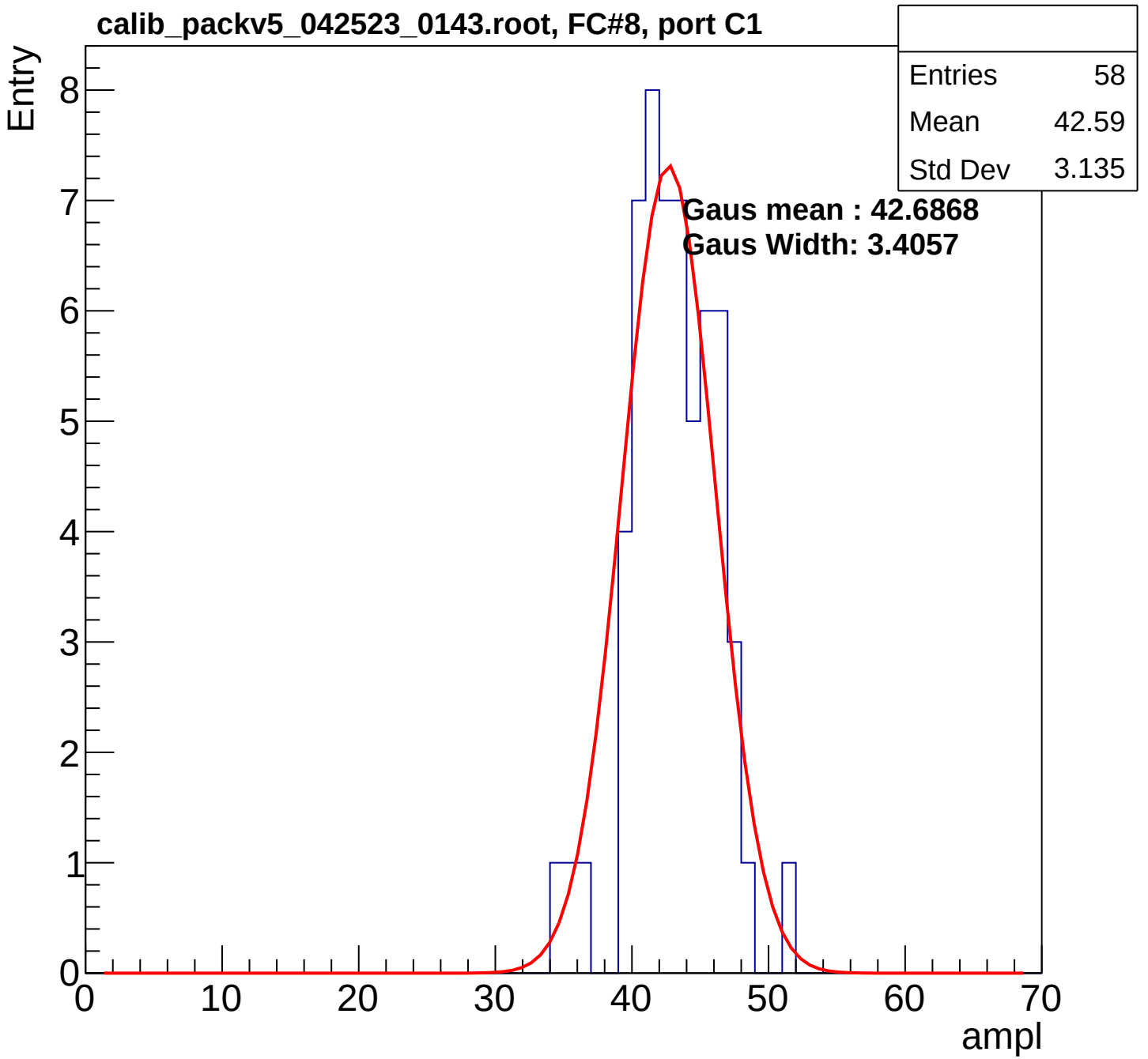

B0L002S, U19-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.68
Std Dev 11.48
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

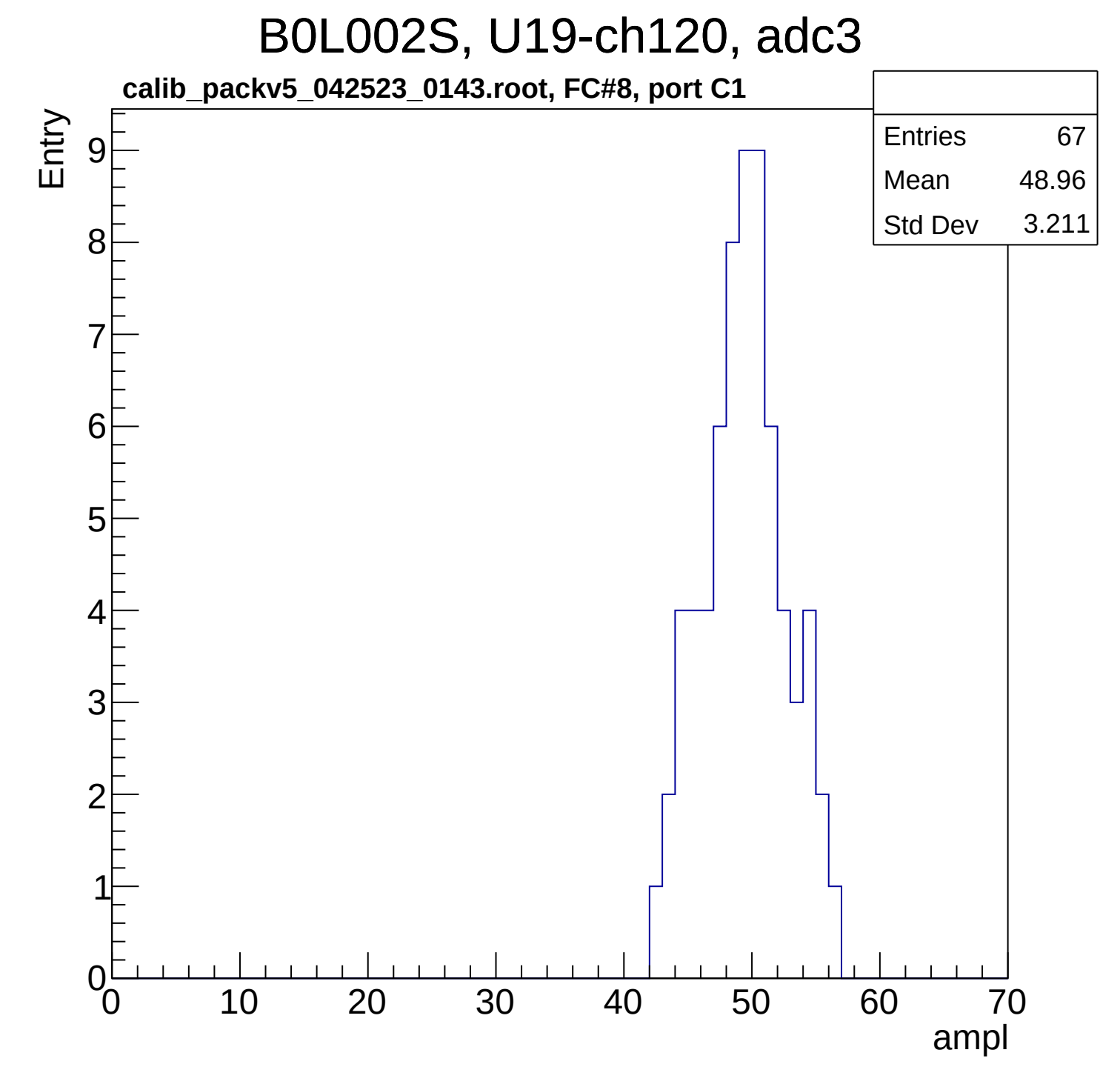

B0L002S, U19-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

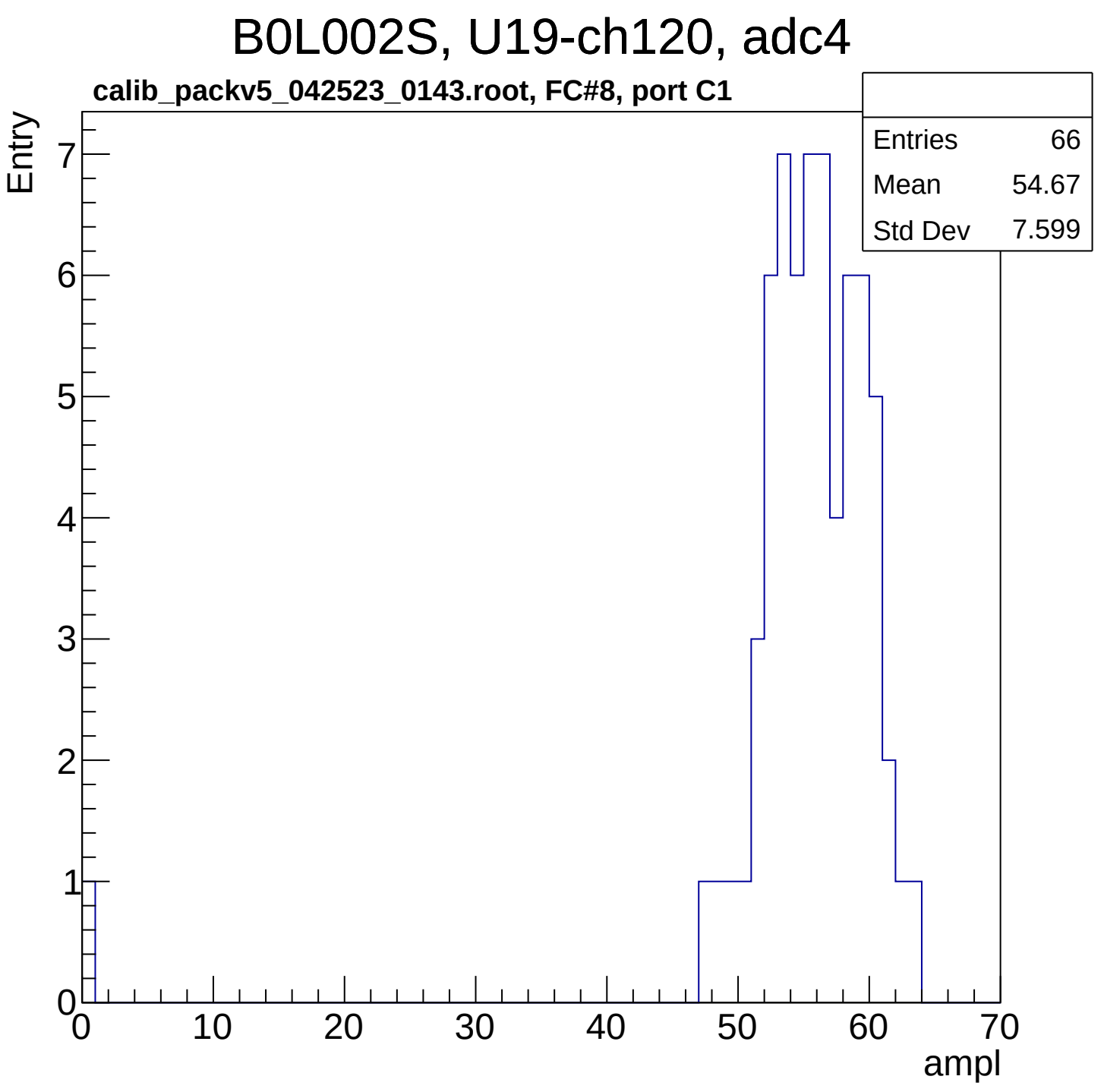

B0L002S, U19-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

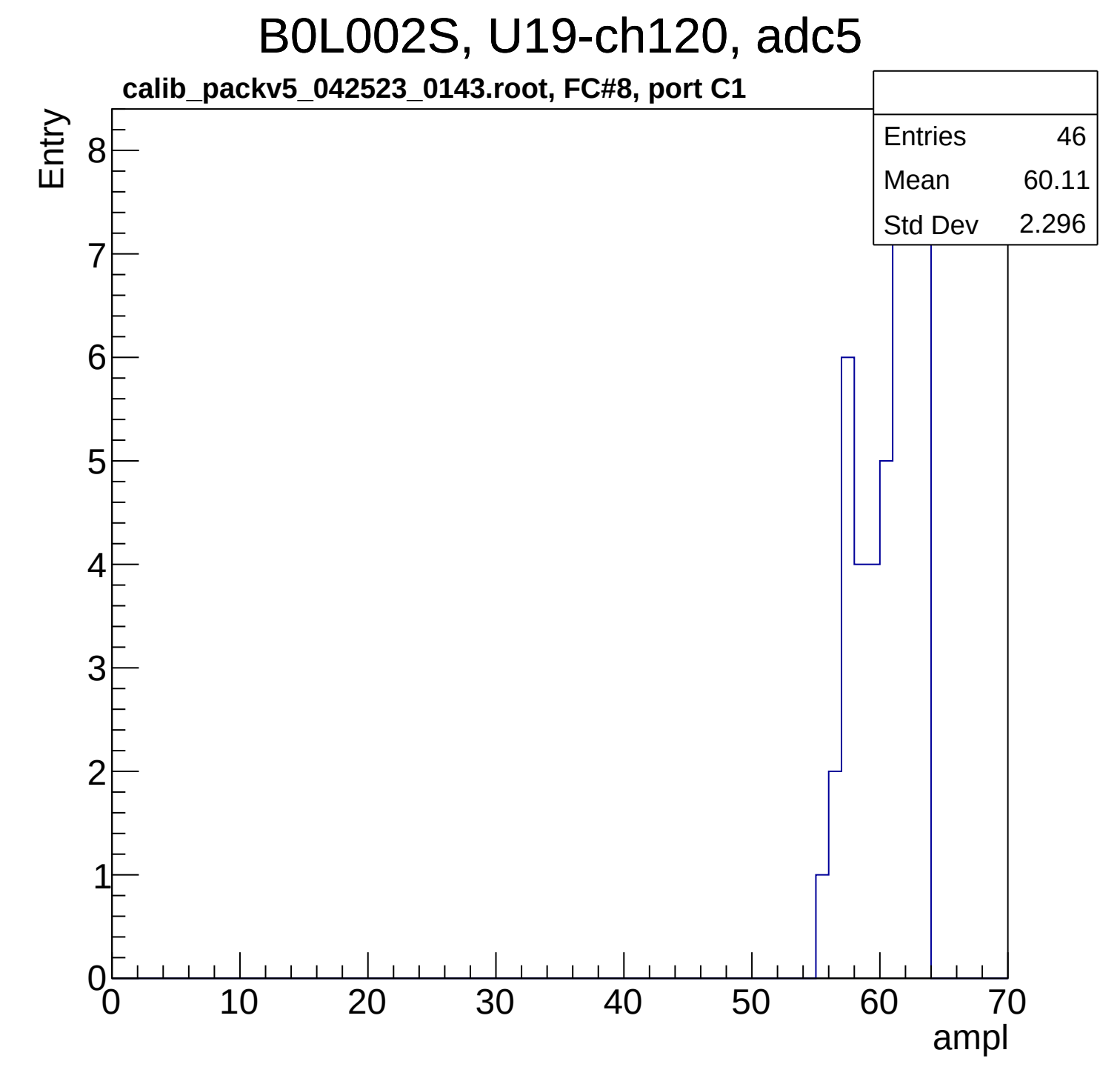

B0L002S, U19-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 29.57
Std Dev 4.921
Gaus mean : 30.6582
Gaus Width: 3.9836
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

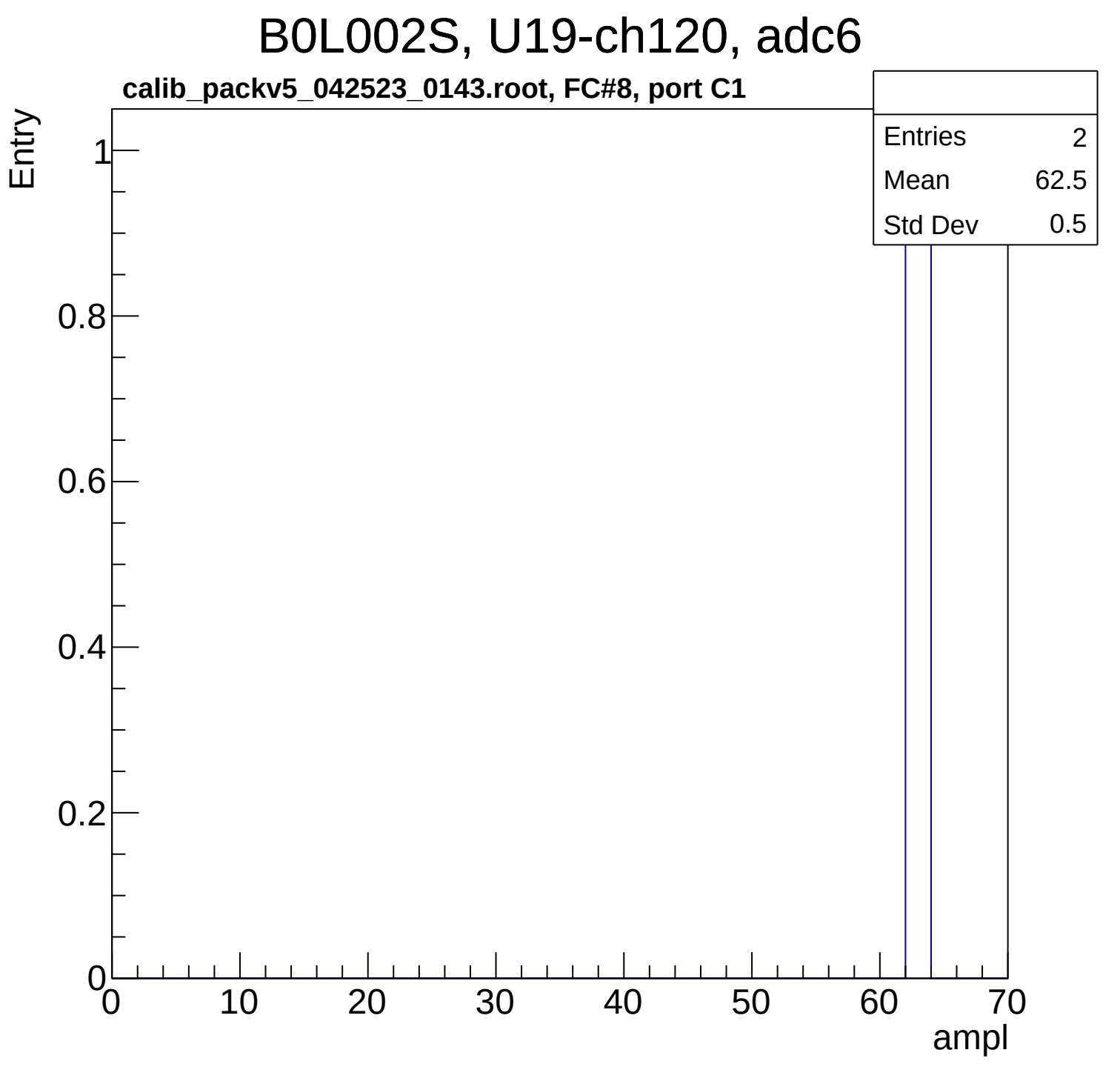

B0L002S, U19-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 36.86
Std Dev 3.011
Gaus mean : 37.3732
Gaus Width: 3.1058
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

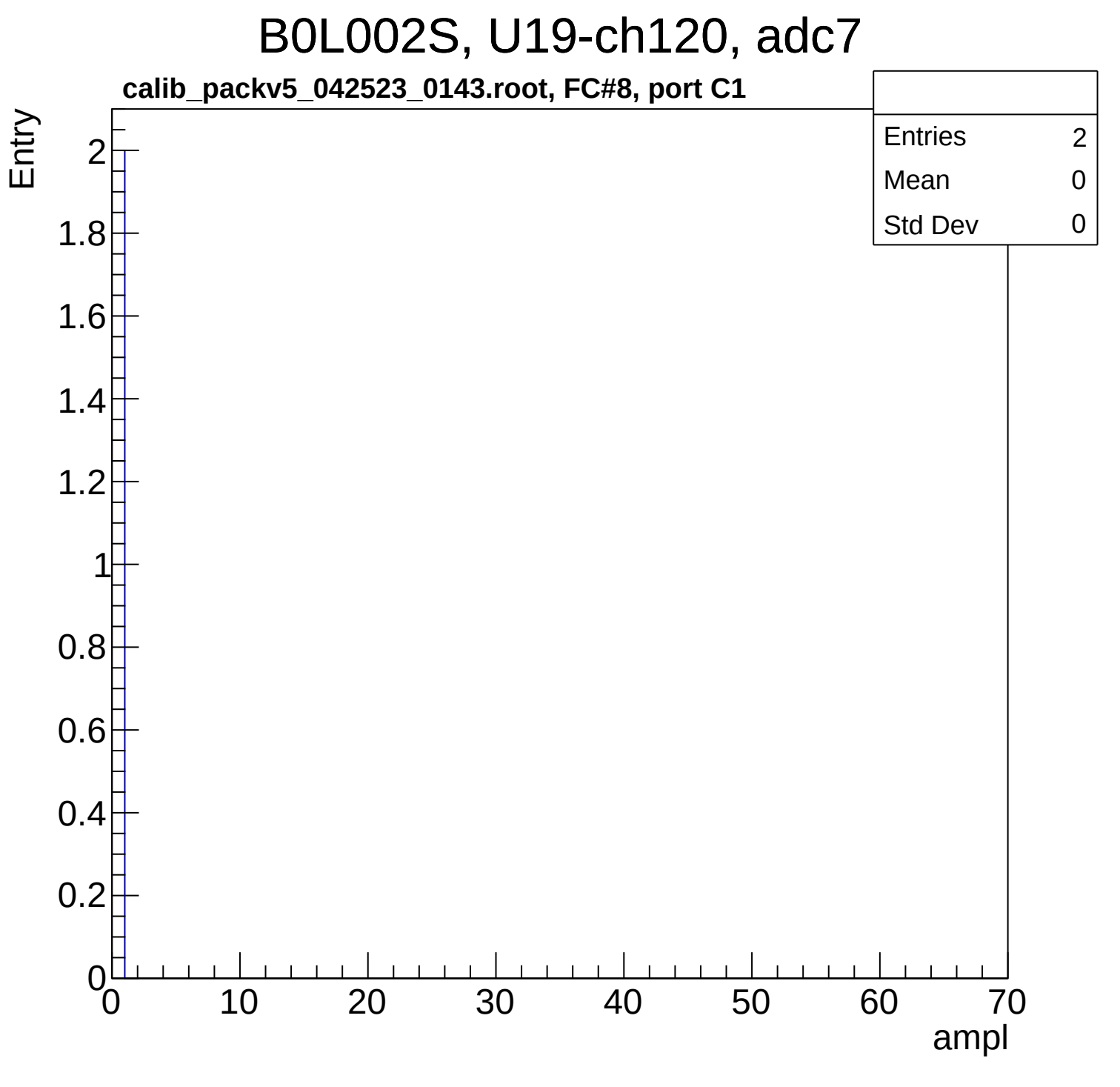

B0L002S, U19-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 42.59
Std Dev 3.135
Gaus mean : 42.6868
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

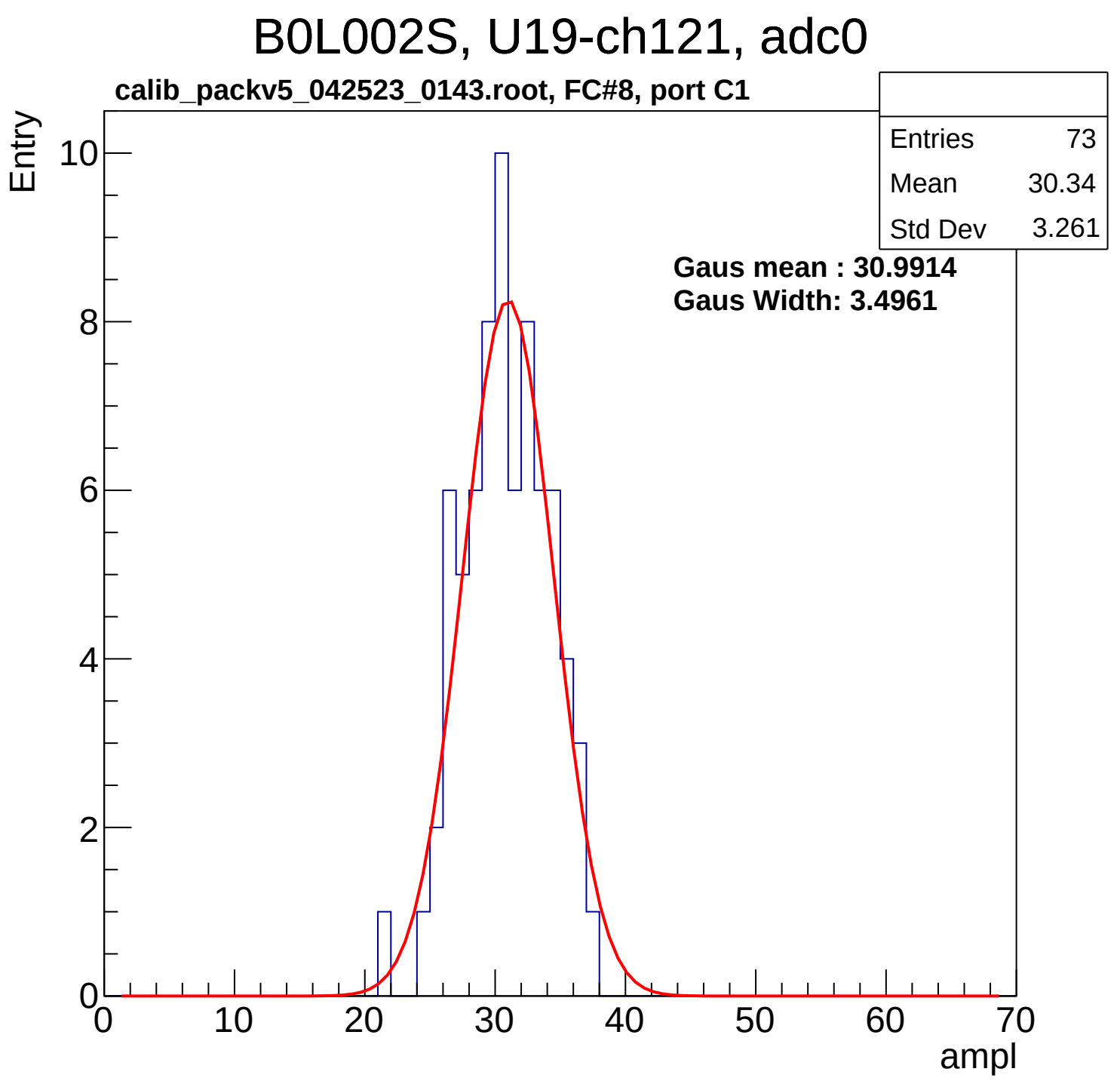

B0L002S, U19-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 48.96
Std Dev 3.211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

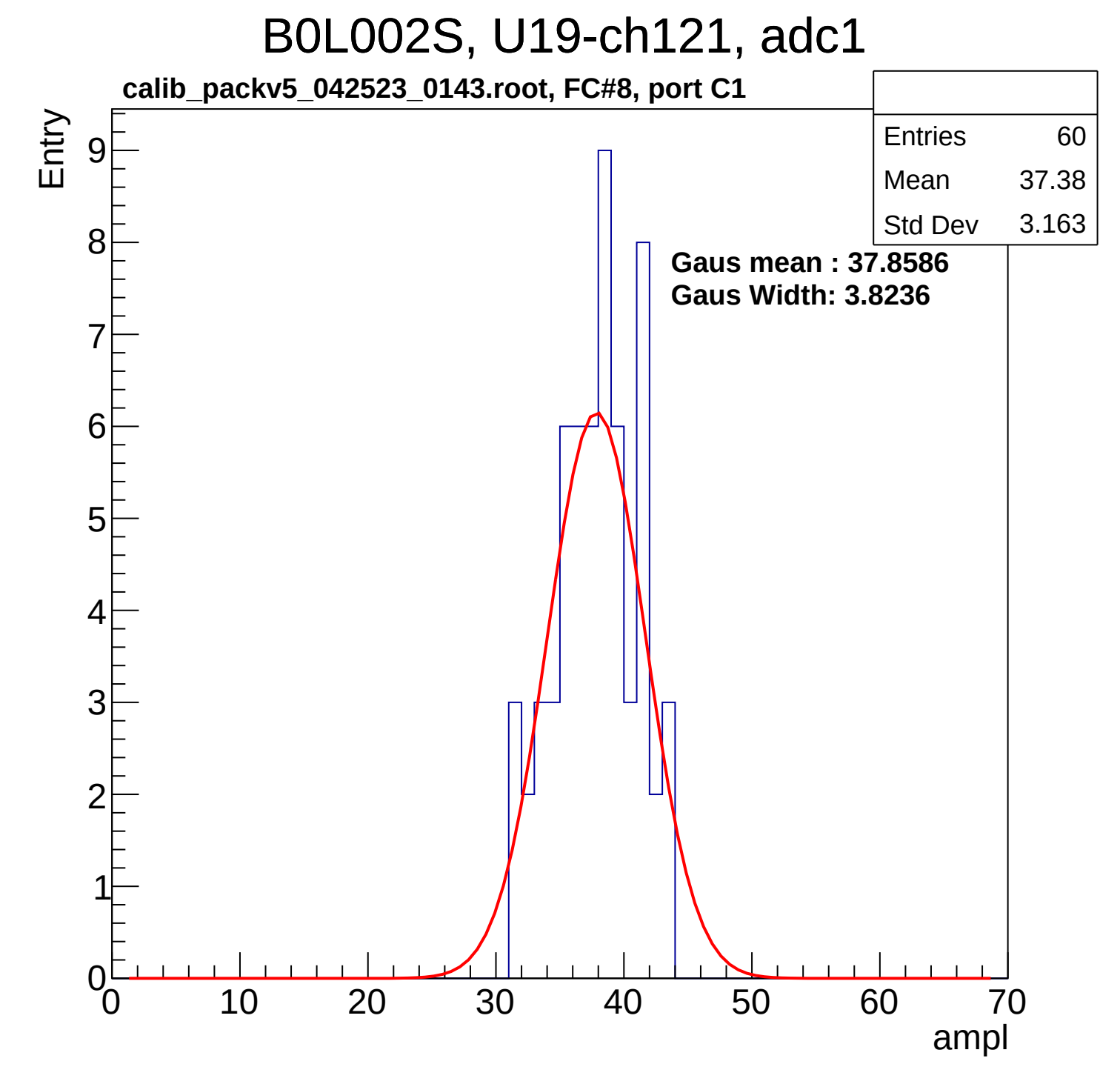

B0L002S, U19-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 54.67
Std Dev 7.599
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

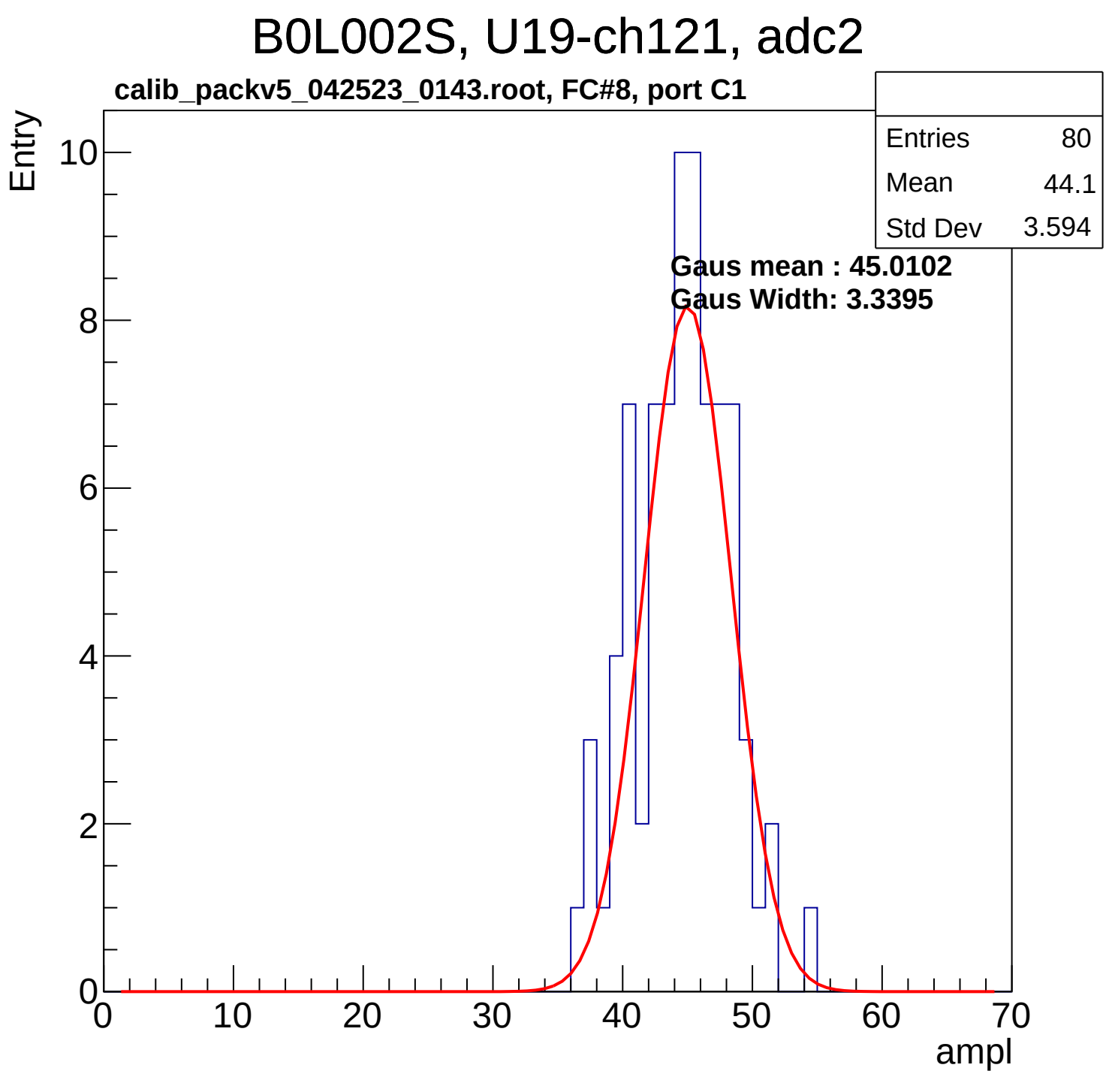

B0L002S, U19-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 60.11
Std Dev 2.296
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

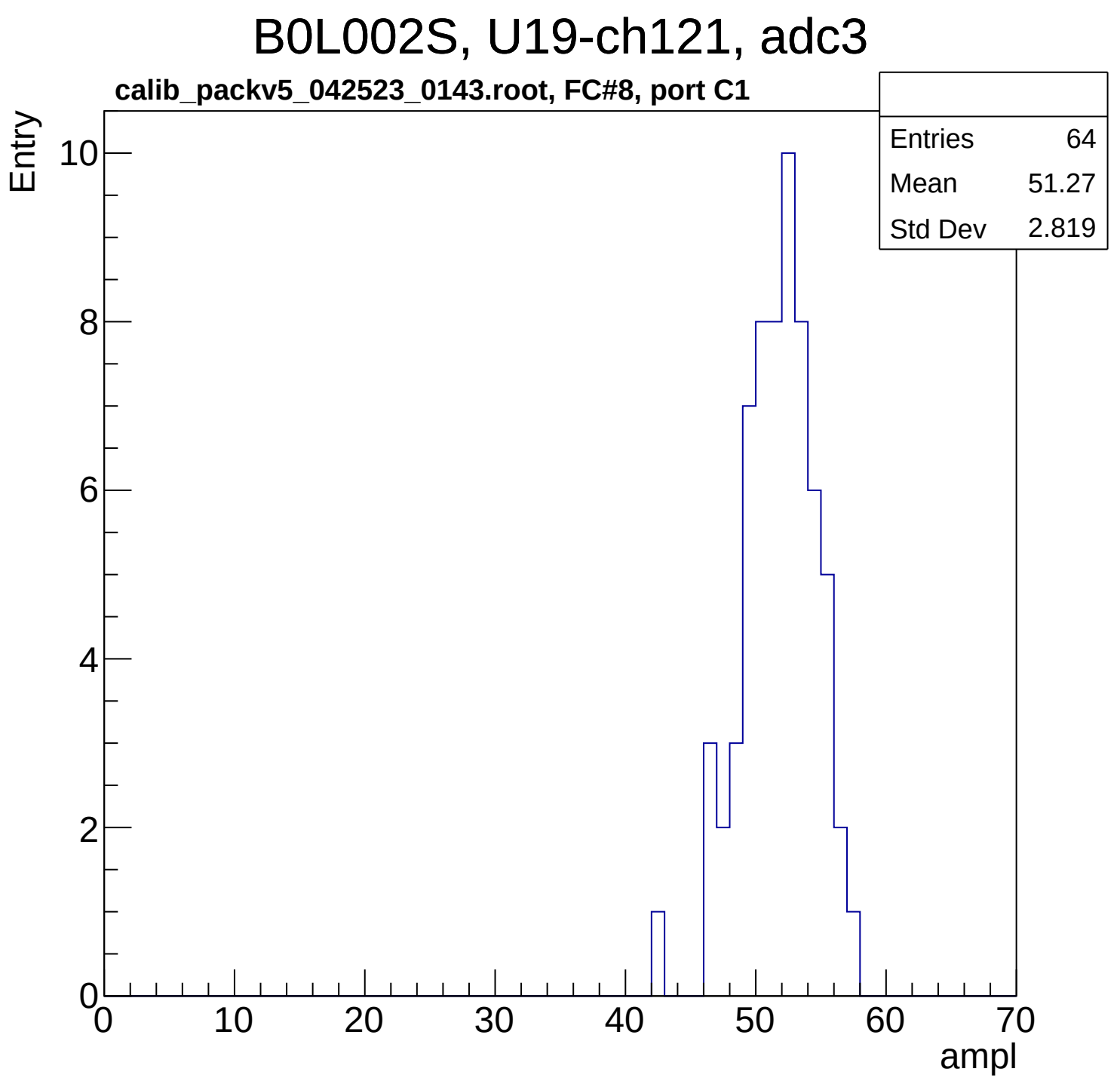

B0L002S, U19-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

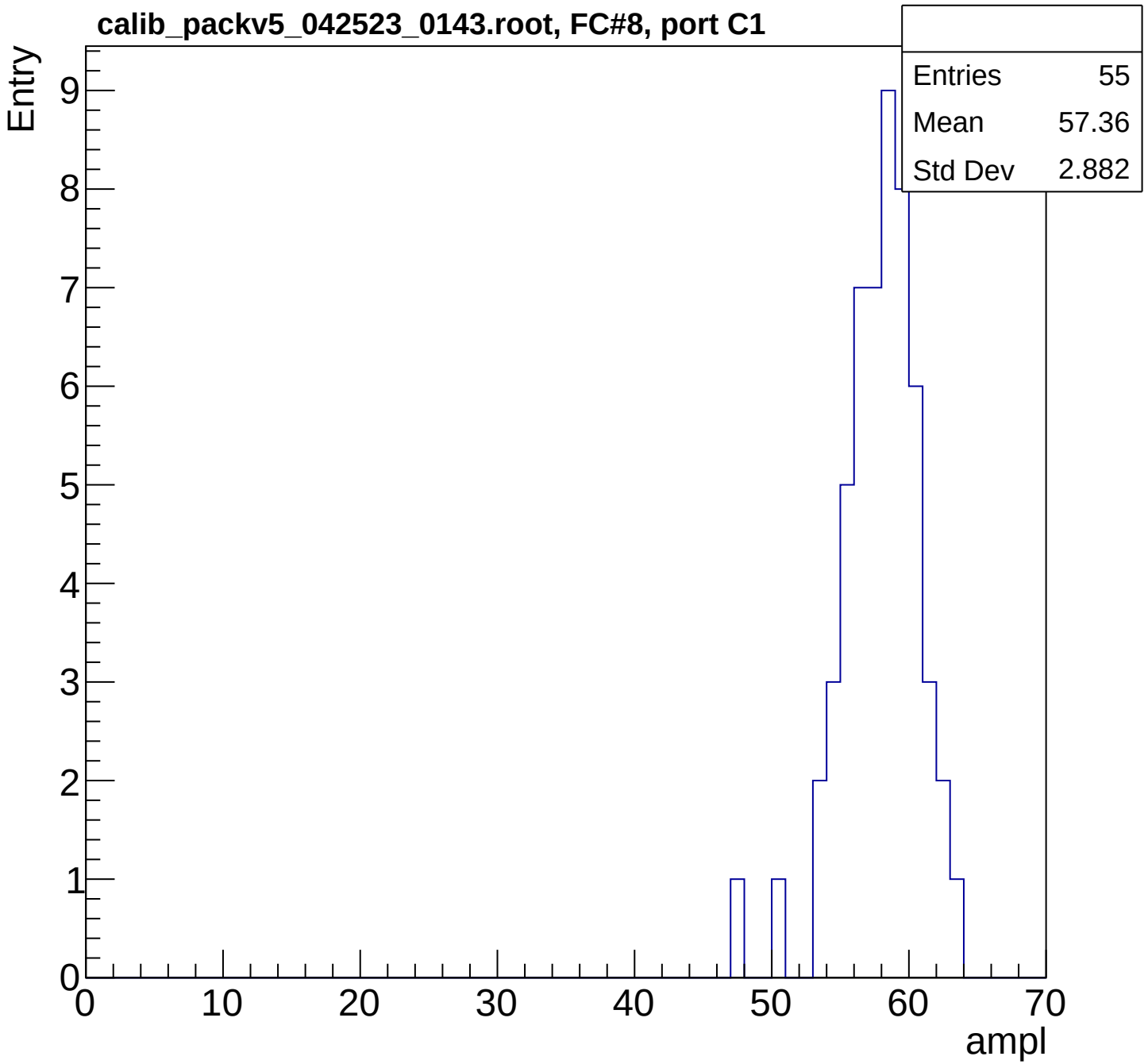

B0L002S, U19-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

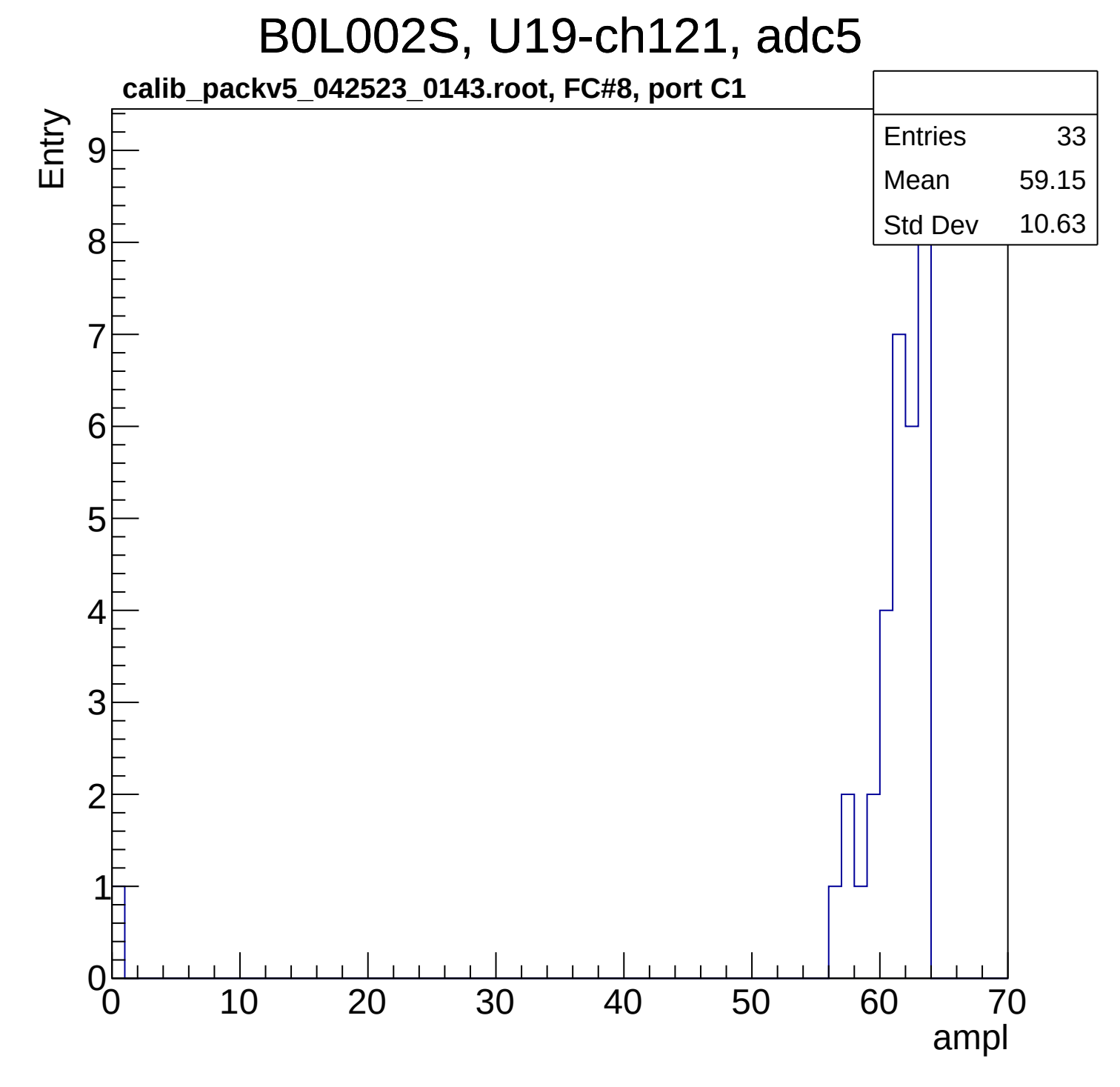

B0L002S, U19-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.34
Std Dev 3.261
Gaus mean : 30.9914
Gaus Width: 3.4961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

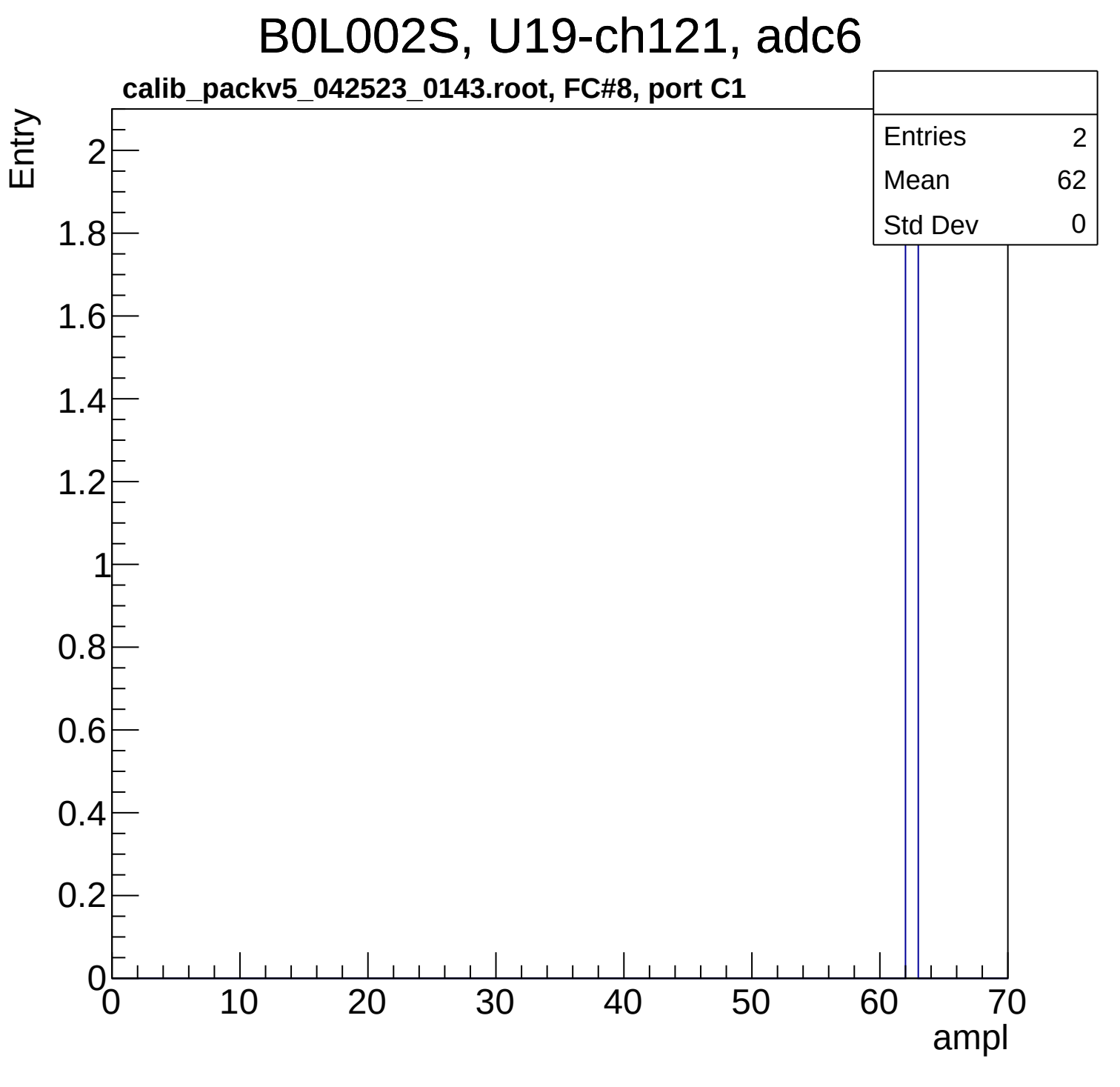

B0L002S, U19-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.38
Std Dev 3.163
Gaus mean : 37.8586
Gaus Width: 3.8236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

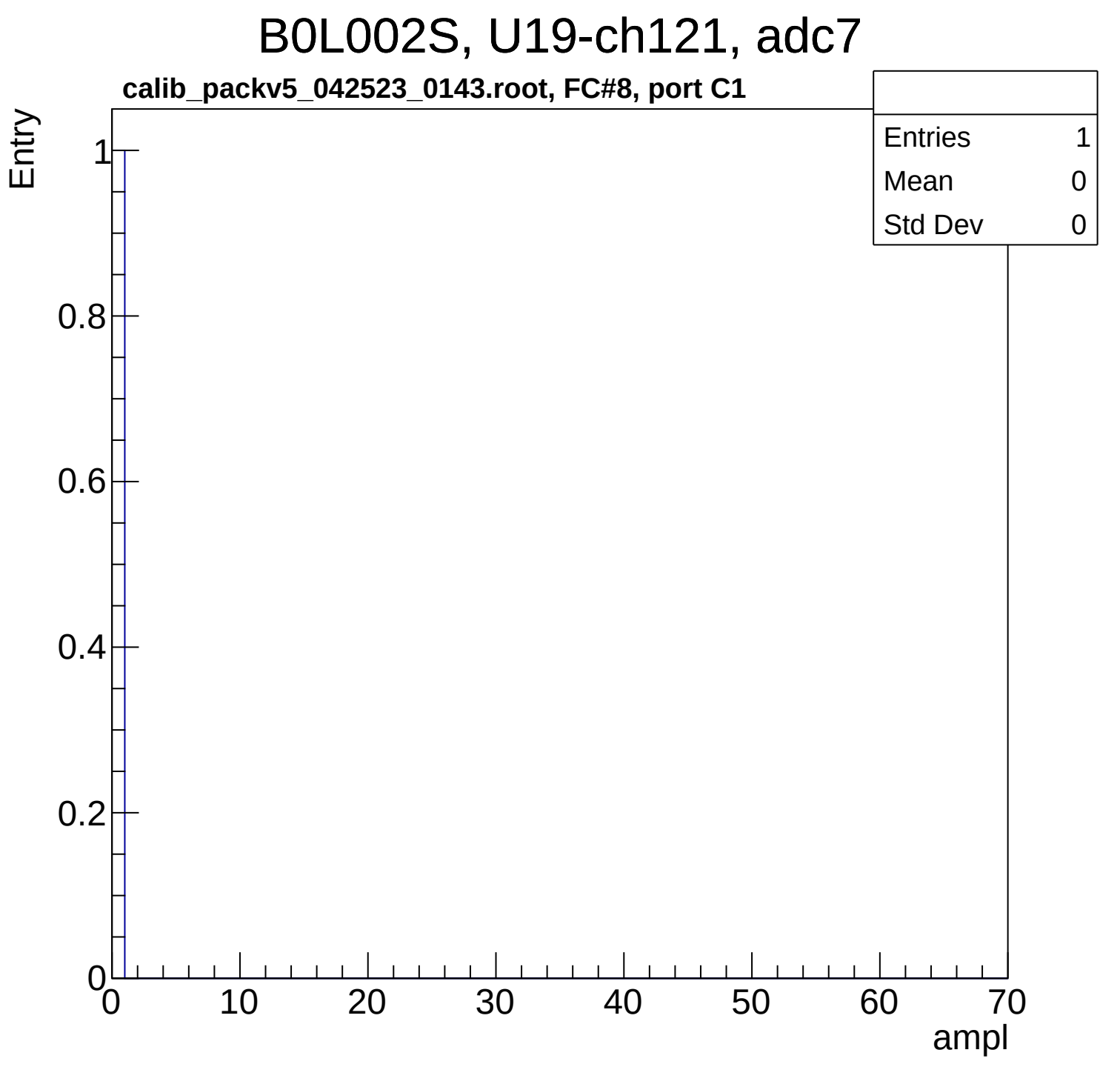

B0L002S, U19-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 44.1
Std Dev 3.594
Gaus mean : 45.0102
Gaus Width: 3.3395
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

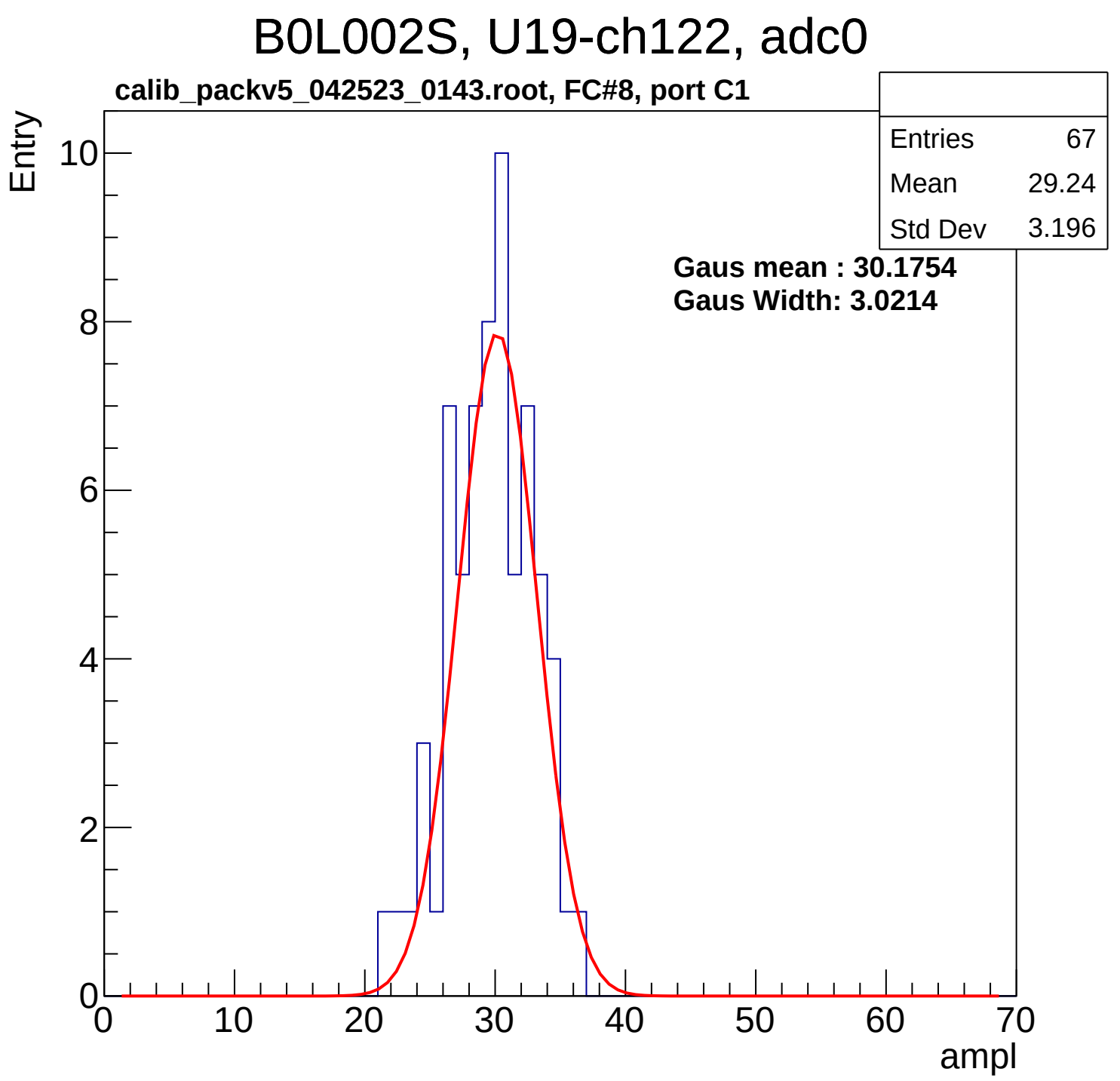

B0L002S, U19-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.27
Std Dev 2.819
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

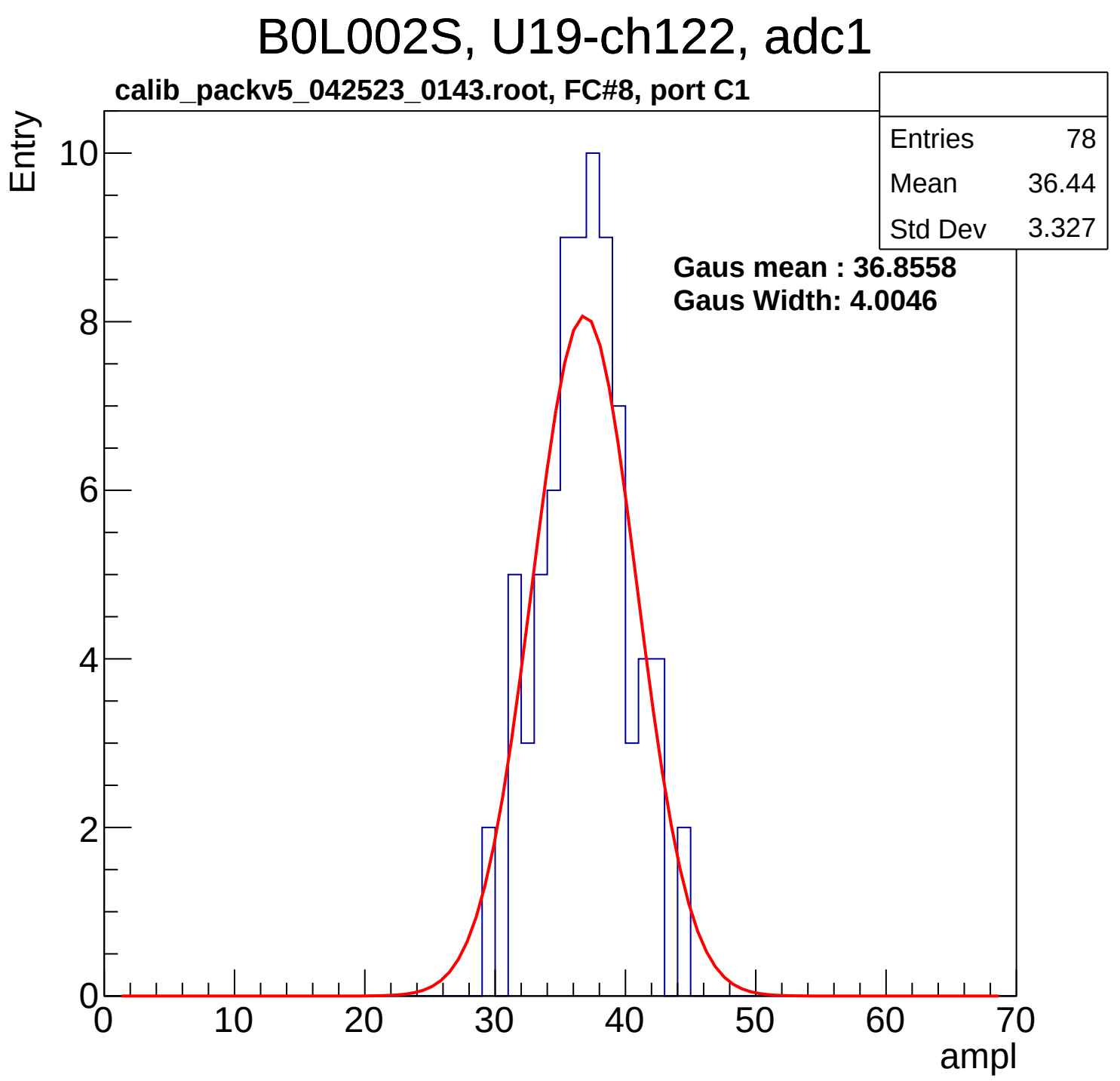

B0L002S, U19-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.36
Std Dev 2.882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

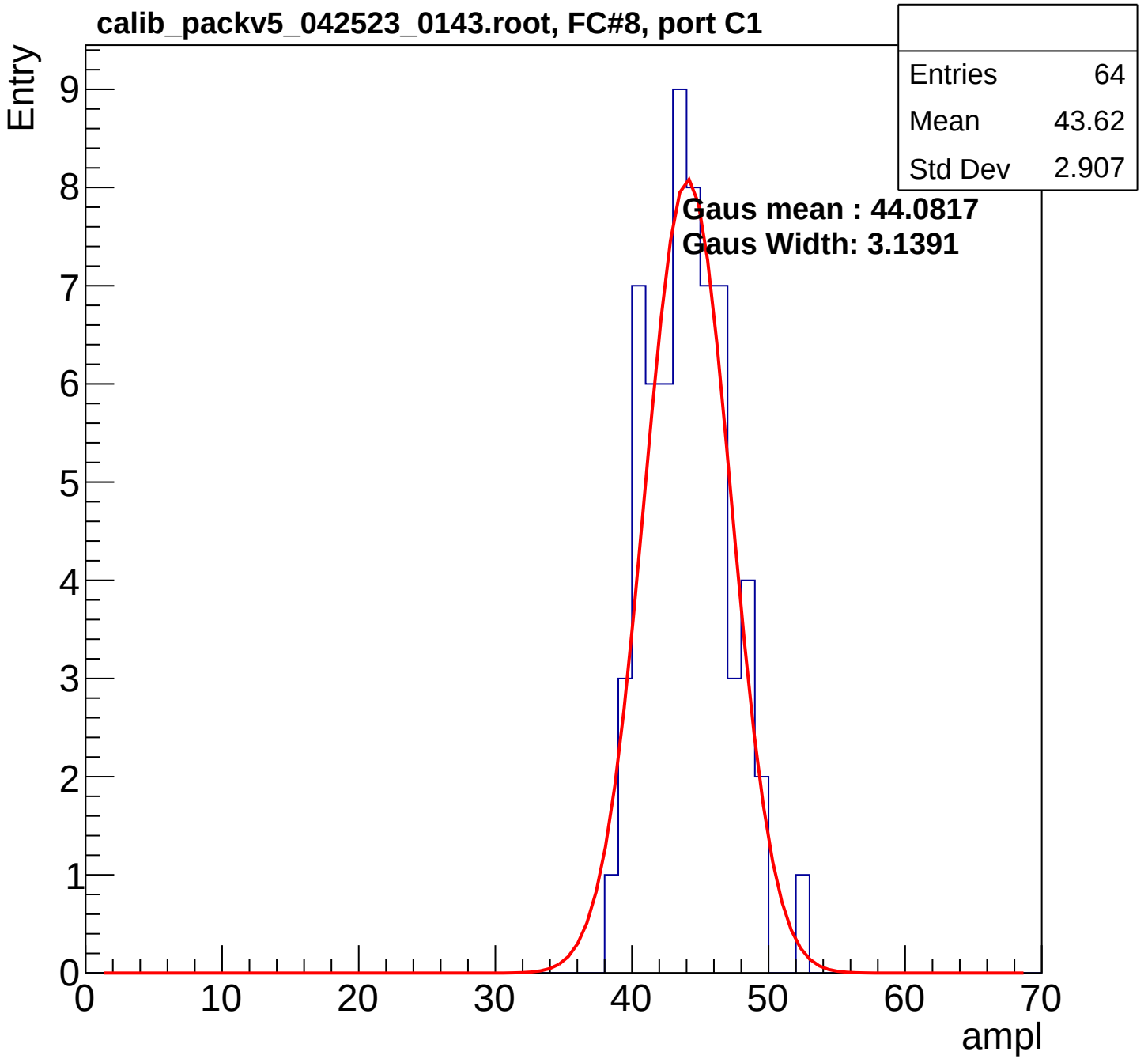

B0L002S, U19-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.15
Std Dev 10.63
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

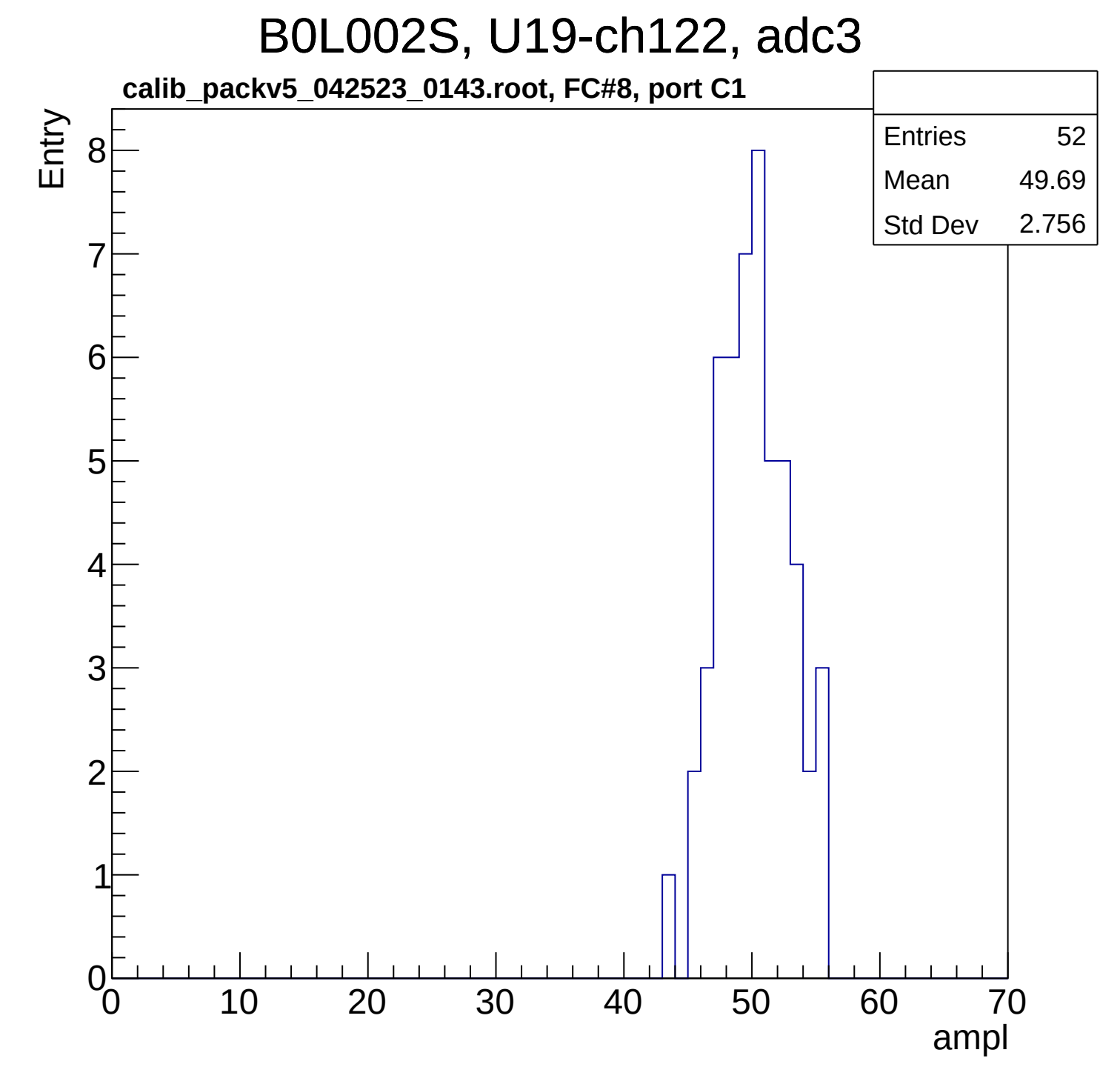

B0L002S, U19-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

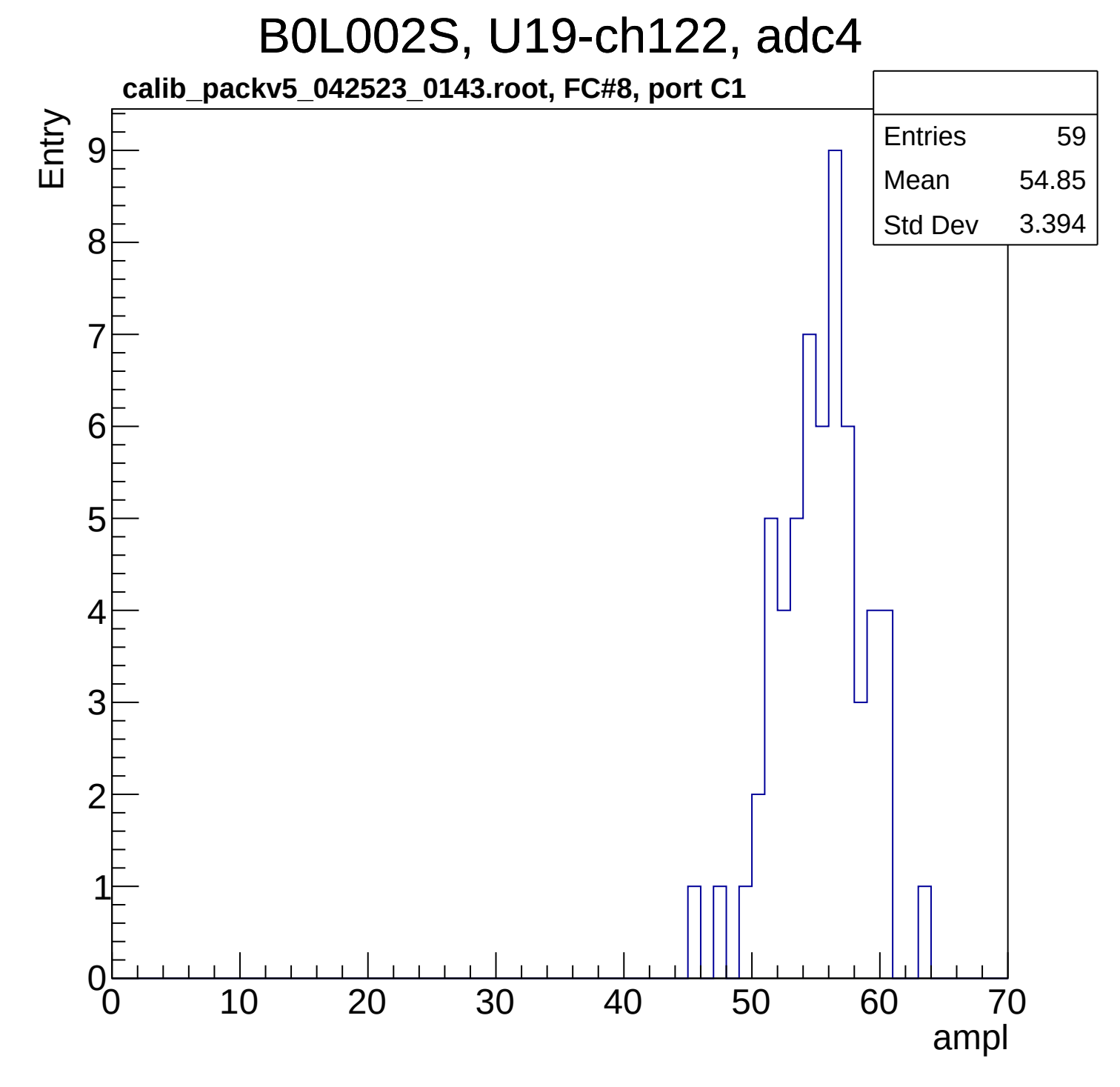

B0L002S, U19-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

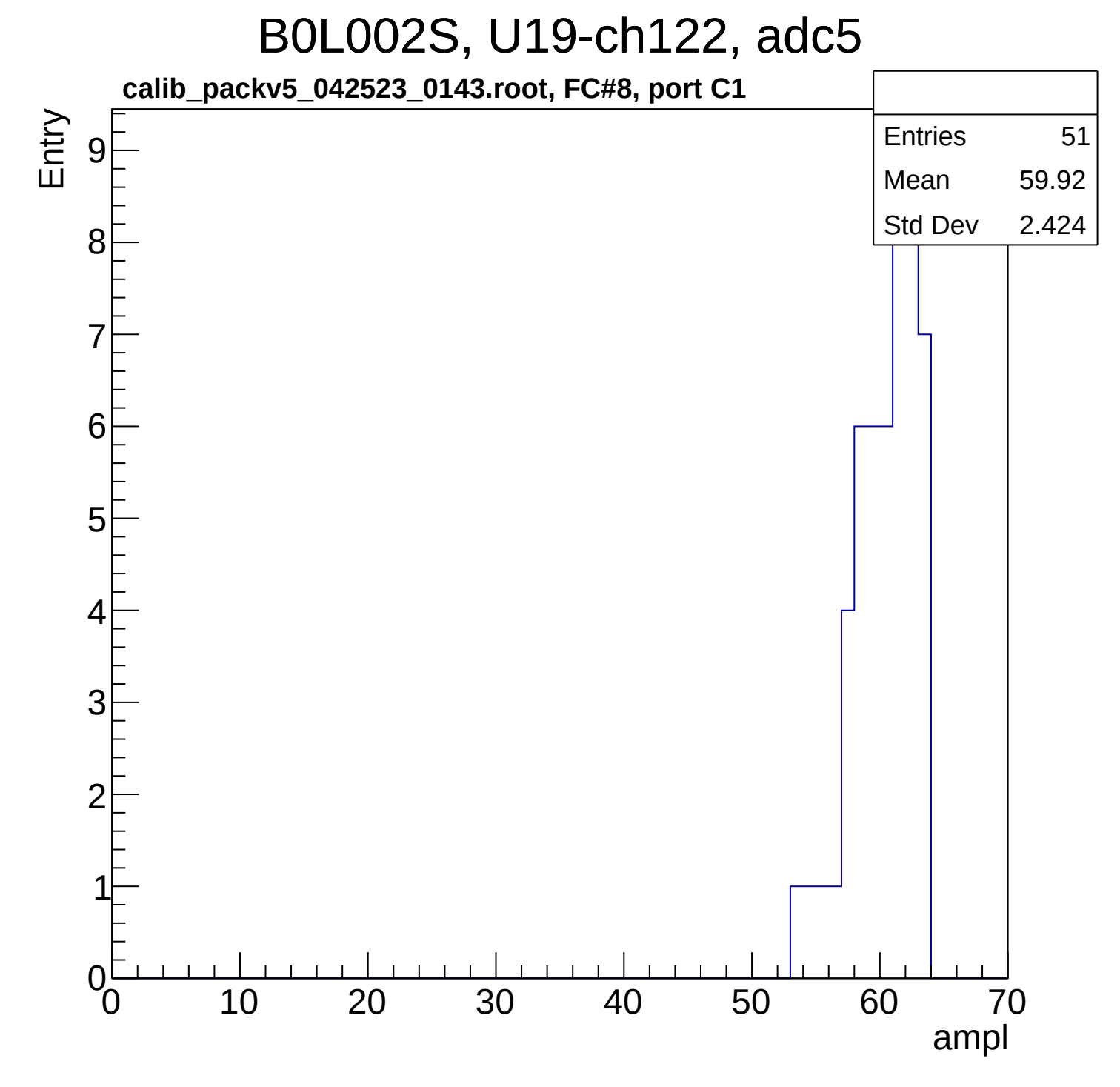

B0L002S, U19-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.24
Std Dev 3.196
Gaus mean : 30.1754
Gaus Width: 3.0214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

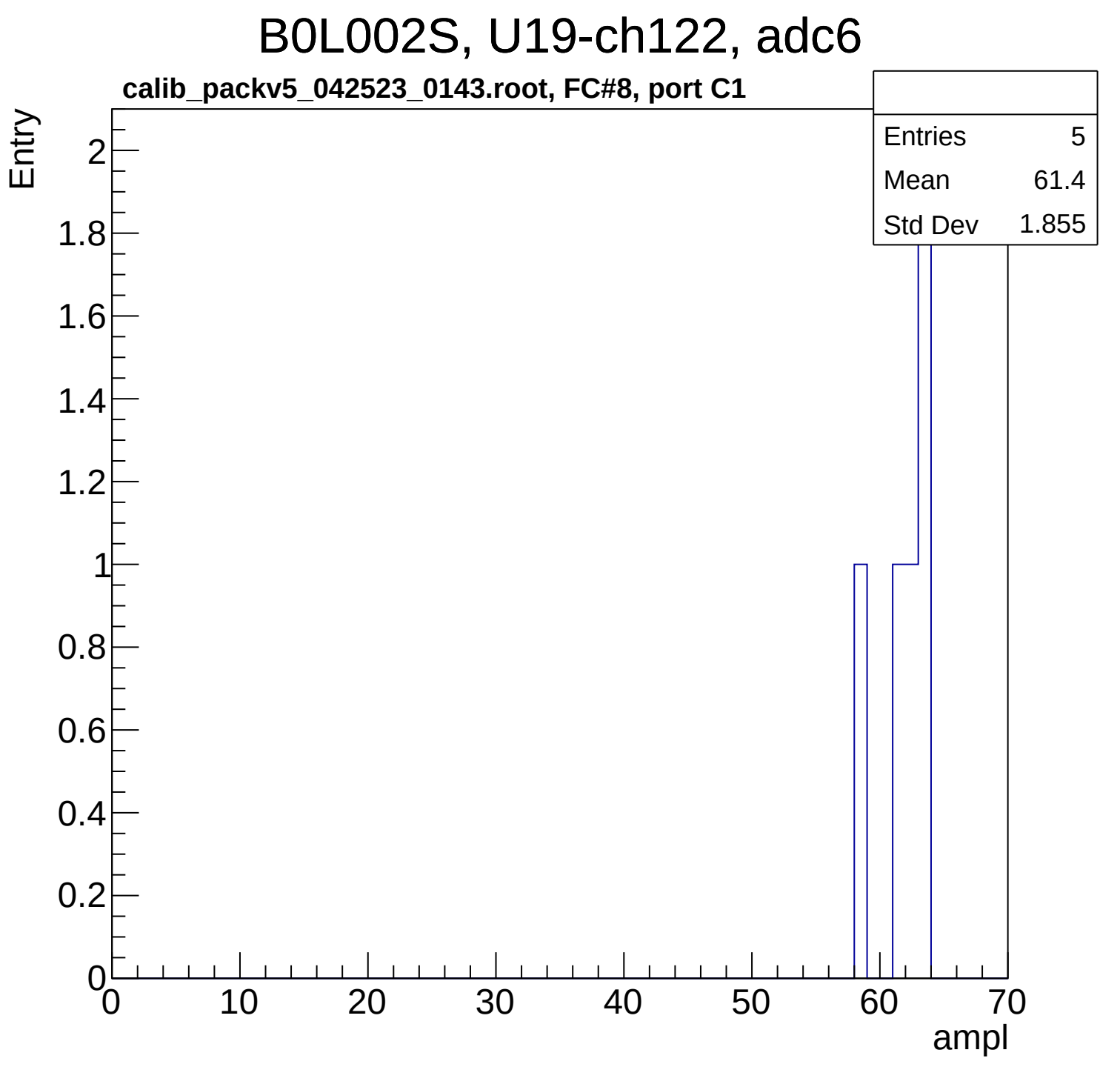

B0L002S, U19-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 36.44
Std Dev 3.327
Gaus mean : 36.8558
Gaus Width: 4.0046
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

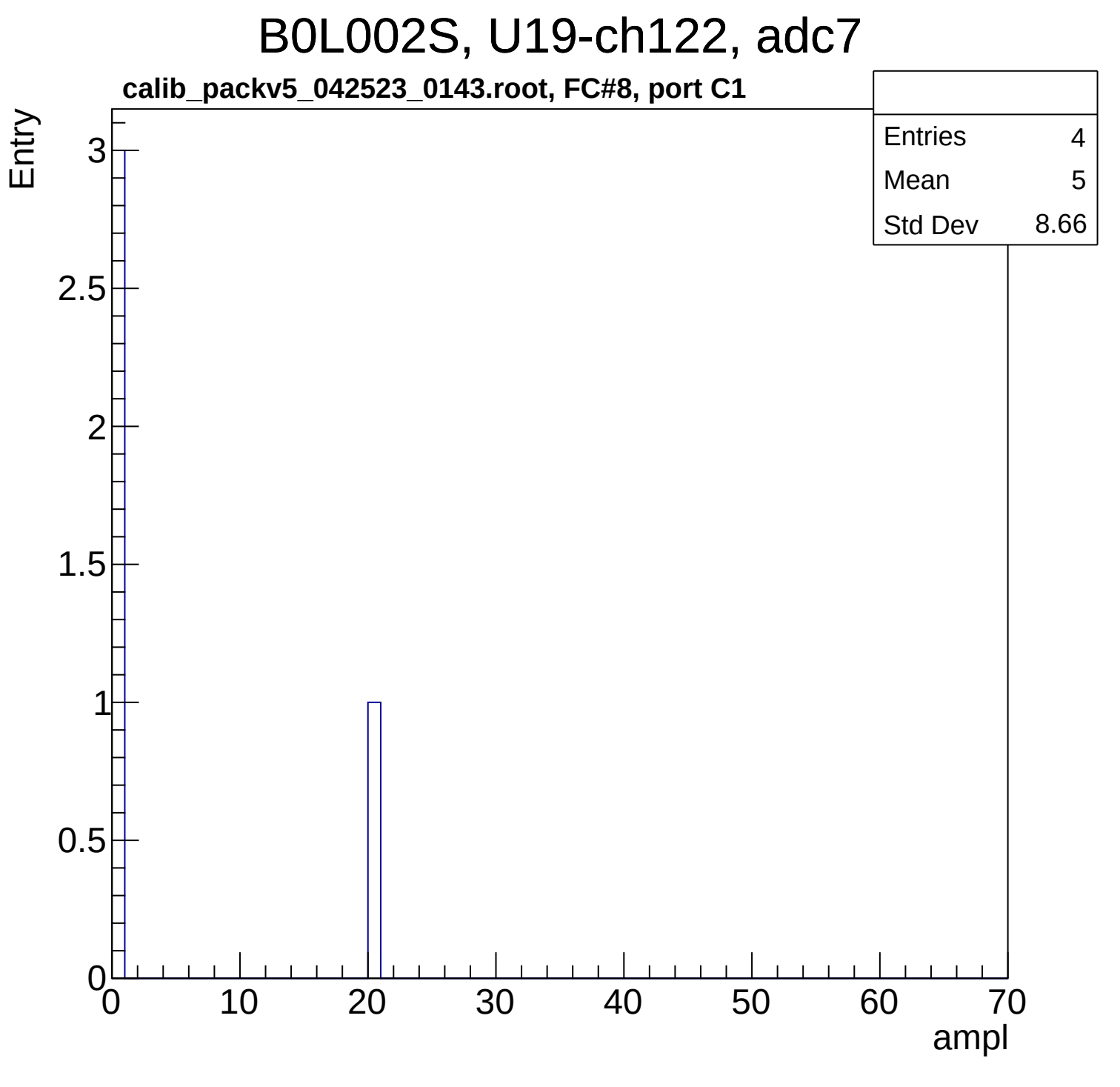

B0L002S, U19-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.62
Std Dev 2.907
Gaus mean : 44.0817
Gaus Width: 3.1391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

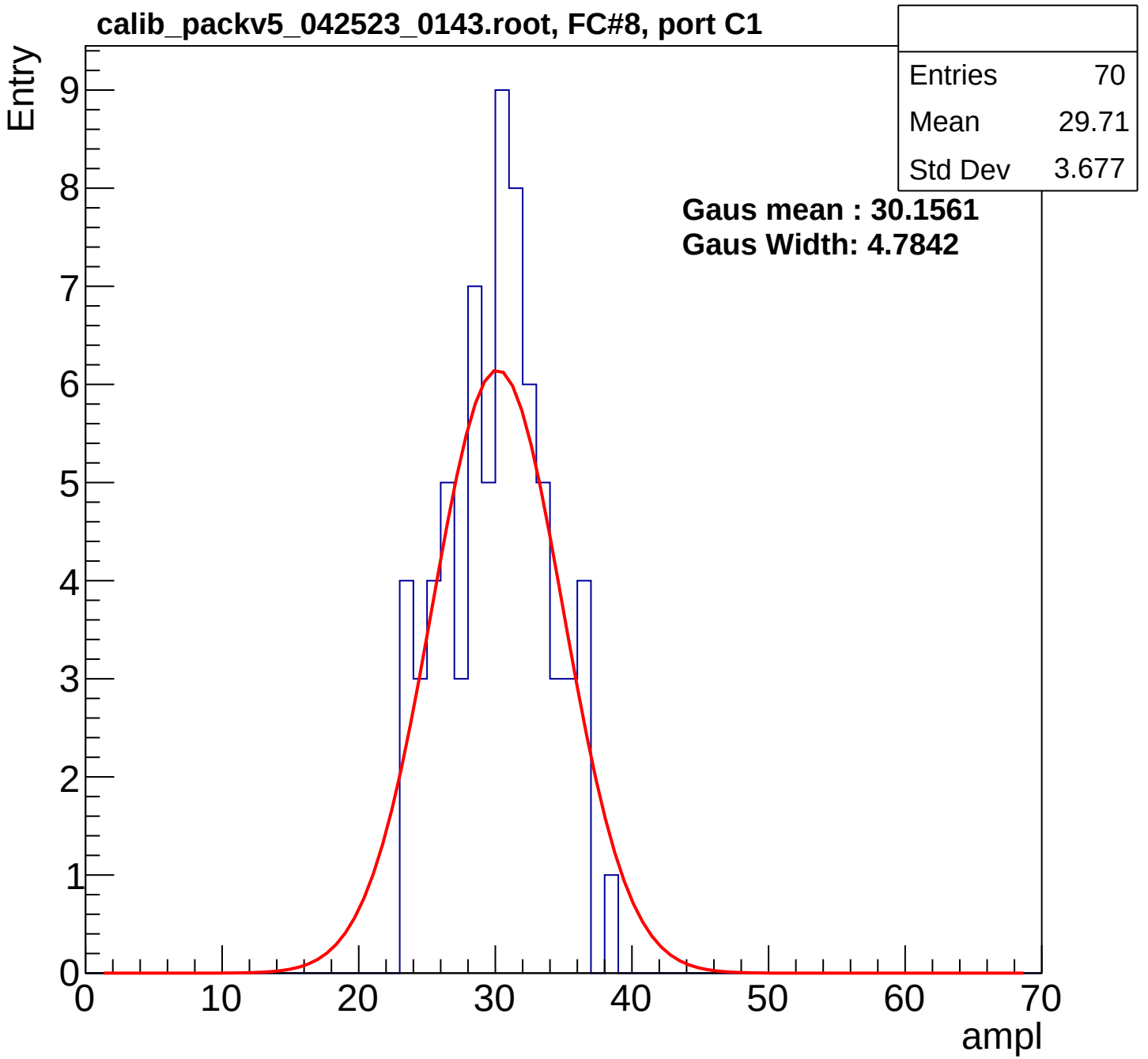

B0L002S, U19-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 49.69
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

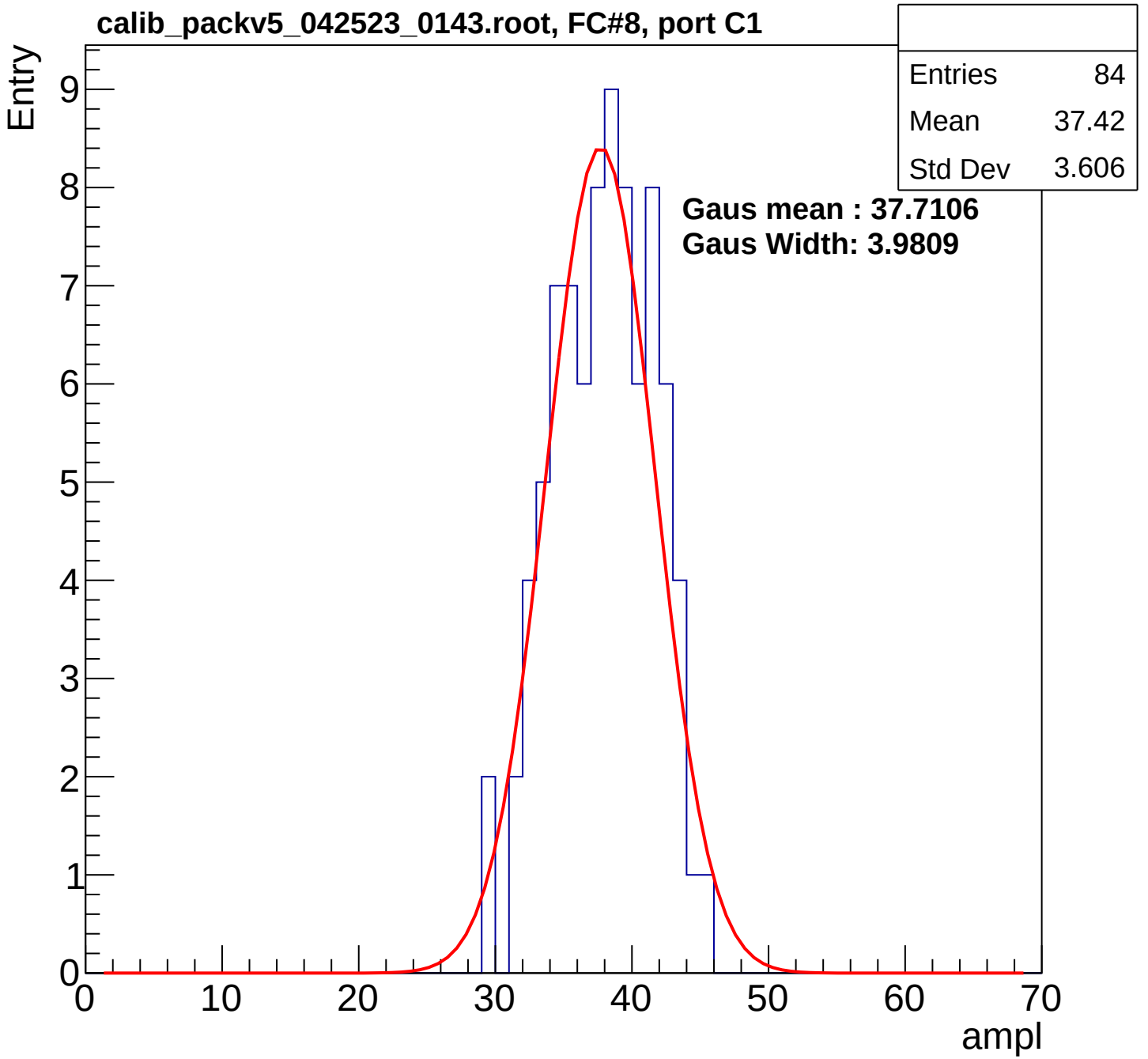

B0L002S, U19-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 54.85
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

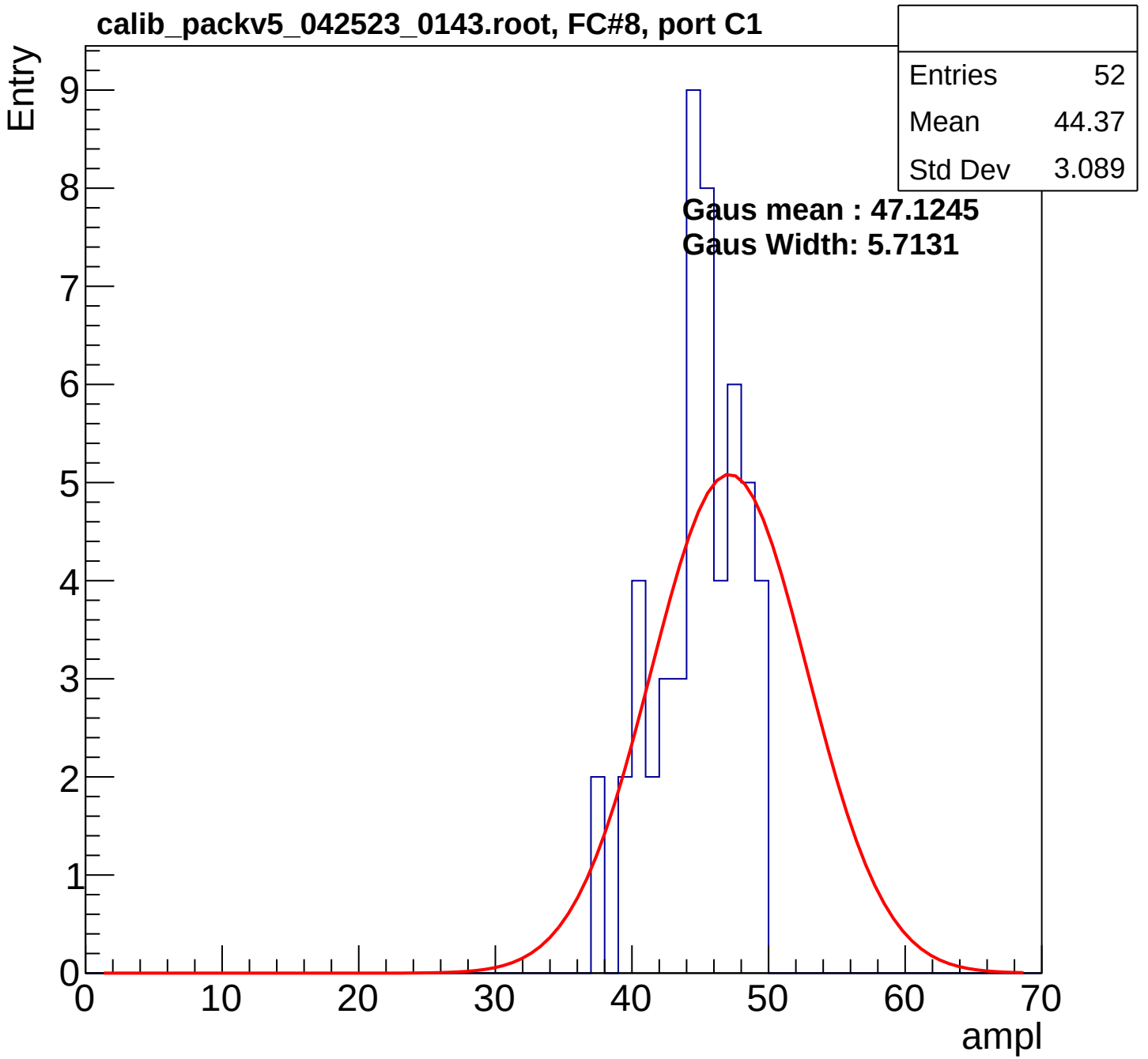

B0L002S, U19-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 59.92
Std Dev 2.424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

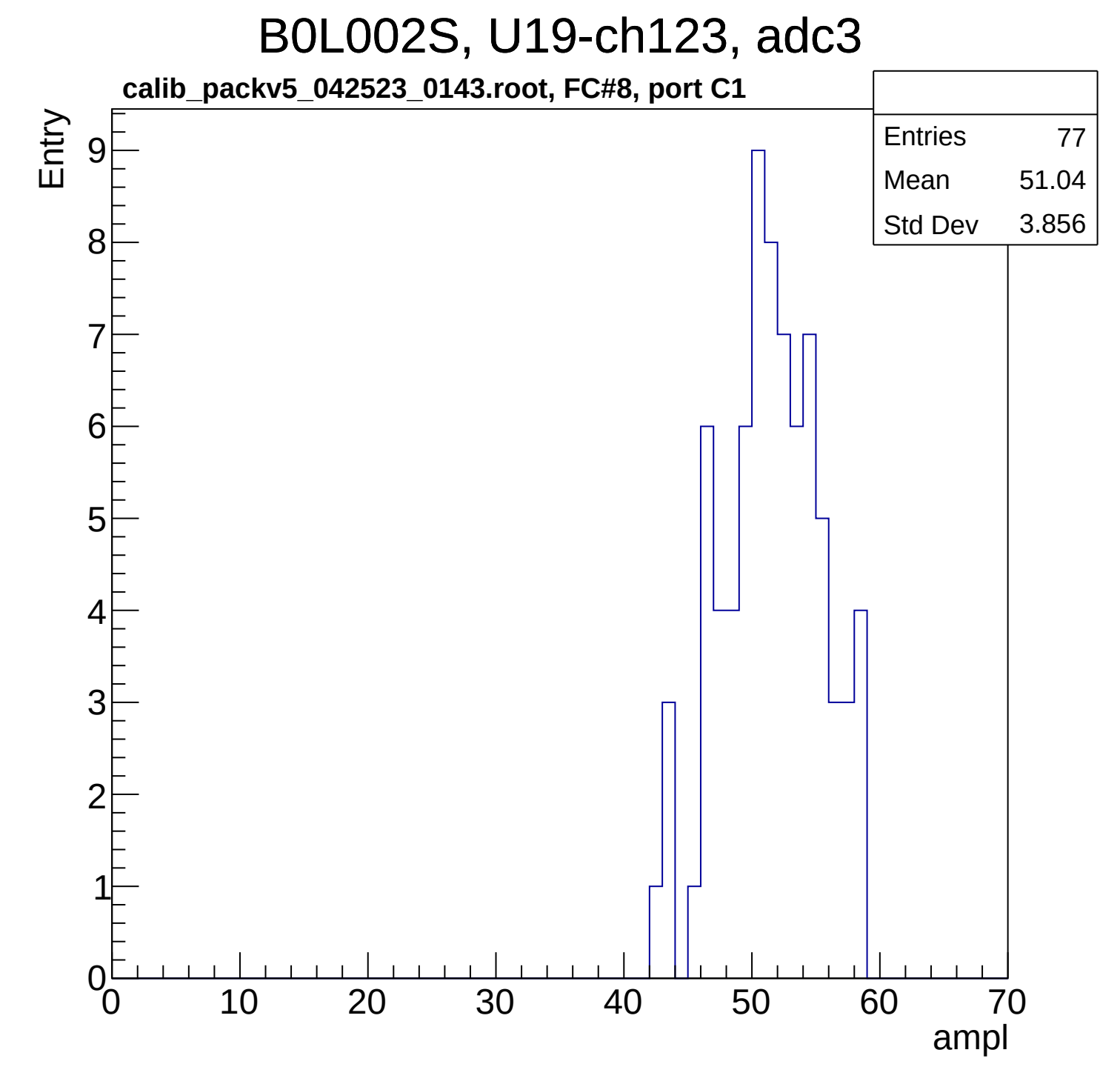

B0L002S, U19-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.4
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

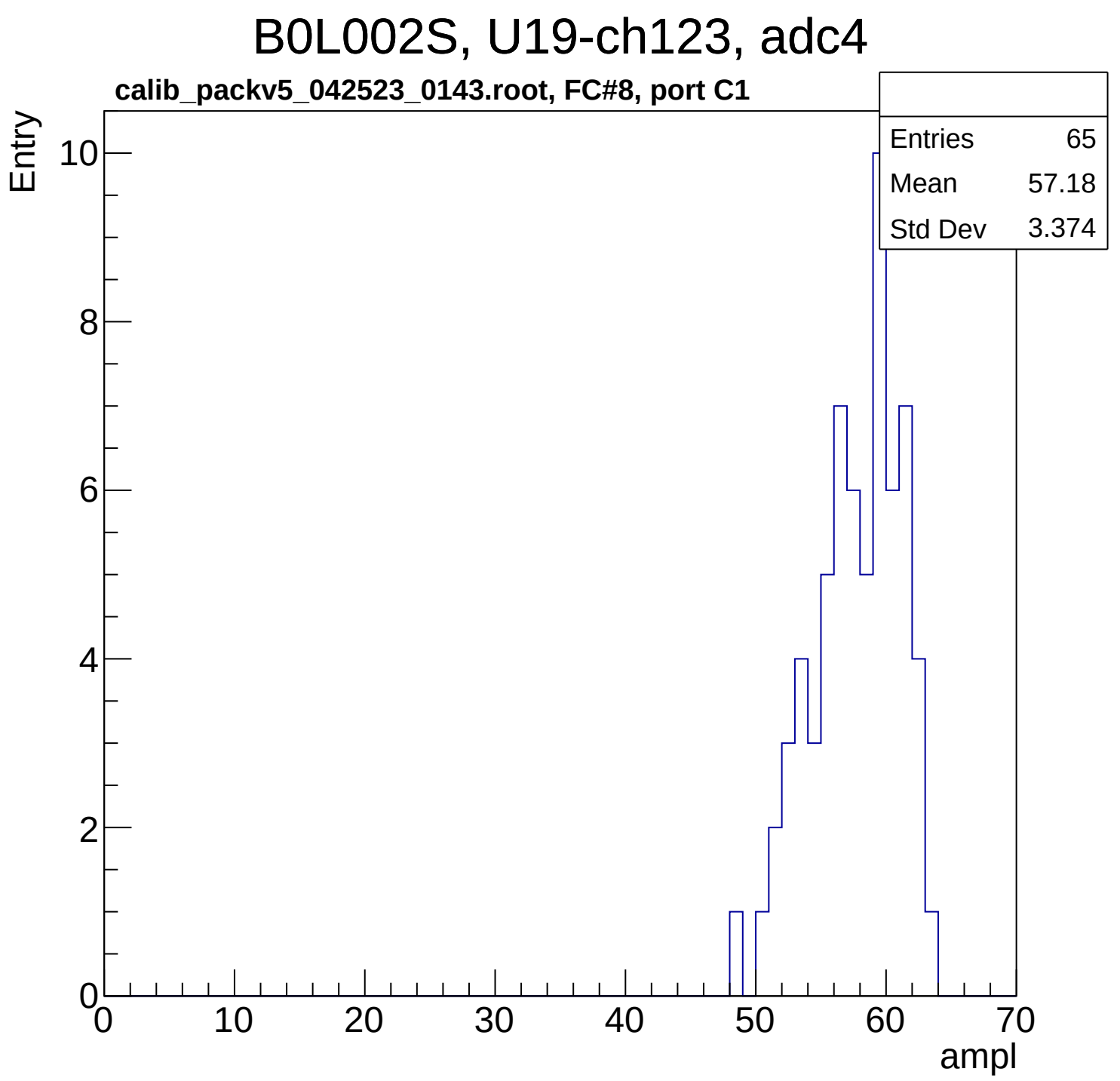

B0L002S, U19-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 5
Std Dev 8.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

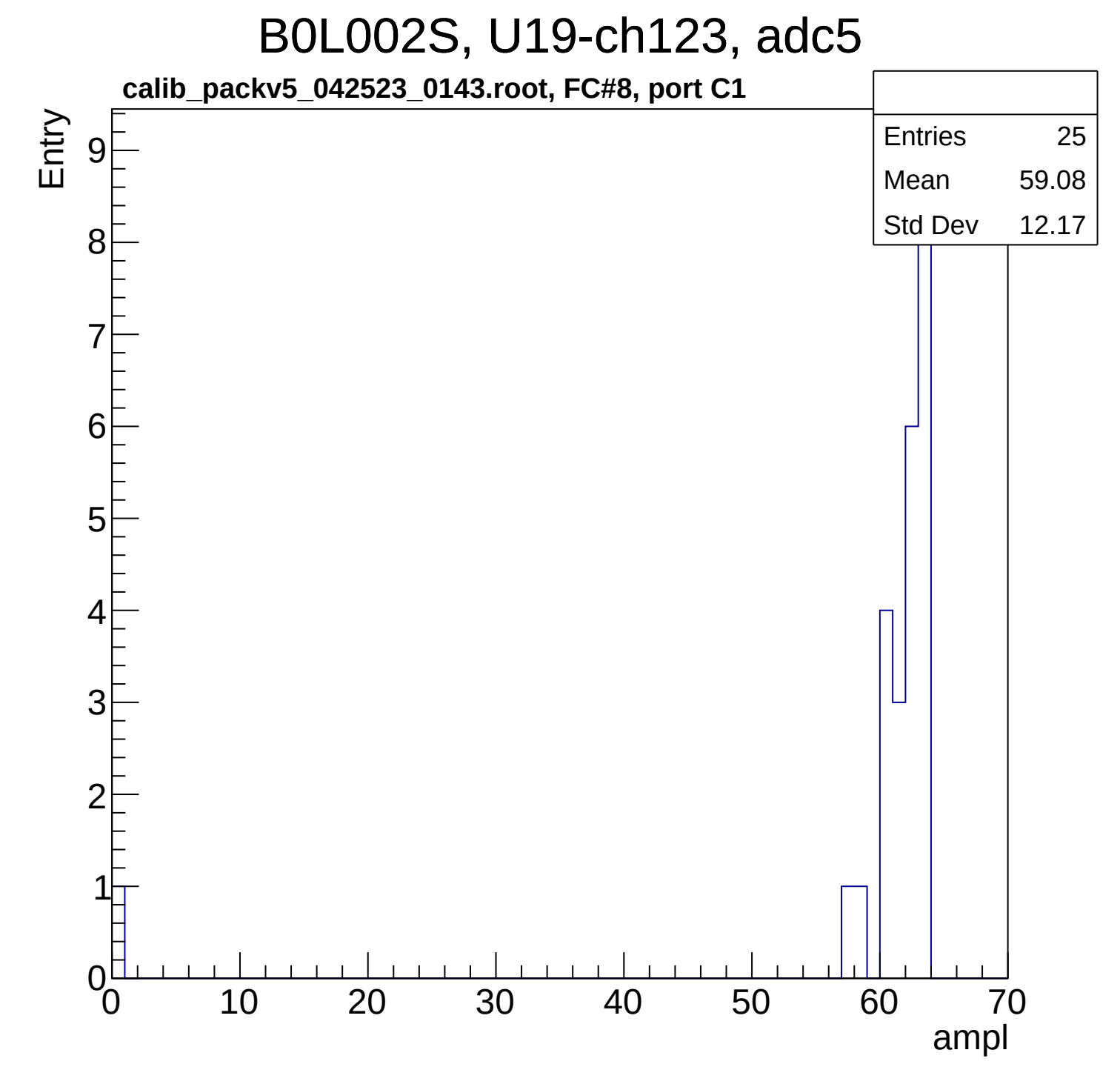

B0L002S, U19-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 29.71
Std Dev 3.677
Gaus mean : 30.1561
Gaus Width: 4.7842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

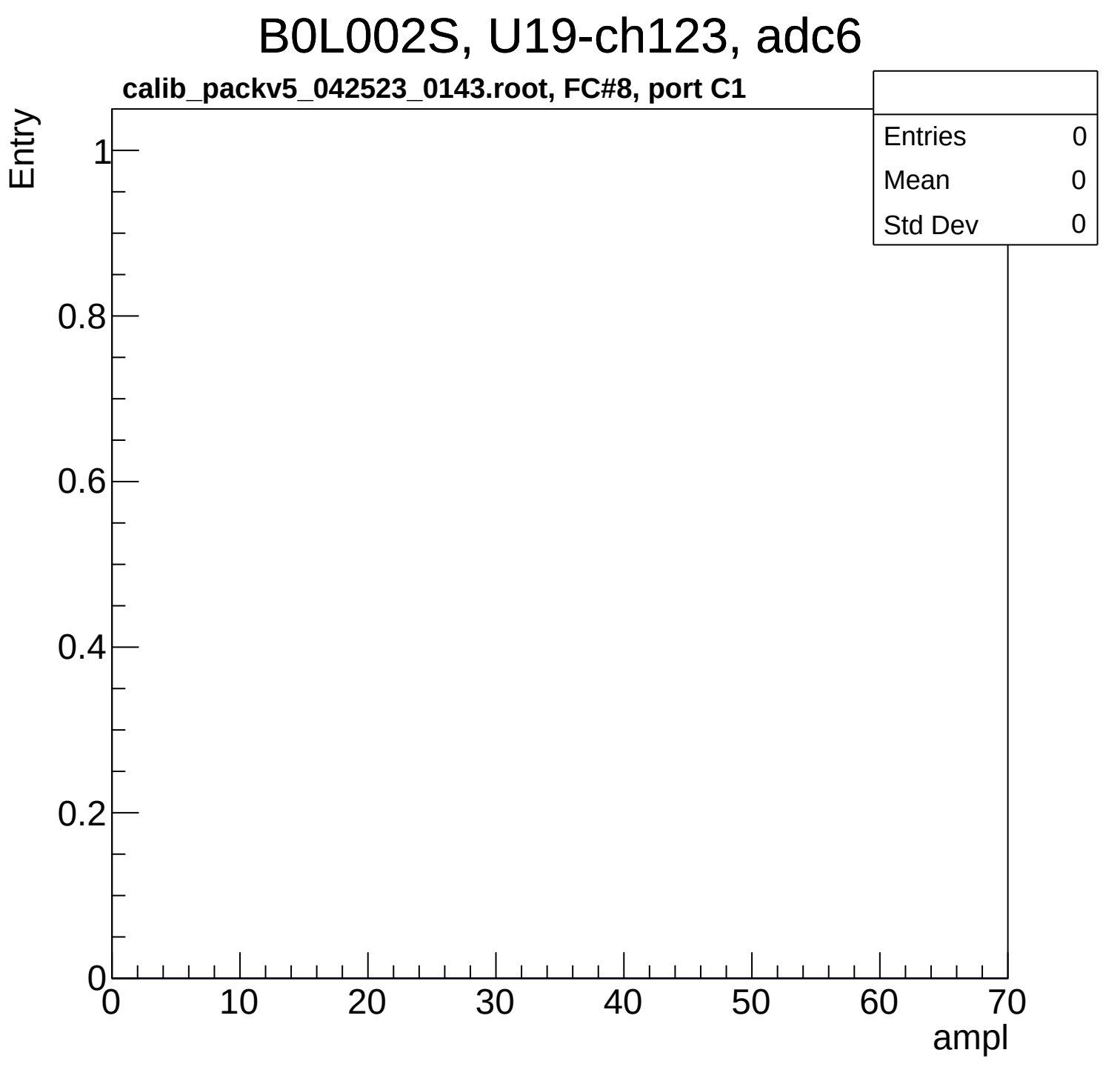

B0L002S, U19-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 37.42
Std Dev 3.606
Gaus mean : 37.7106
Gaus Width: 3.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

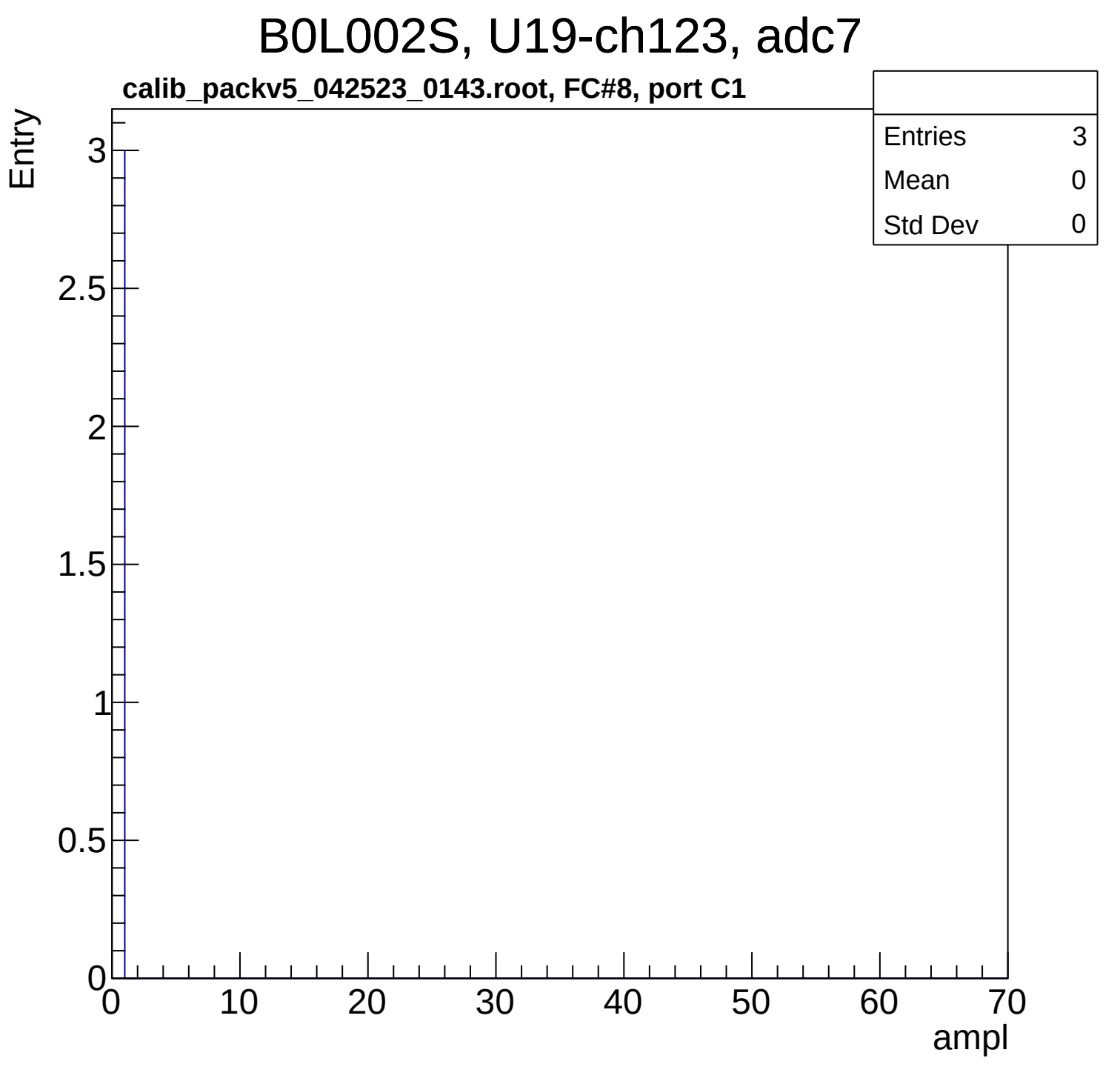

B0L002S, U19-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 44.37
Std Dev 3.089
Gaus mean : 47.1245
Gaus Width: 5.7131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

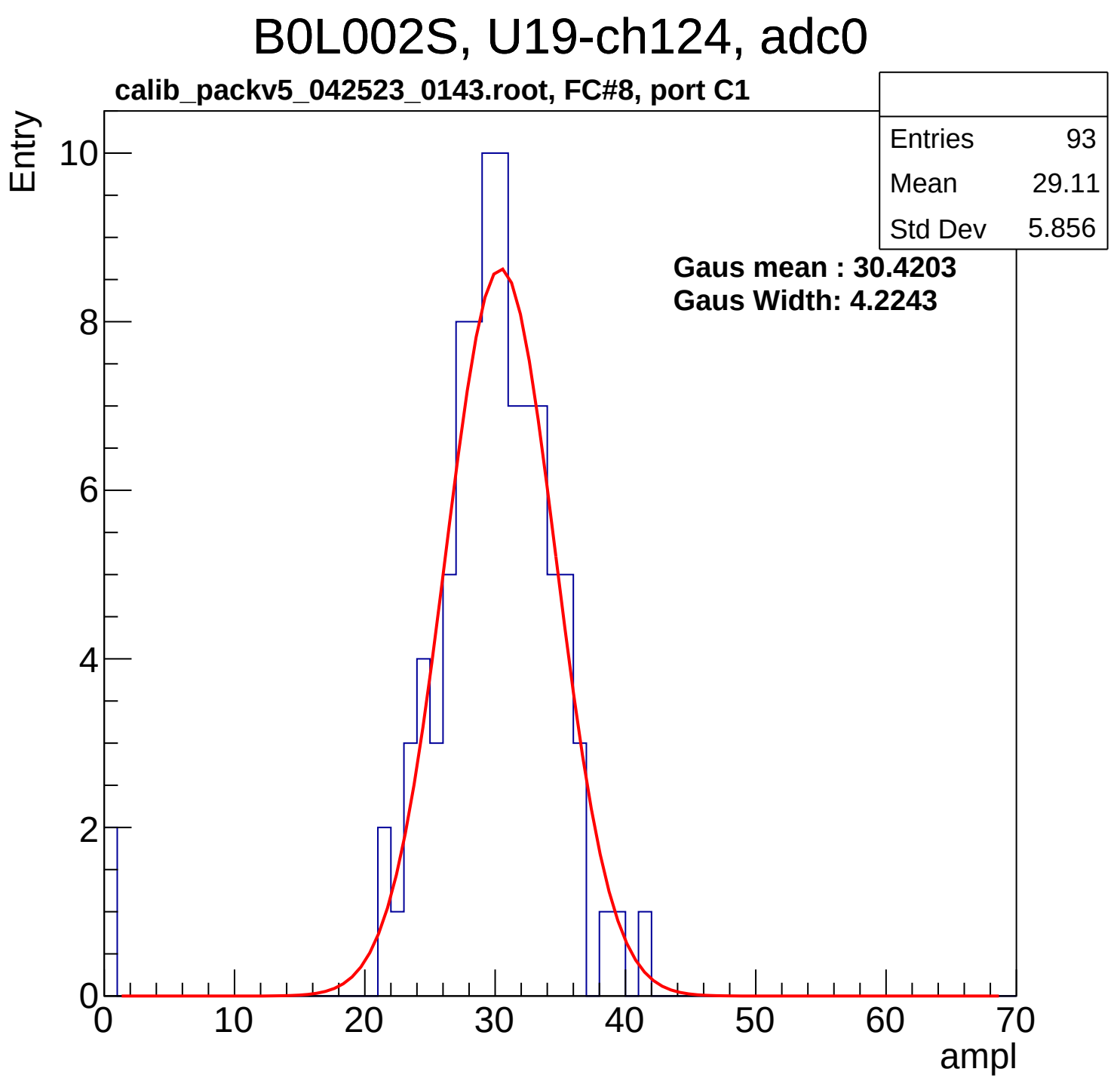

B0L002S, U19-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.04
Std Dev 3.856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

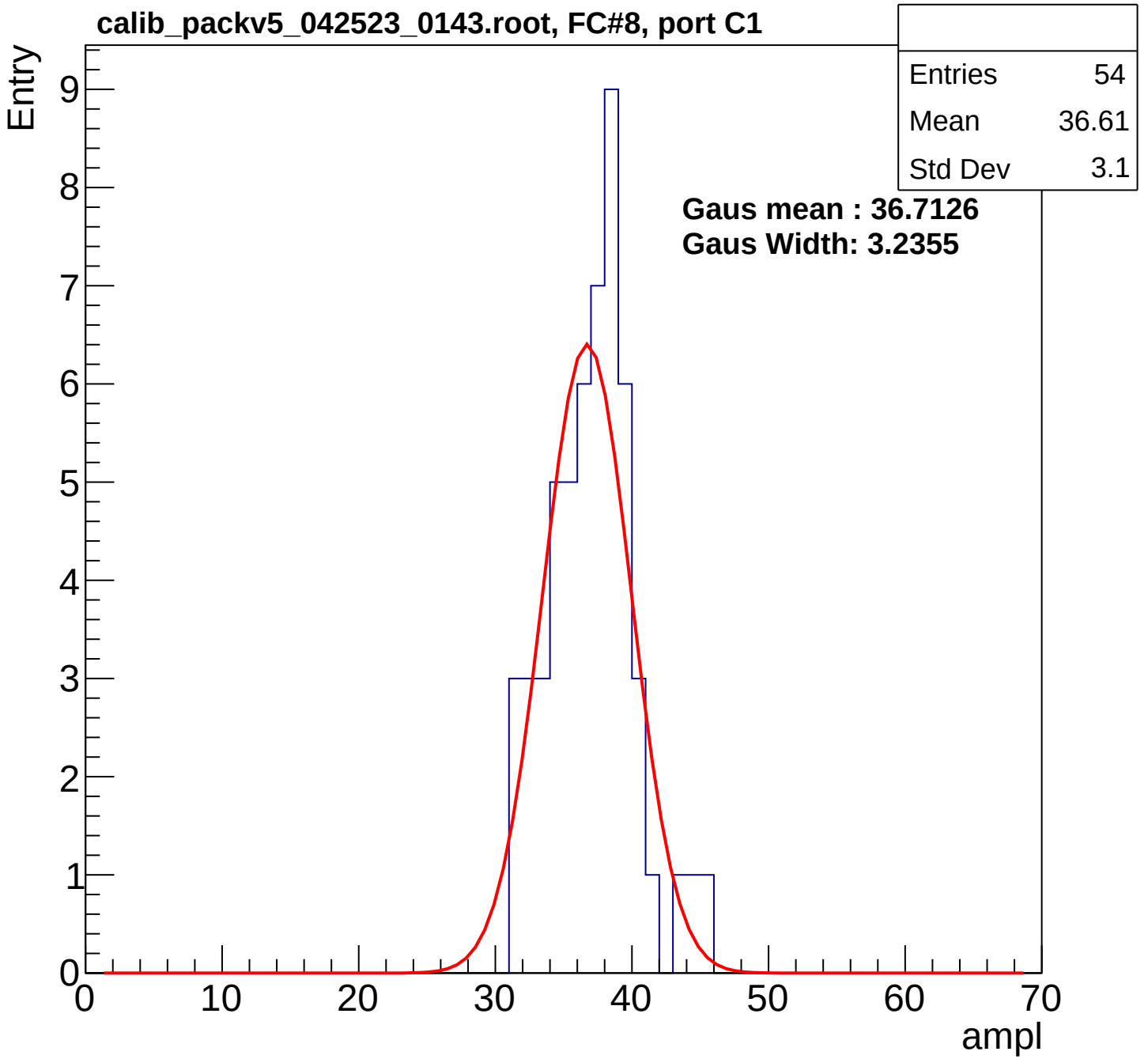

B0L002S, U19-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.18
Std Dev 3.374
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

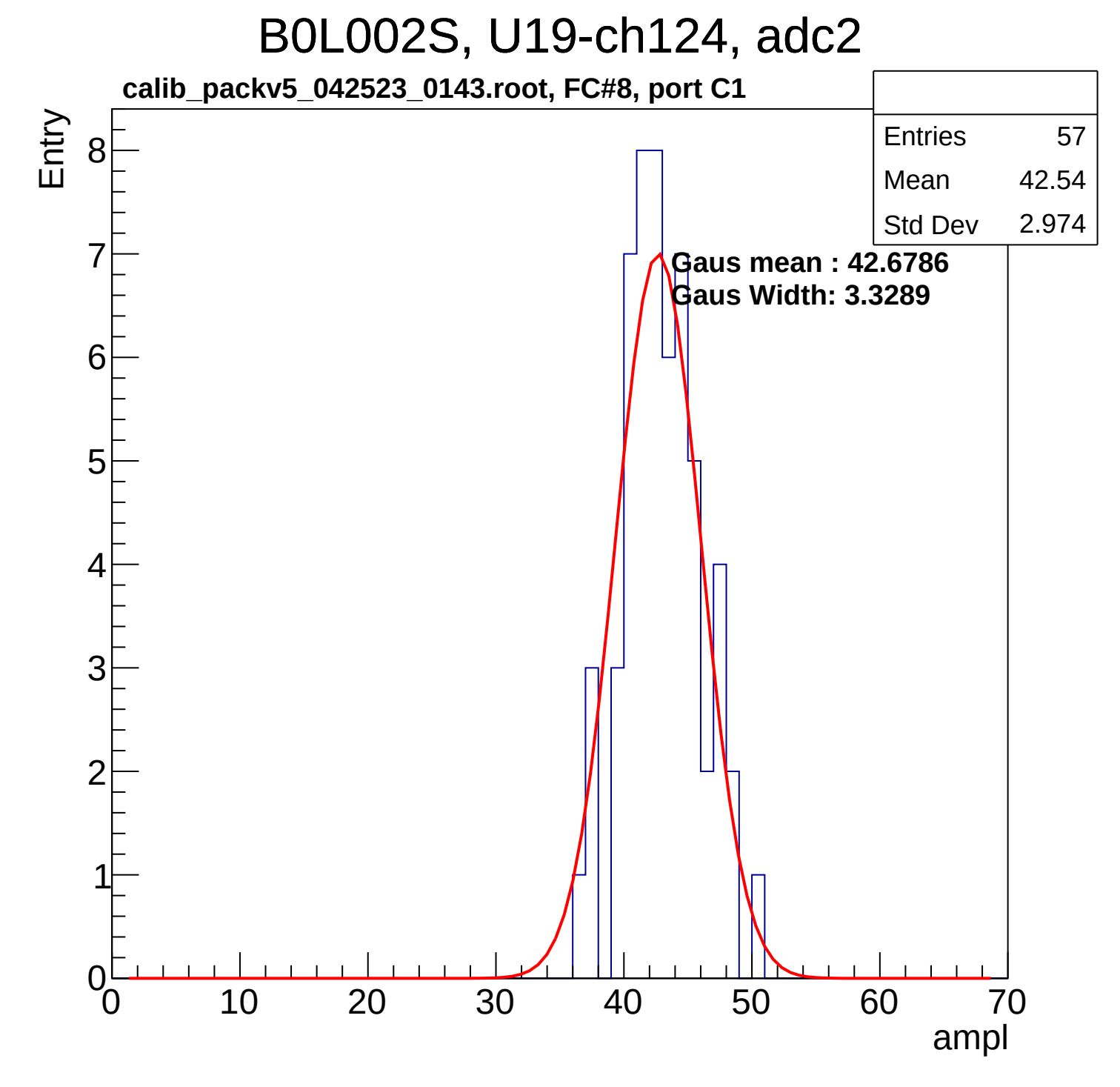

B0L002S, U19-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 59.08
Std Dev 12.17
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

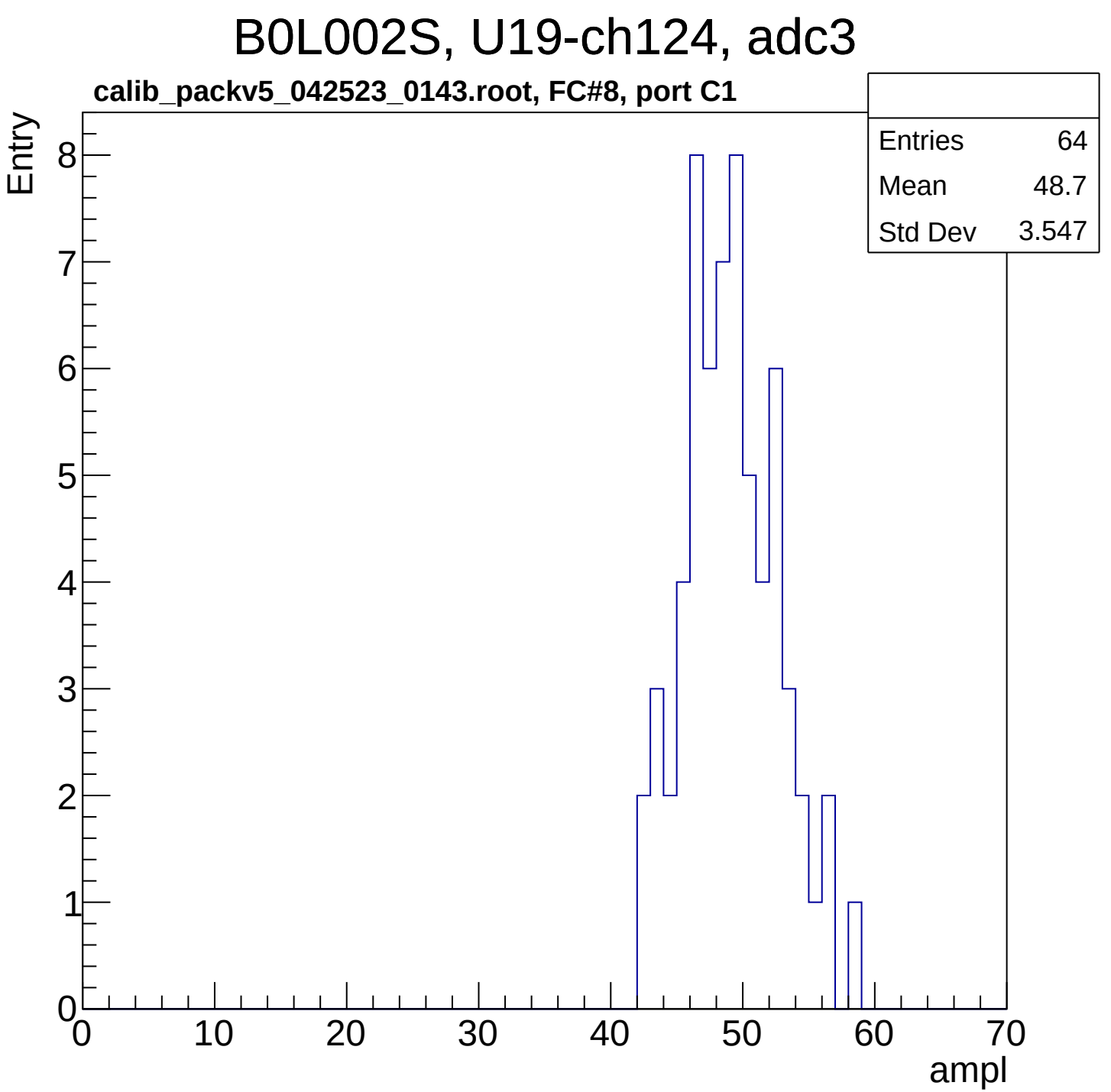

B0L002S, U19-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

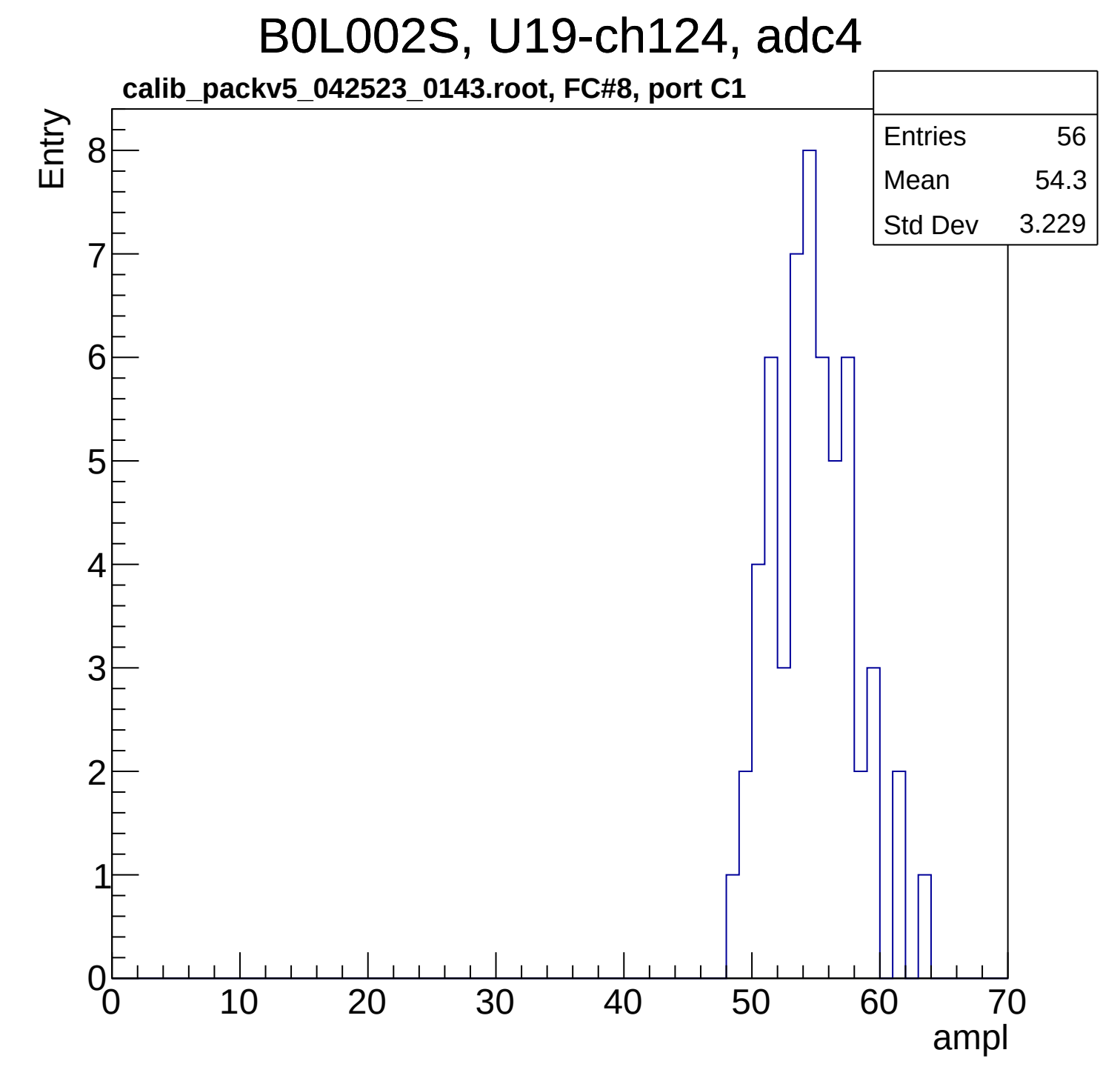

B0L002S, U19-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

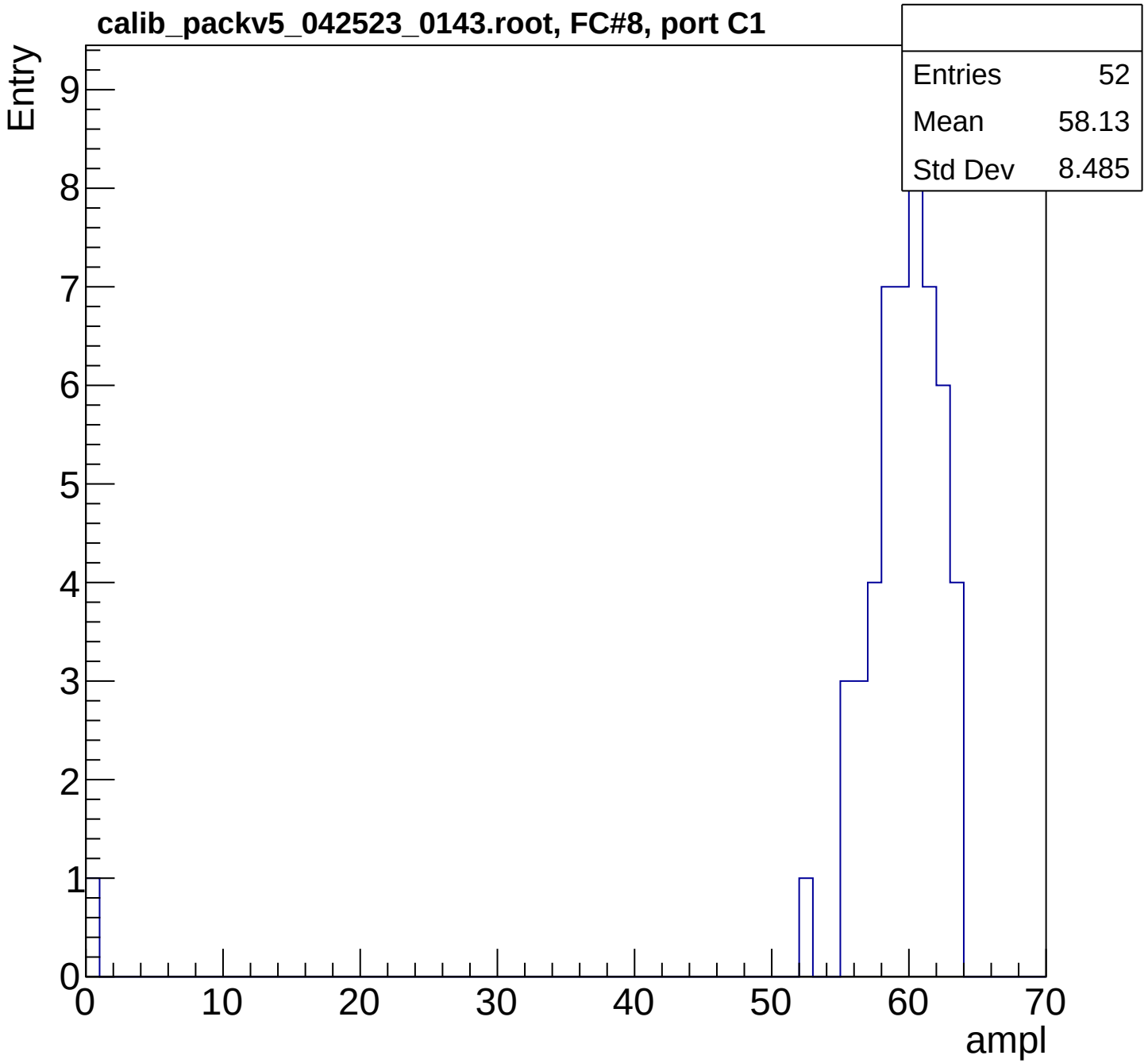

B0L002S, U19-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 93
Mean 29.11
Std Dev 5.856
Gaus mean : 30.4203
Gaus Width: 4.2243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

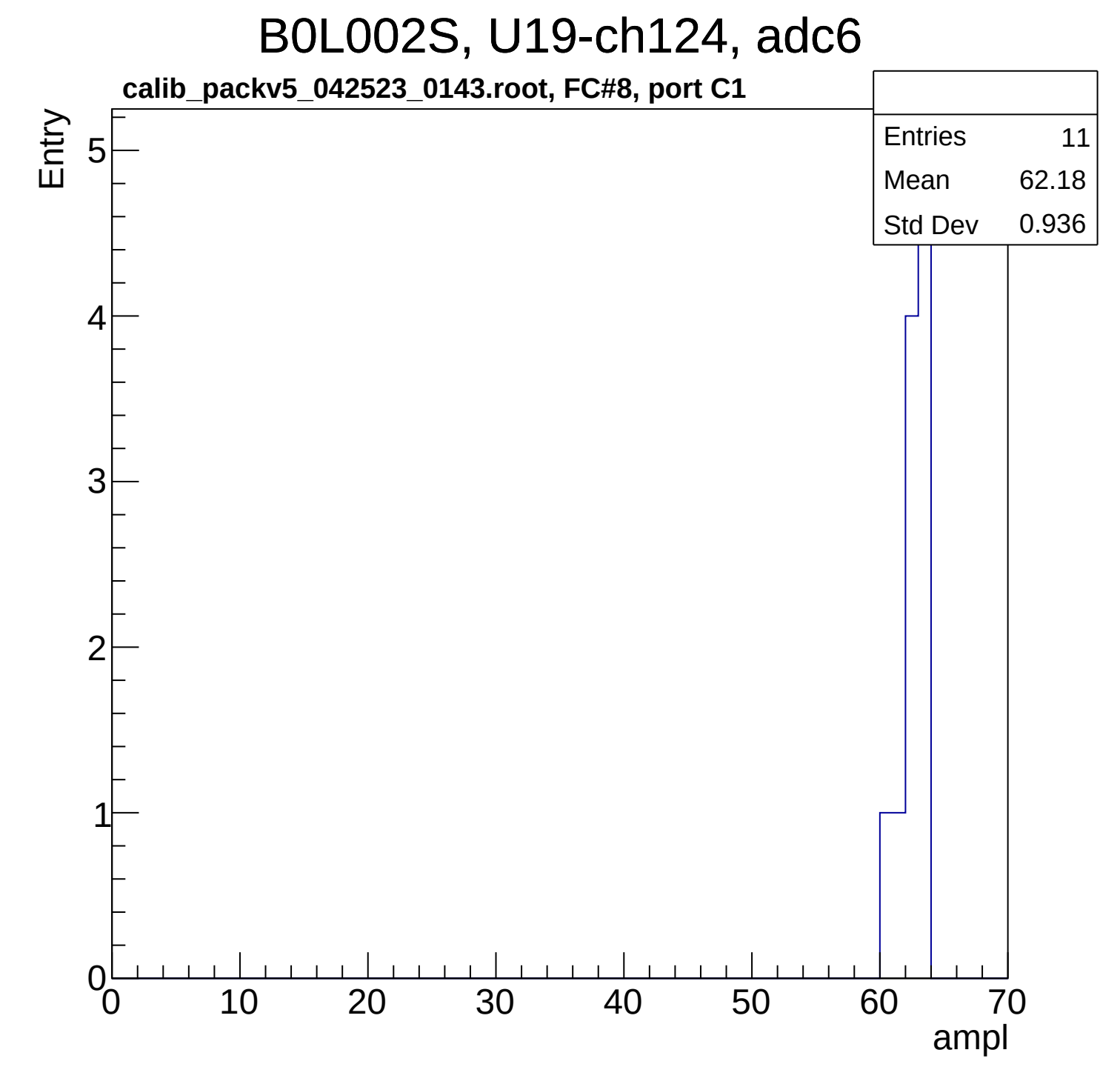

B0L002S, U19-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 36.61
Std Dev 3.1
Gaus mean : 36.7126
Gaus Width: 3.2355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

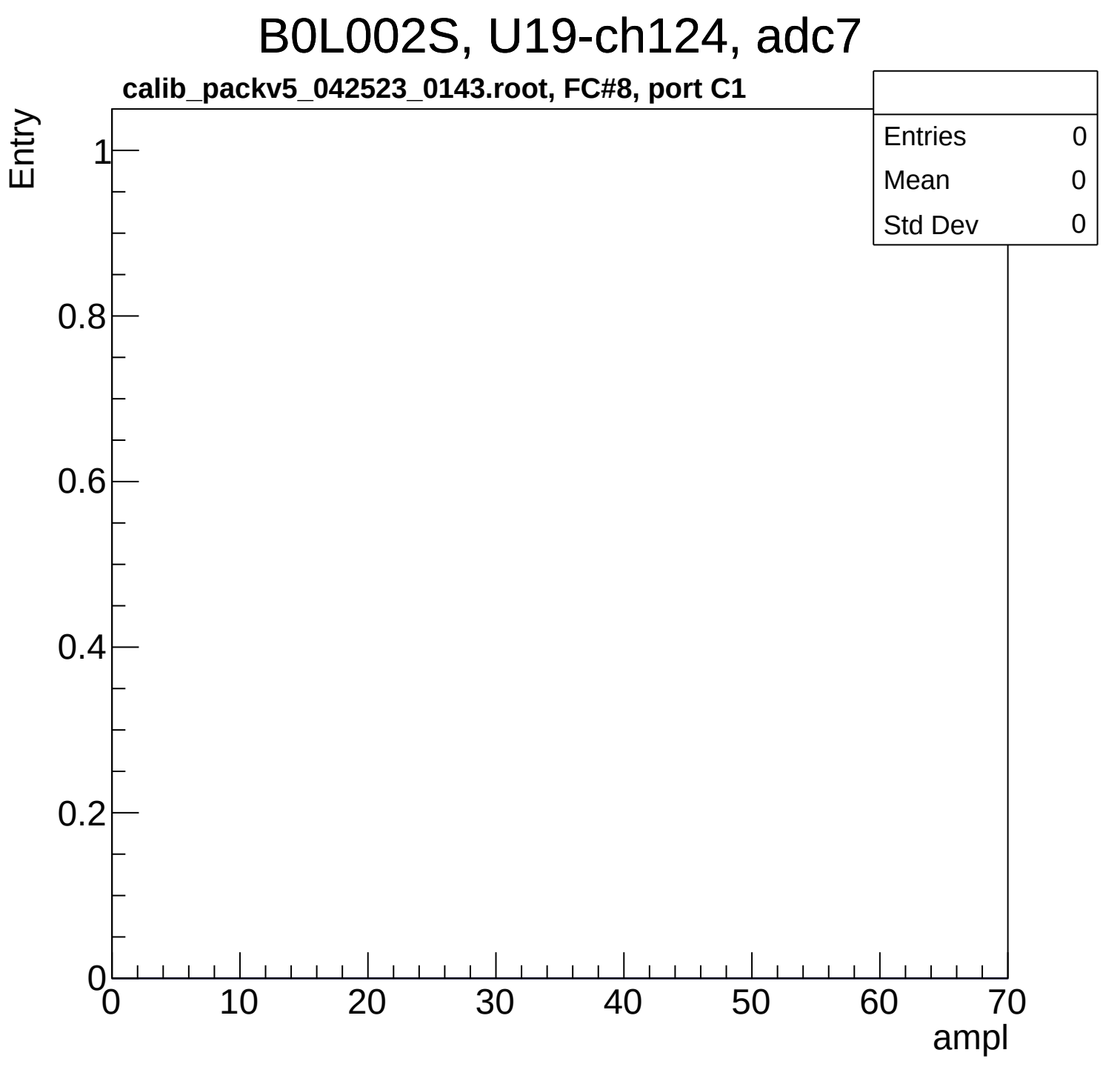

B0L002S, U19-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 42.54
Std Dev 2.974
Gaus mean : 42.6786
Gaus Width: 3.3289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

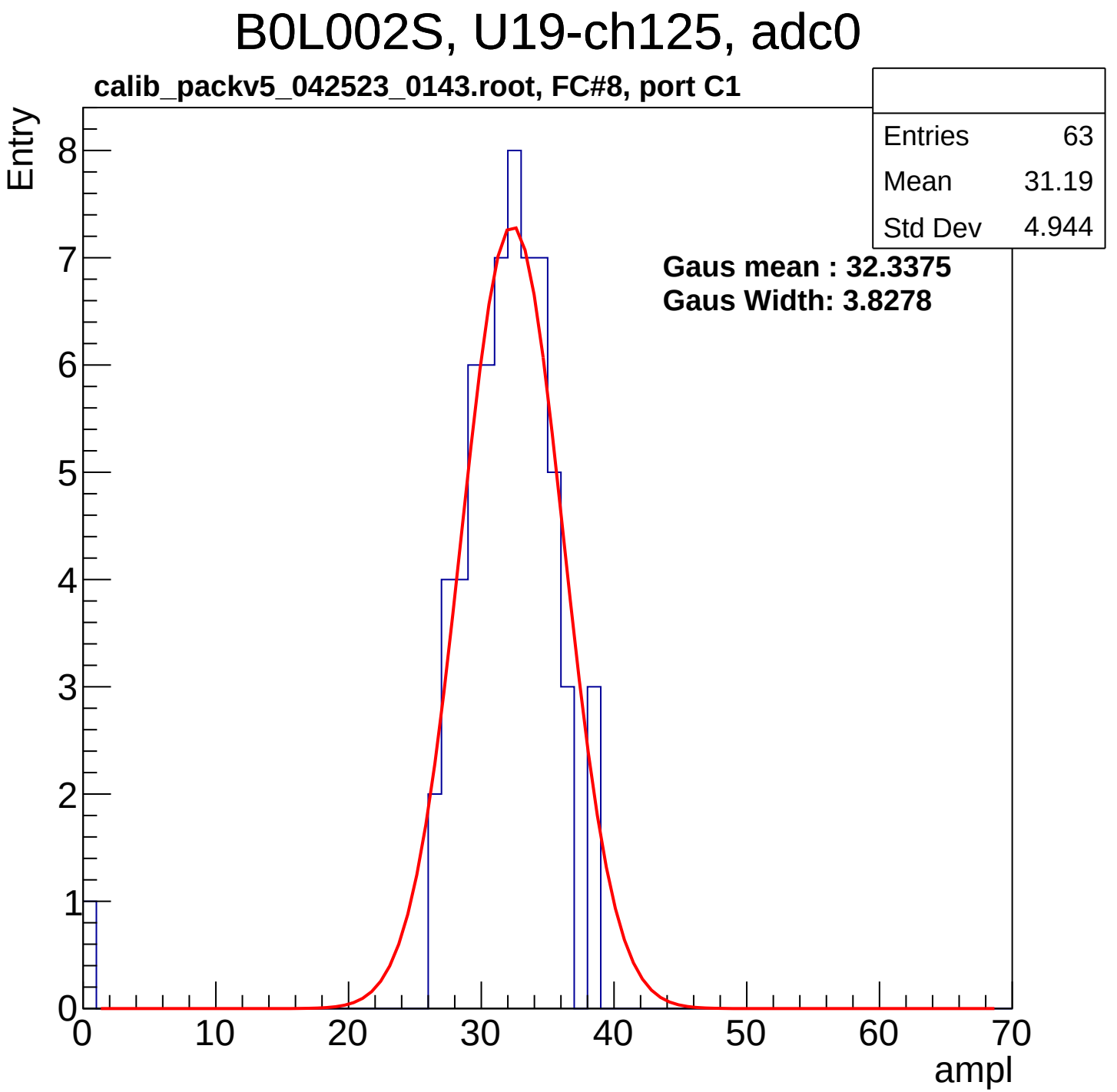

B0L002S, U19-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 48.7
Std Dev 3.547
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

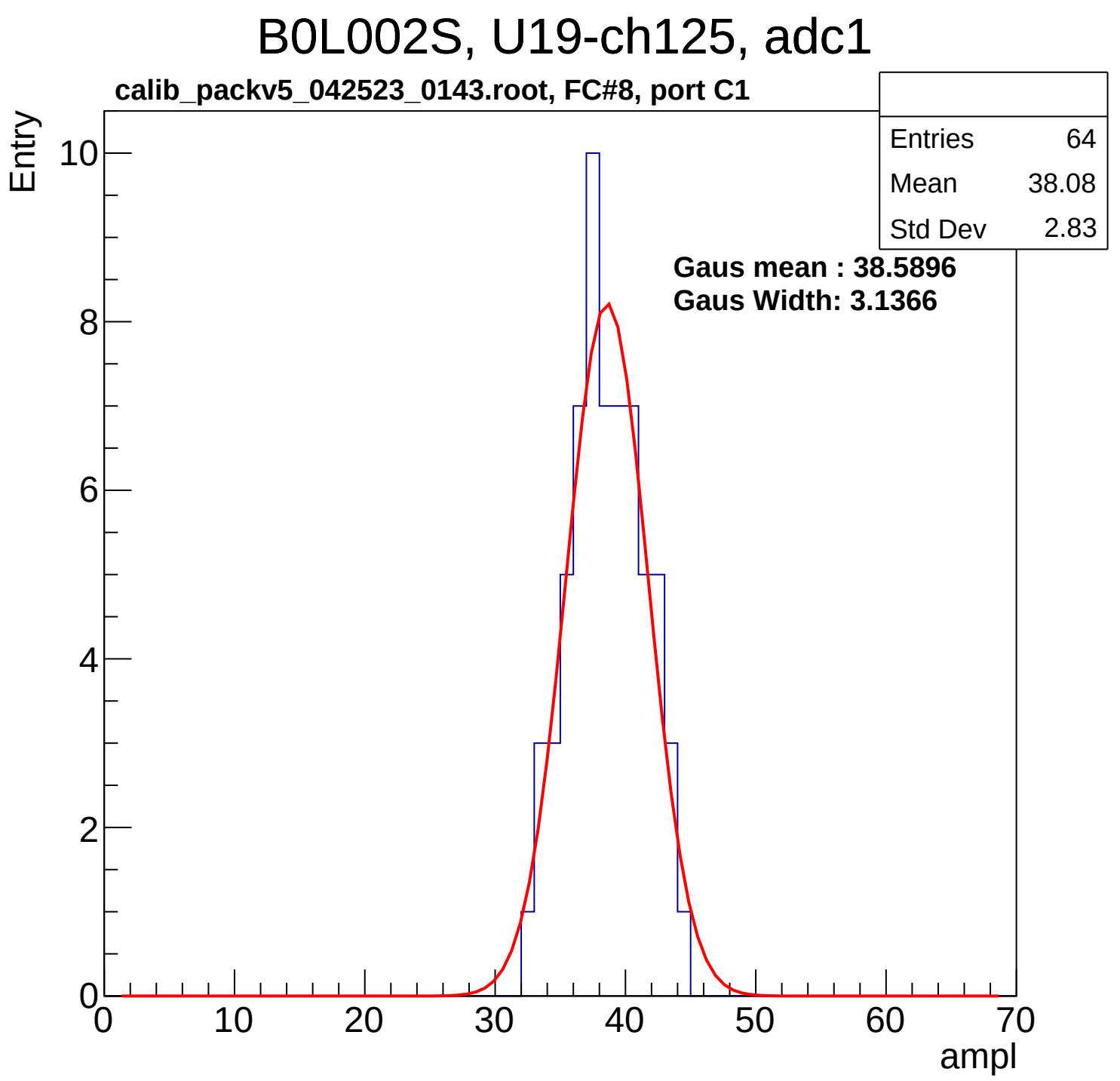

B0L002S, U19-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 54.3
Std Dev 3.229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

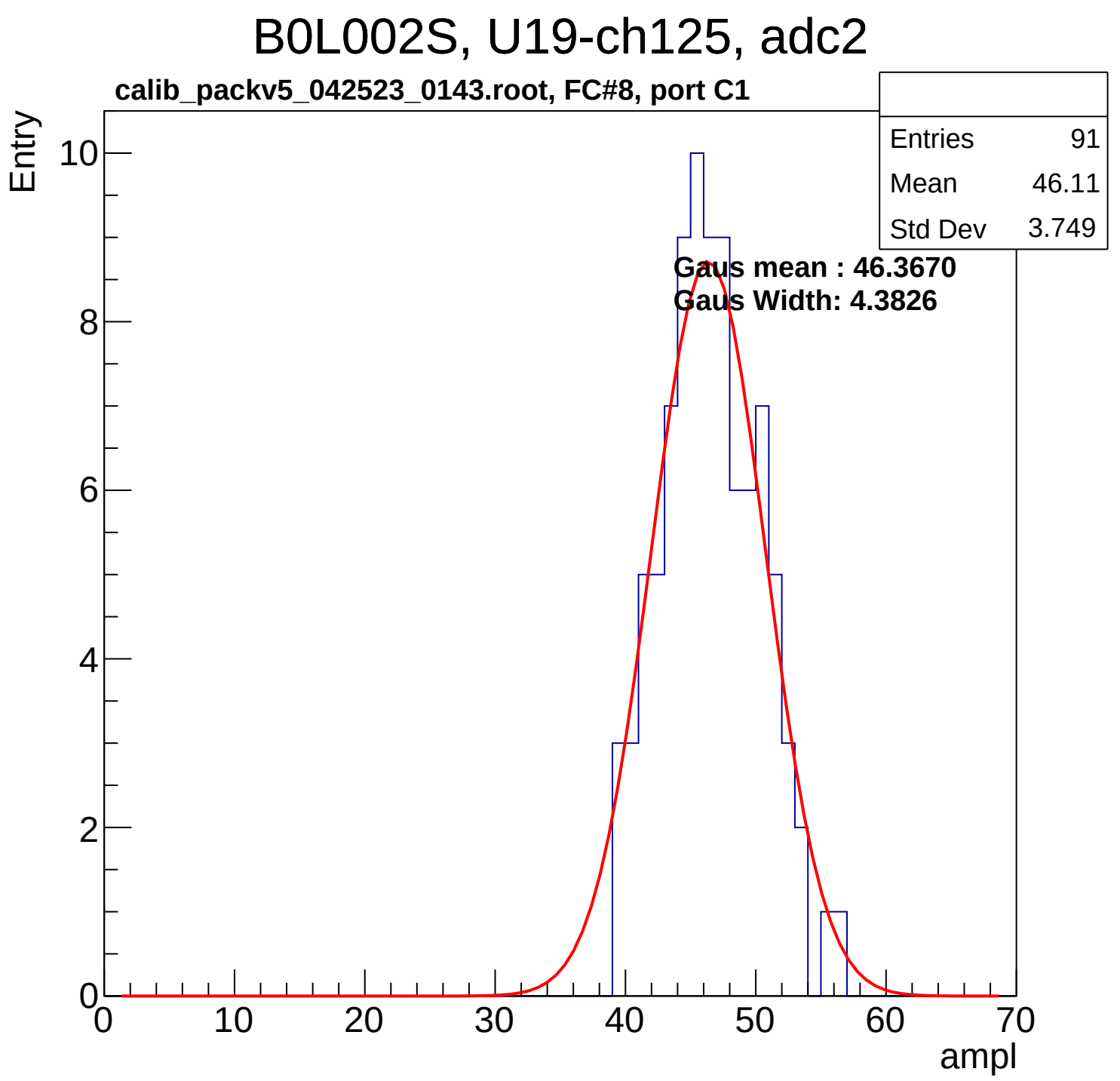

B0L002S, U19-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.13
Std Dev 8.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

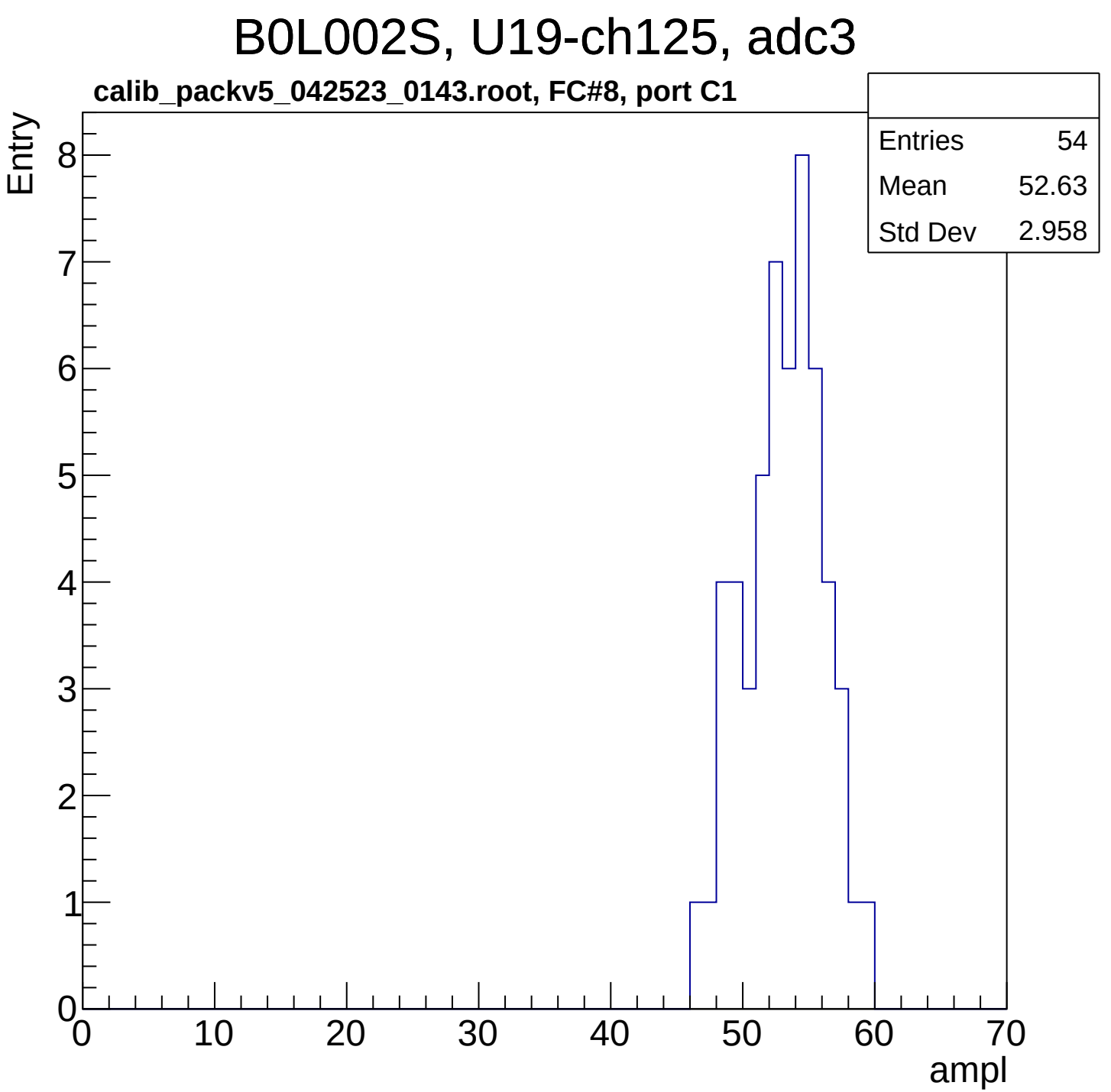

B0L002S, U19-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 11
Mean 62.18
Std Dev 0.936
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

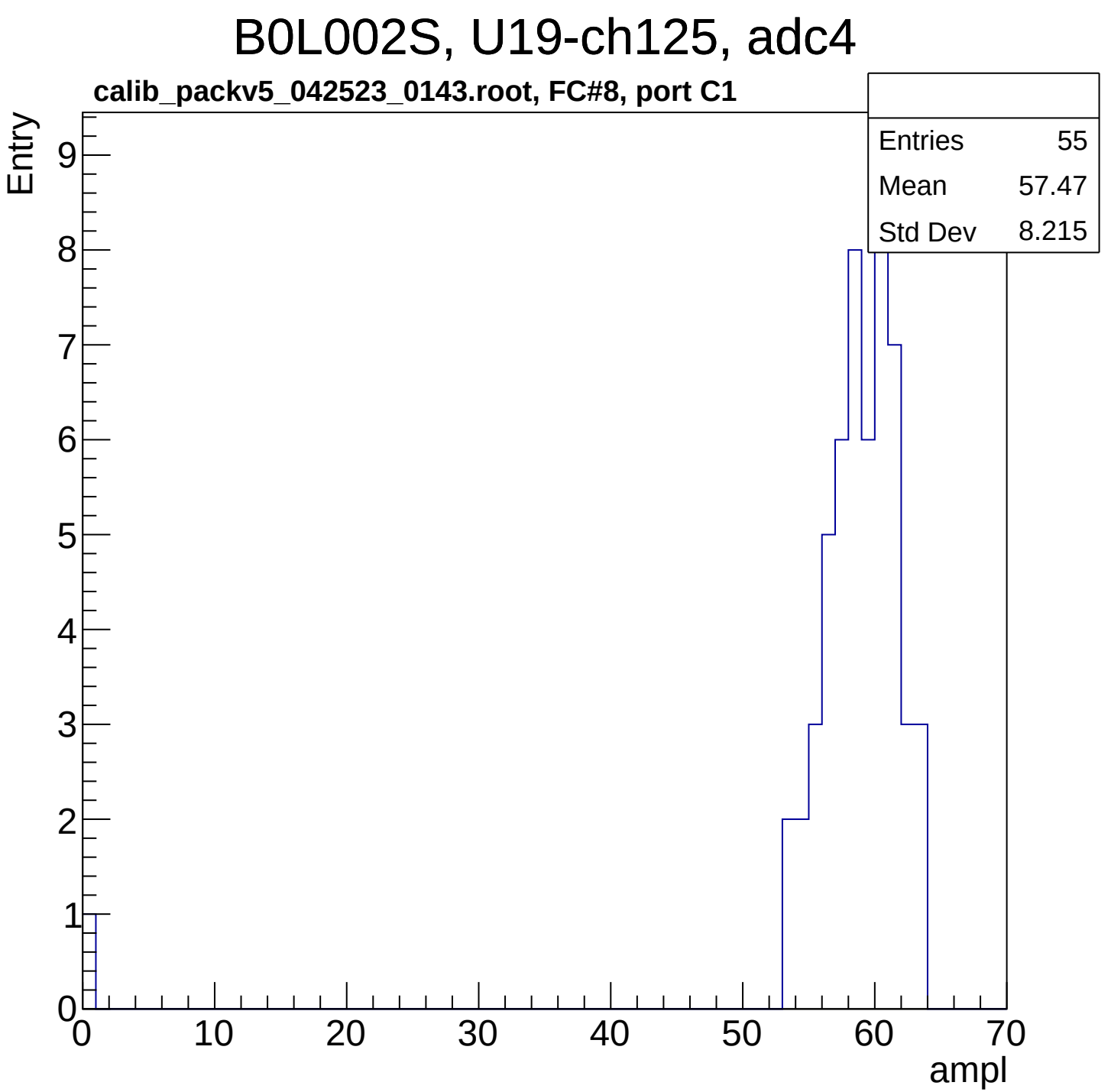

B0L002S, U19-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

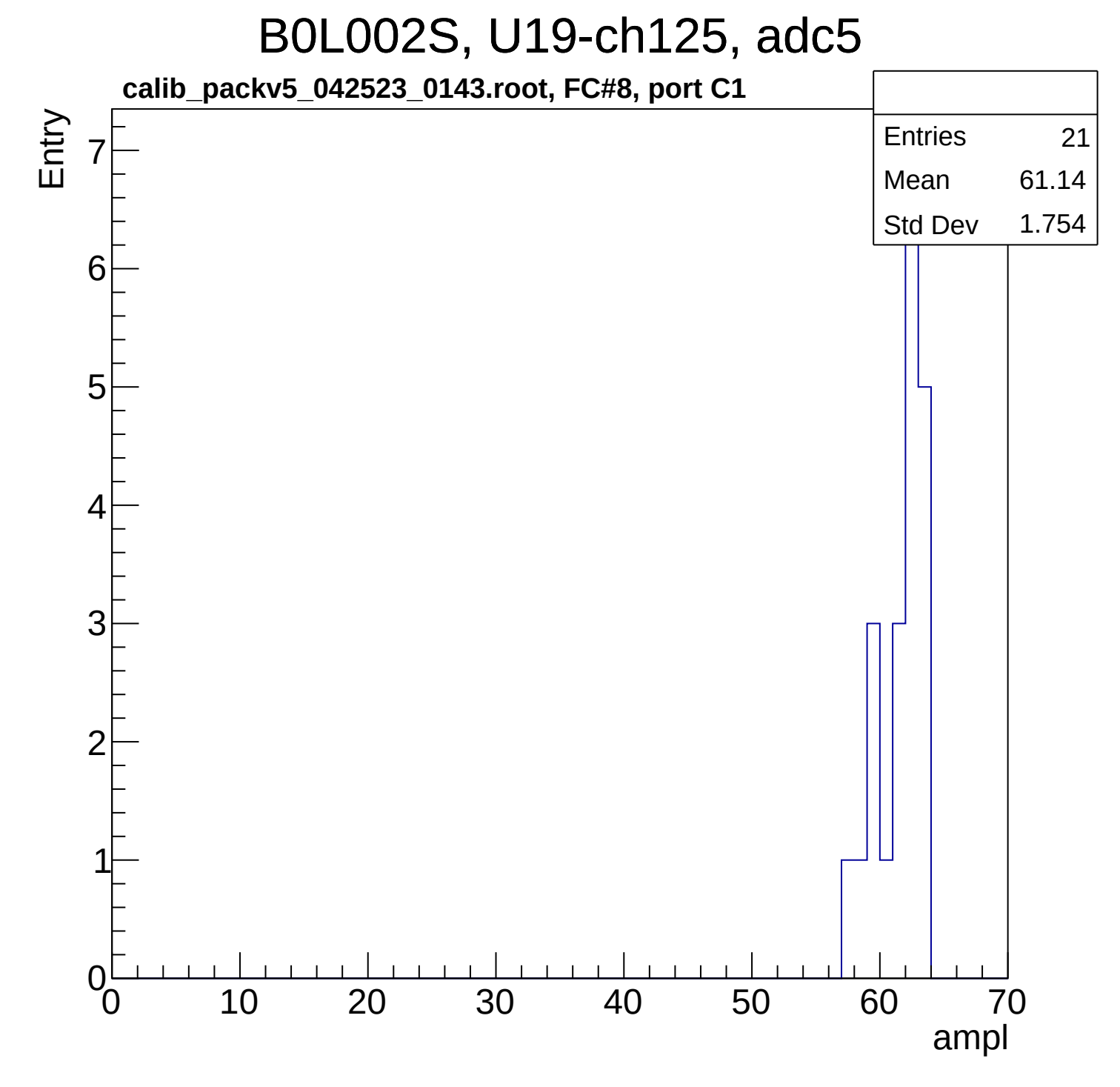

B0L002S, U19-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.19
Std Dev 4.944
Gaus mean : 32.3375
Gaus Width: 3.8278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

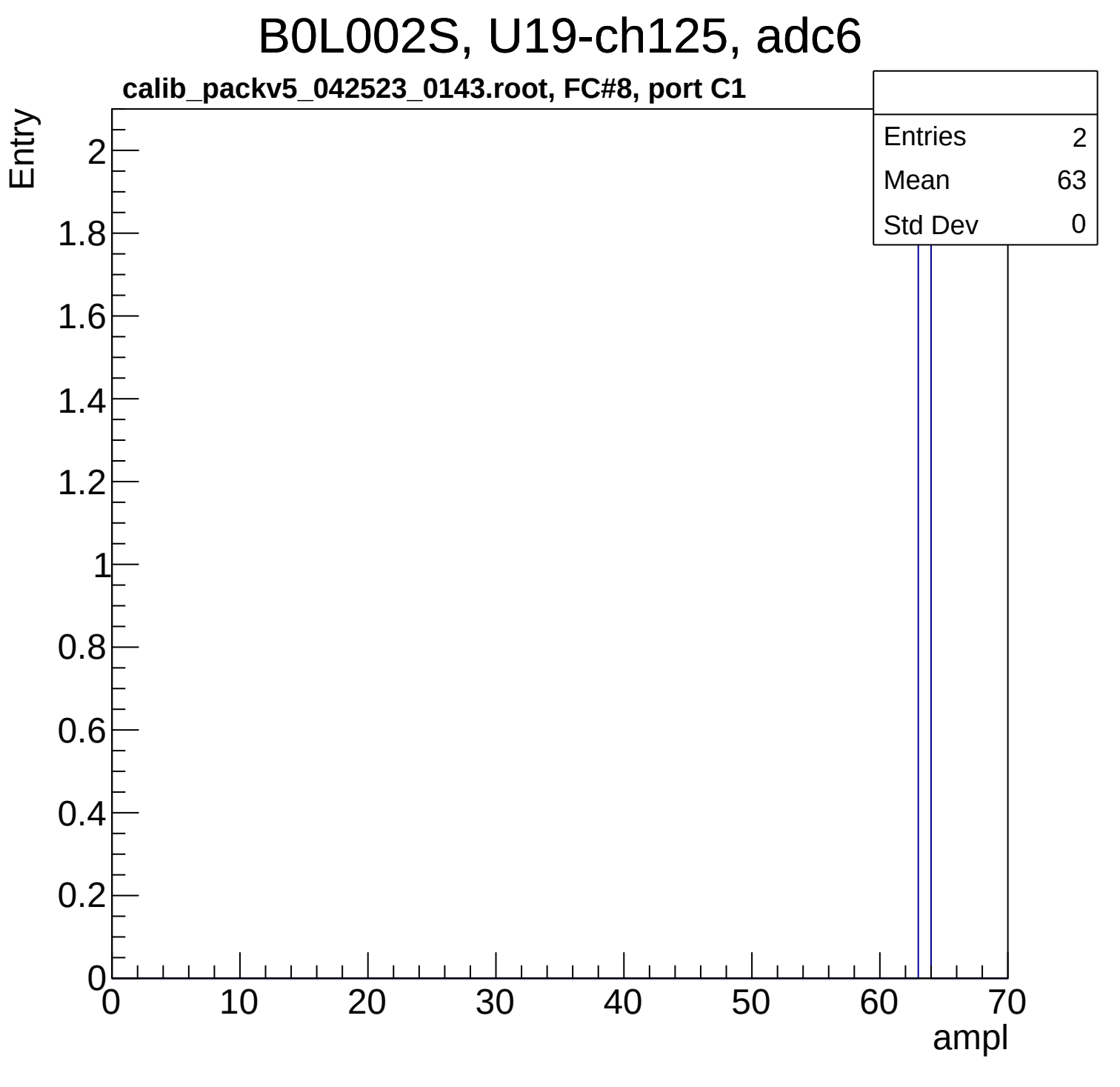

B0L002S, U19-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.08
Std Dev 2.83
Gaus mean : 38.5896
Gaus Width: 3.1366
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

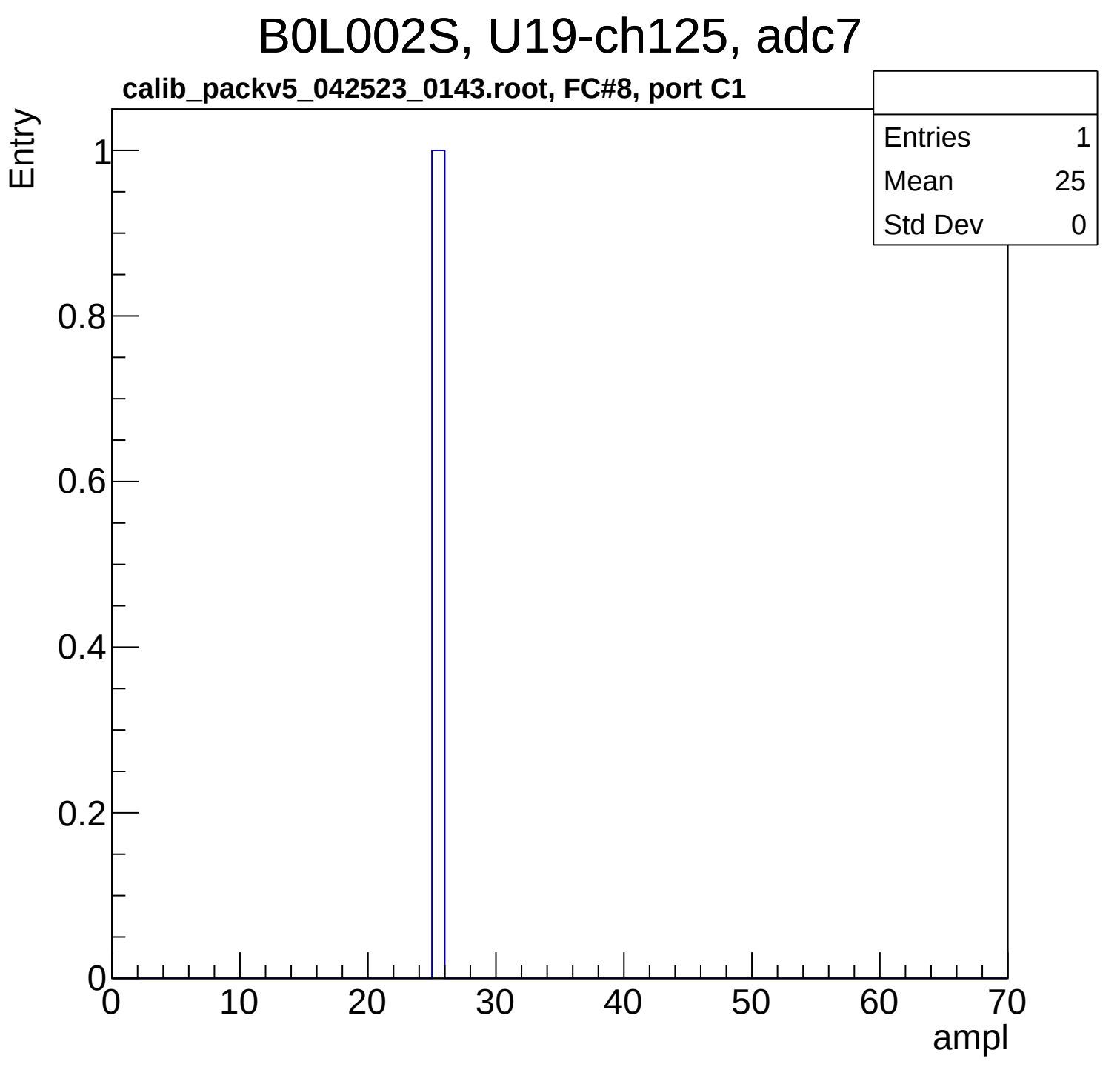

B0L002S, U19-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 46.11
Std Dev 3.749
Gaus mean : 46.3670
Gaus Width: 4.3826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

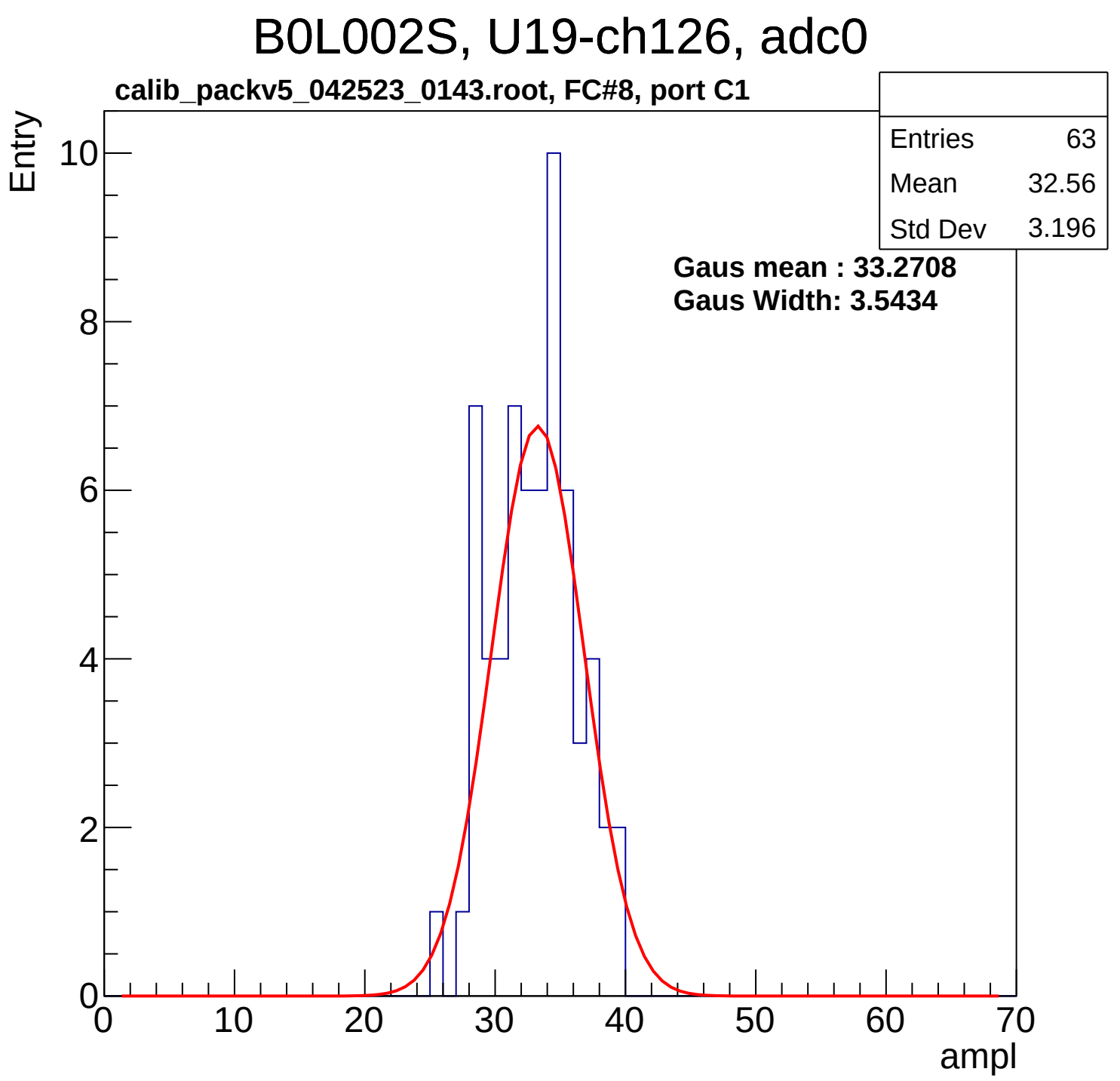

B0L002S, U19-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 52.63
Std Dev 2.958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

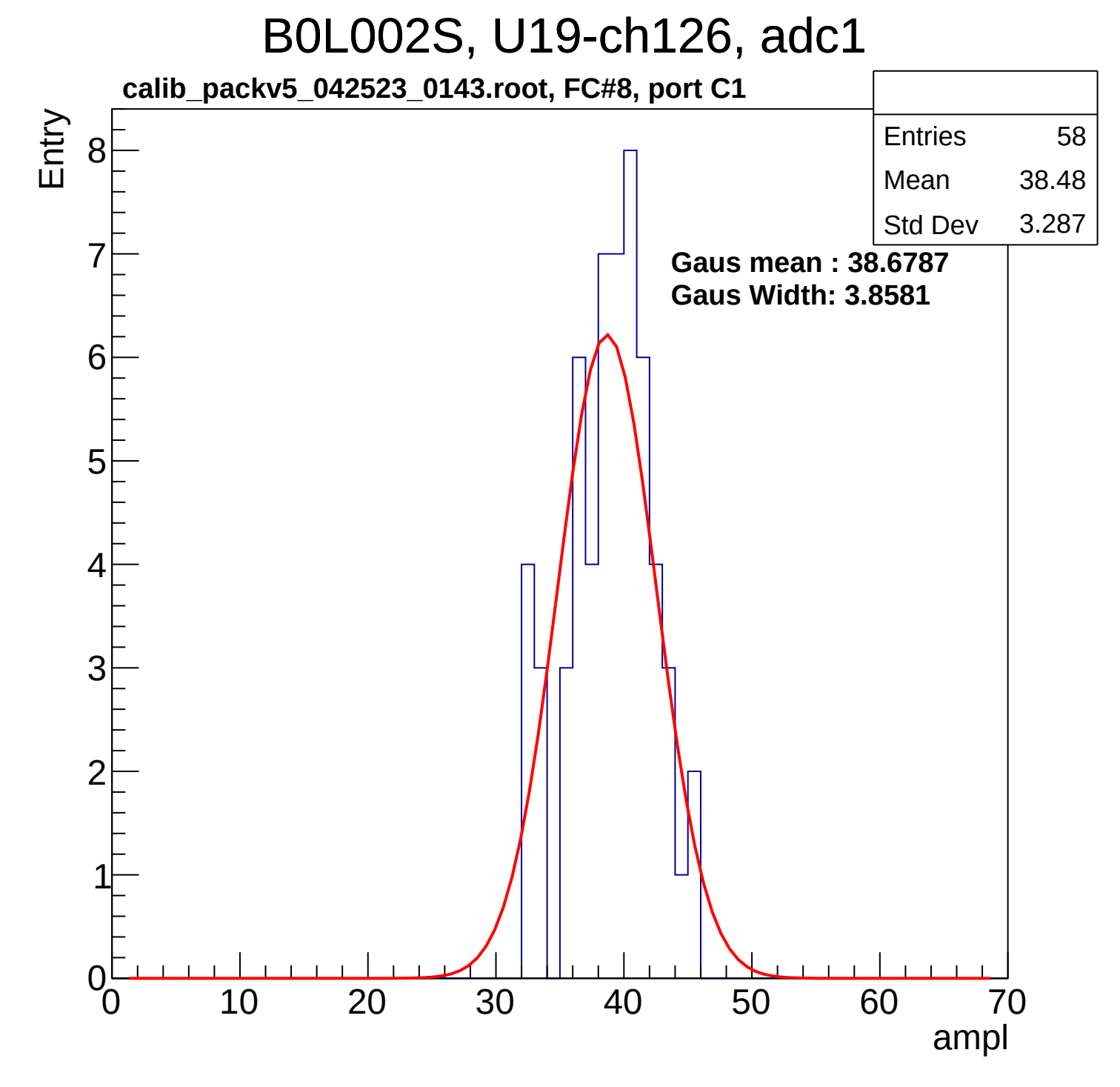

B0L002S, U19-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.47
Std Dev 8.215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

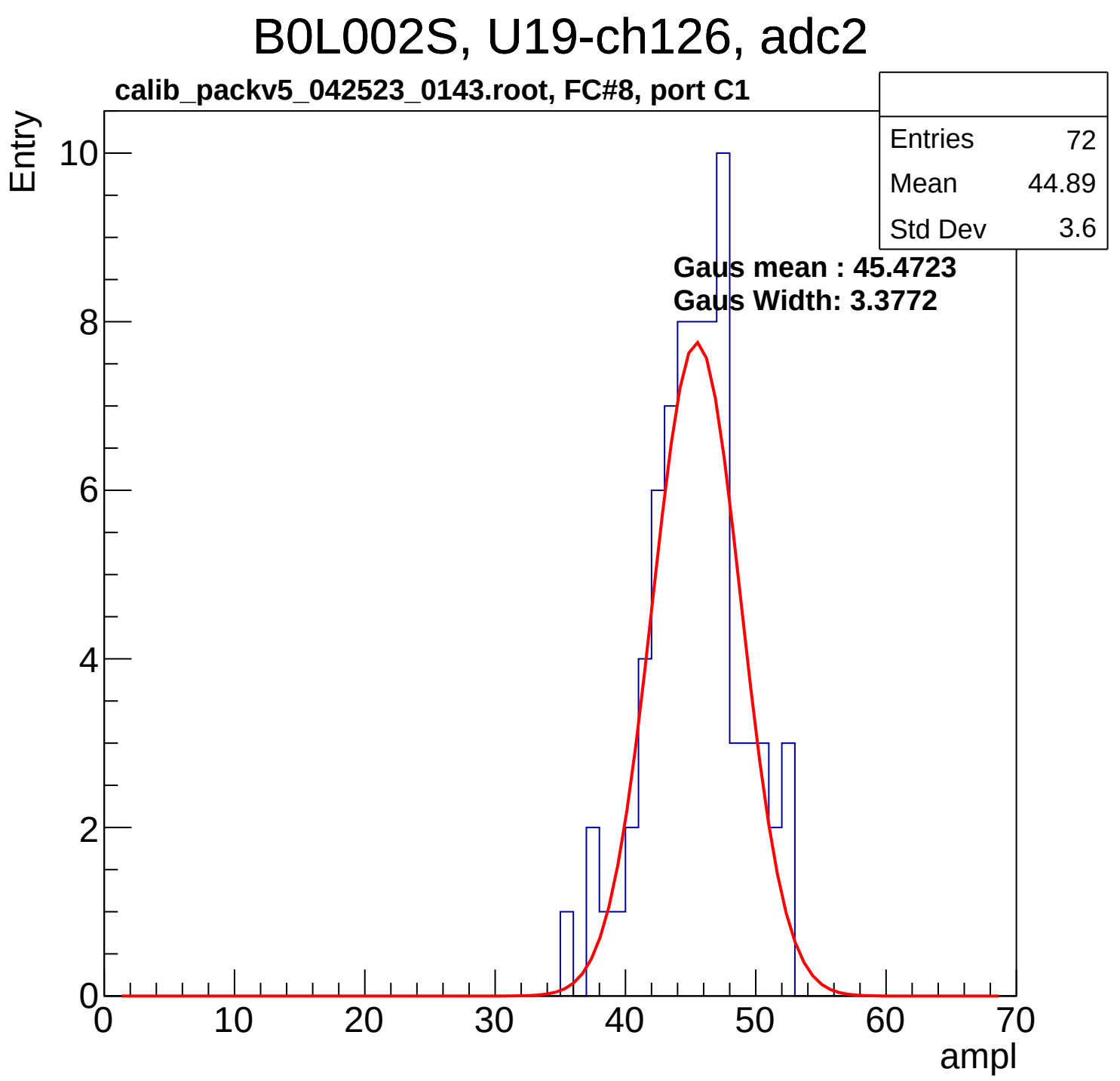

B0L002S, U19-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.14
Std Dev 1.754
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

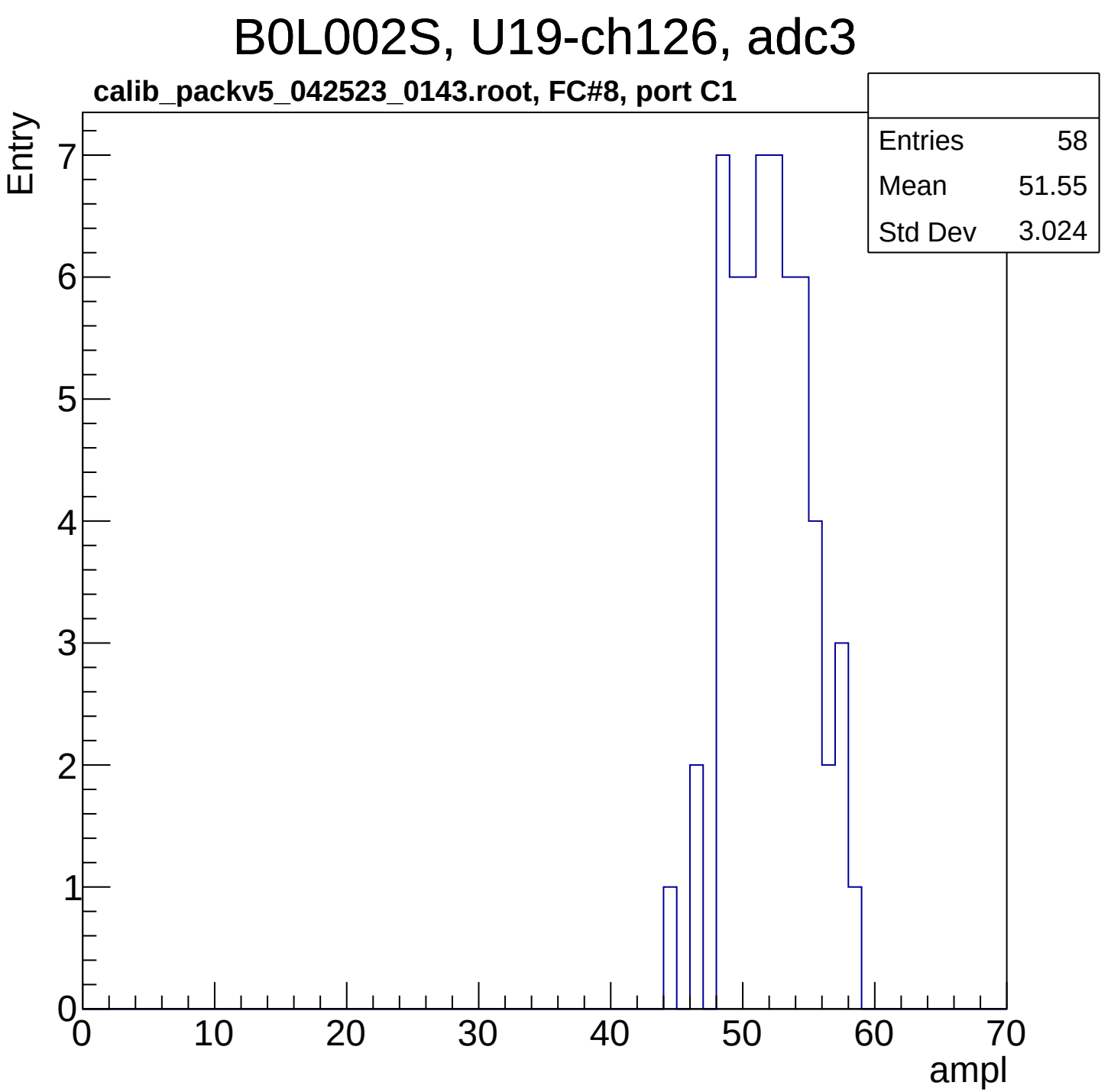

B0L002S, U19-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

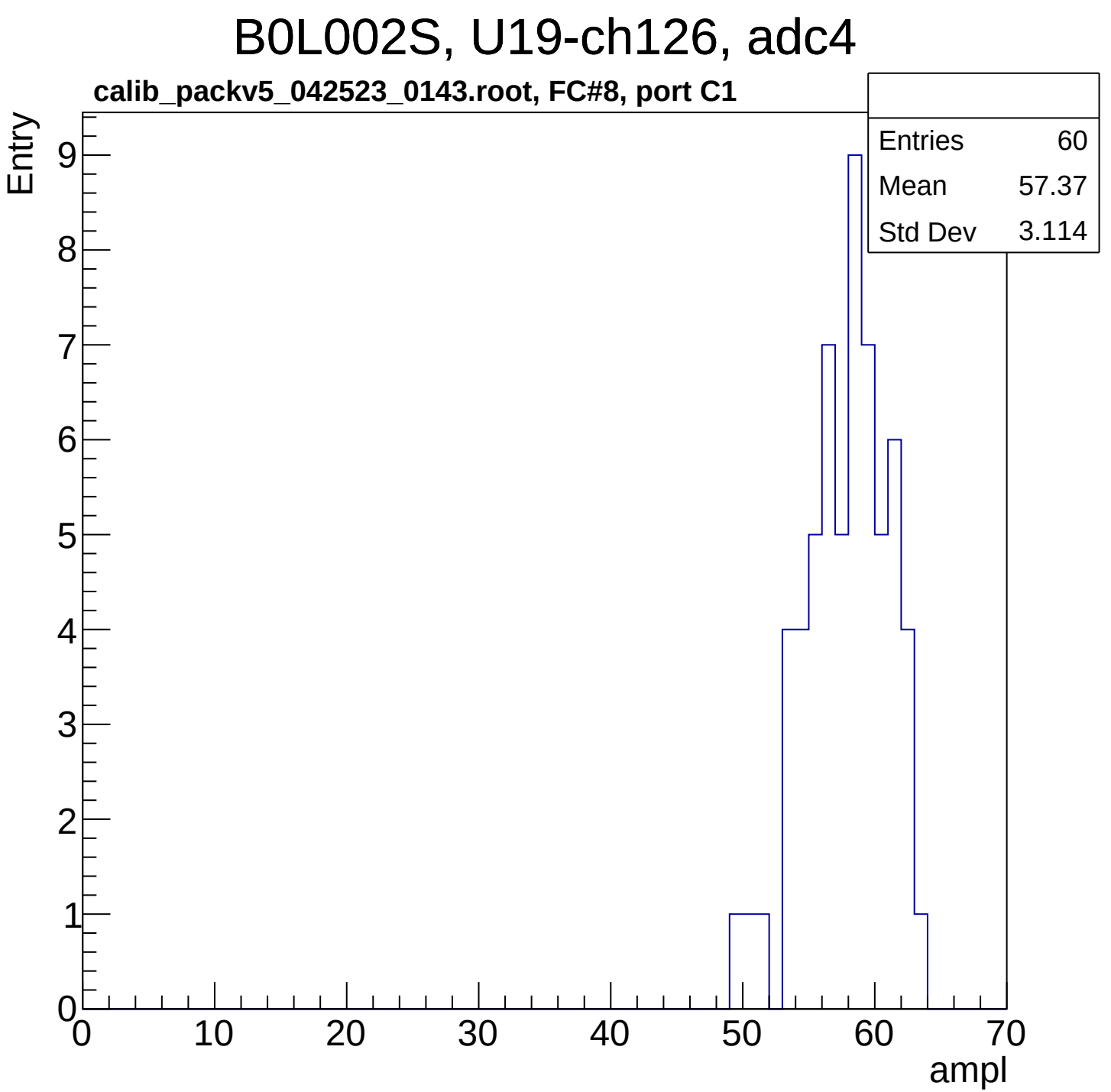

B0L002S, U19-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

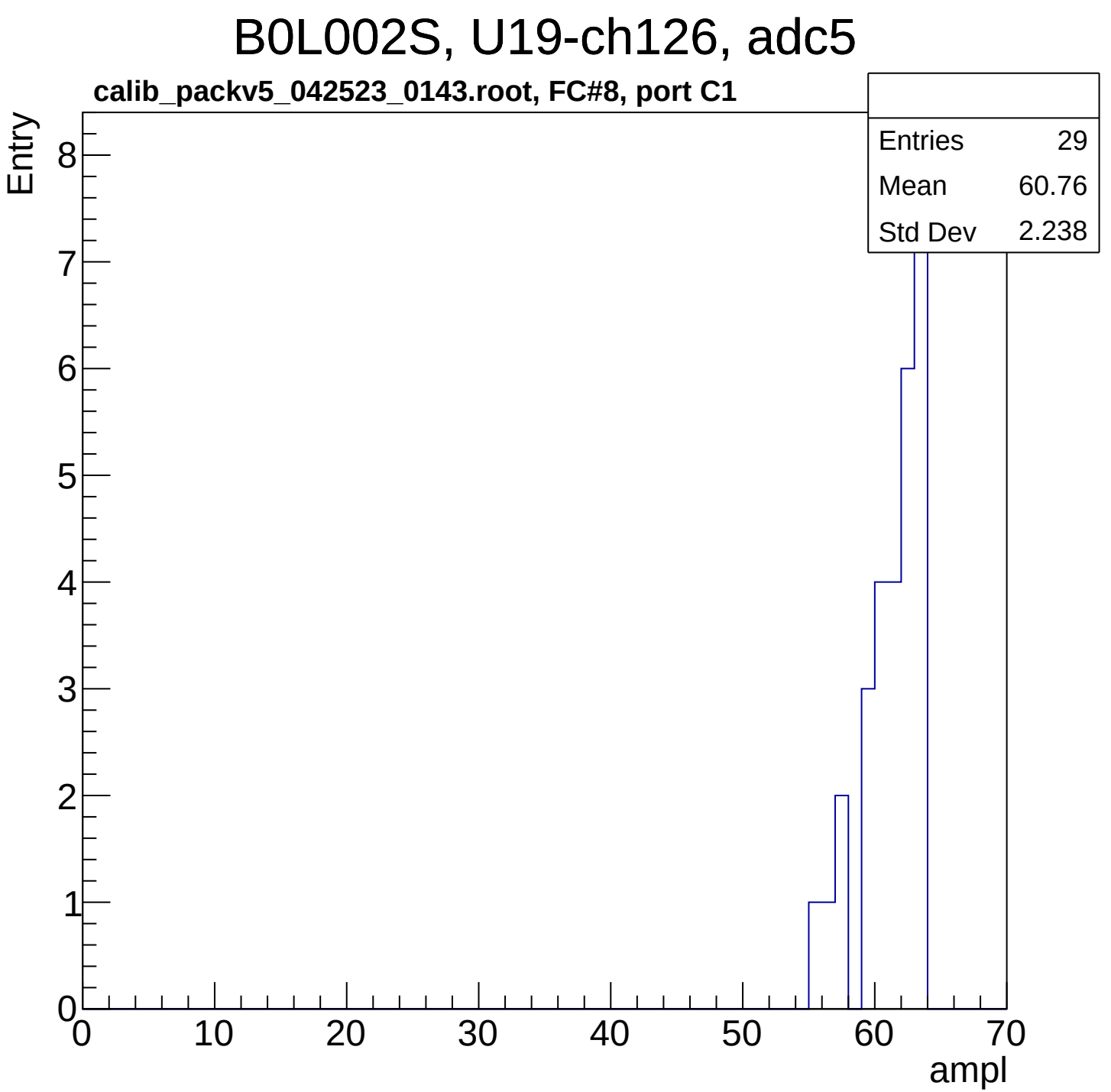

B0L002S, U19-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 32.56
Std Dev 3.196
Gaus mean : 33.2708
Gaus Width: 3.5434
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

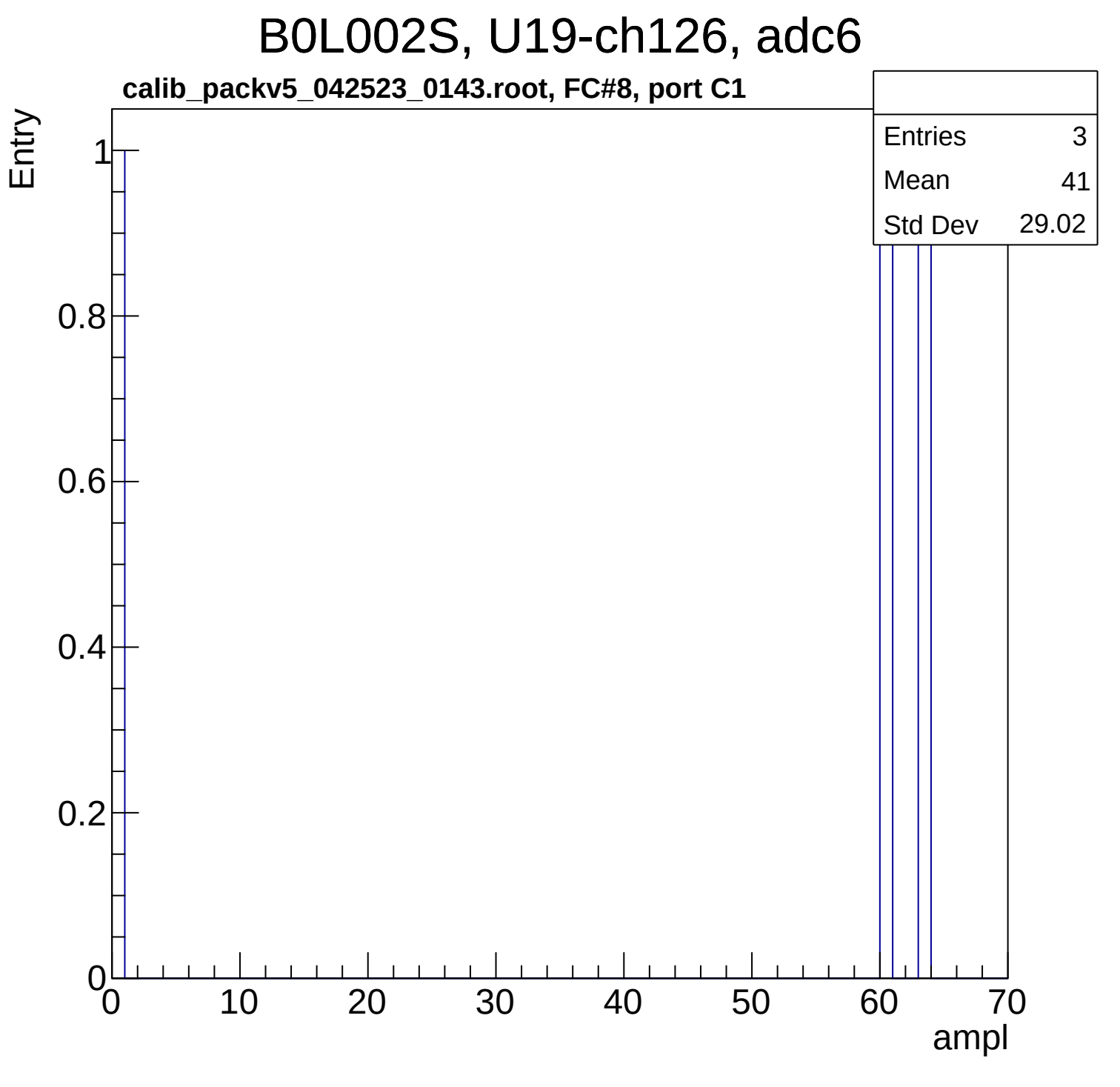

B0L002S, U19-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 38.48
Std Dev 3.287
Gaus mean : 38.6787
Gaus Width: 3.8581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

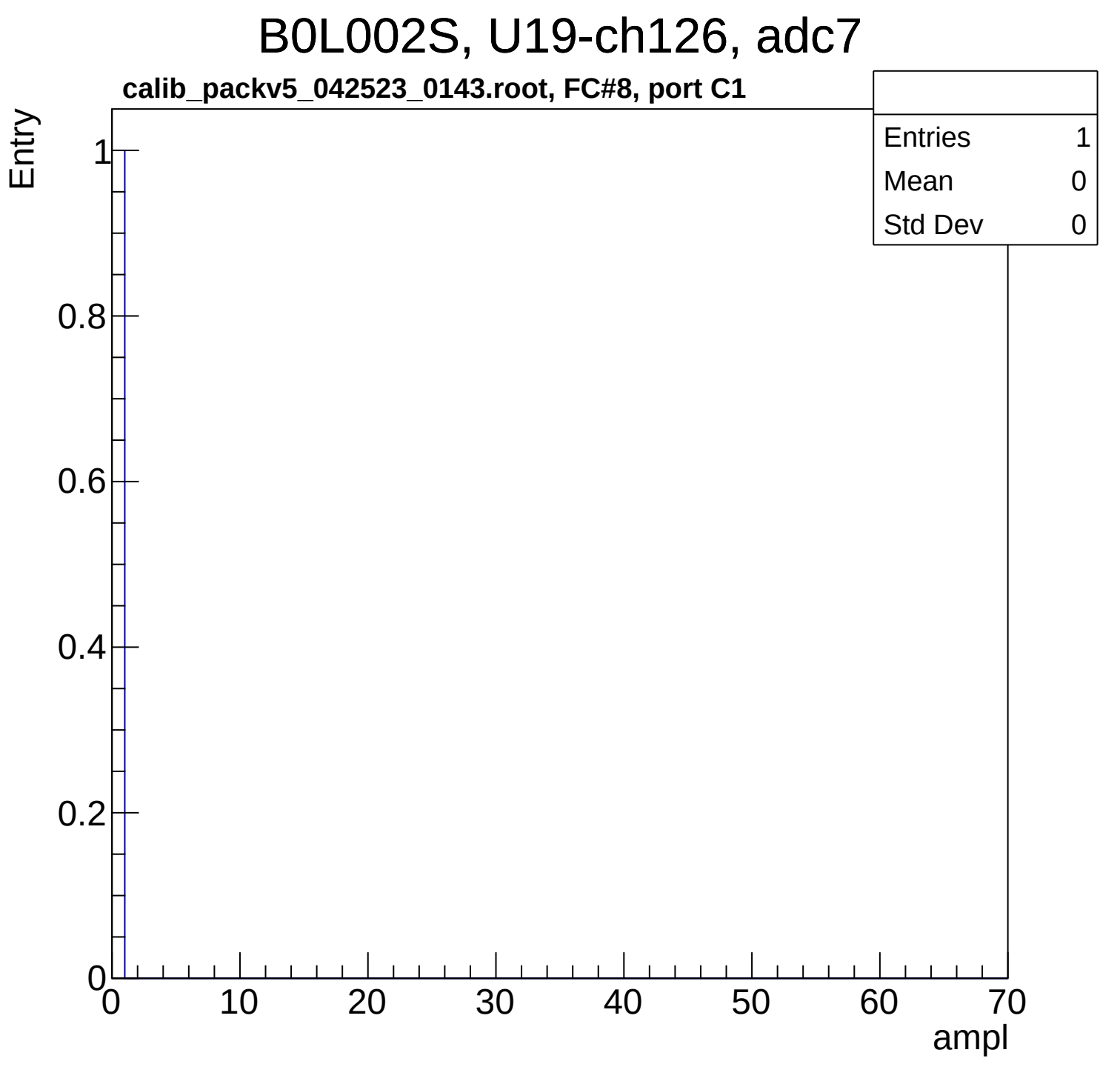

B0L002S, U19-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.89
Std Dev 3.6
Gaus mean : 45.4723
Gaus Width: 3.3772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

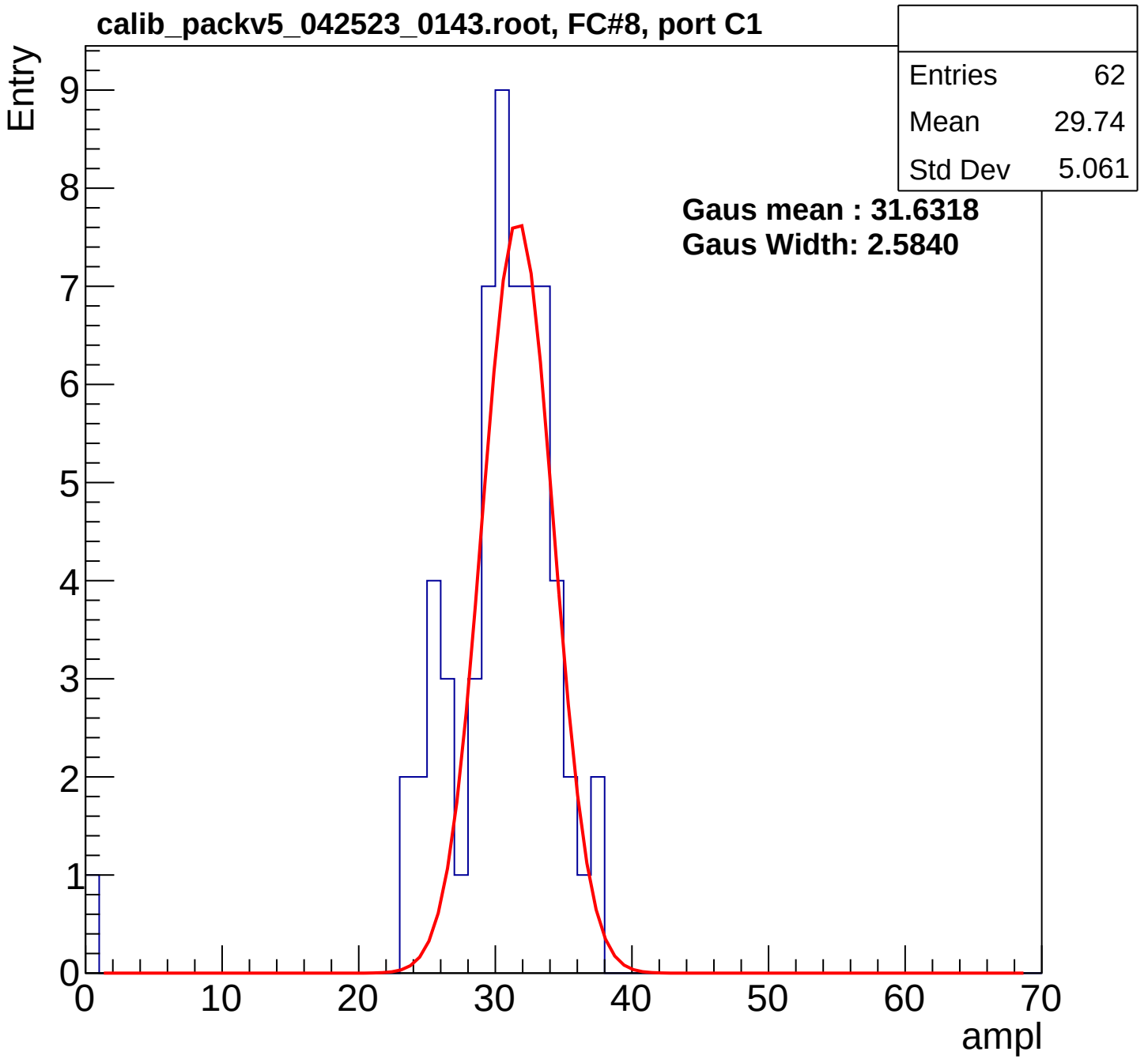

B0L002S, U19-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.55
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

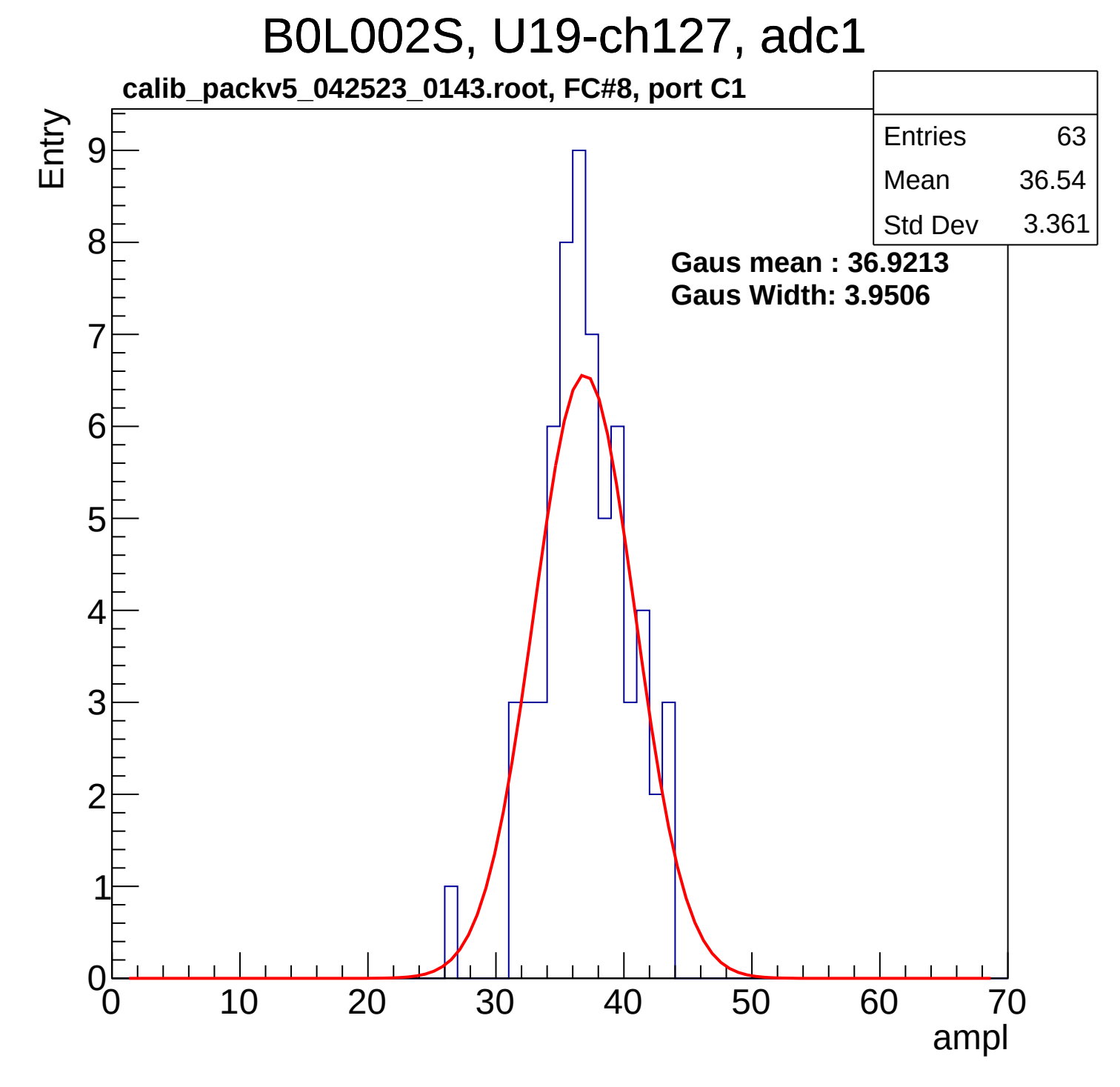

B0L002S, U19-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.37
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

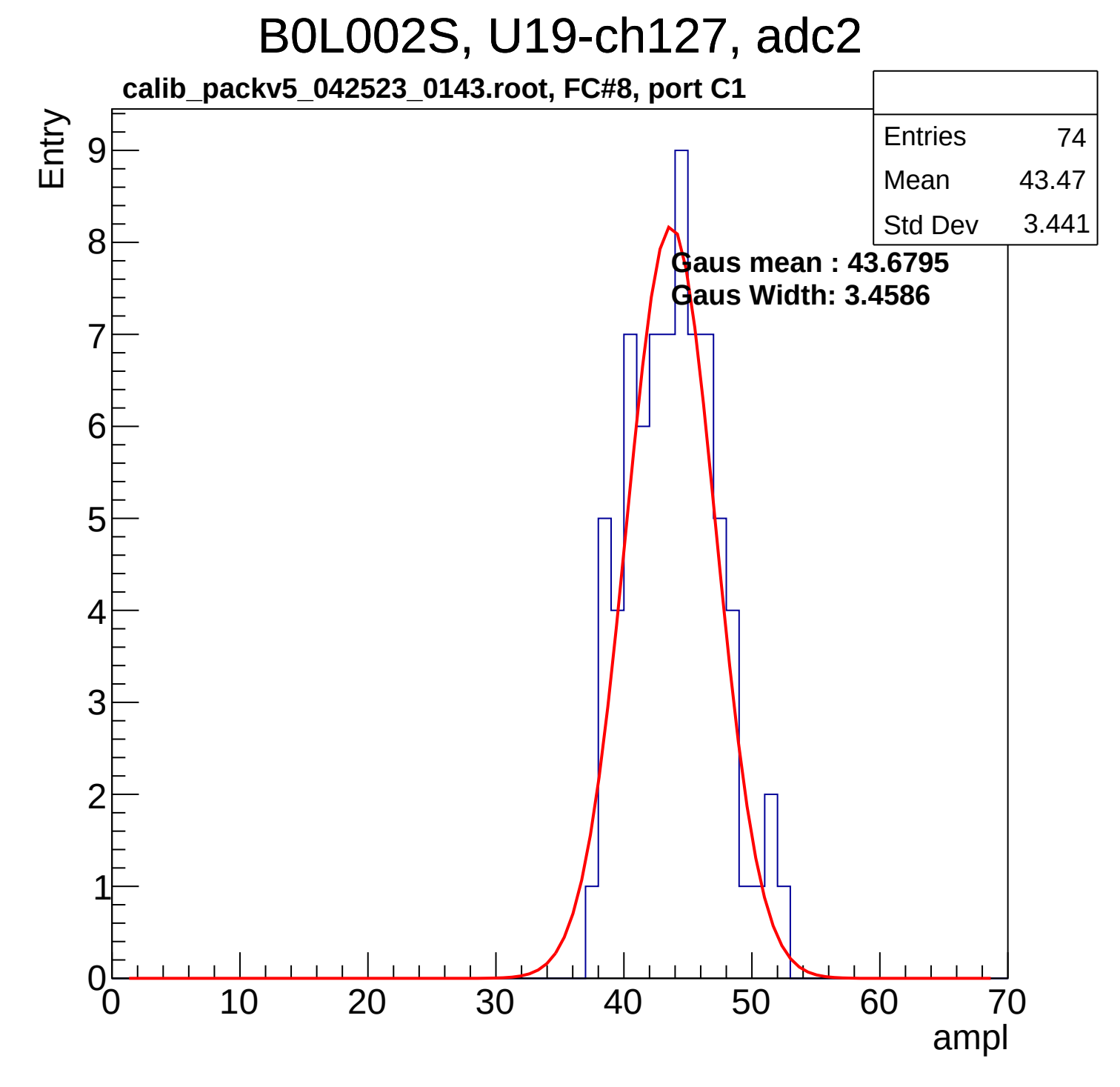

B0L002S, U19-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 60.76
Std Dev 2.238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

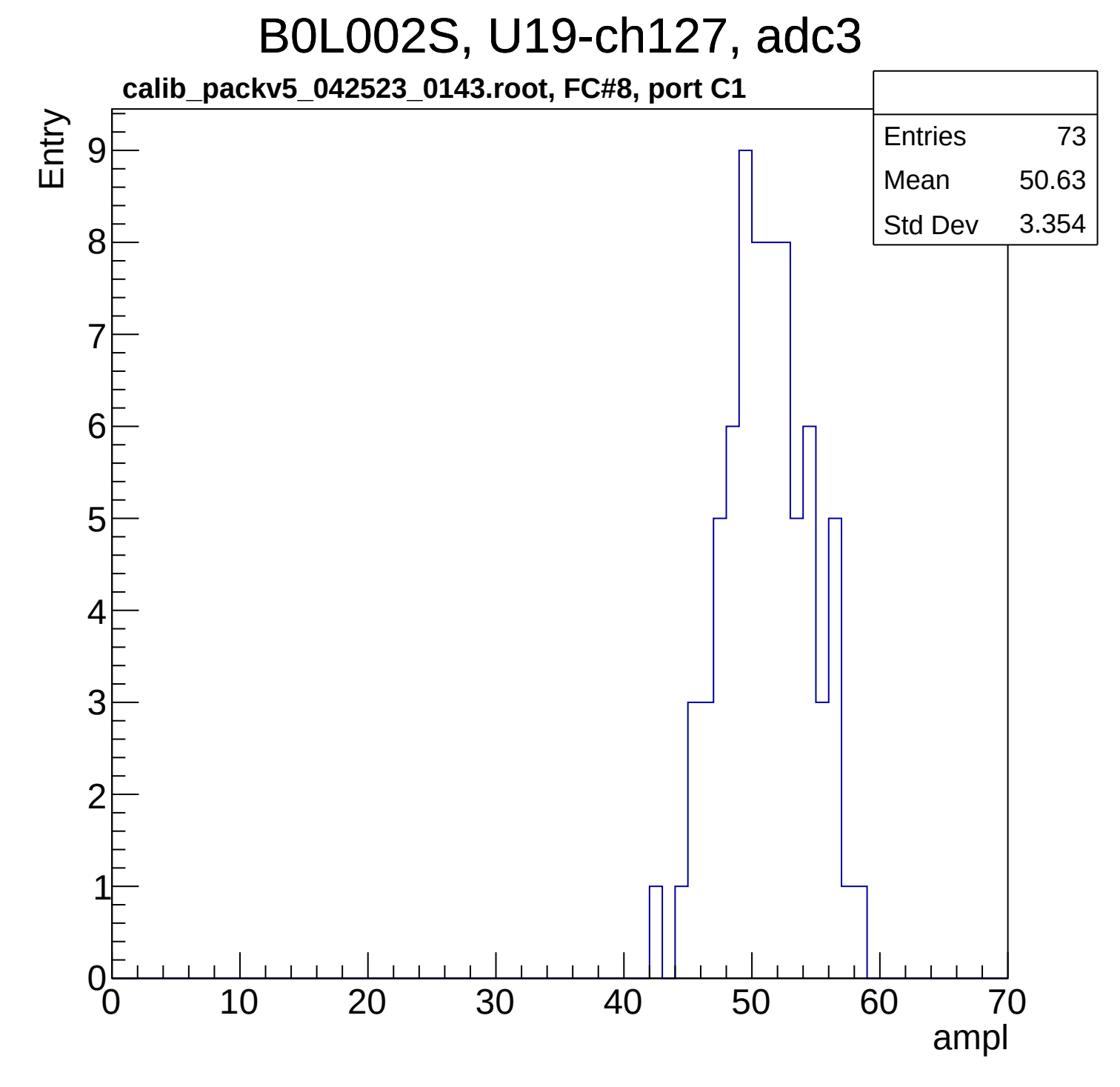

B0L002S, U19-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

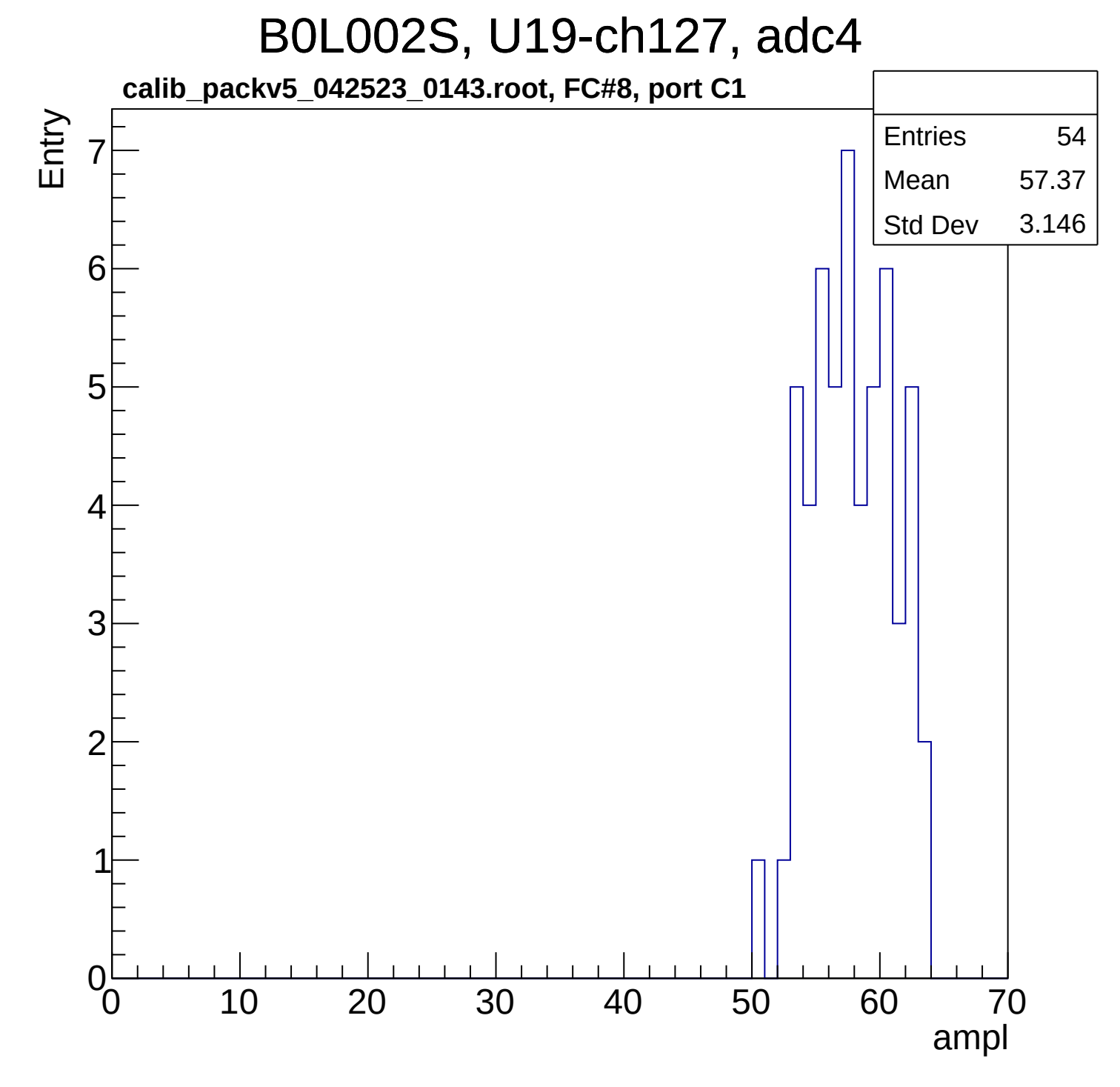

B0L002S, U19-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

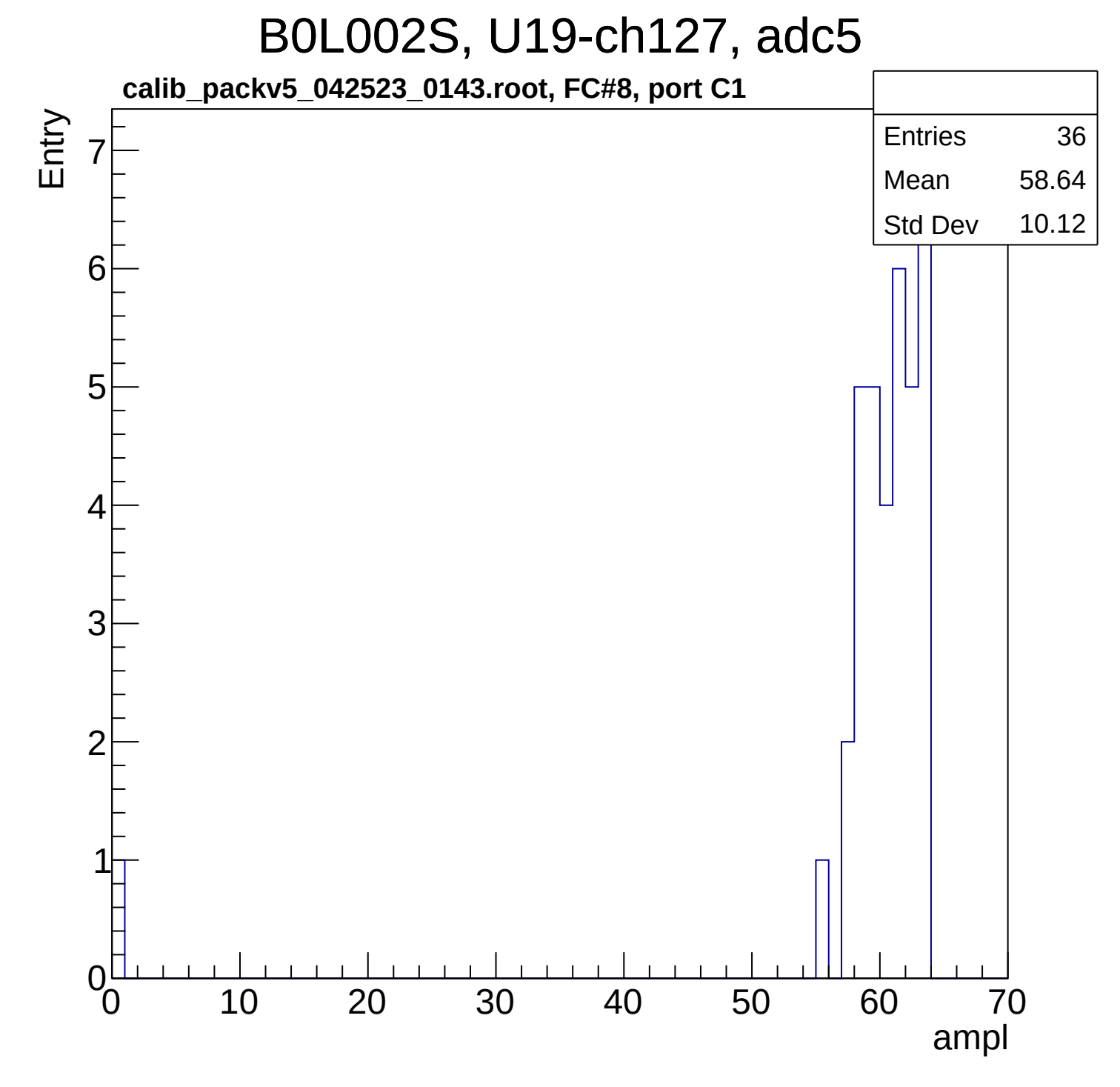

B0L002S, U19-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 29.74
Std Dev 5.061
Gaus mean : 31.6318
Gaus Width: 2.5840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

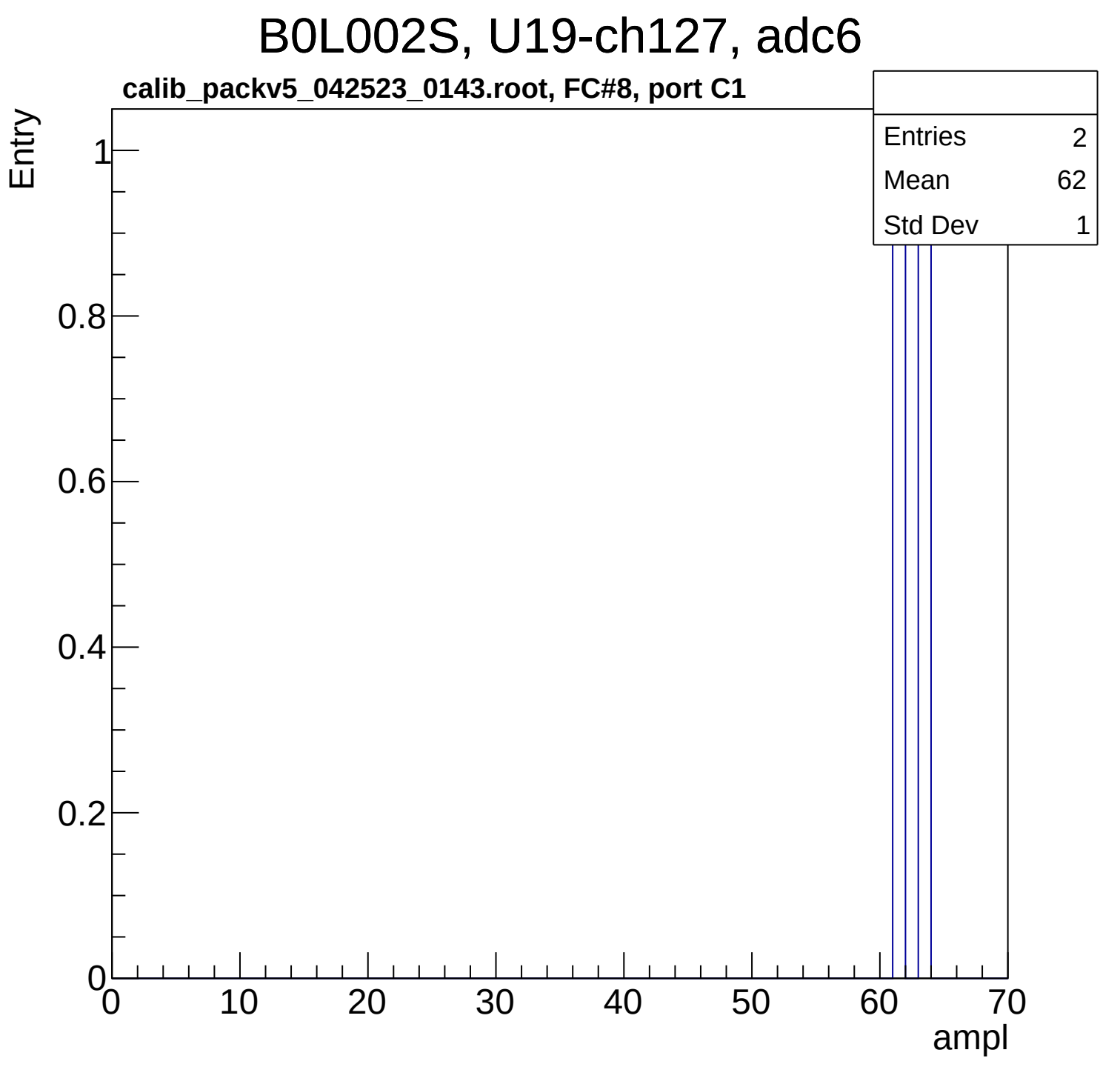

B0L002S, U19-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 36.54
Std Dev 3.361
Gaus mean : 36.9213
Gaus Width: 3.9506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

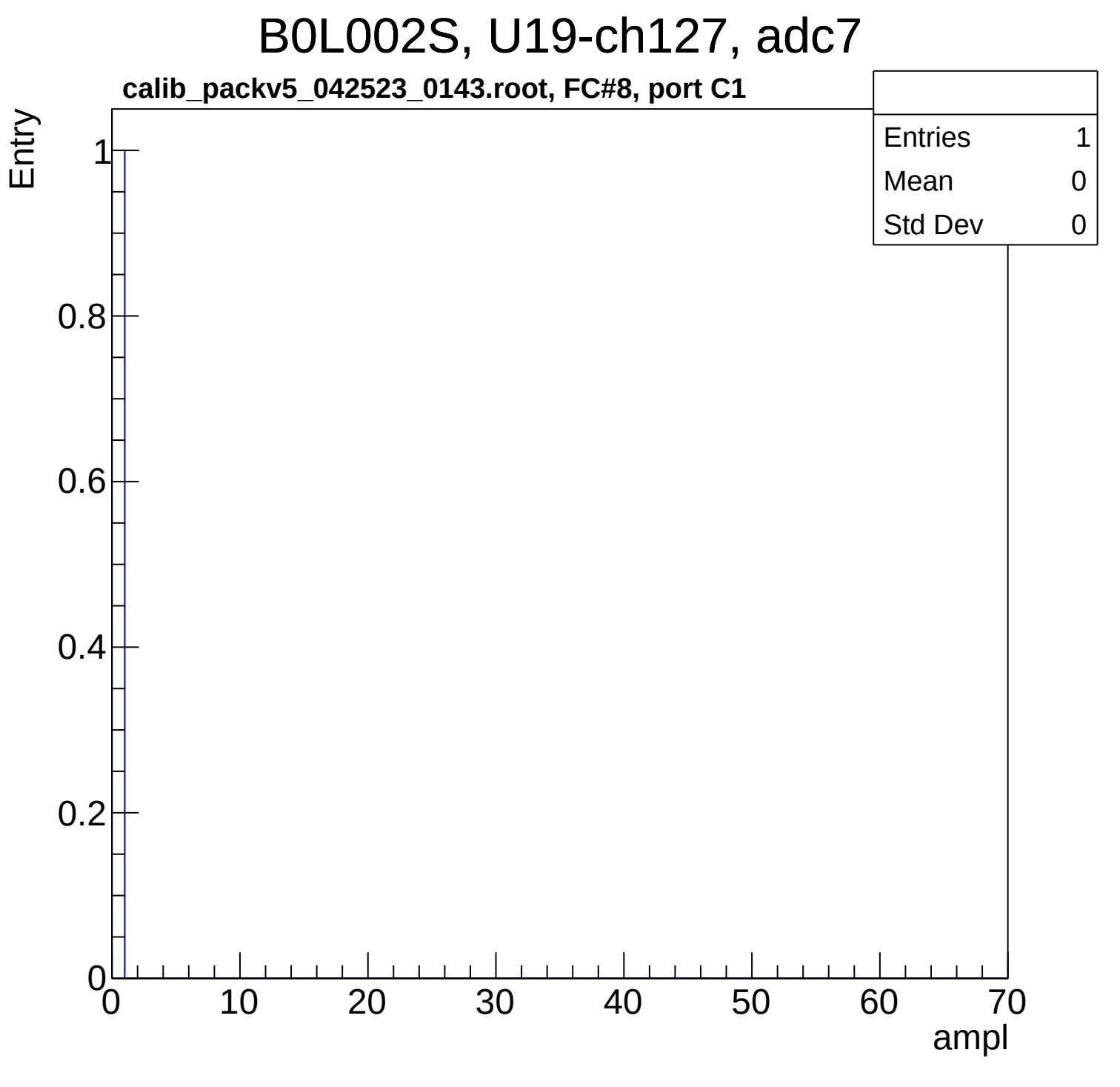

B0L002S, U19-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.47
Std Dev 3.441
Gaus mean : 43.6795
Gaus Width: 3.4586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

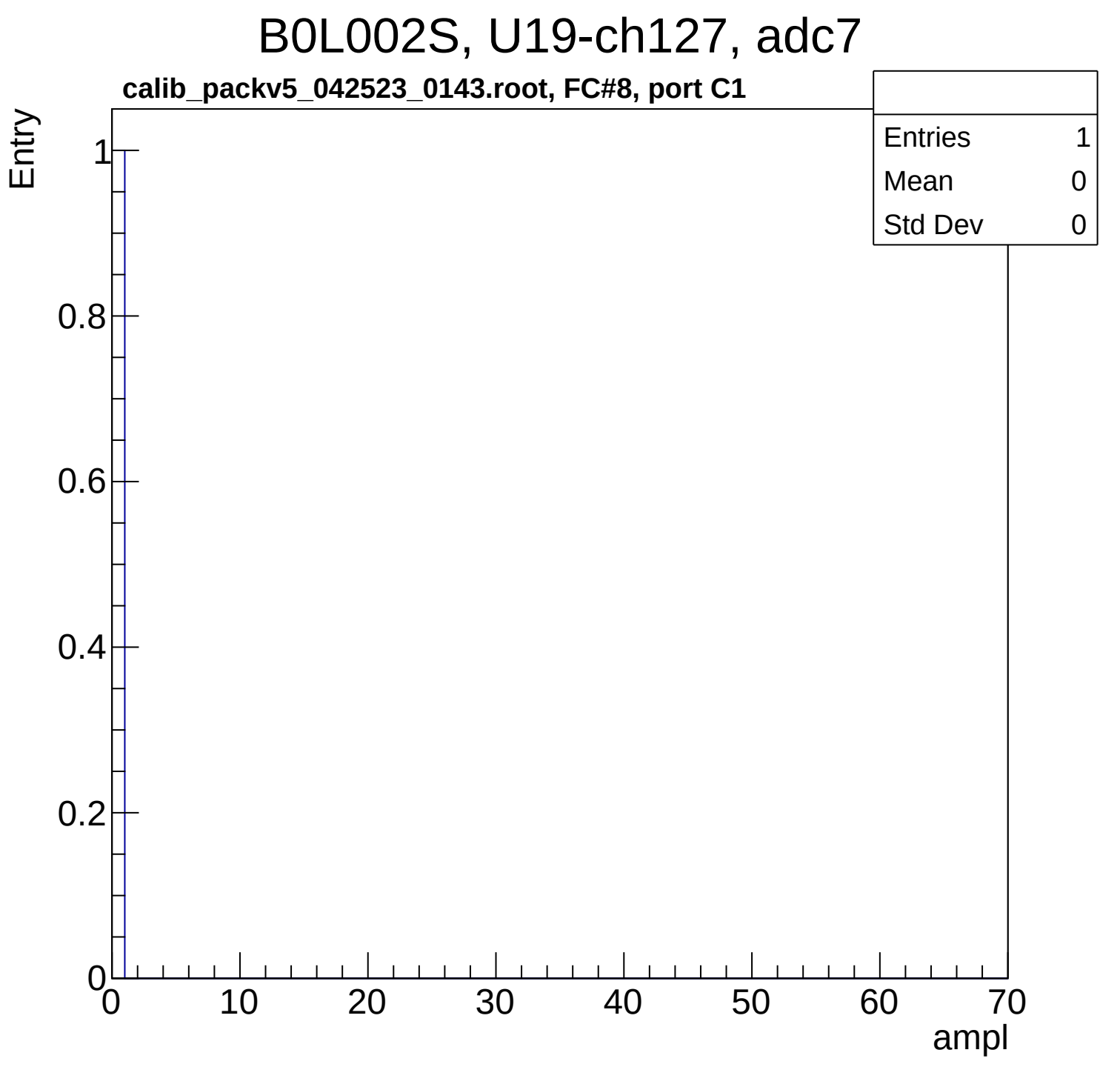

B0L002S, U19-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.63
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U19-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.37
Std Dev 3.146
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U19-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.64
Std Dev 10.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U19-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U19-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U19-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl