

B1L102S, U19-ch0, adc0

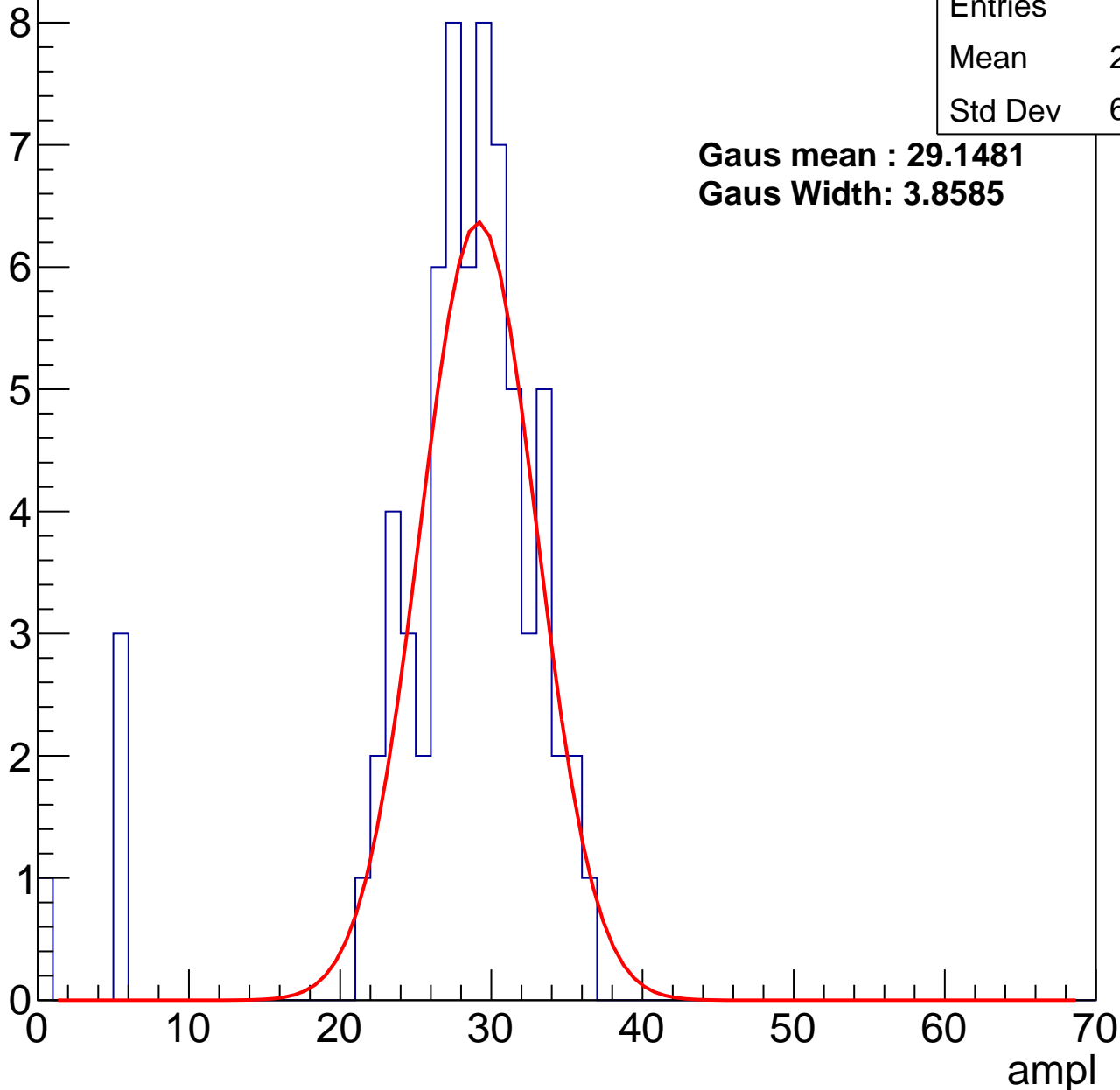
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.04
Std Dev	6.719

Gaus mean : 29.1481

Gaus Width: 3.8585



B1L102S, U19-ch0, adc1

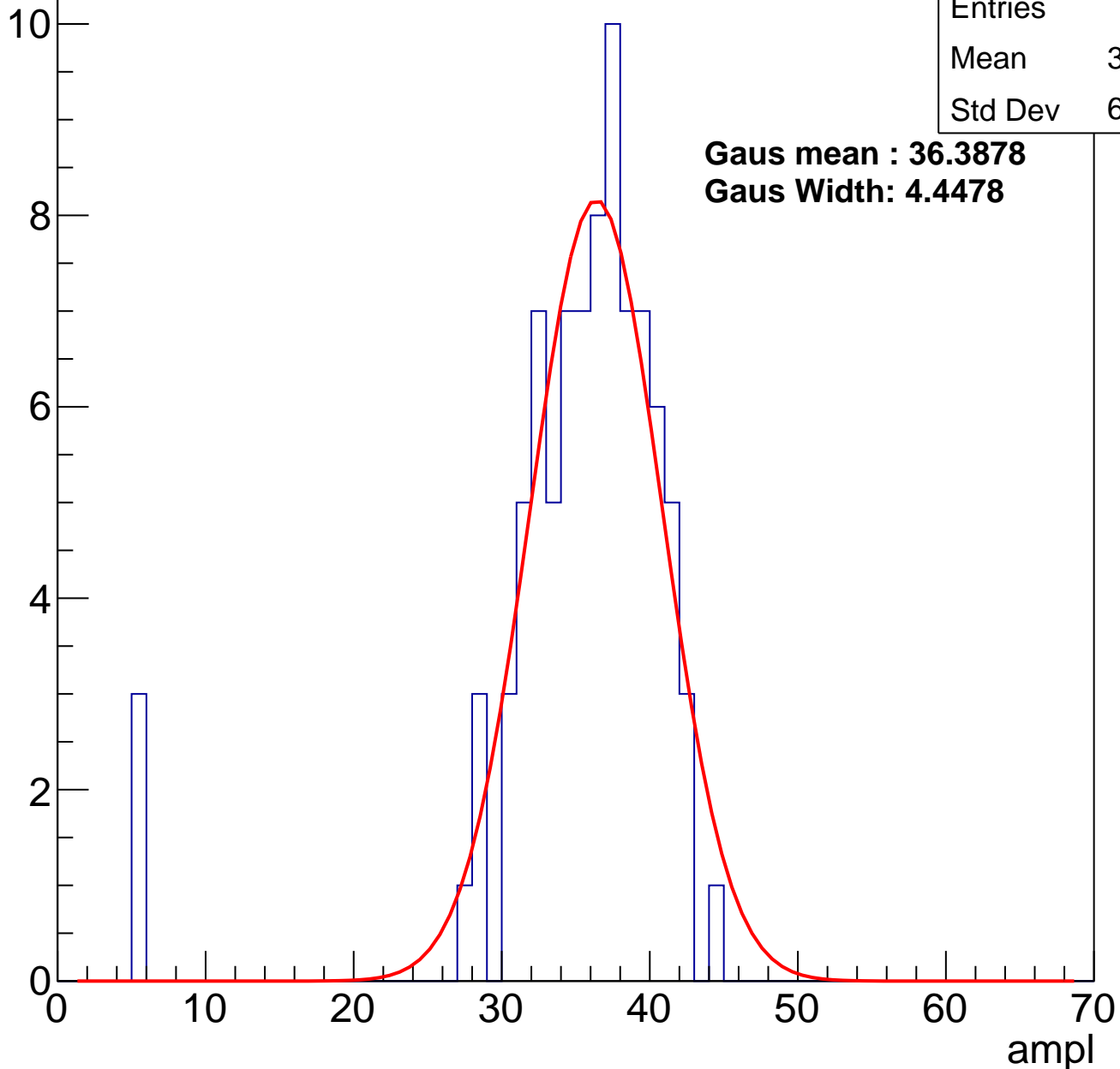
calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	34.72
Std Dev	6.683

Gaus mean : 36.3878

Gaus Width: 4.4478

Entry



B1L102S, U19-ch0, adc2

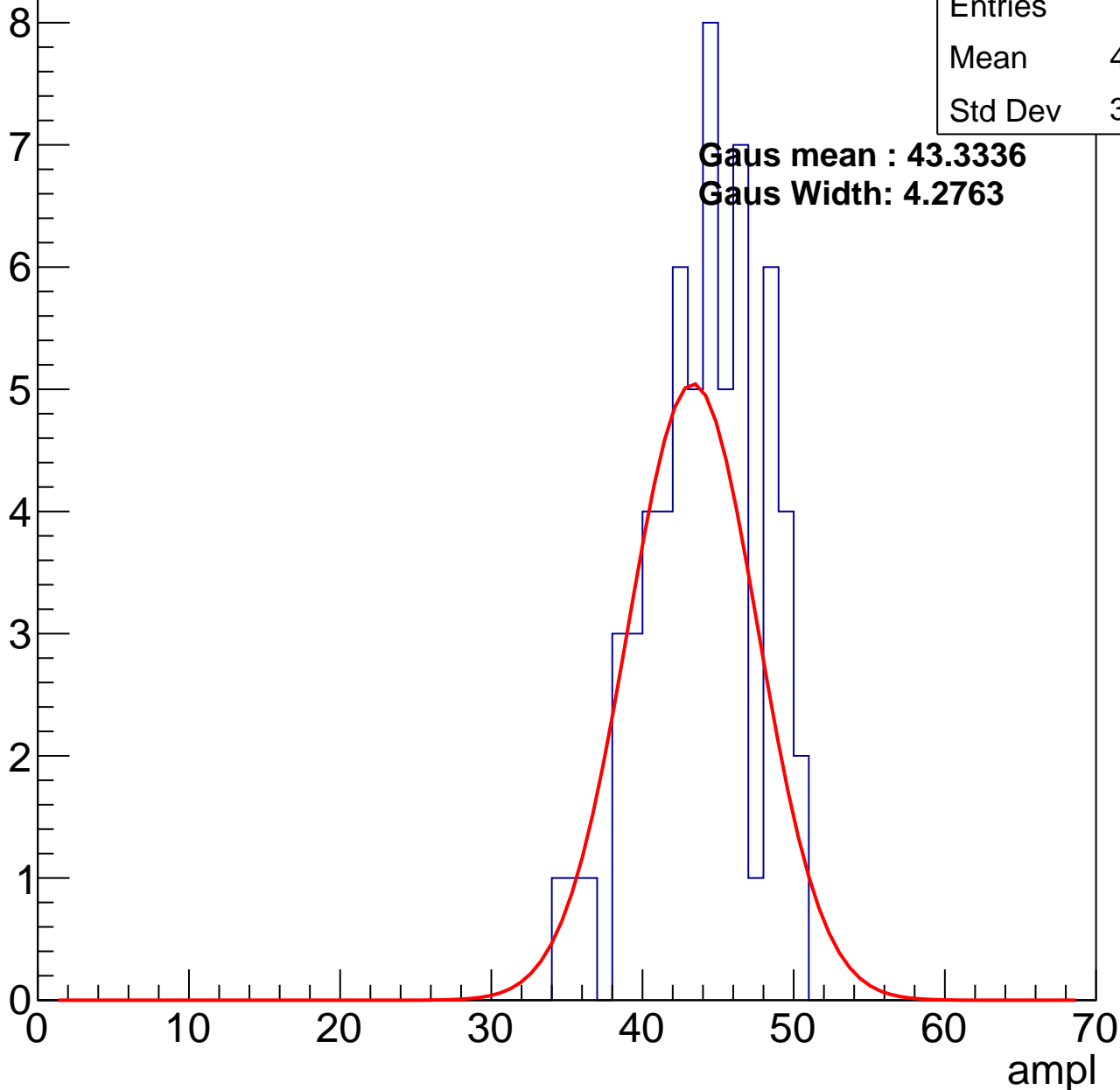
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.56
Std Dev	3.744

Gaus mean : 43.3336

Gaus Width: 4.2763

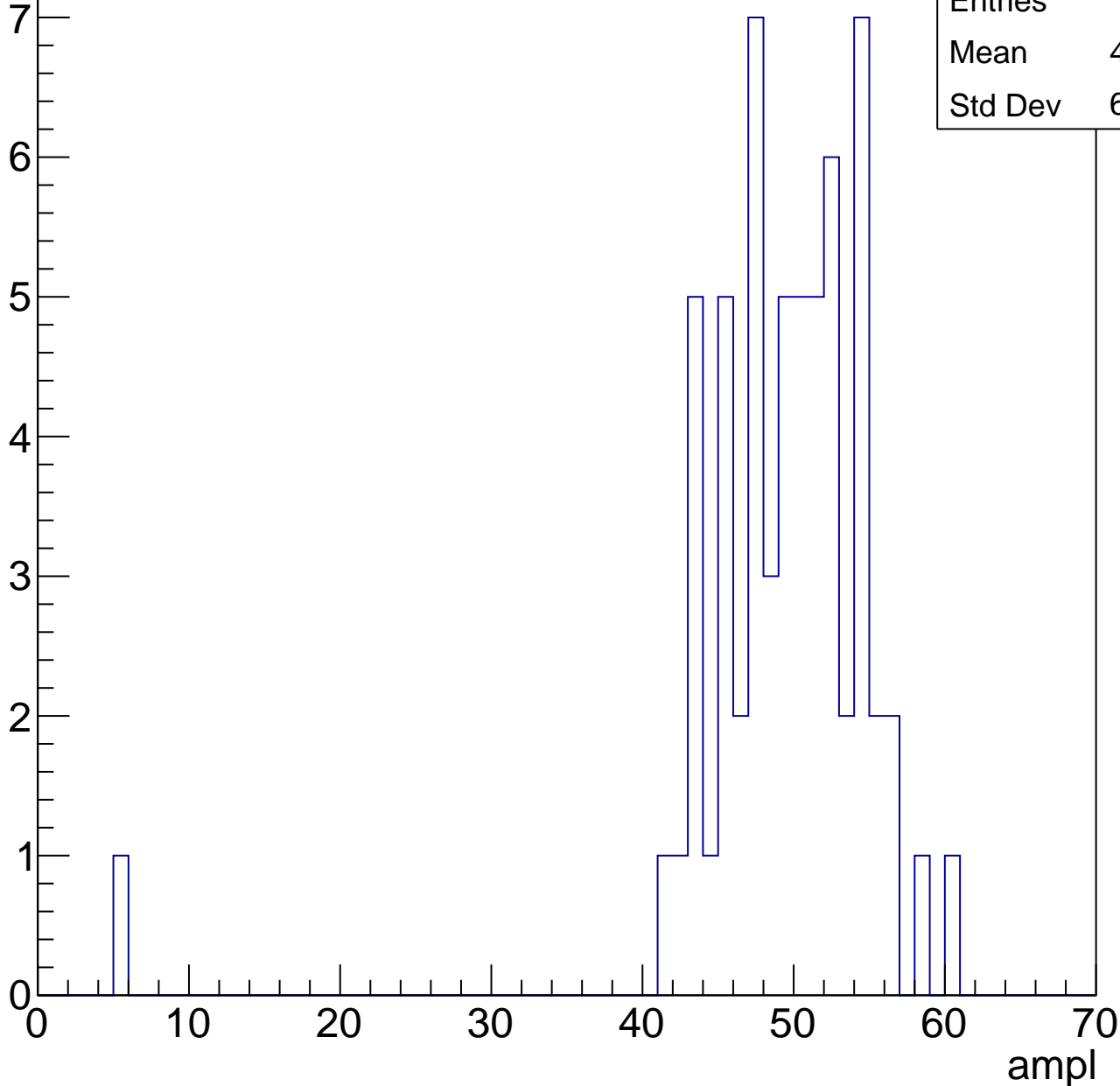


B1L102S, U19-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.76
Std Dev	6.999

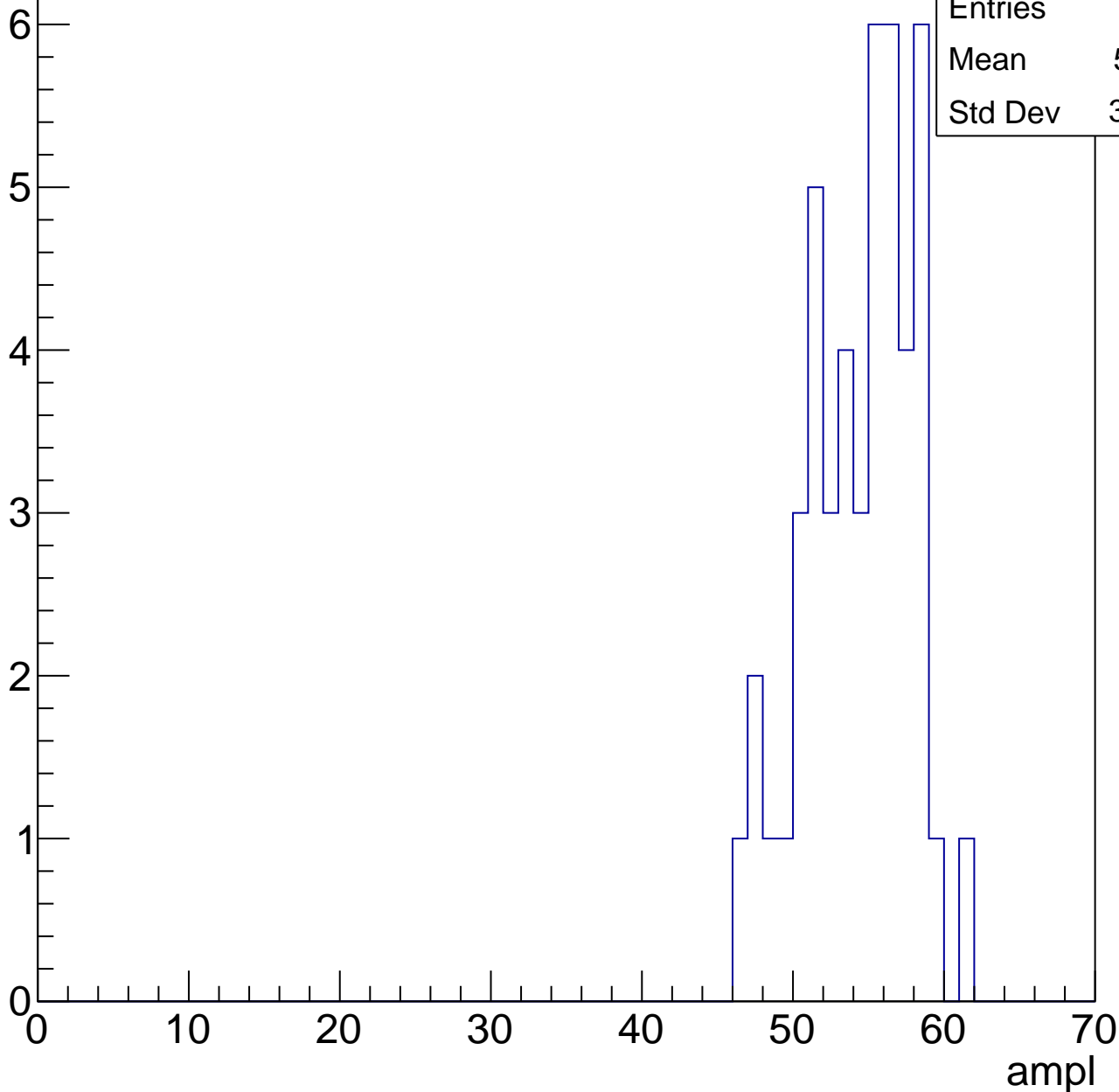


B1L102S, U19-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.91
Std Dev	3.475

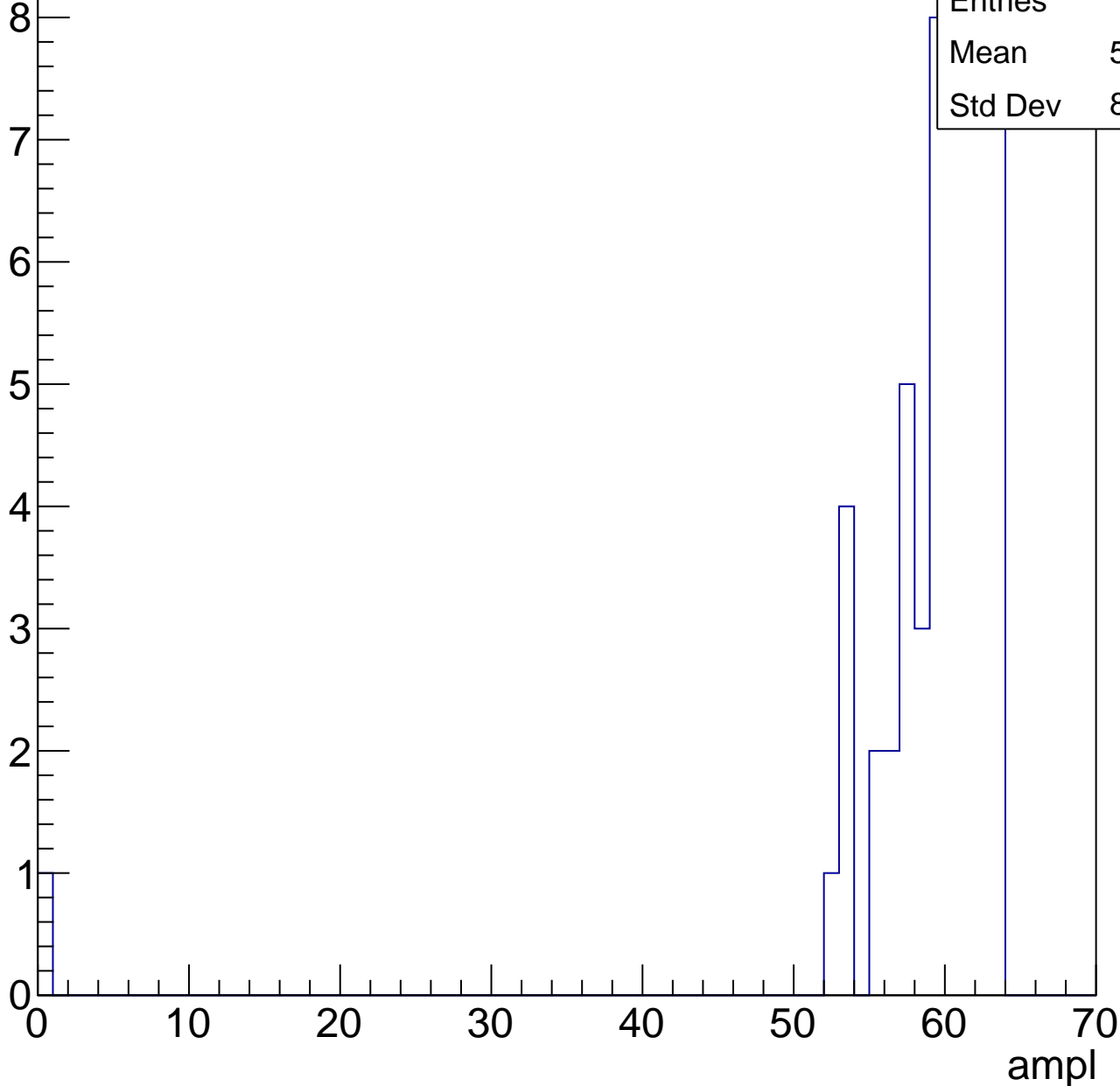


B1L102S, U19-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

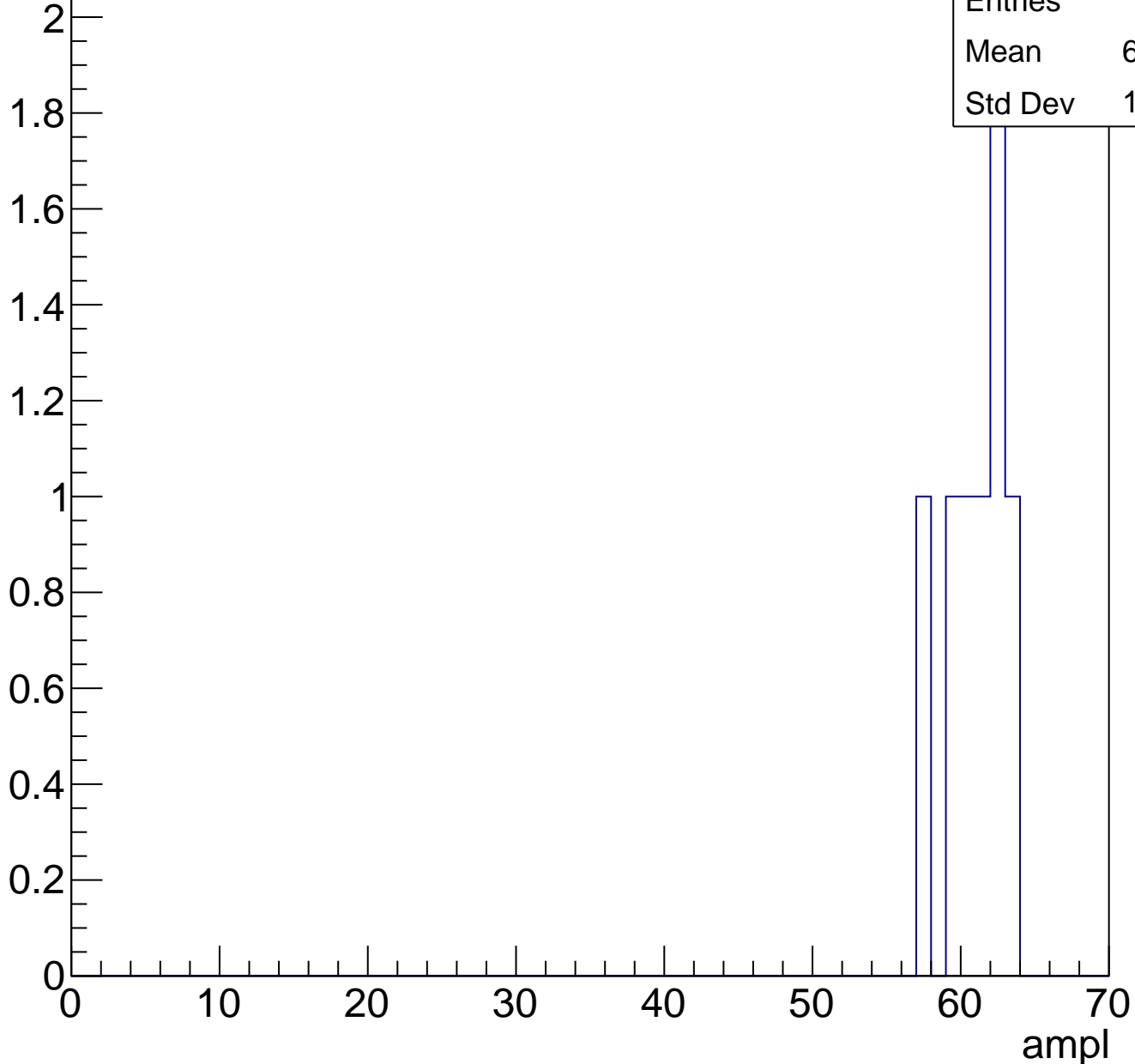
Entries	58
Mean	58.36
Std Dev	8.266



B1L102S, U19-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	39.5
Std Dev	23.5

ampl

B1L102S, U19-ch1, adc0

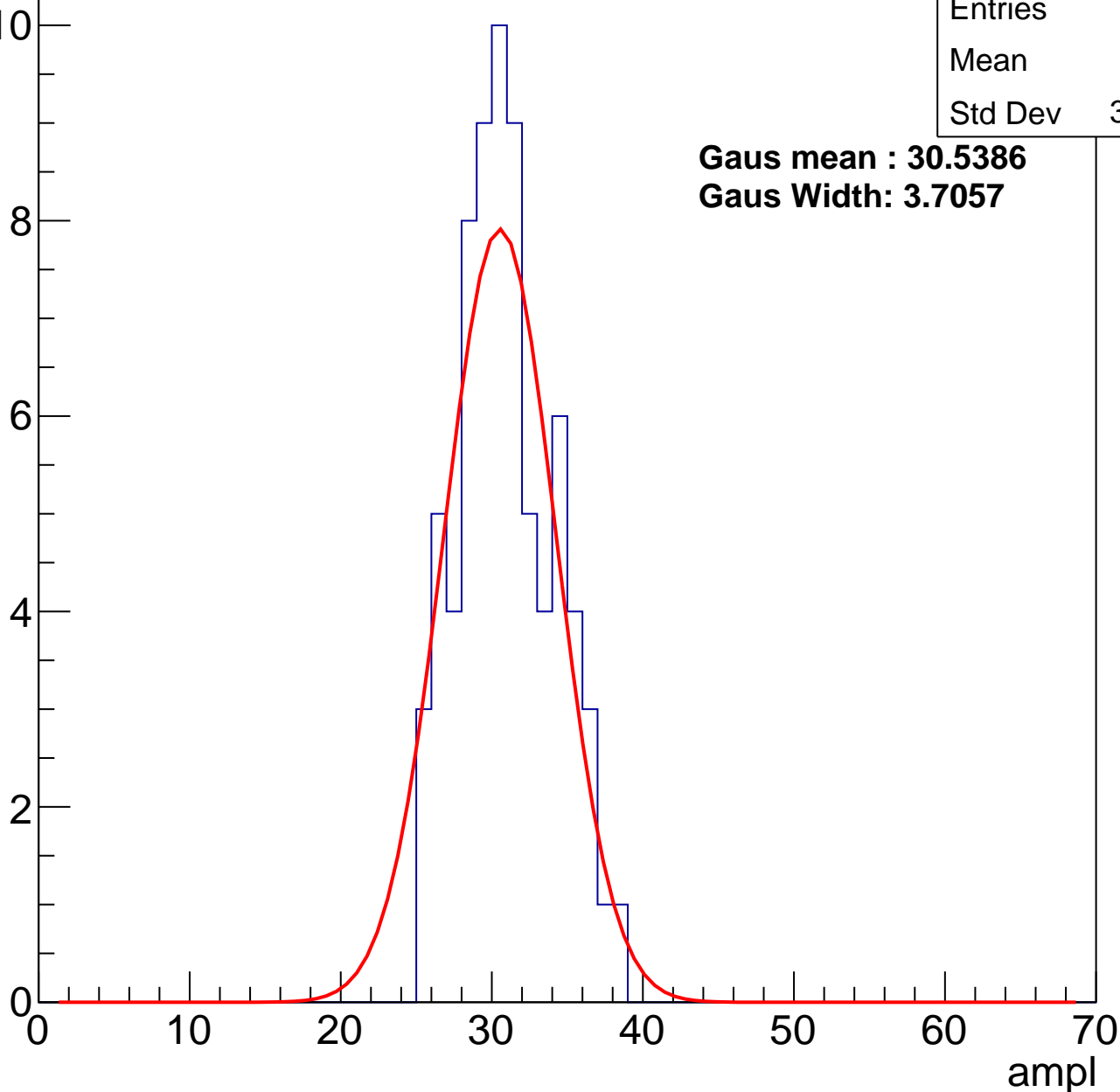
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	30.5
Std Dev	3.109

Gaus mean : 30.5386

Gaus Width: 3.7057



B1L102S, U19-ch1, adc1

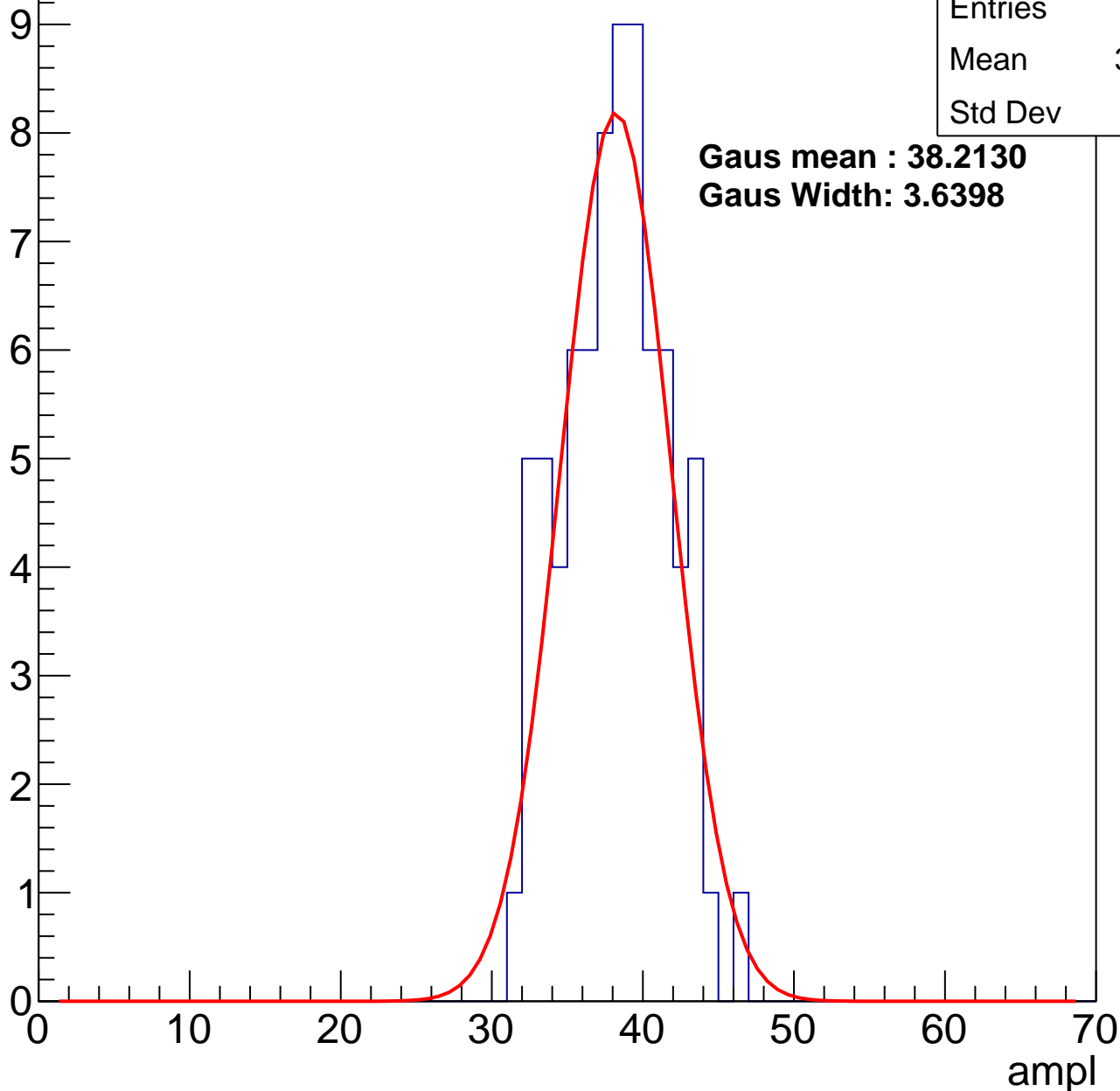
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.71
Std Dev	3.39

Gaus mean : 38.2130

Gaus Width: 3.6398

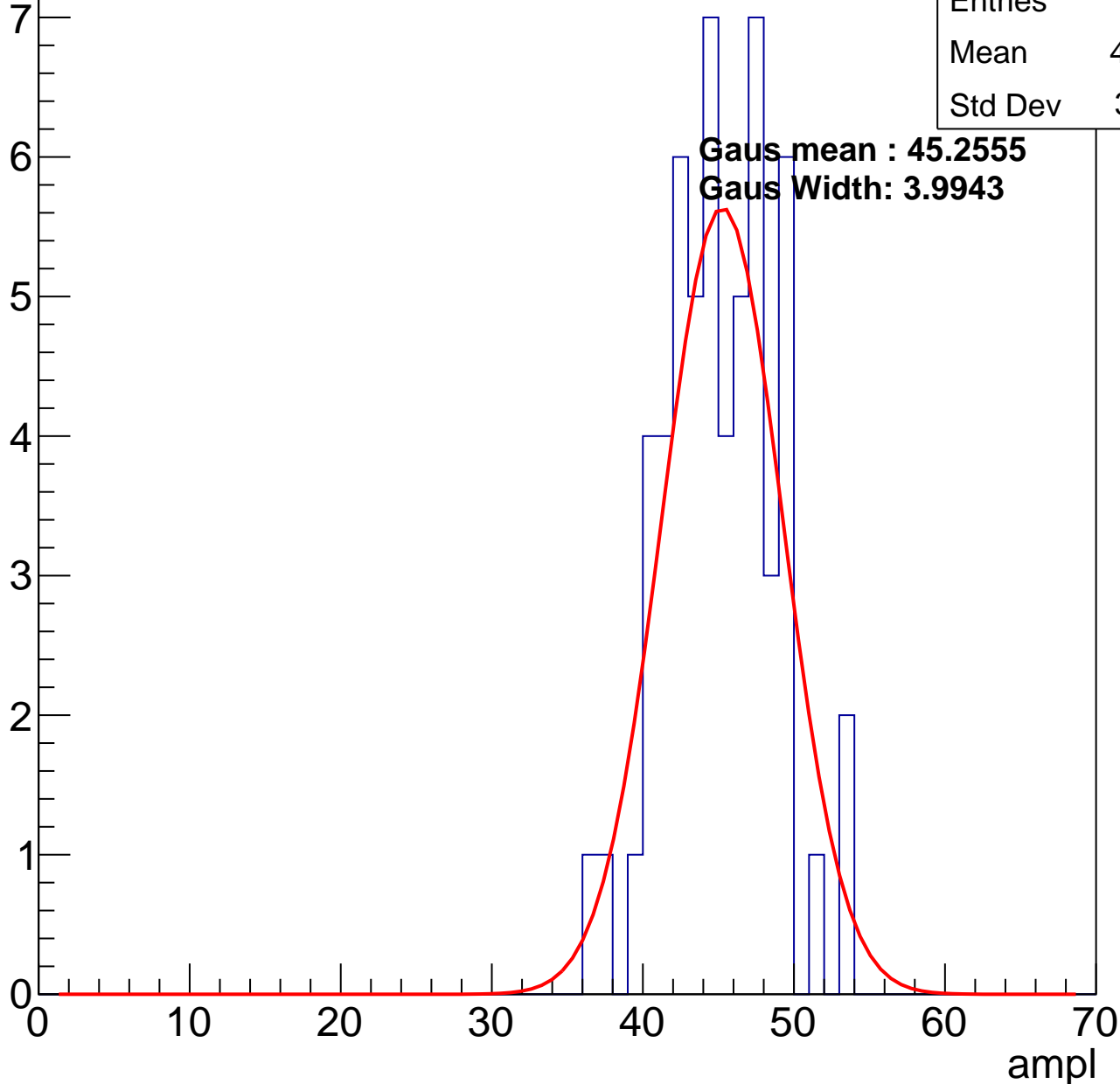


B1L102S, U19-ch1, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	44.65
Std Dev	3.601

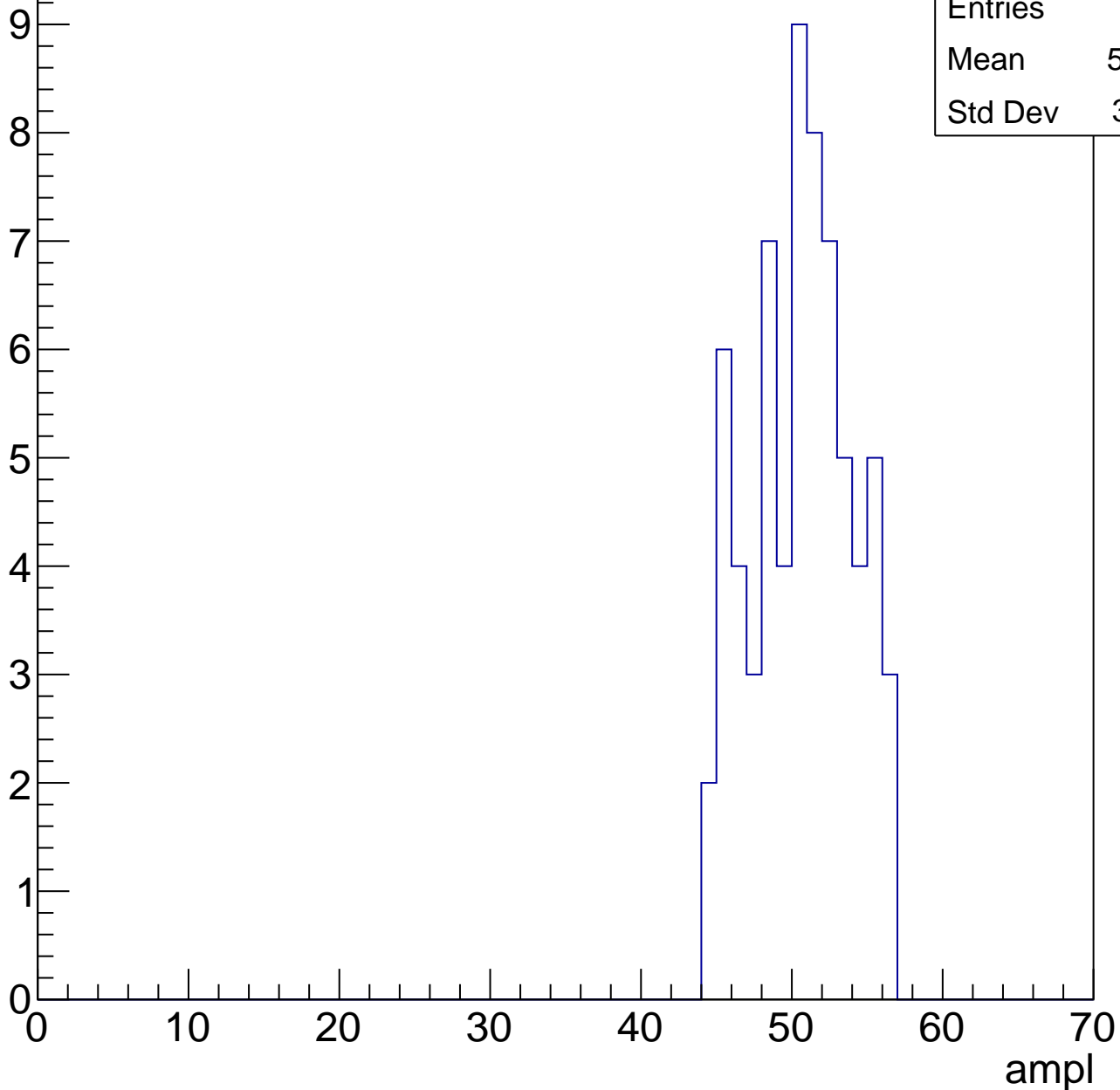


B1L102S, U19-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.16
Std Dev	3.281

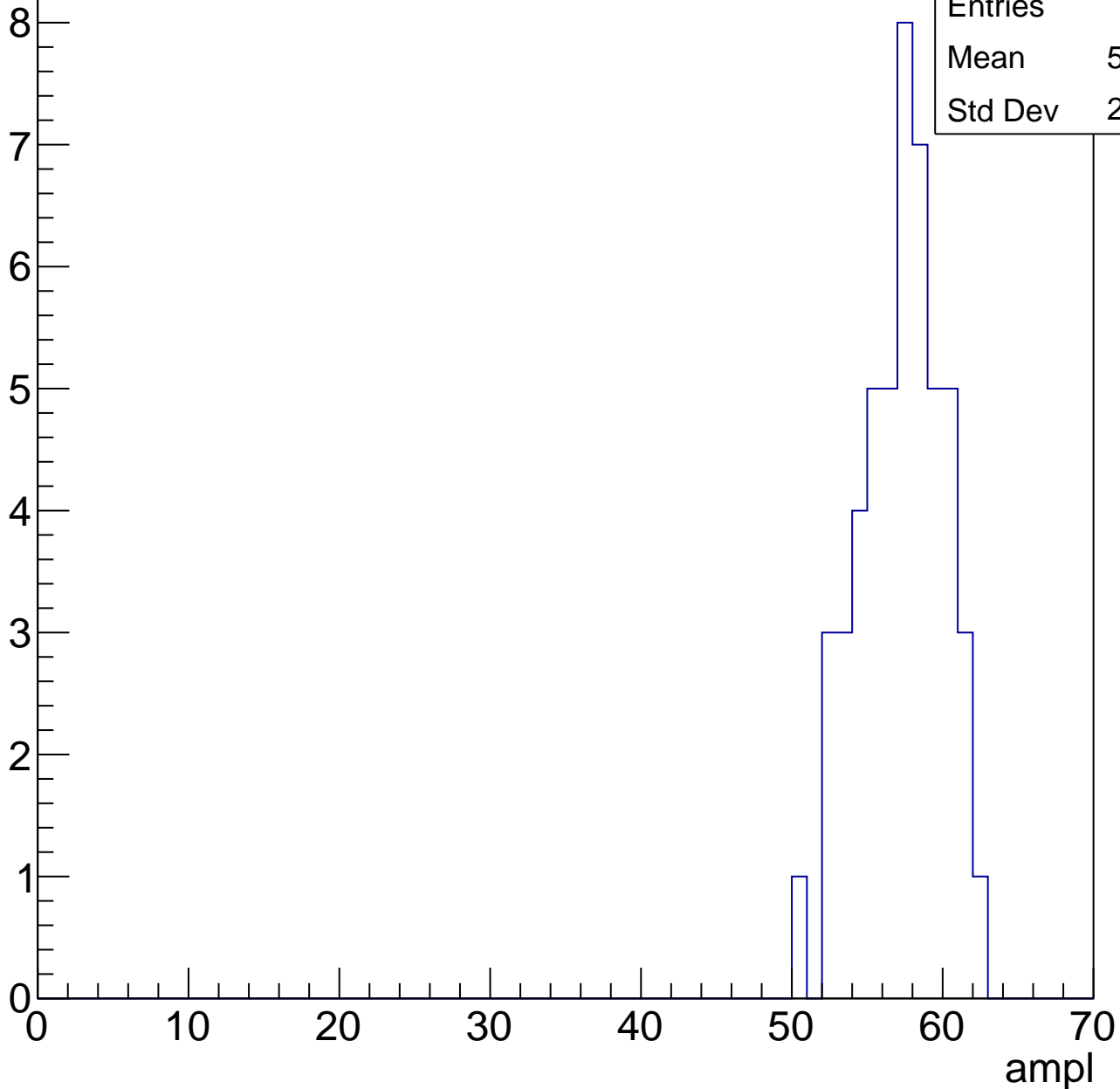


B1L102S, U19-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.76
Std Dev	2.739

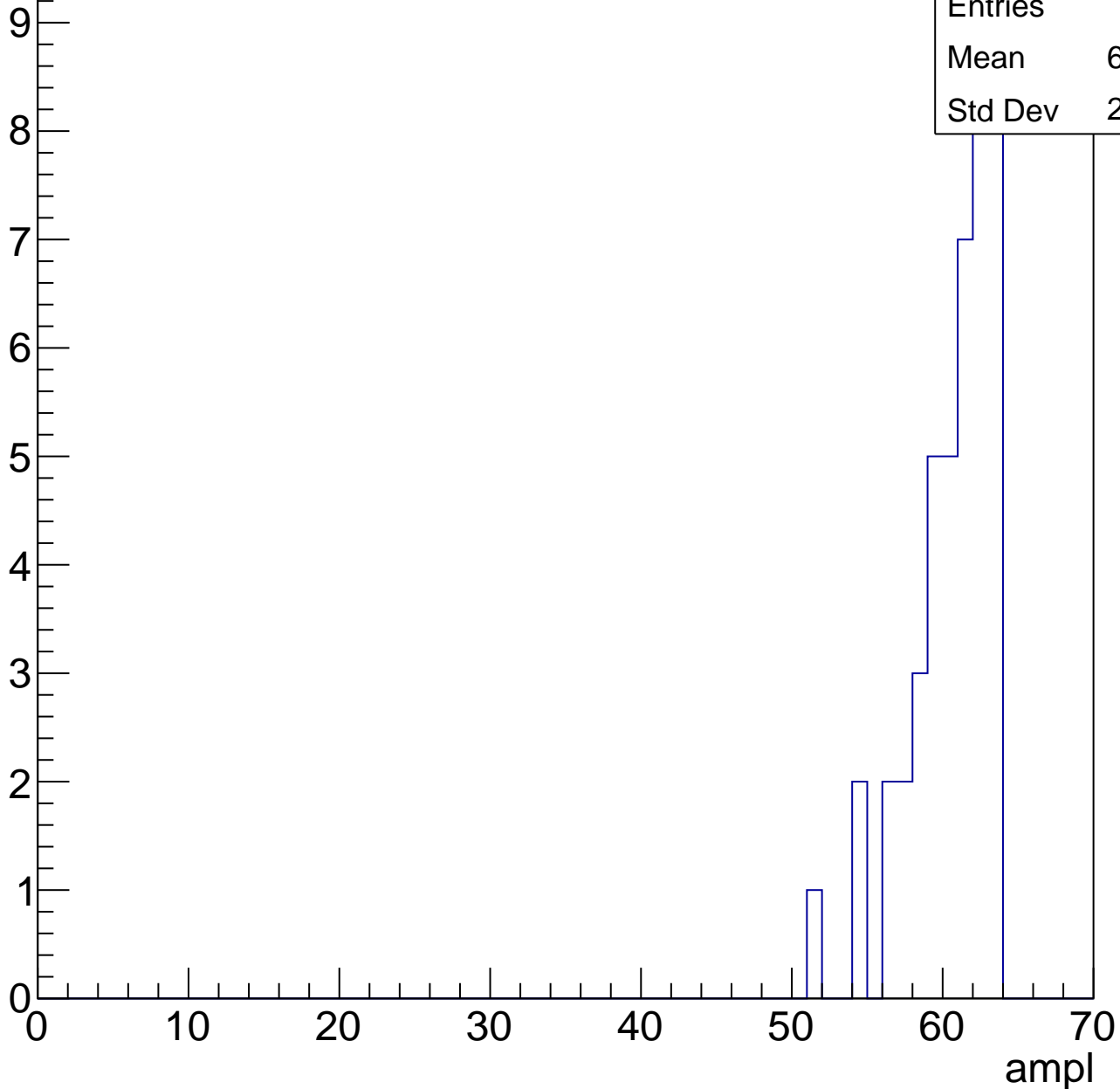


B1L102S, U19-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

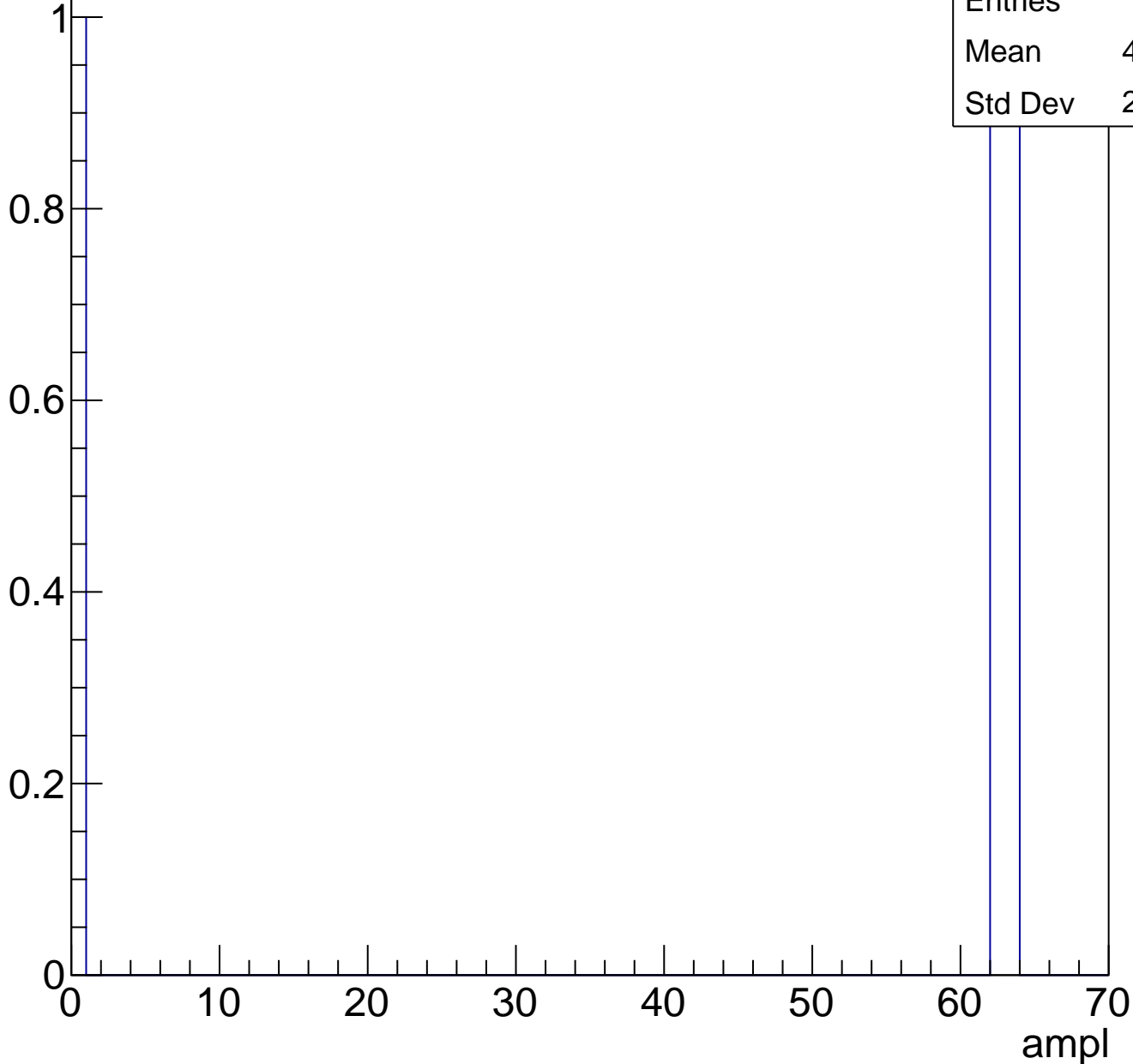
Entries	44
Mean	60.09
Std Dev	2.778



B1L102S, U19-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch2, adc0

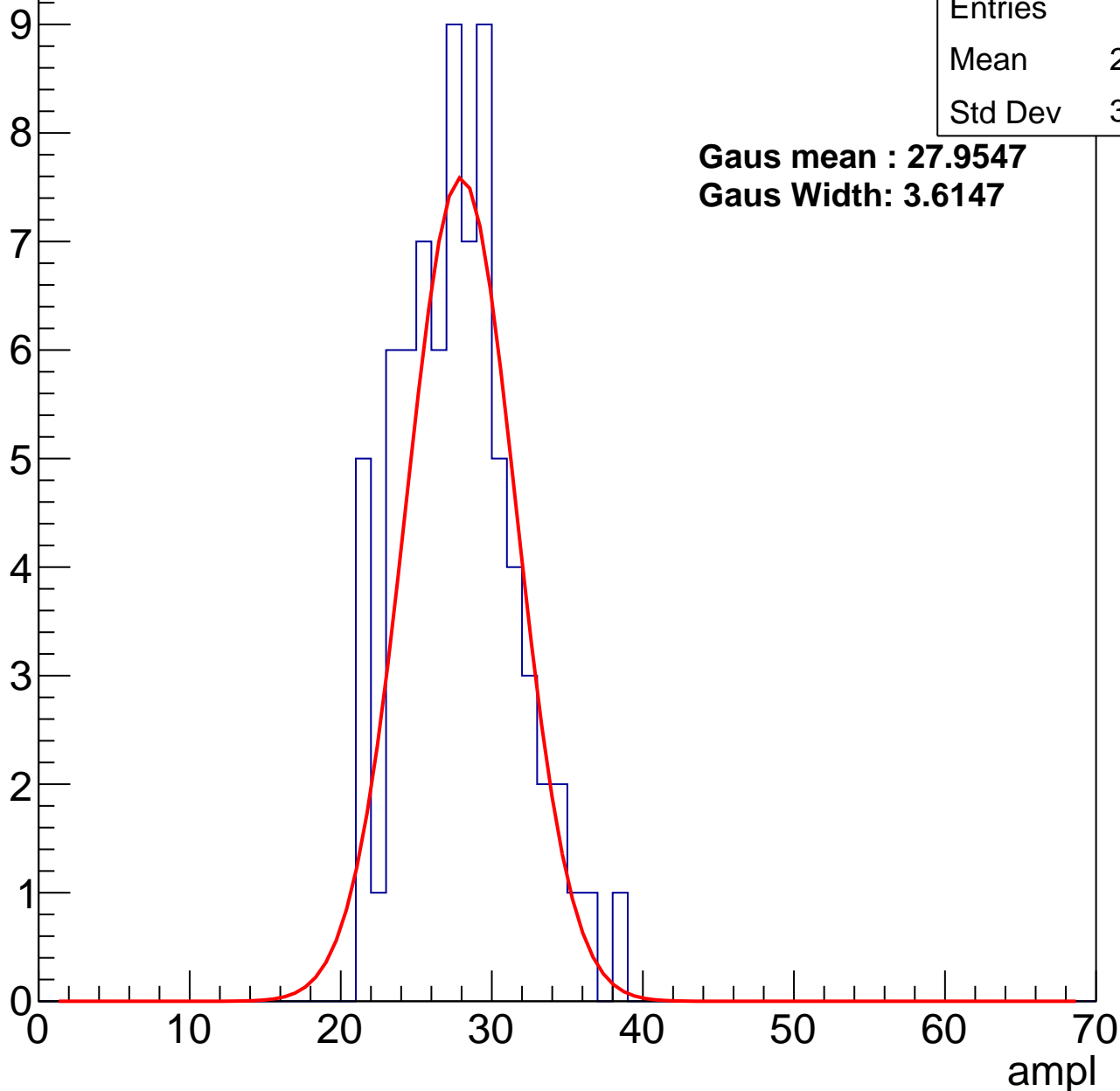
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.37
Std Dev	3.727

Gaus mean : 27.9547

Gaus Width: 3.6147



B1L102S, U19-ch2, adc1

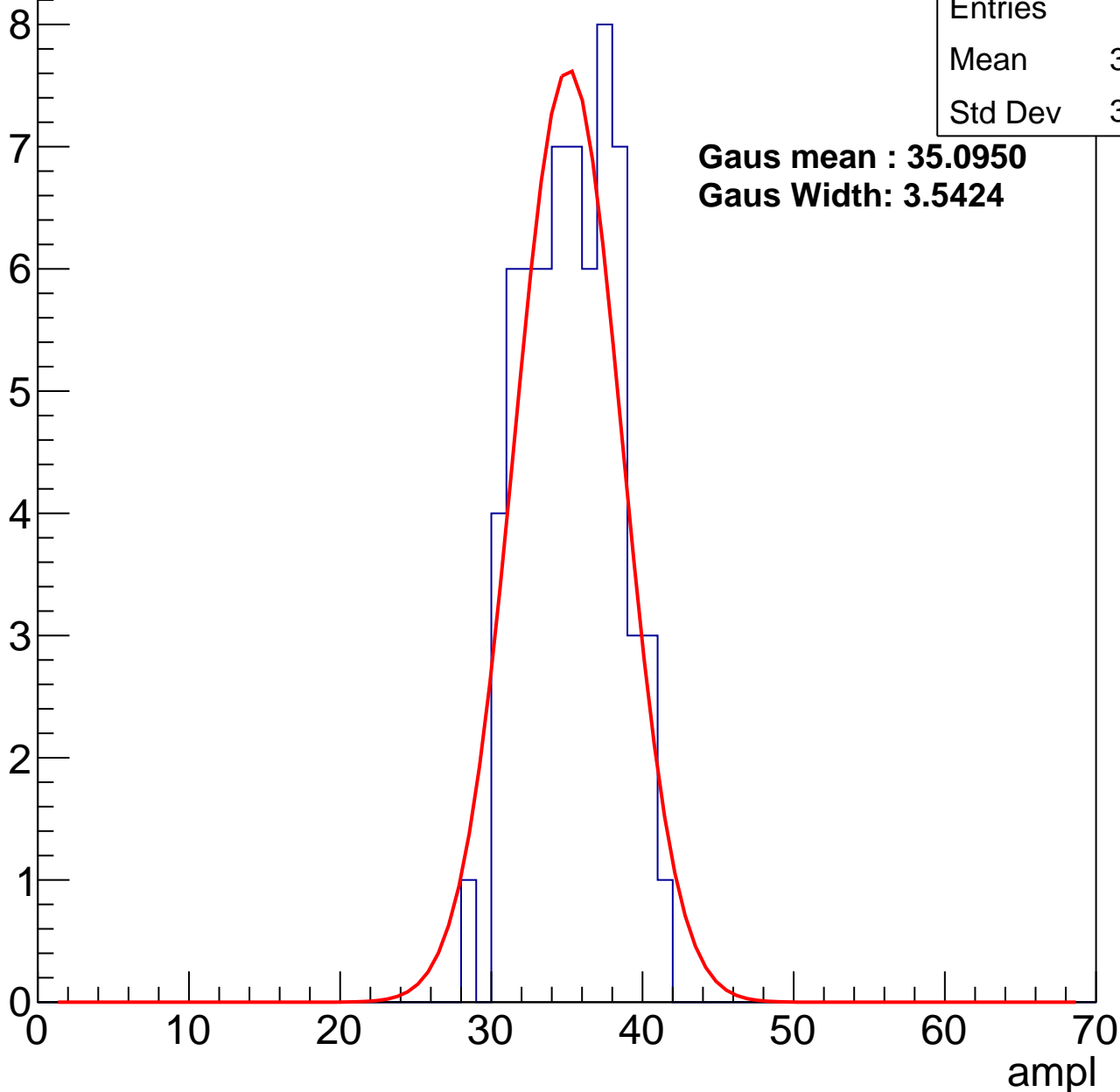
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.82
Std Dev	3.007

Gaus mean : 35.0950

Gaus Width: 3.5424



B1L102S, U19-ch2, adc2

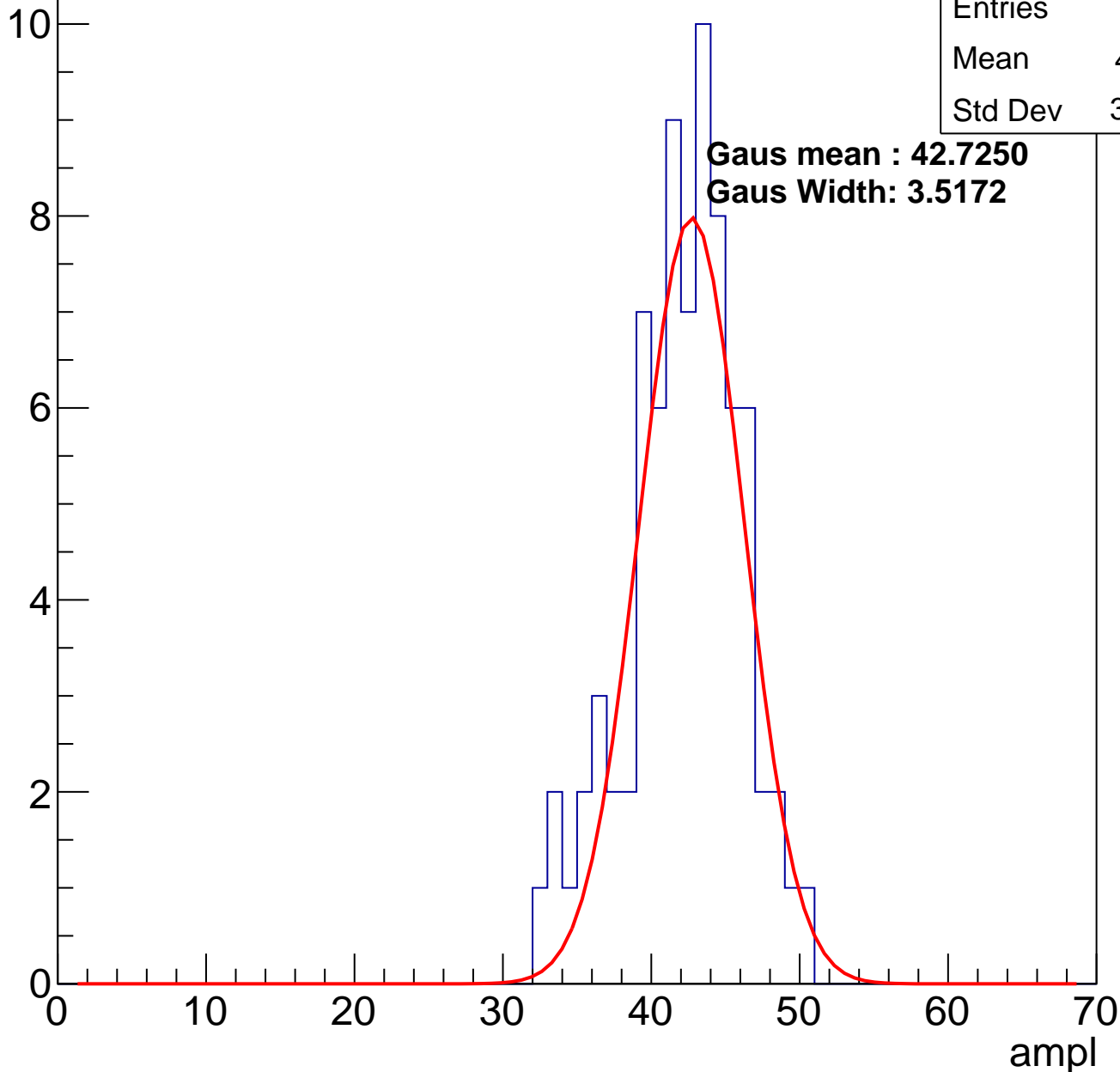
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	41.71
Std Dev	3.807

Gaus mean : 42.7250

Gaus Width: 3.5172

Entry

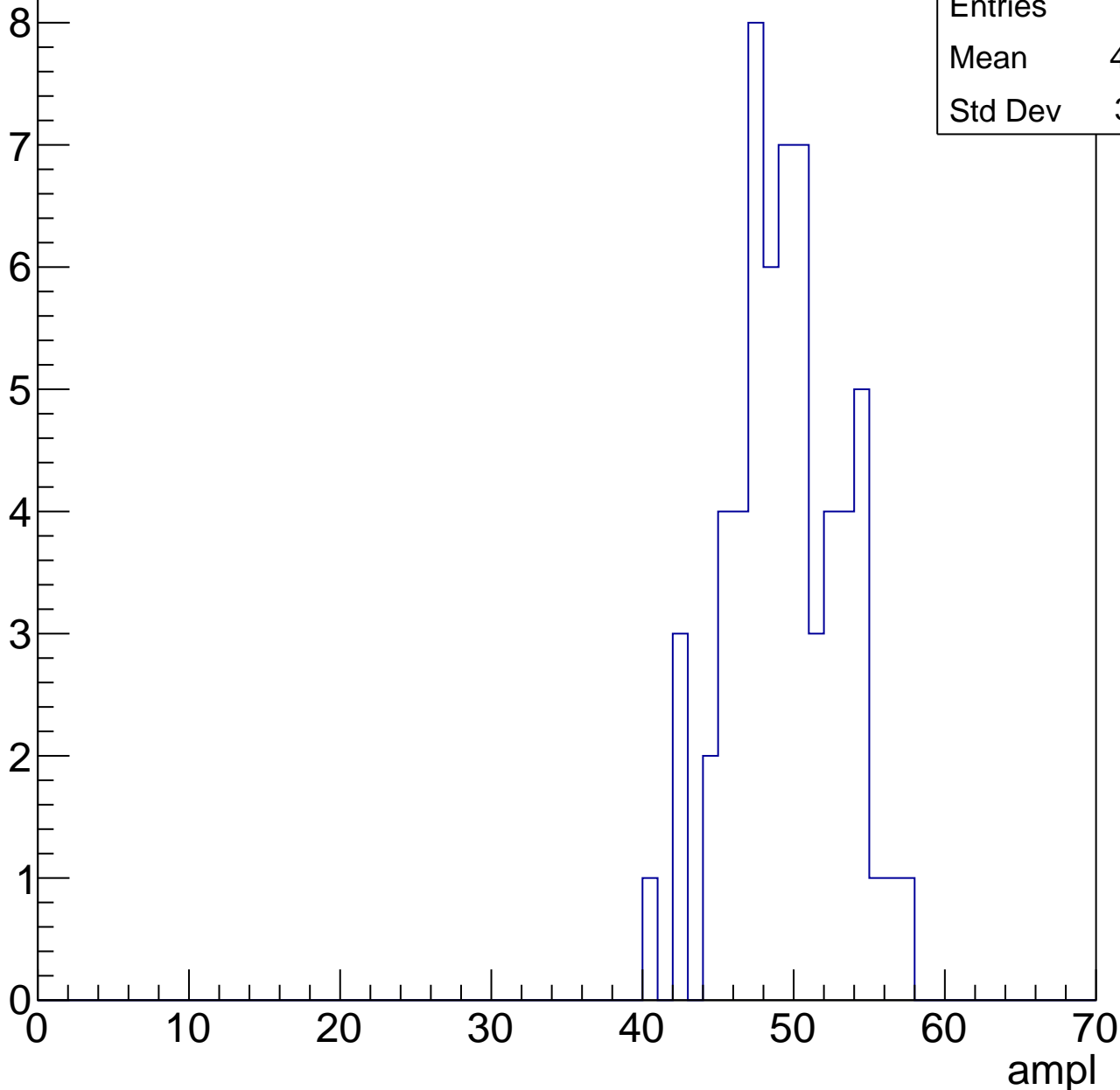


B1L102S, U19-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.95
Std Dev	3.641

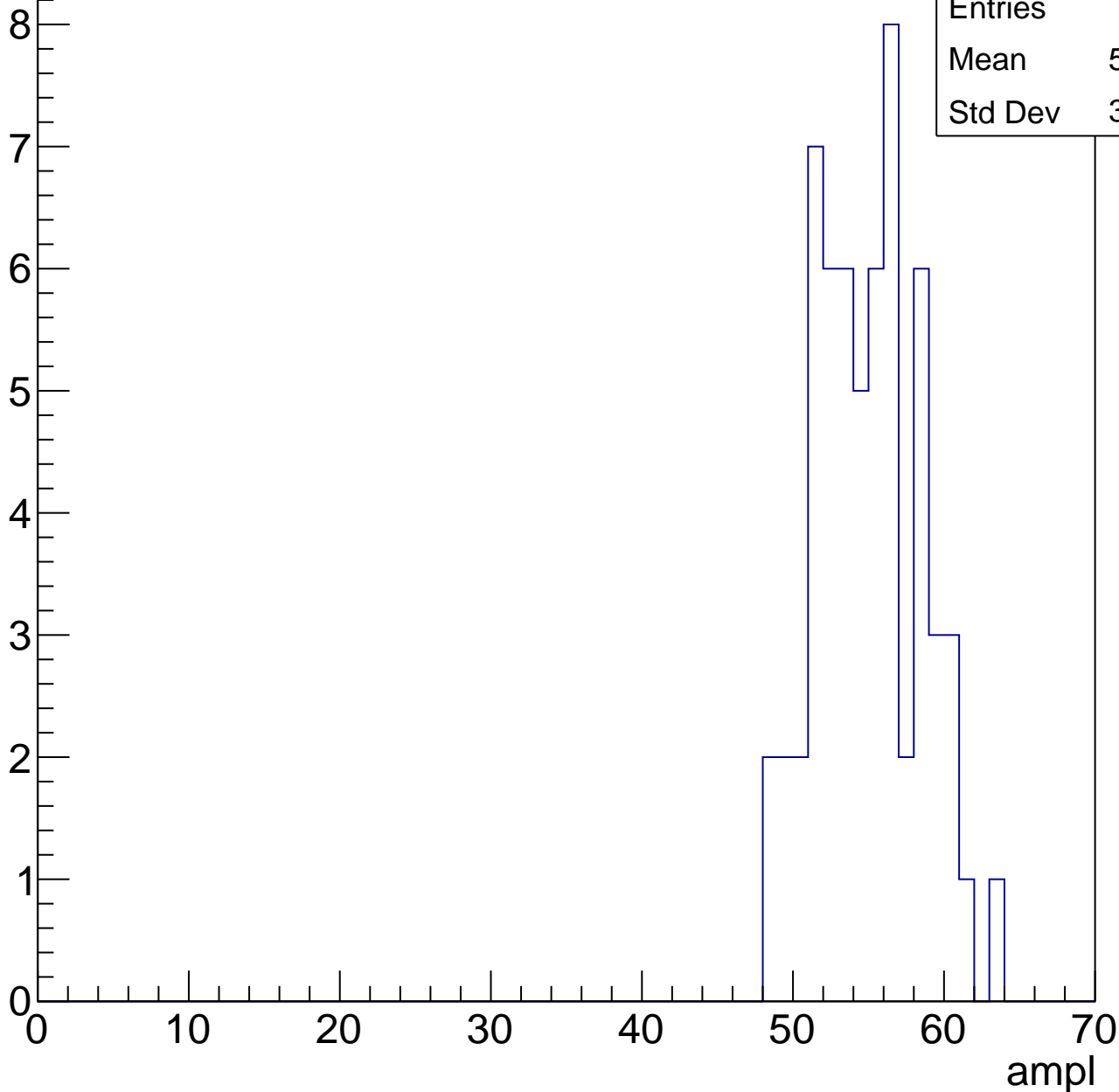


B1L102S, U19-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.53
Std Dev	3.403

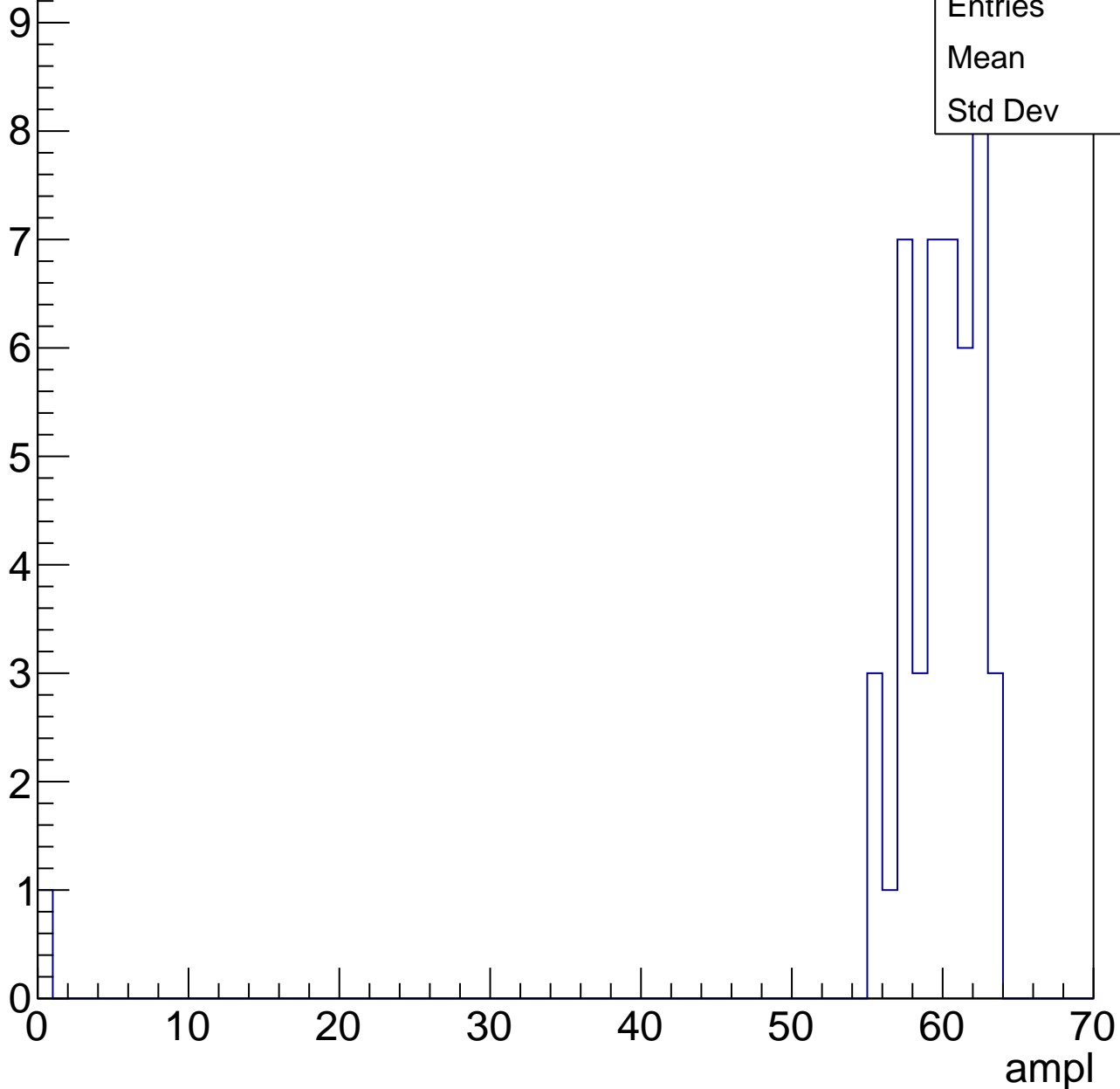


B1L102S, U19-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.3
Std Dev	8.88

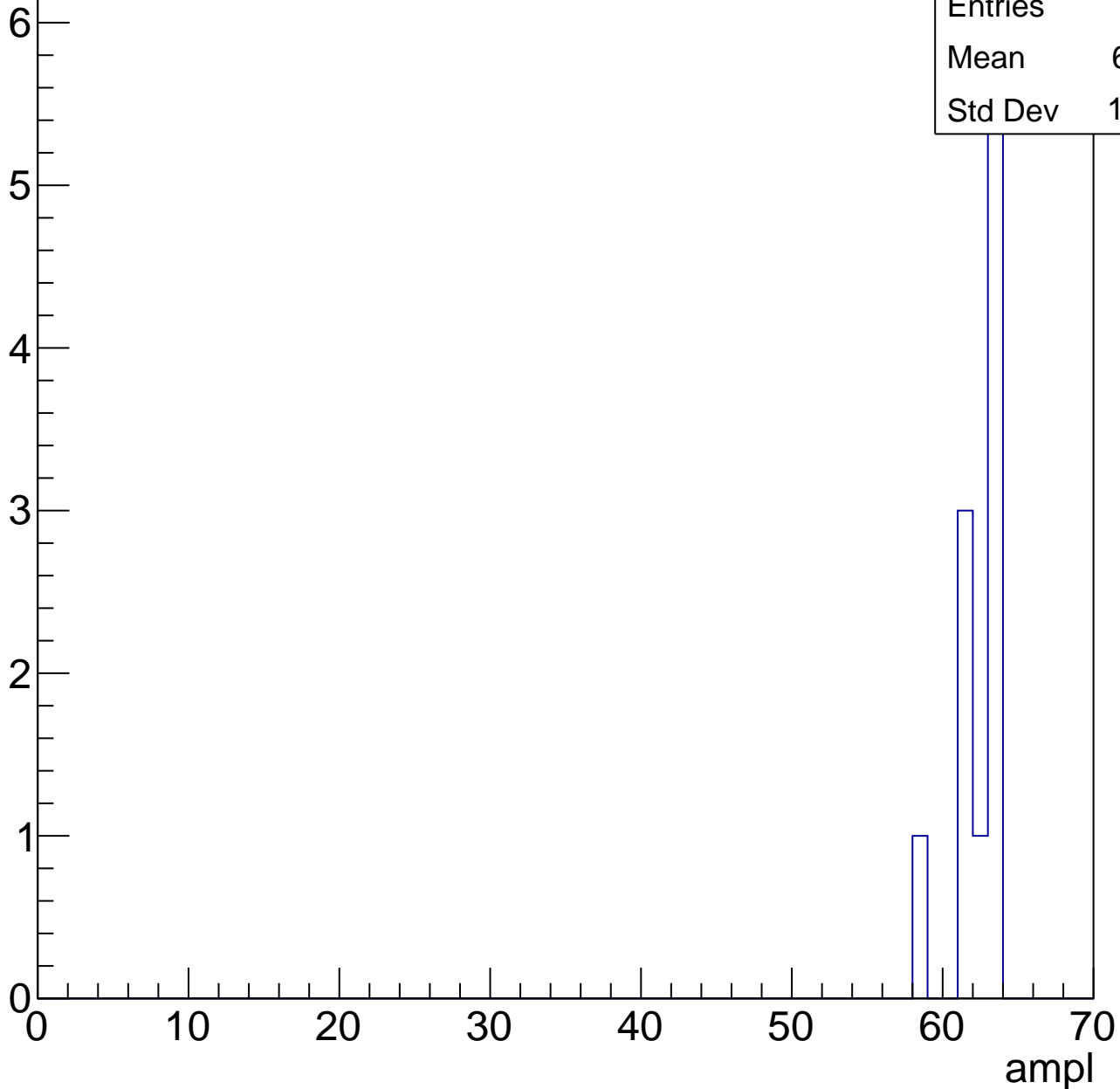


B1L102S, U19-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.91
Std Dev	1.505



B1L102S, U19-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch3, adc0

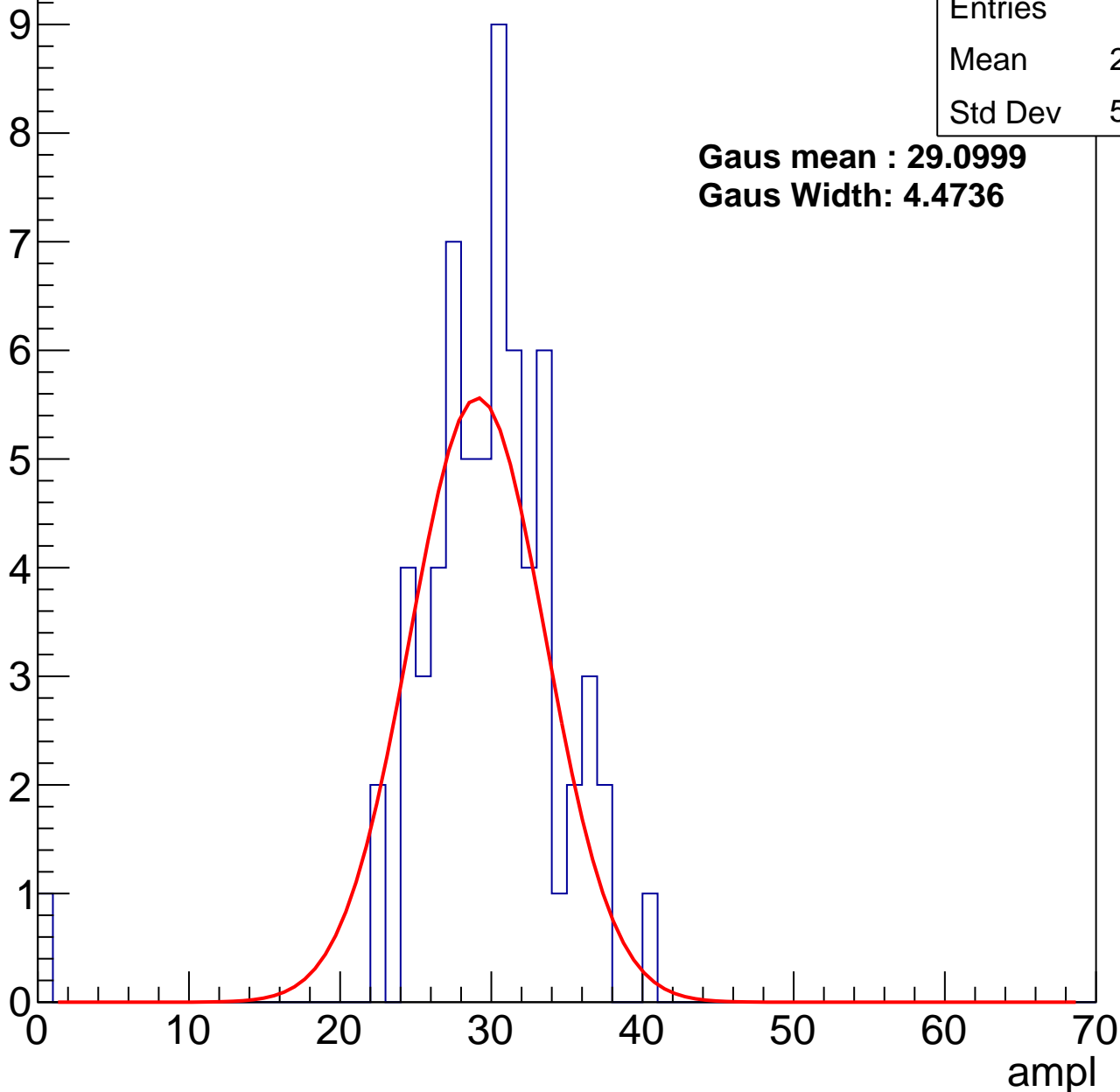
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.25
Std Dev	5.277

Gaus mean : 29.0999

Gaus Width: 4.4736



B1L102S, U19-ch3, adc1

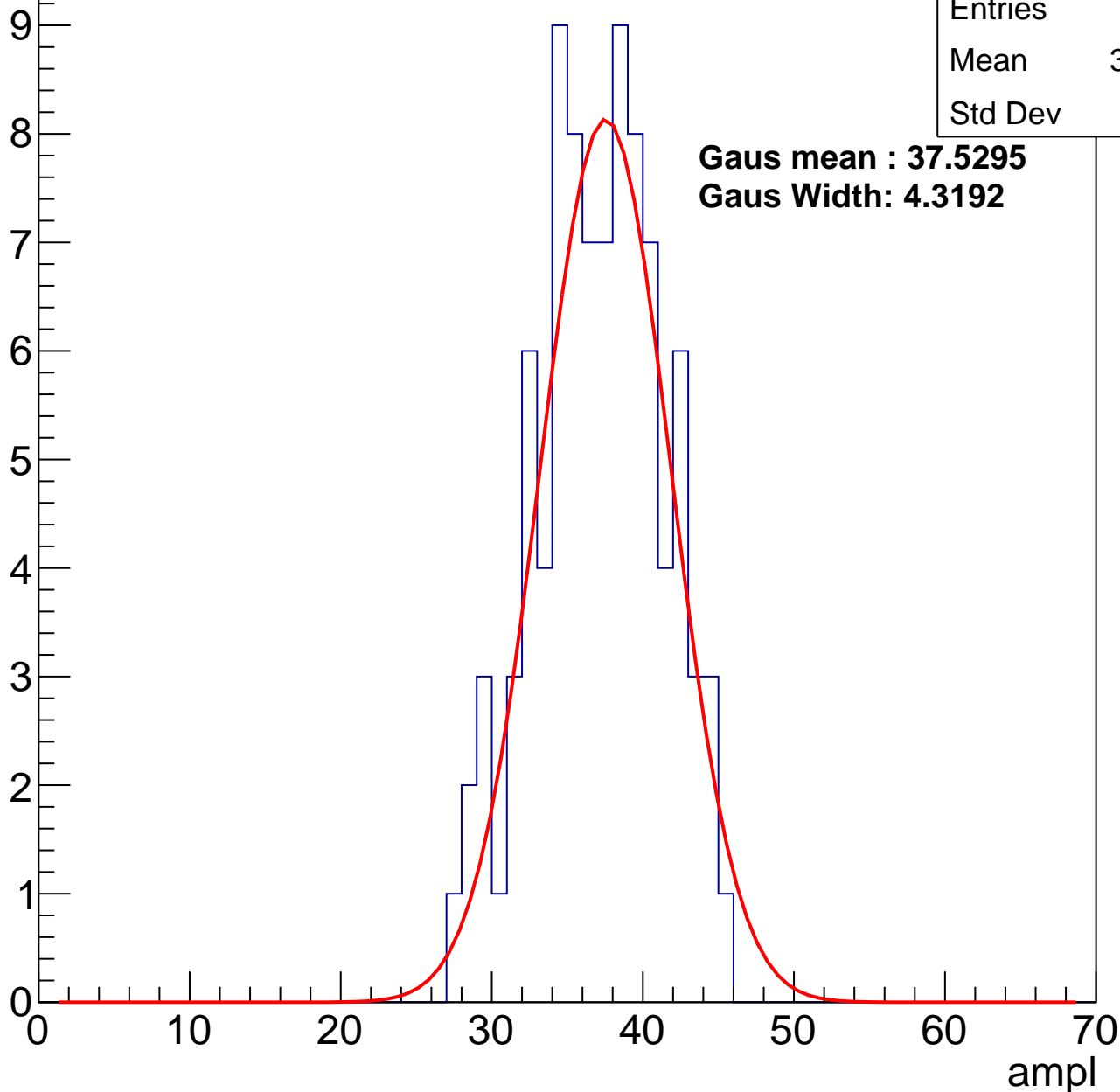
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	92
Mean	36.63
Std Dev	4.13

Gaus mean : 37.5295

Gaus Width: 4.3192



B1L102S, U19-ch3, adc2

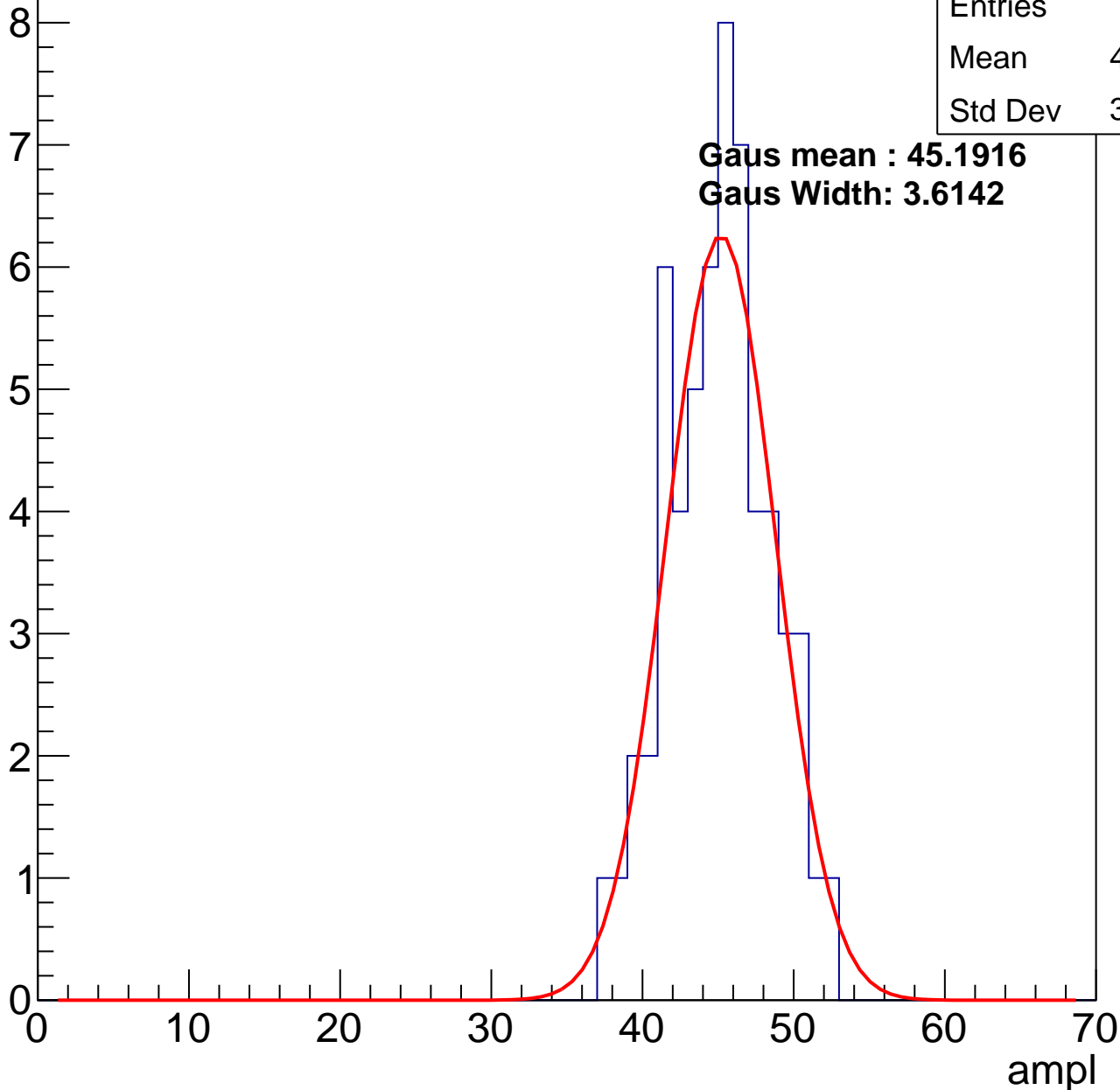
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	44.62
Std Dev	3.352

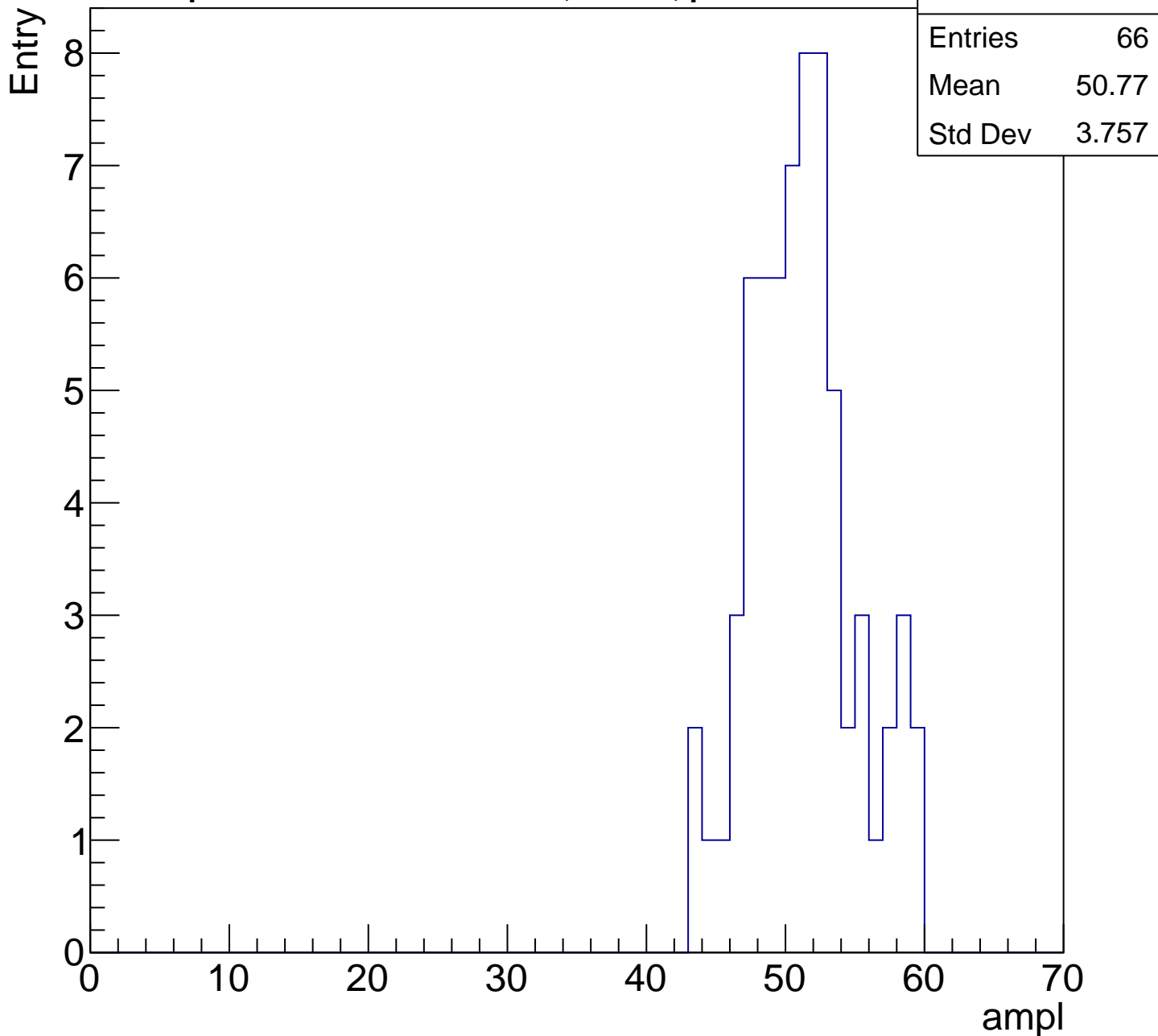
Gaus mean : 45.1916

Gaus Width: 3.6142



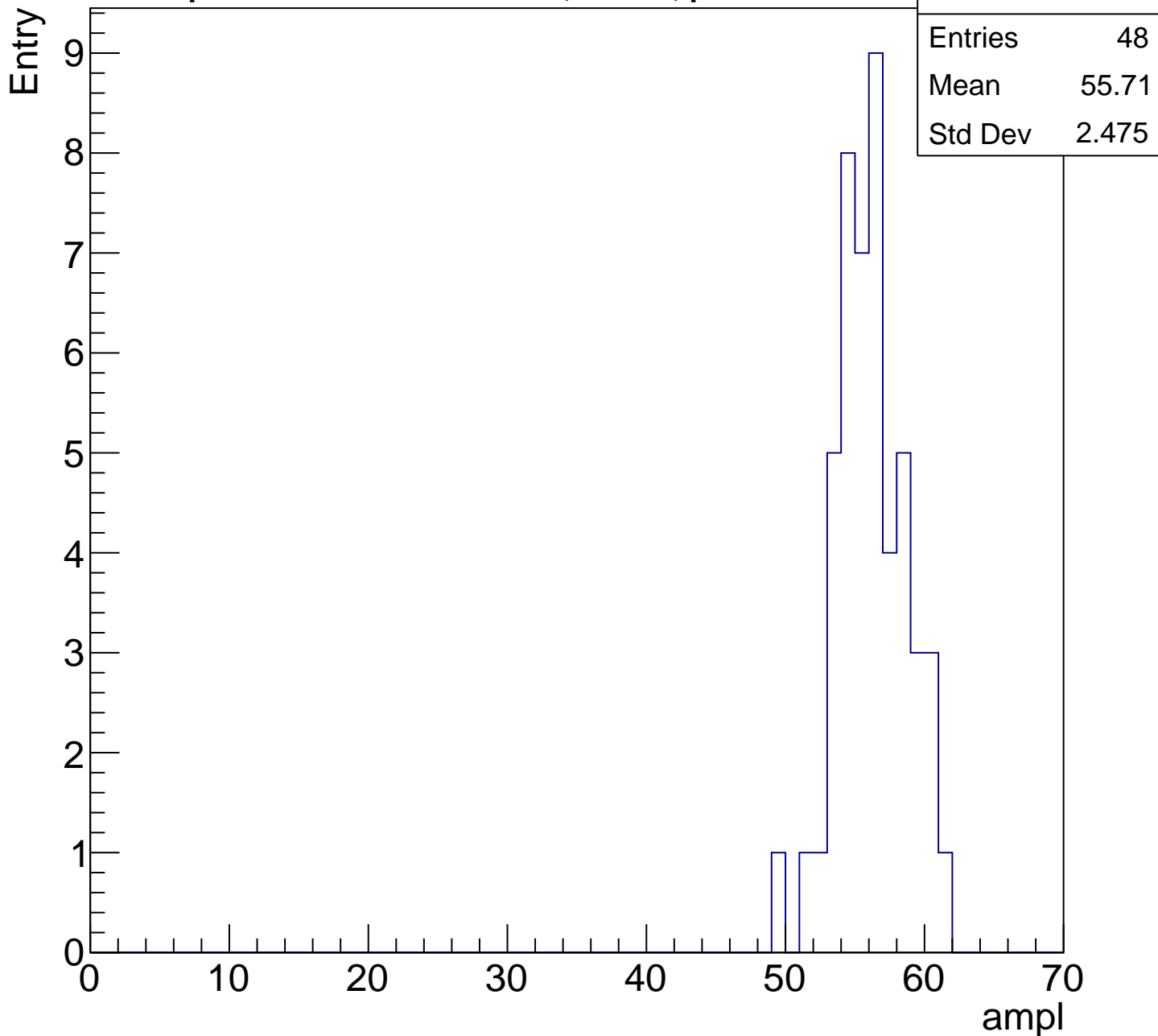
B1L102S, U19-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U19-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

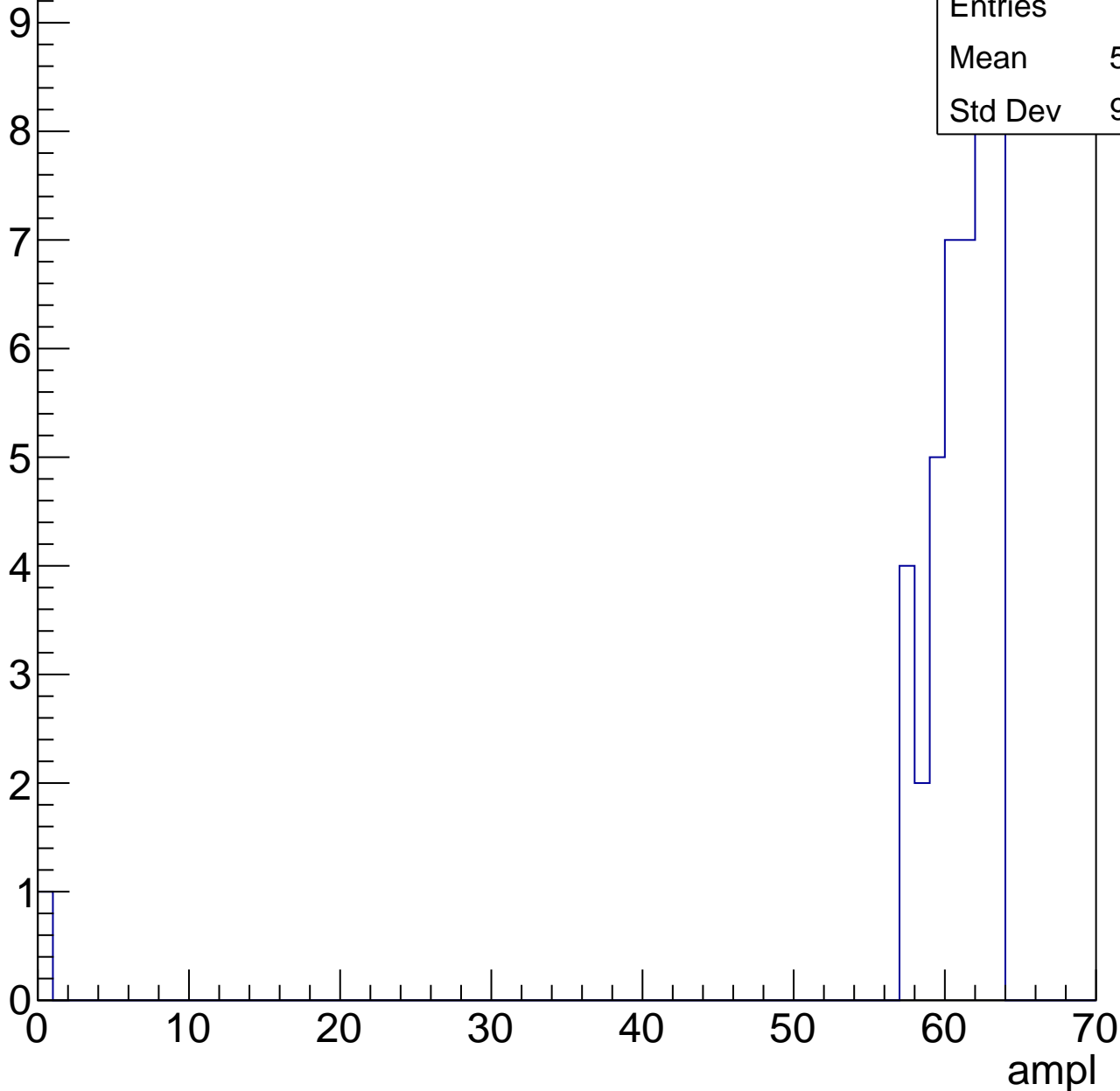


B1L102S, U19-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.34
Std Dev	9.237



B1L102S, U19-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L102S, U19-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch4, adc0

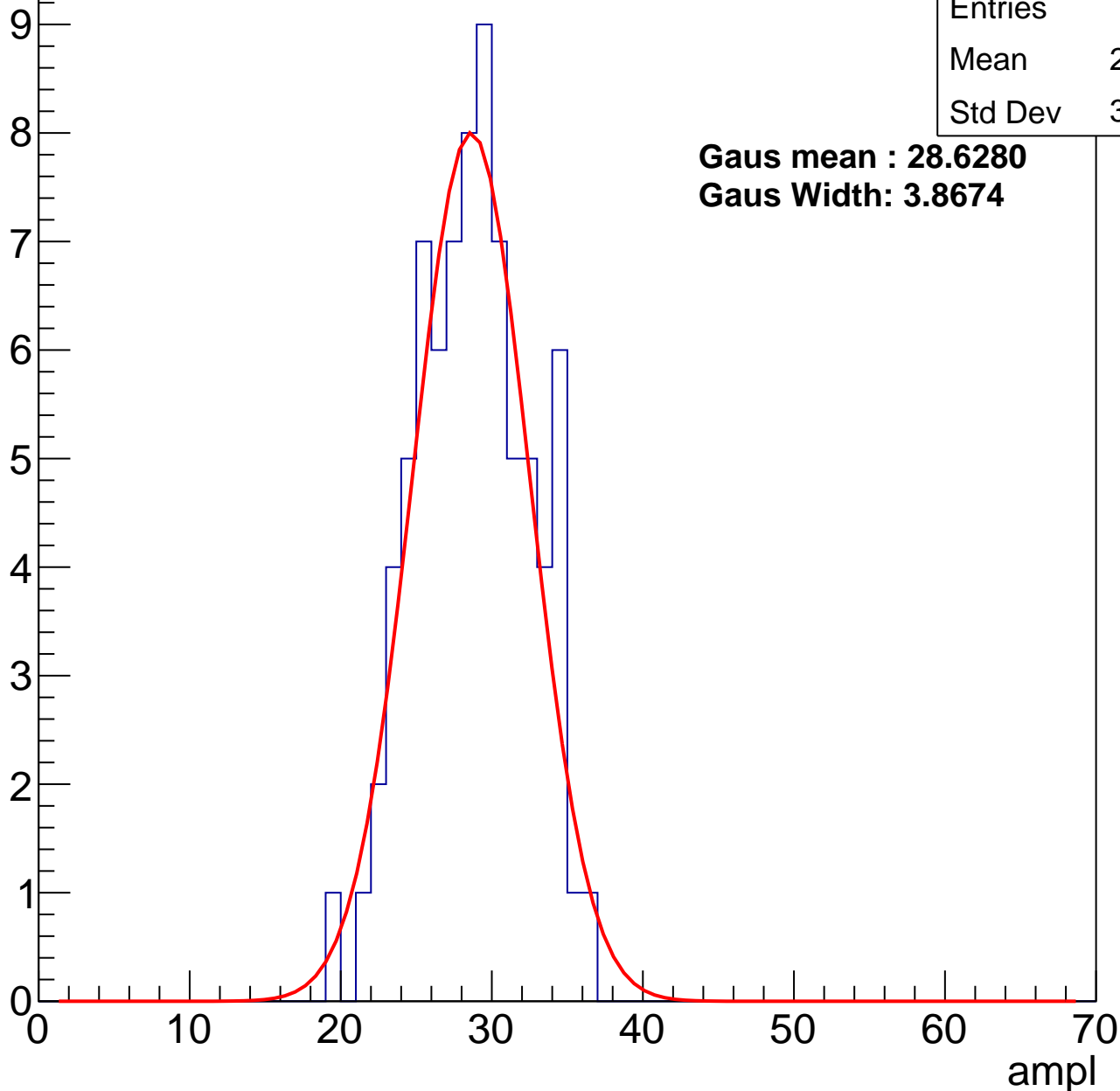
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.27
Std Dev	3.665

Gaus mean : 28.6280

Gaus Width: 3.8674



B1L102S, U19-ch4, adc1

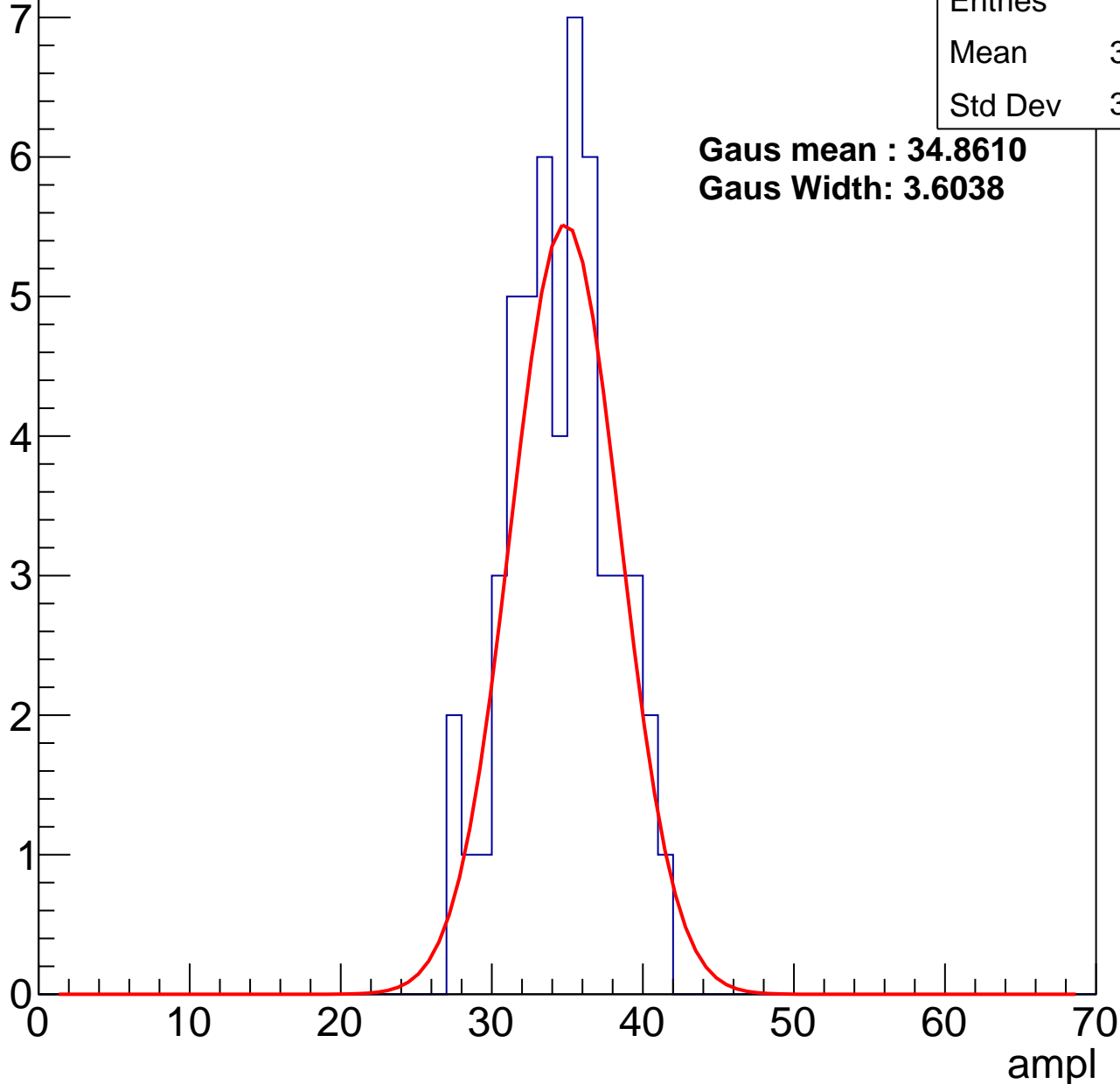
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.12
Std Dev	3.338

Gaus mean : 34.8610

Gaus Width: 3.6038



B1L102S, U19-ch4, adc2

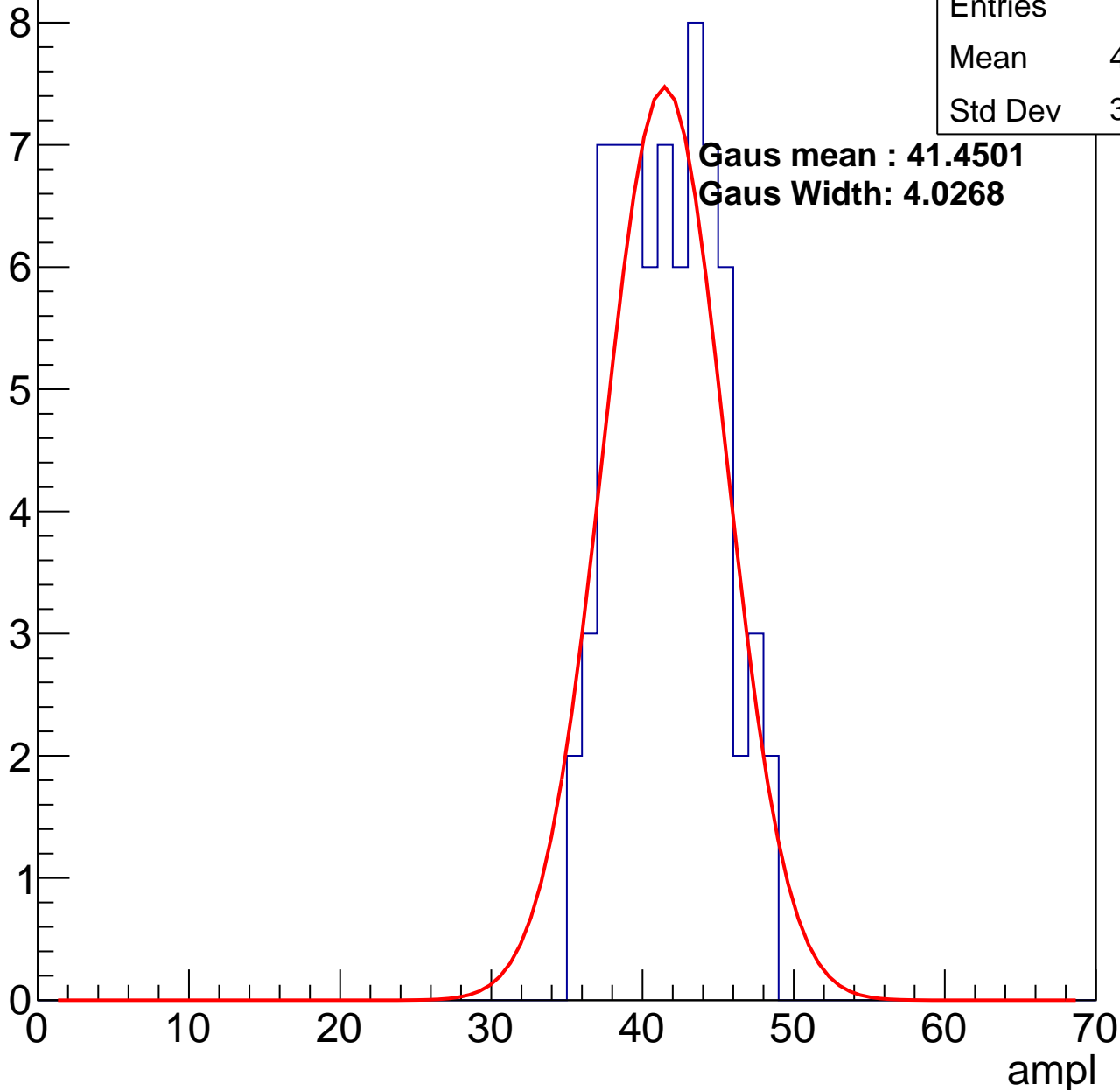
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.18
Std Dev	3.324

Gaus mean : 41.4501

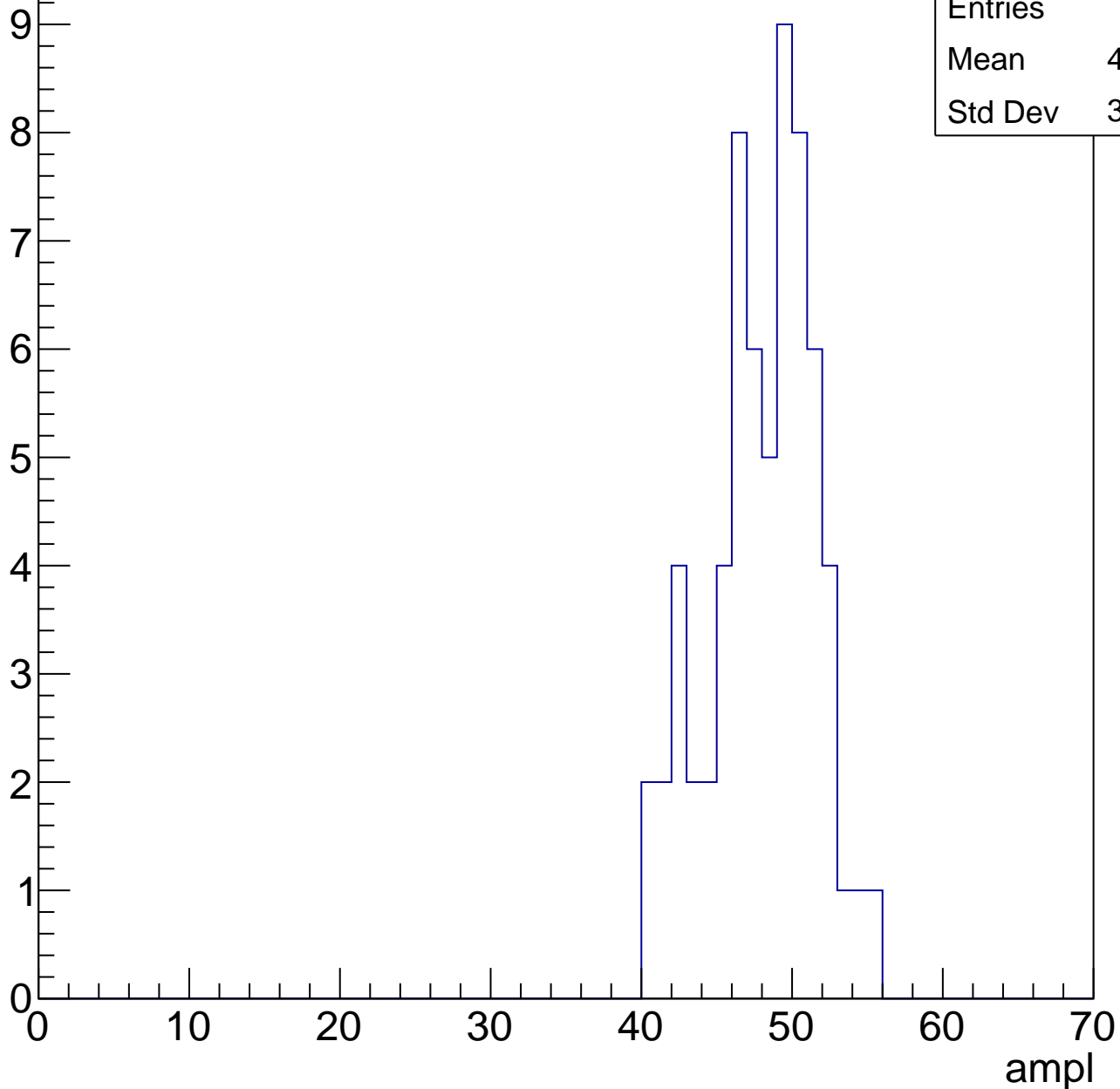
Gaus Width: 4.0268



B1L102S, U19-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

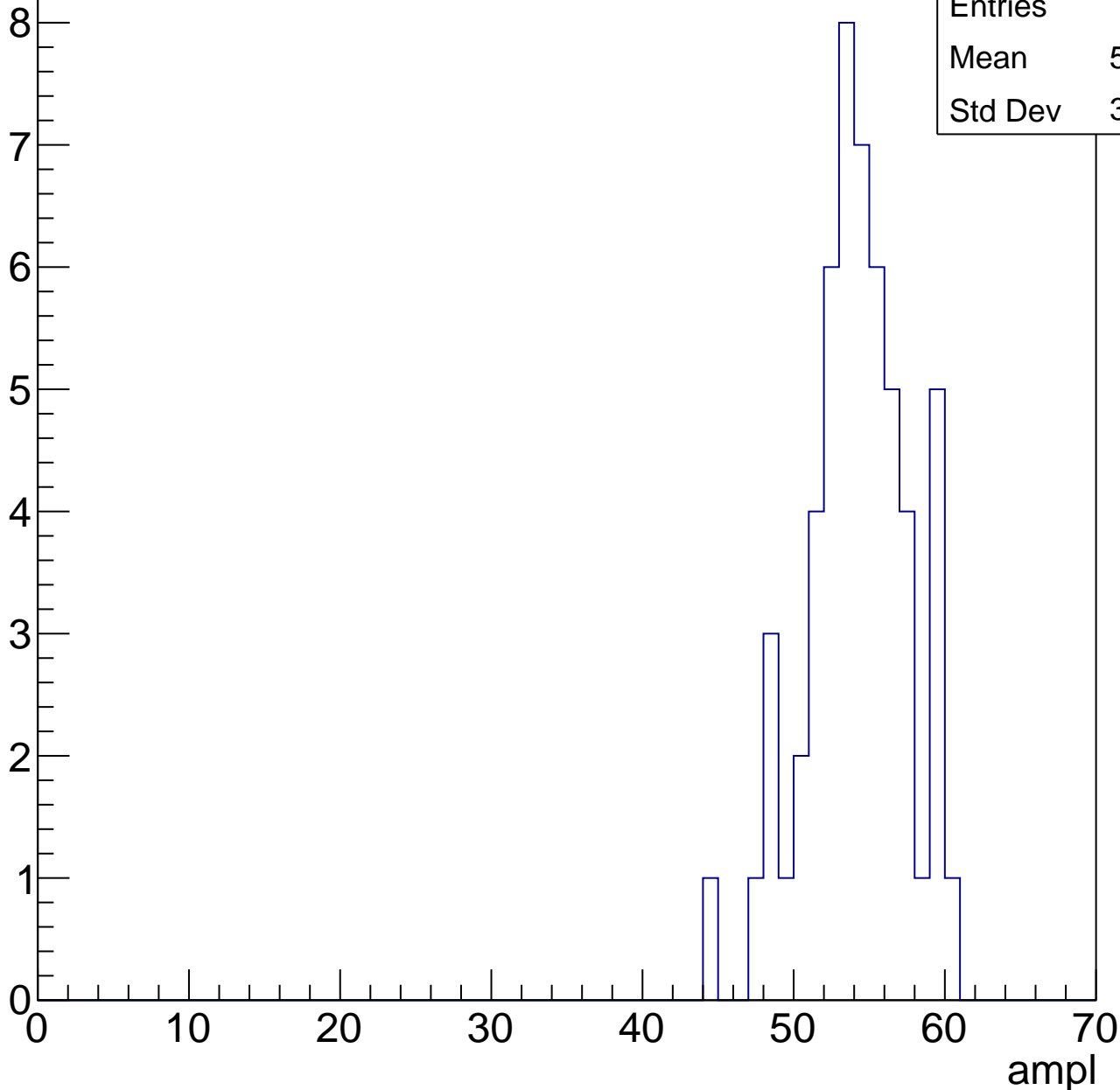


B1L102S, U19-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

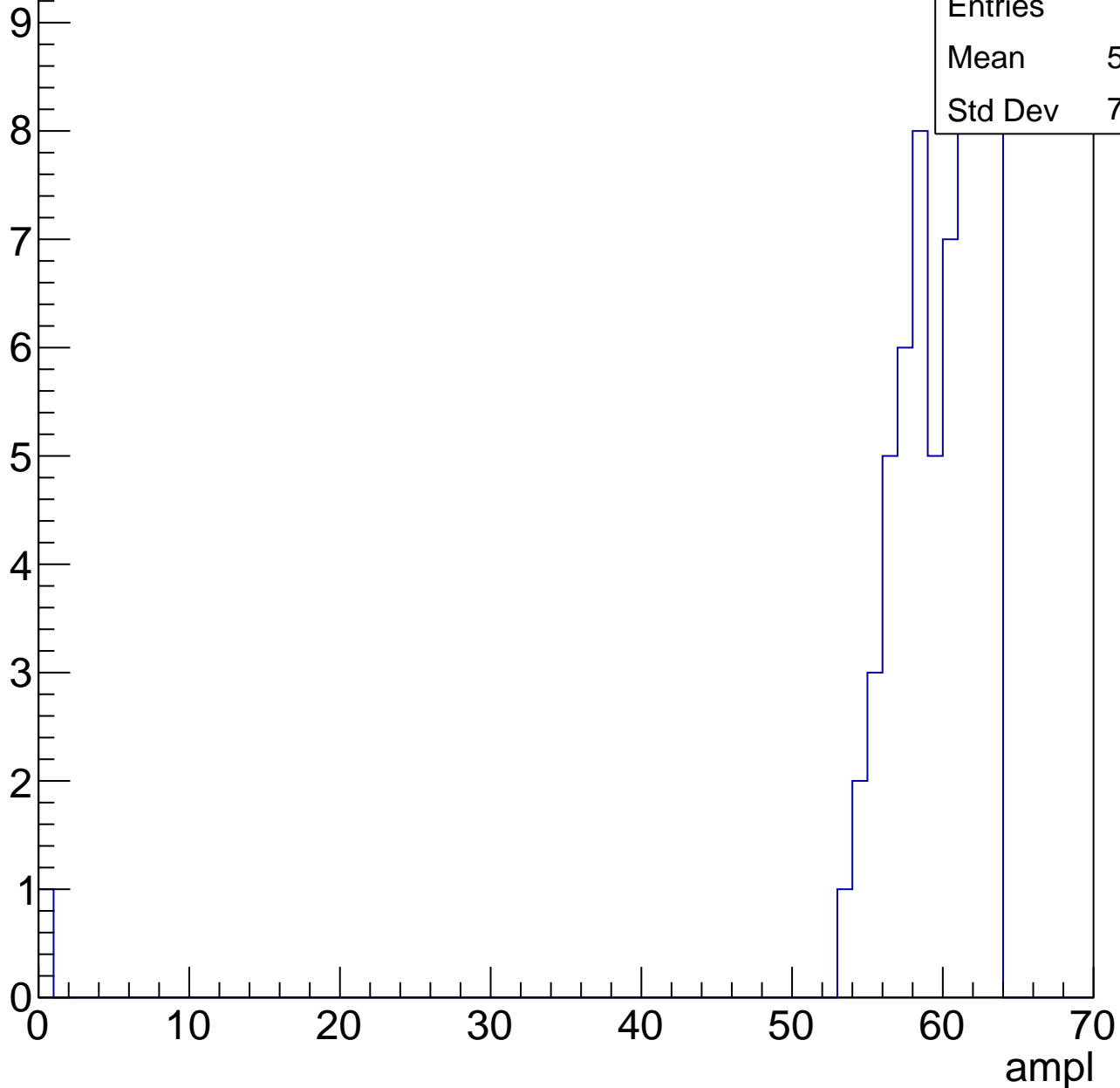
Entries	55
Mean	53.69
Std Dev	3.346



B1L102S, U19-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

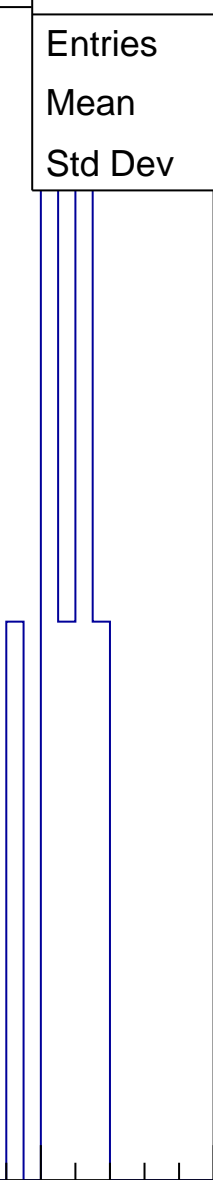
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.86
Std Dev	1.552

0 10 20 30 40 50 60 70

ampl



B1L102S, U19-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch5, adc0

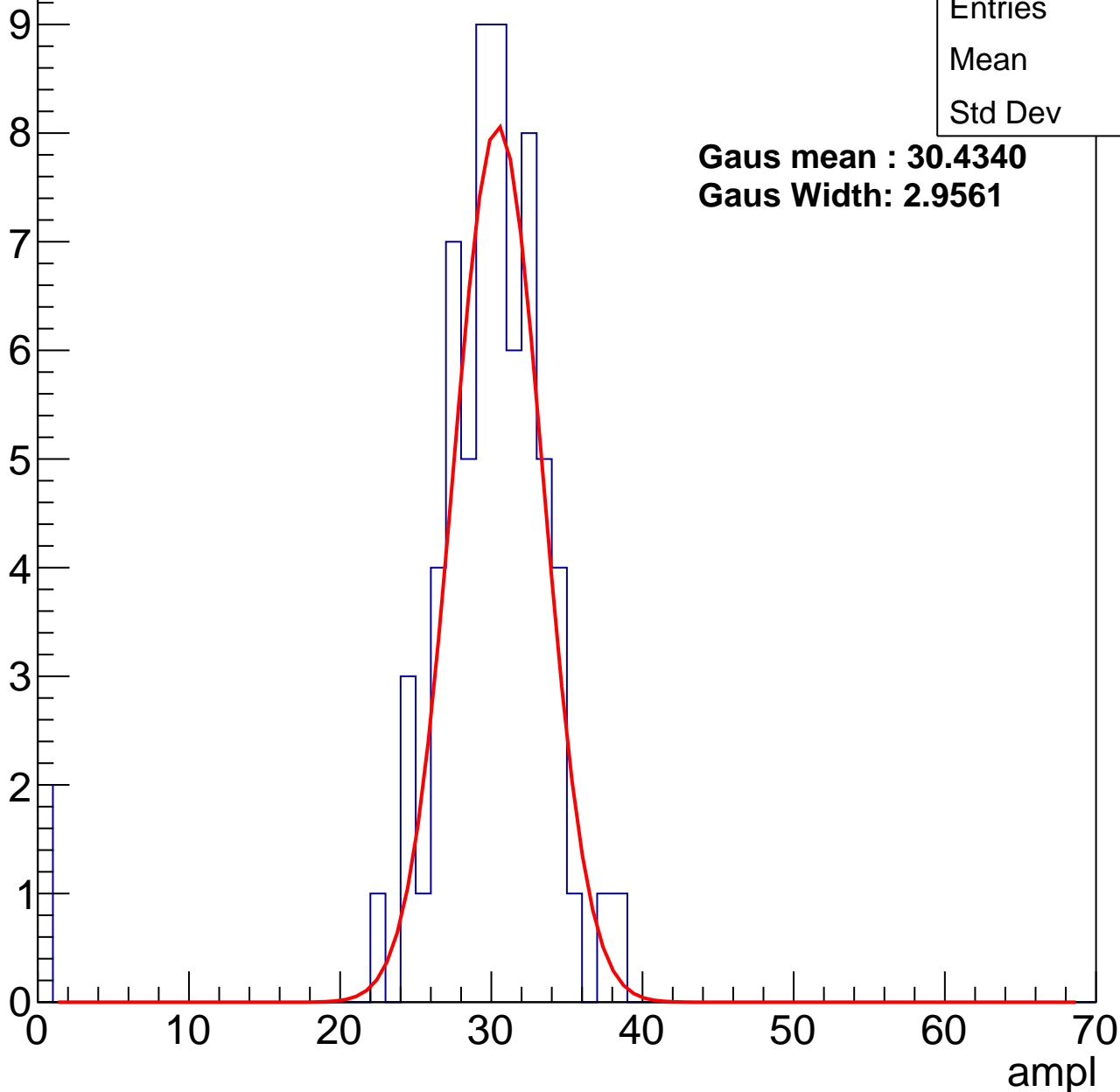
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.9
Std Dev	5.93

Gaus mean : 30.4340

Gaus Width: 2.9561



B1L102S, U19-ch5, adc1

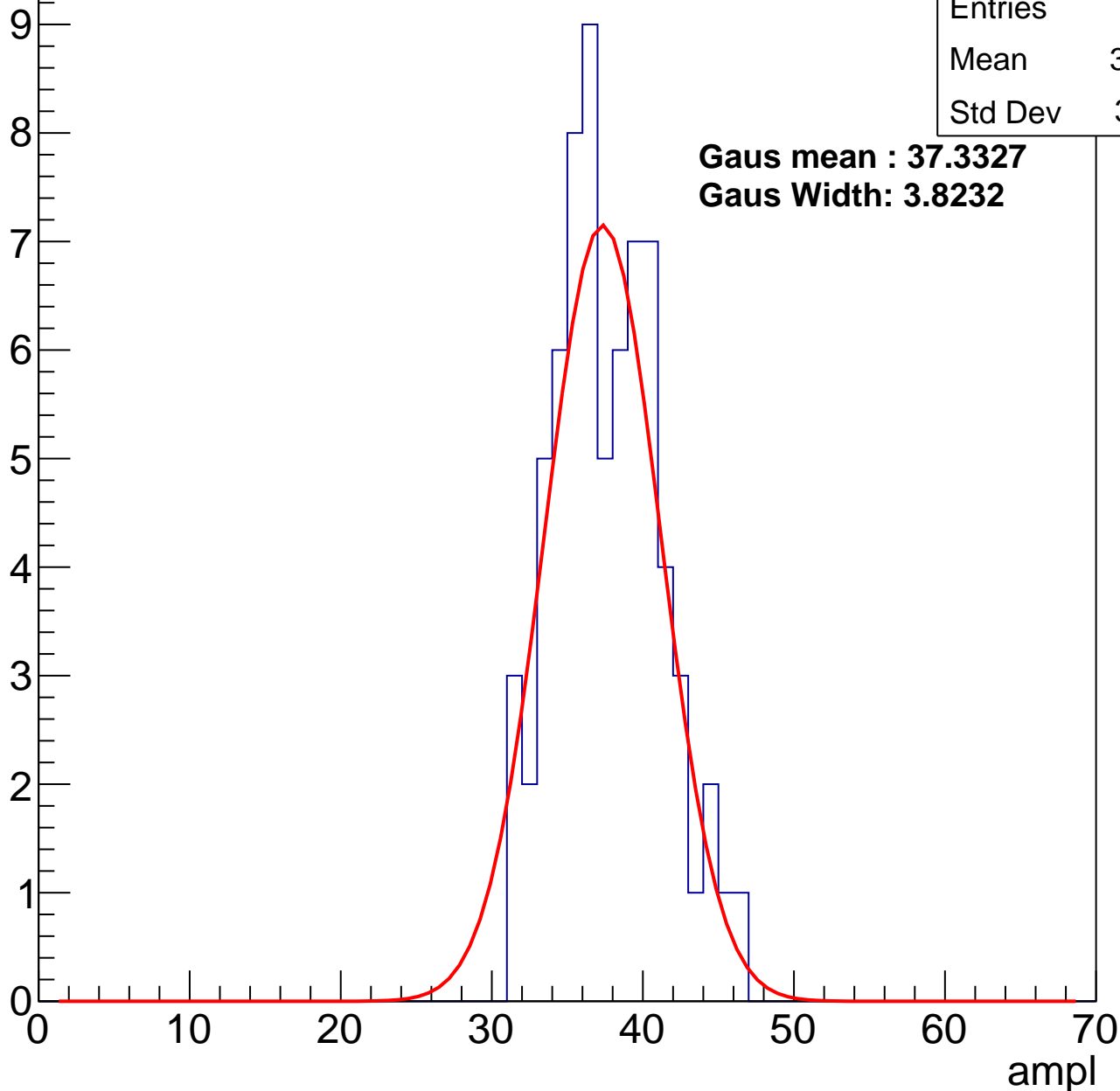
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	37.26
Std Dev	3.471

Gaus mean : 37.3327

Gaus Width: 3.8232



B1L102S, U19-ch5, adc2

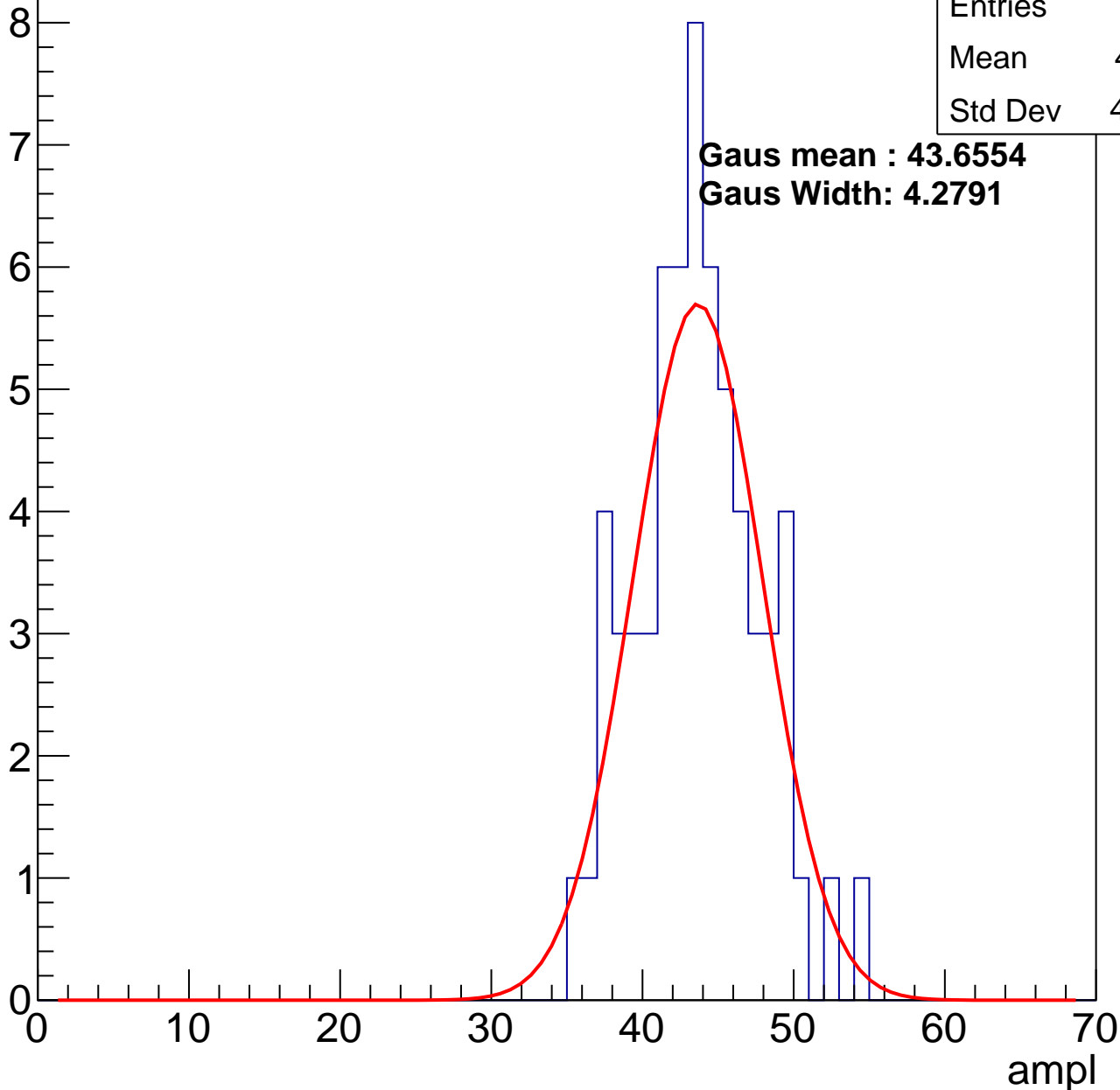
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.21
Std Dev	4.013

Gaus mean : 43.6554

Gaus Width: 4.2791

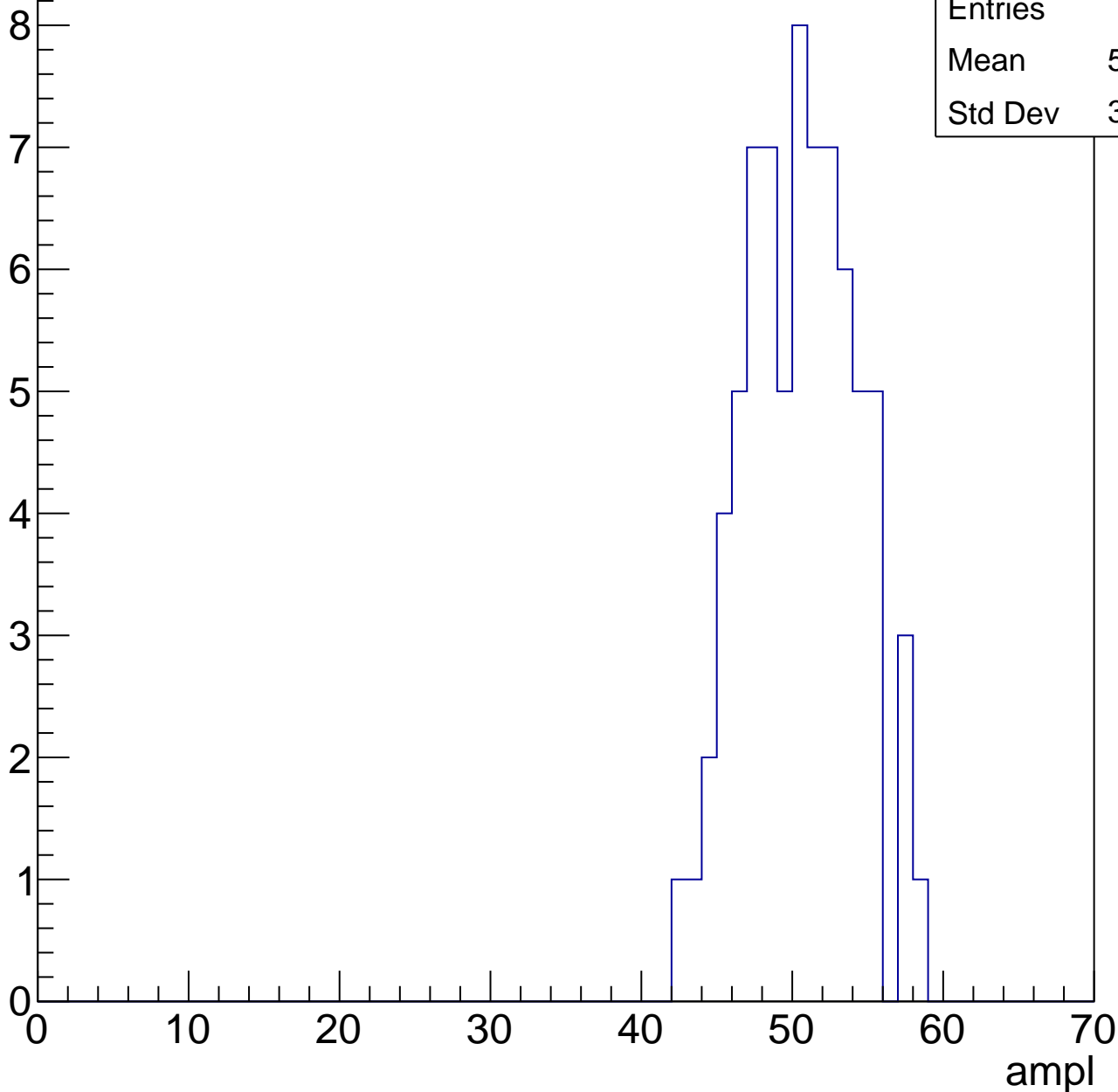


B1L102S, U19-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

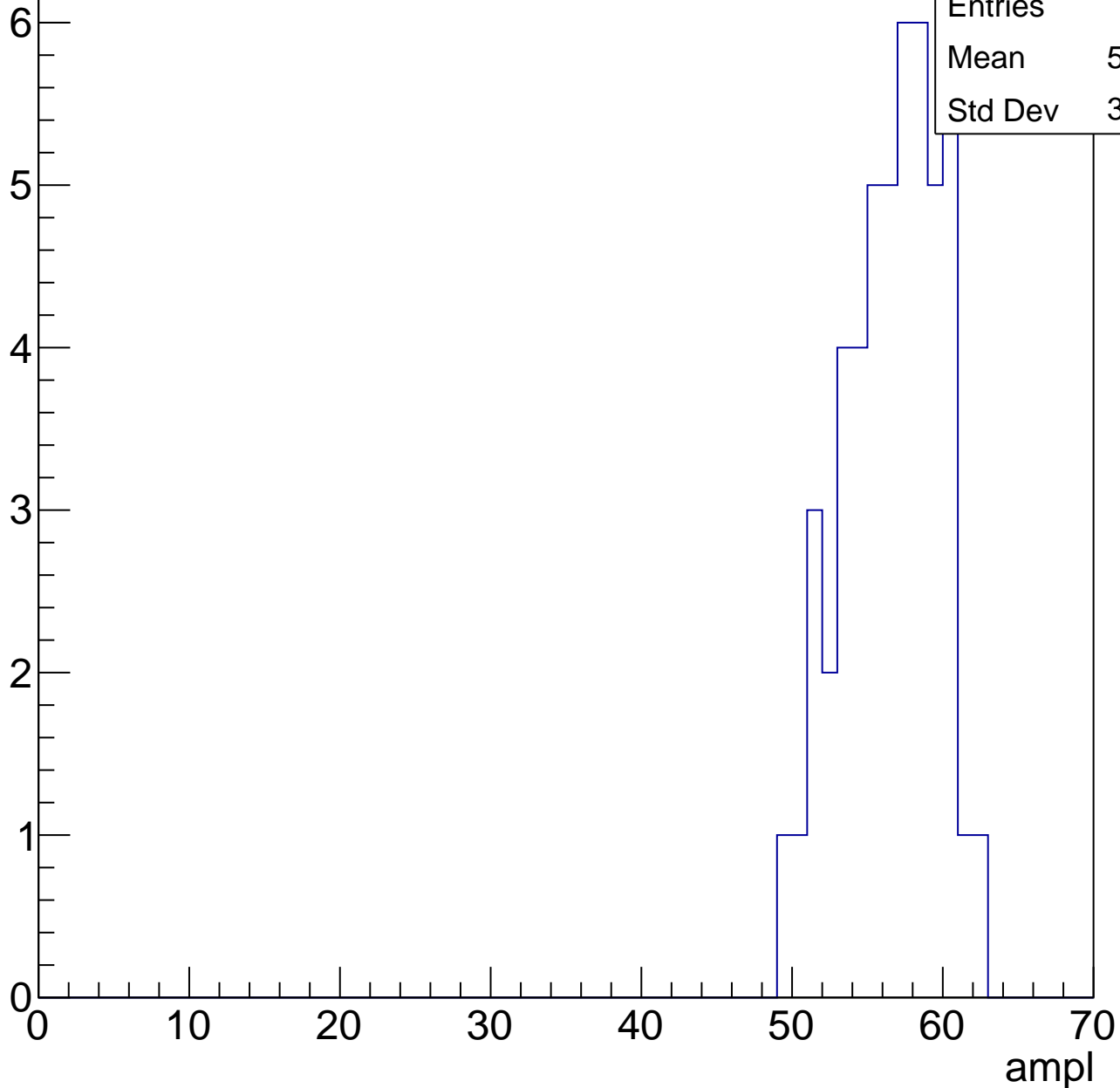
Entries	74
Mean	50.08
Std Dev	3.612



B1L102S, U19-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

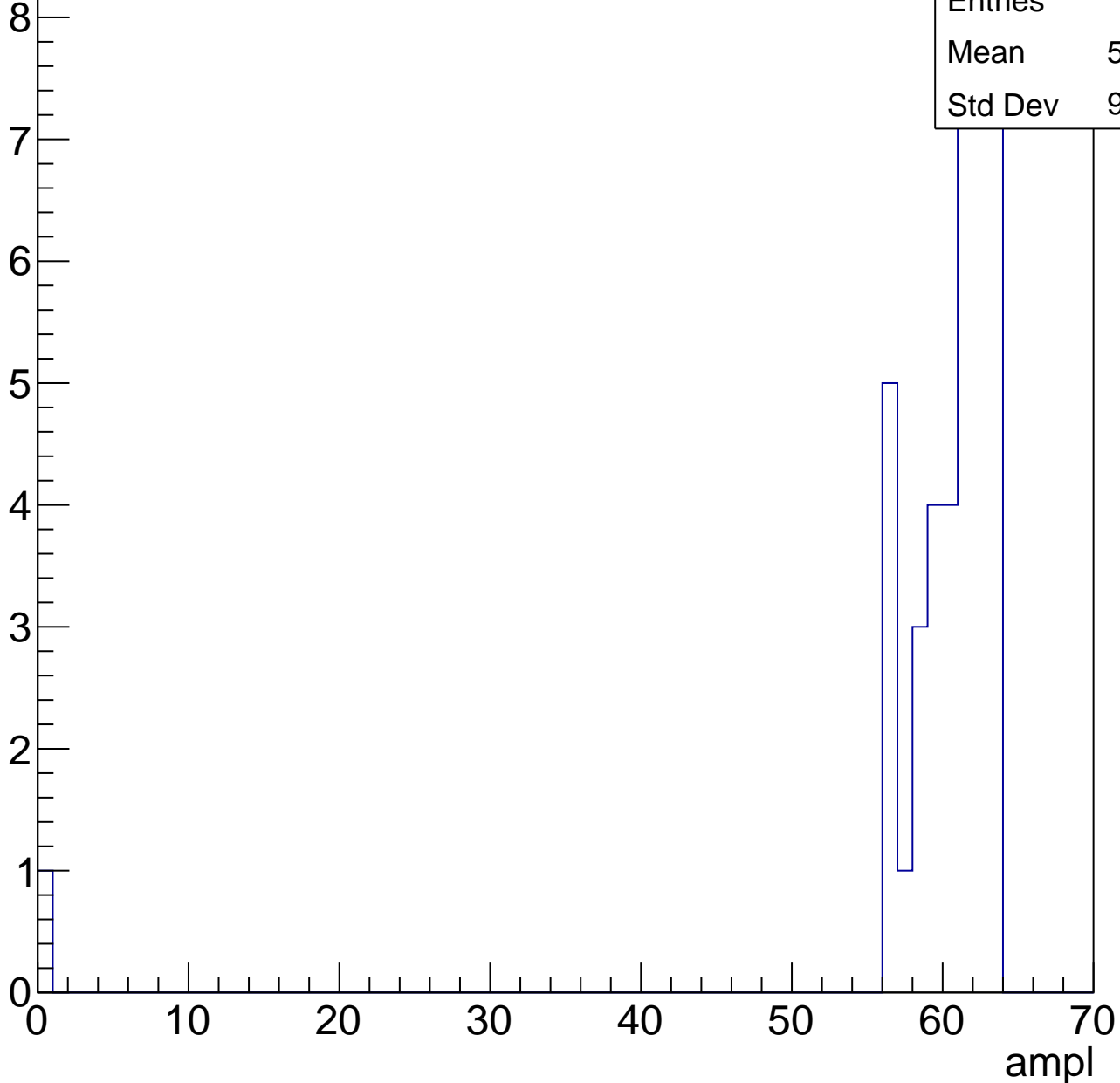


B1L102S, U19-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

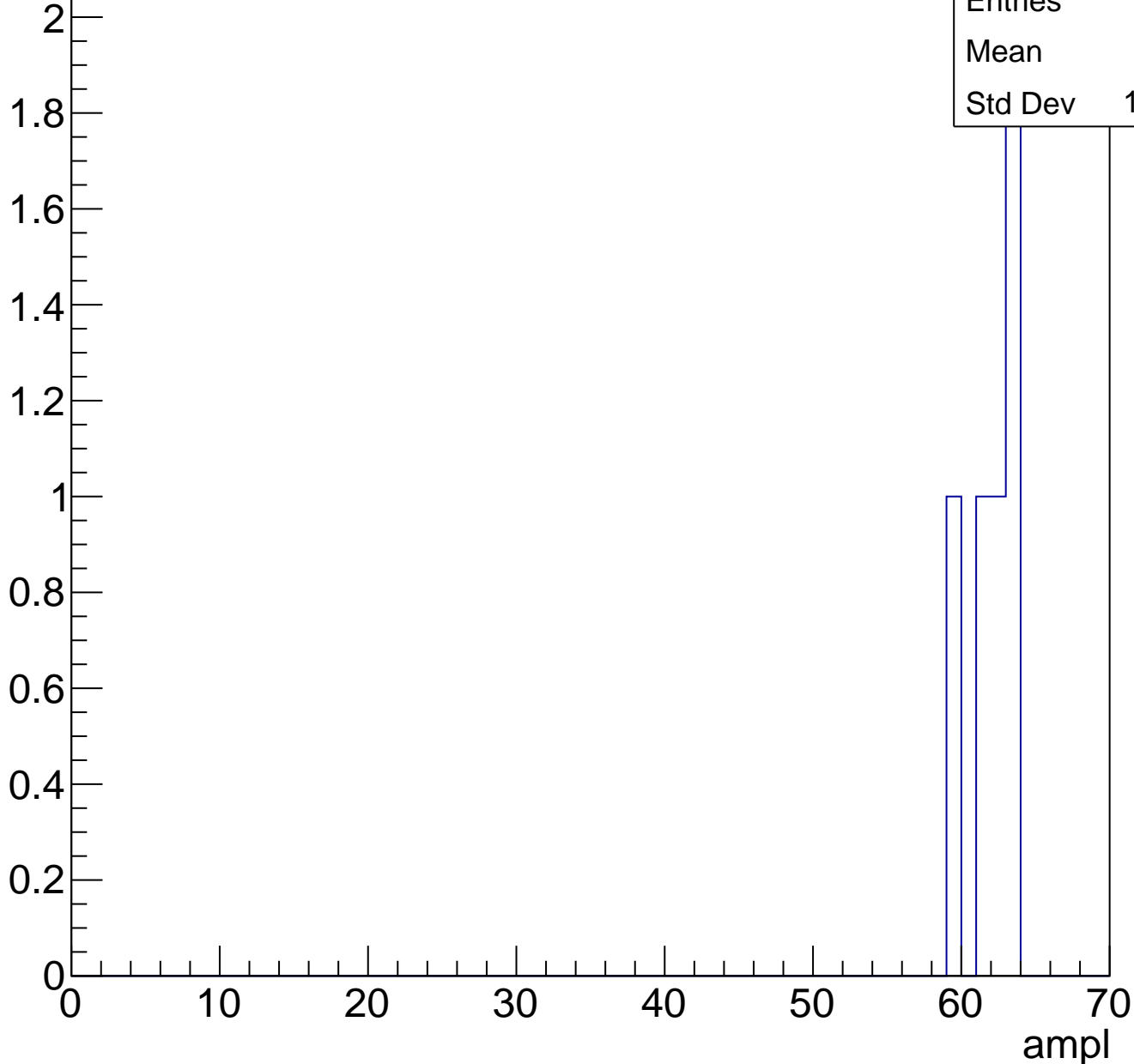
Entries	42
Mean	58.93
Std Dev	9.473



B1L102S, U19-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch6, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	29.24
Std Dev	4.966

Gaus mean : 30.0074

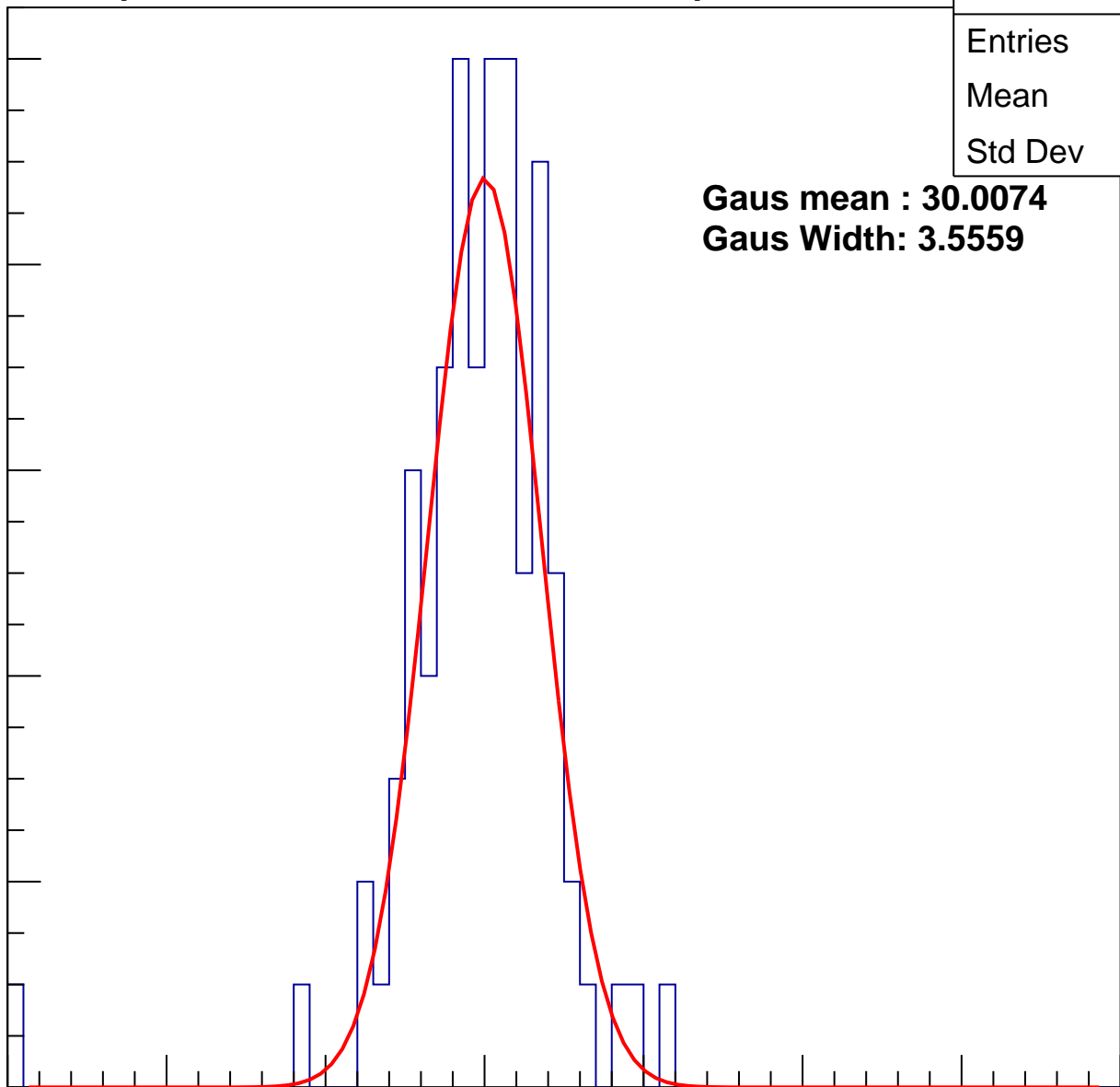
Gaus Width: 3.5559

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch6, adc1

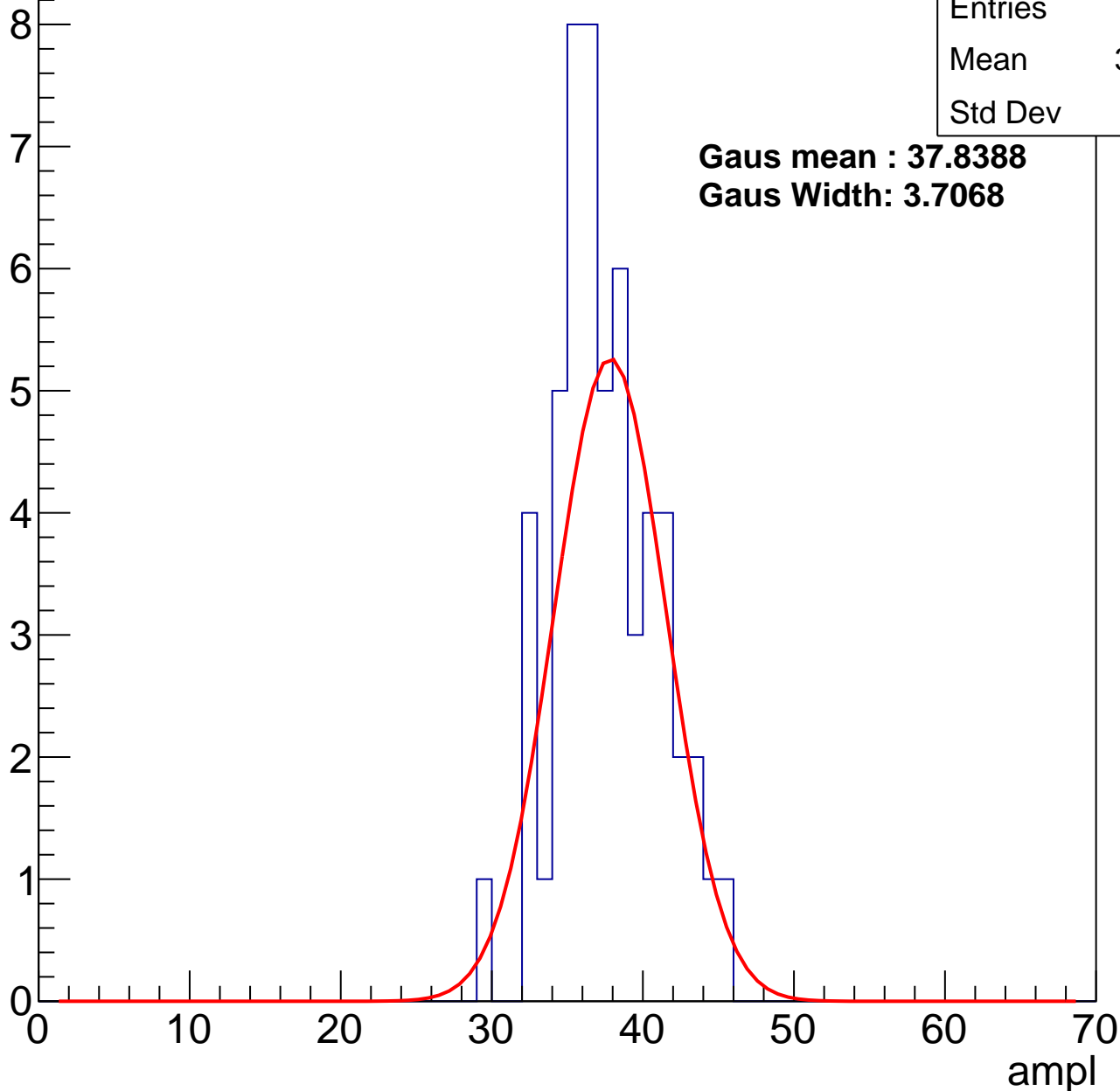
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.11
Std Dev	3.35

Gaus mean : 37.8388

Gaus Width: 3.7068

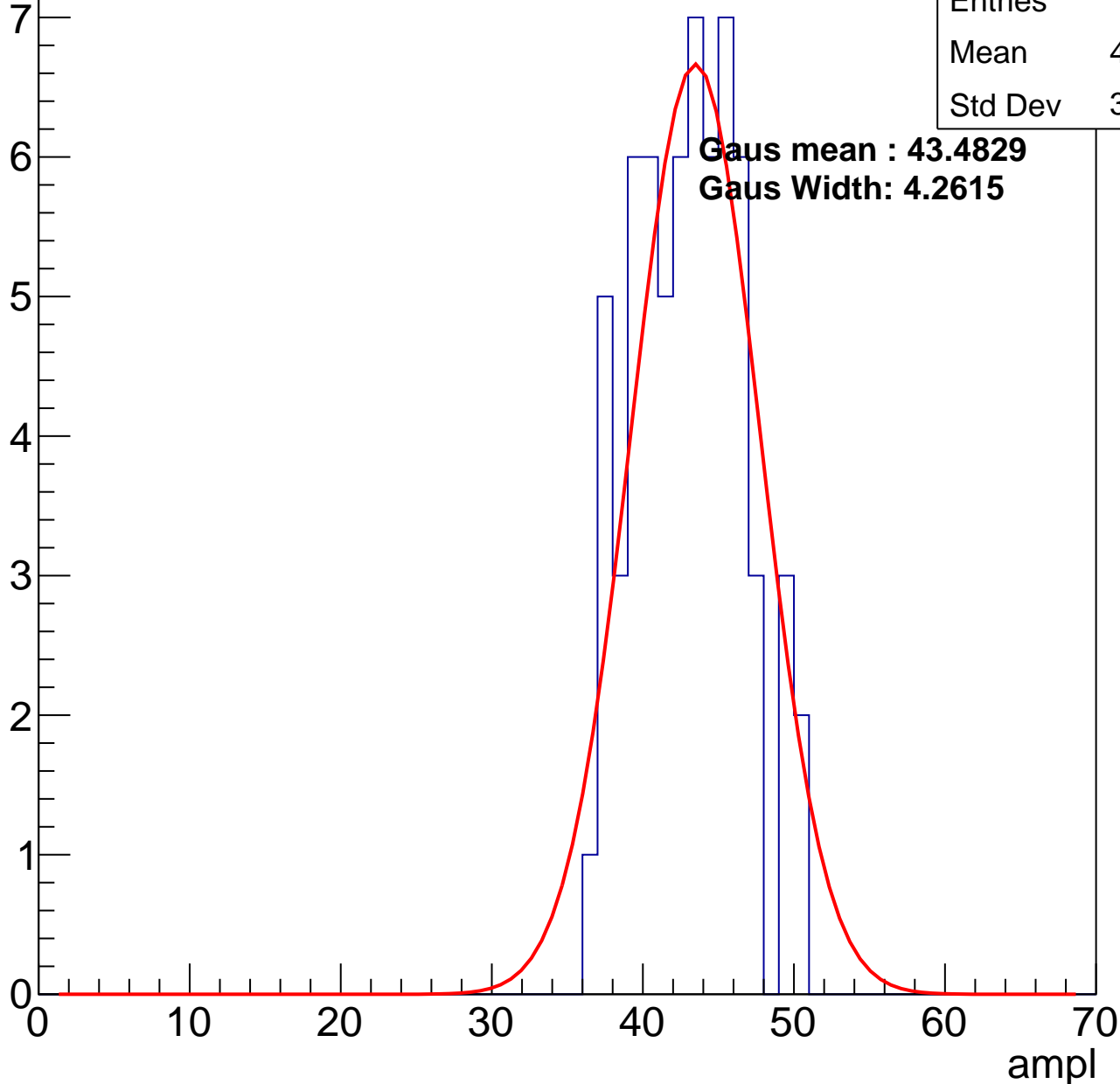


B1L102S, U19-ch6, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.58
Std Dev	3.504

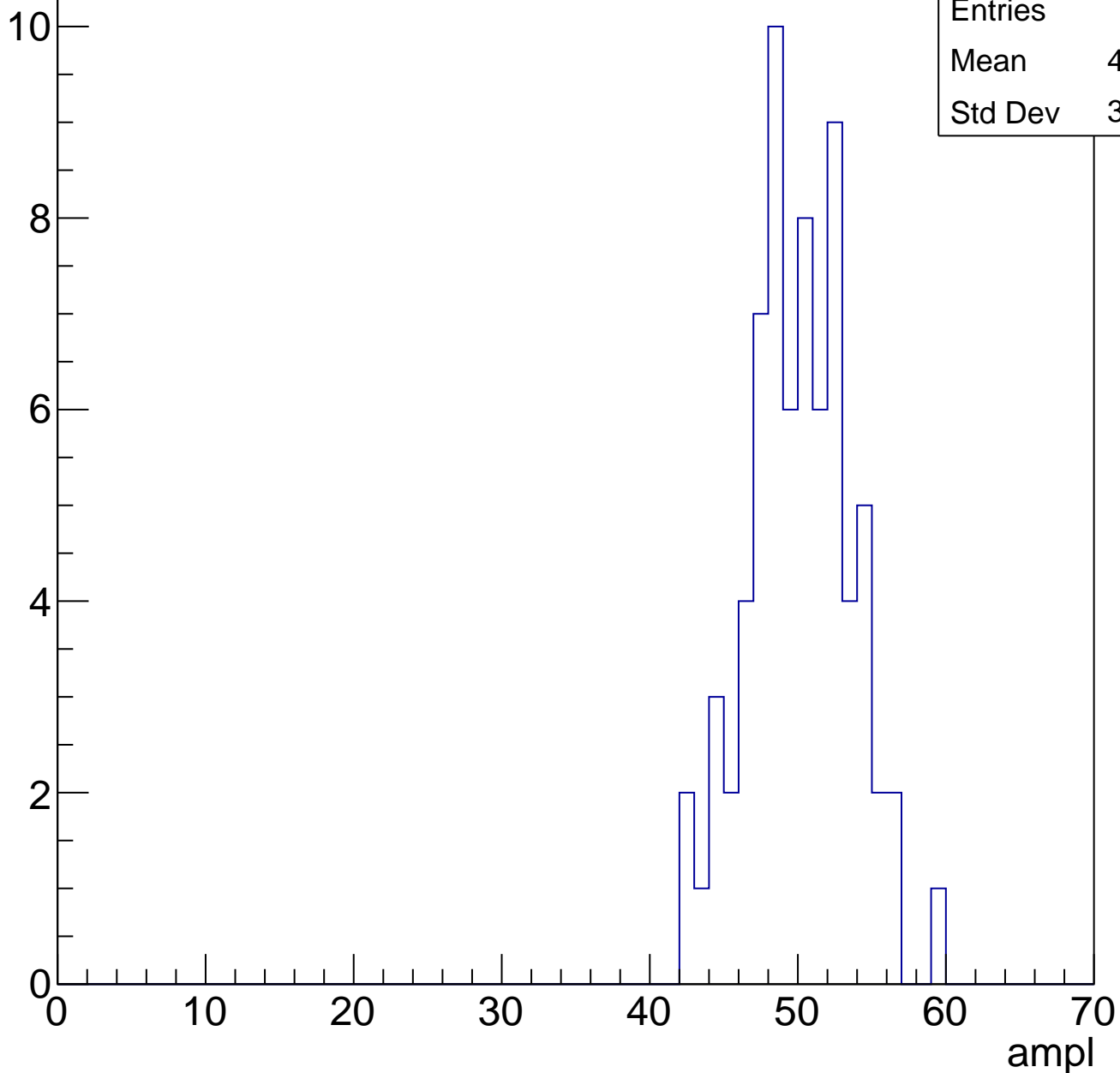


B1L102S, U19-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.62
Std Dev	3.454

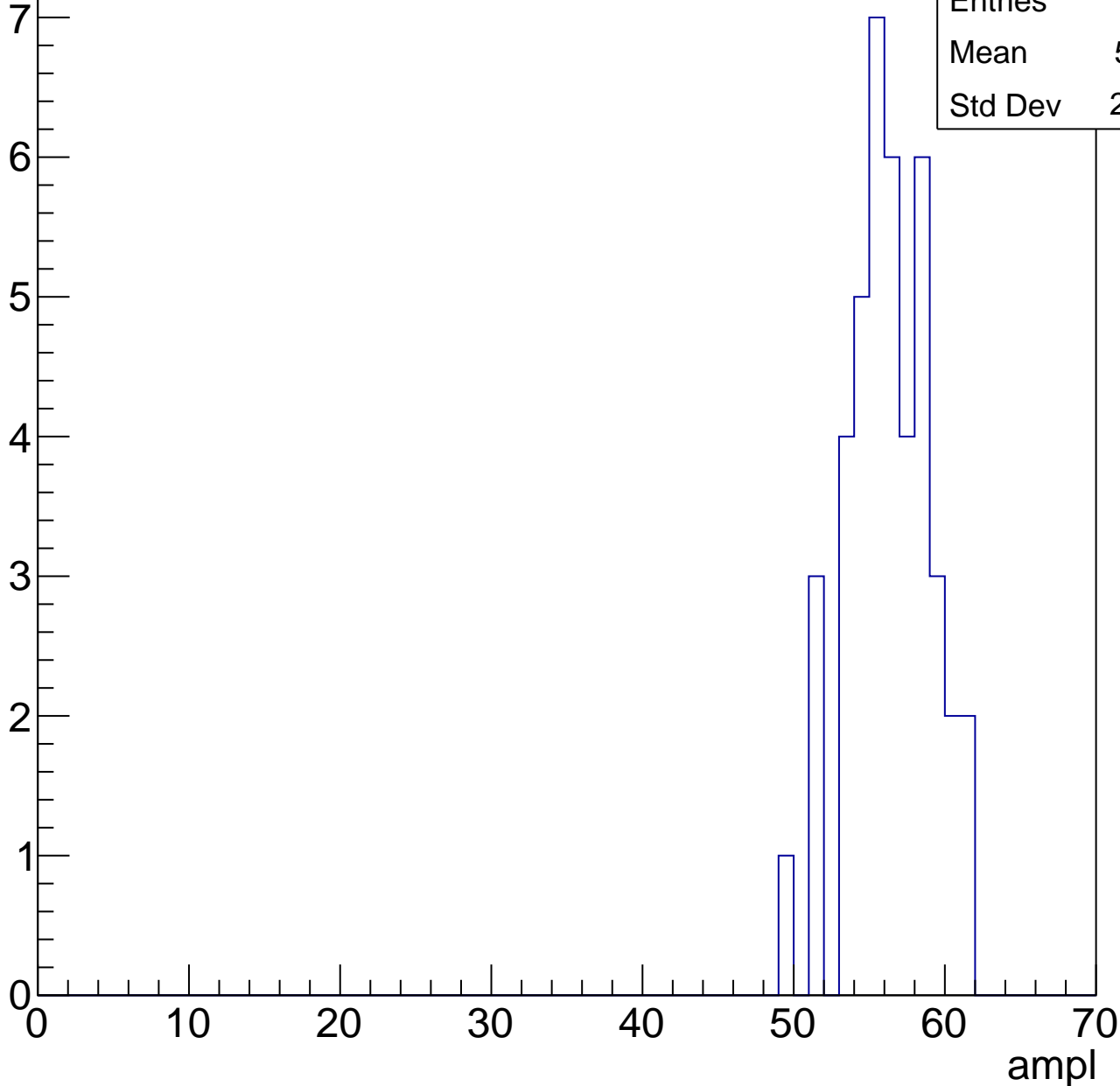


B1L102S, U19-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.81
Std Dev	2.739

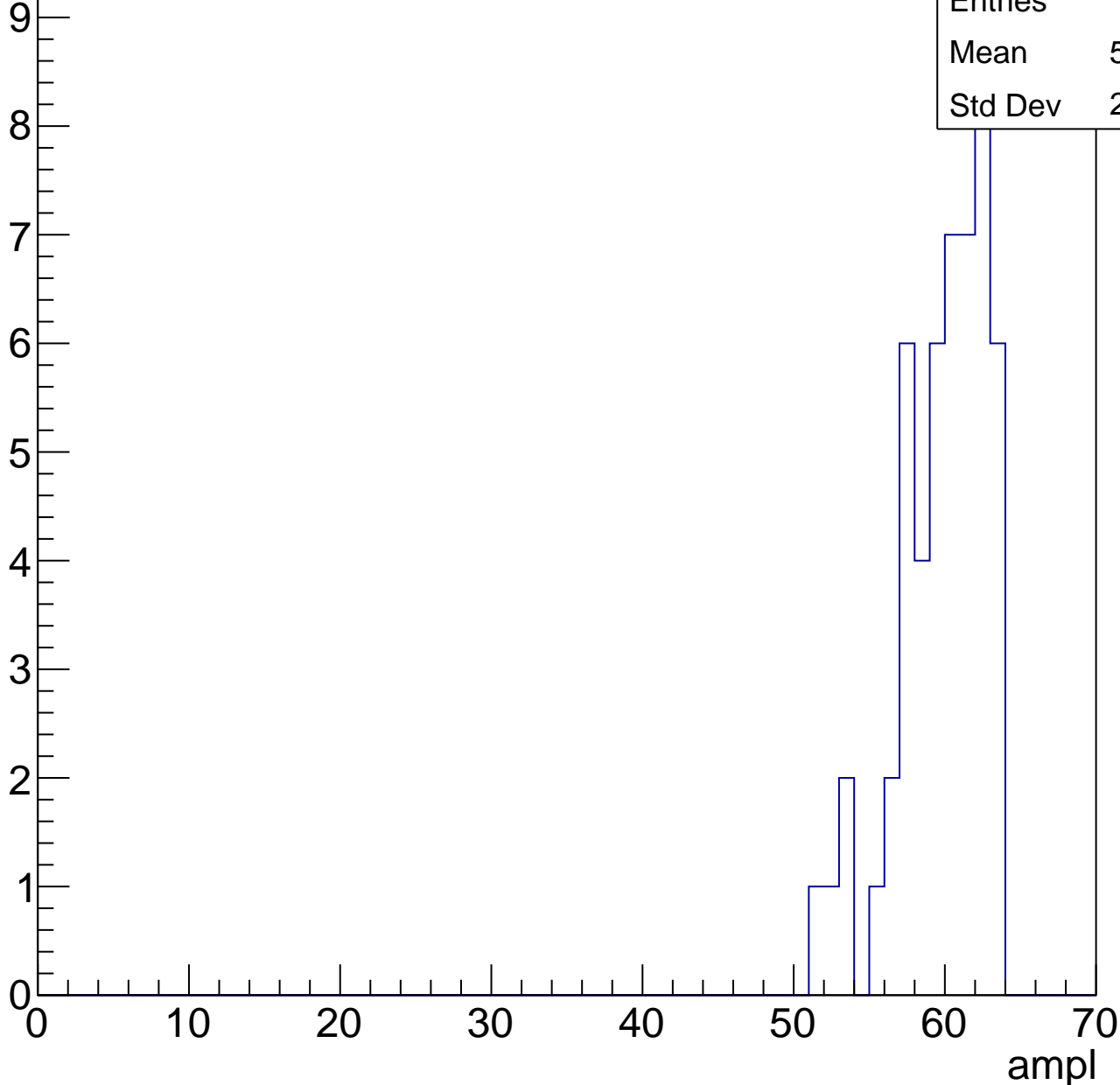


B1L102S, U19-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

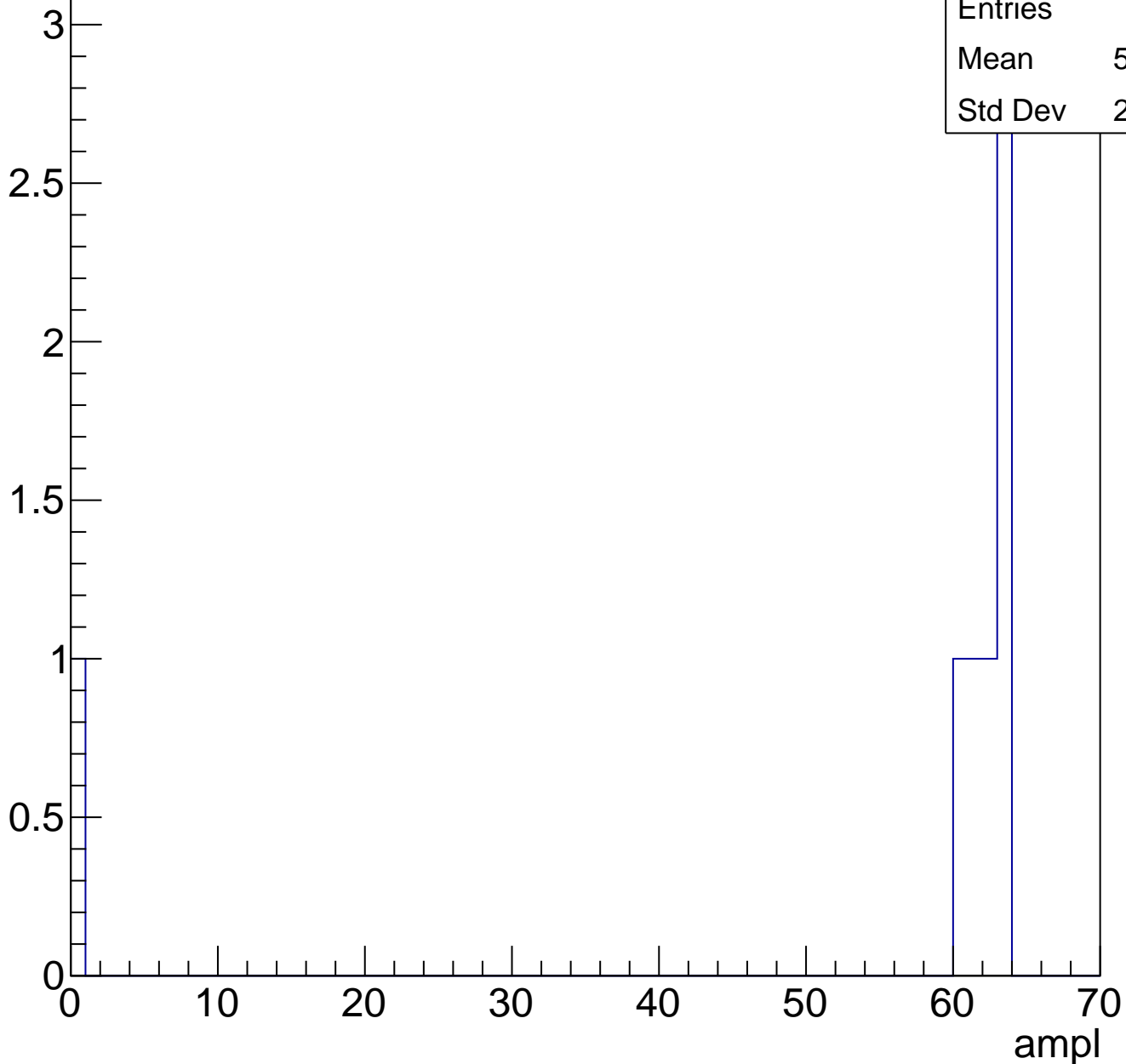
Entries	52
Mean	59.37
Std Dev	2.948



B1L102S, U19-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U19-ch7, adc0

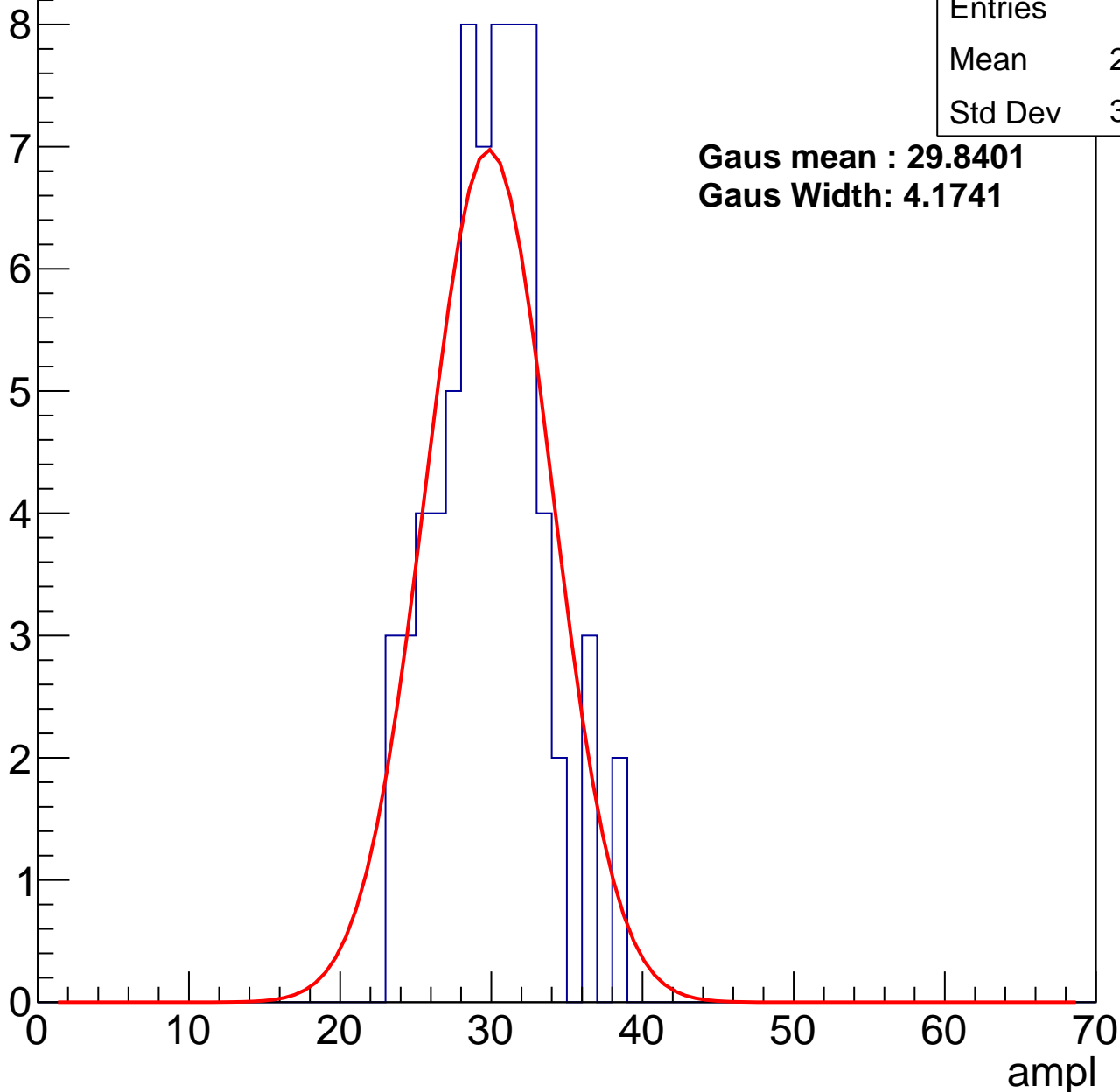
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.49
Std Dev	3.467

Gaus mean : 29.8401

Gaus Width: 4.1741



B1L102S, U19-ch7, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.75
Std Dev	3.538

Gaus mean : 37.1674

Gaus Width: 3.6566

10

8

6

4

2

0

0

10

20

30

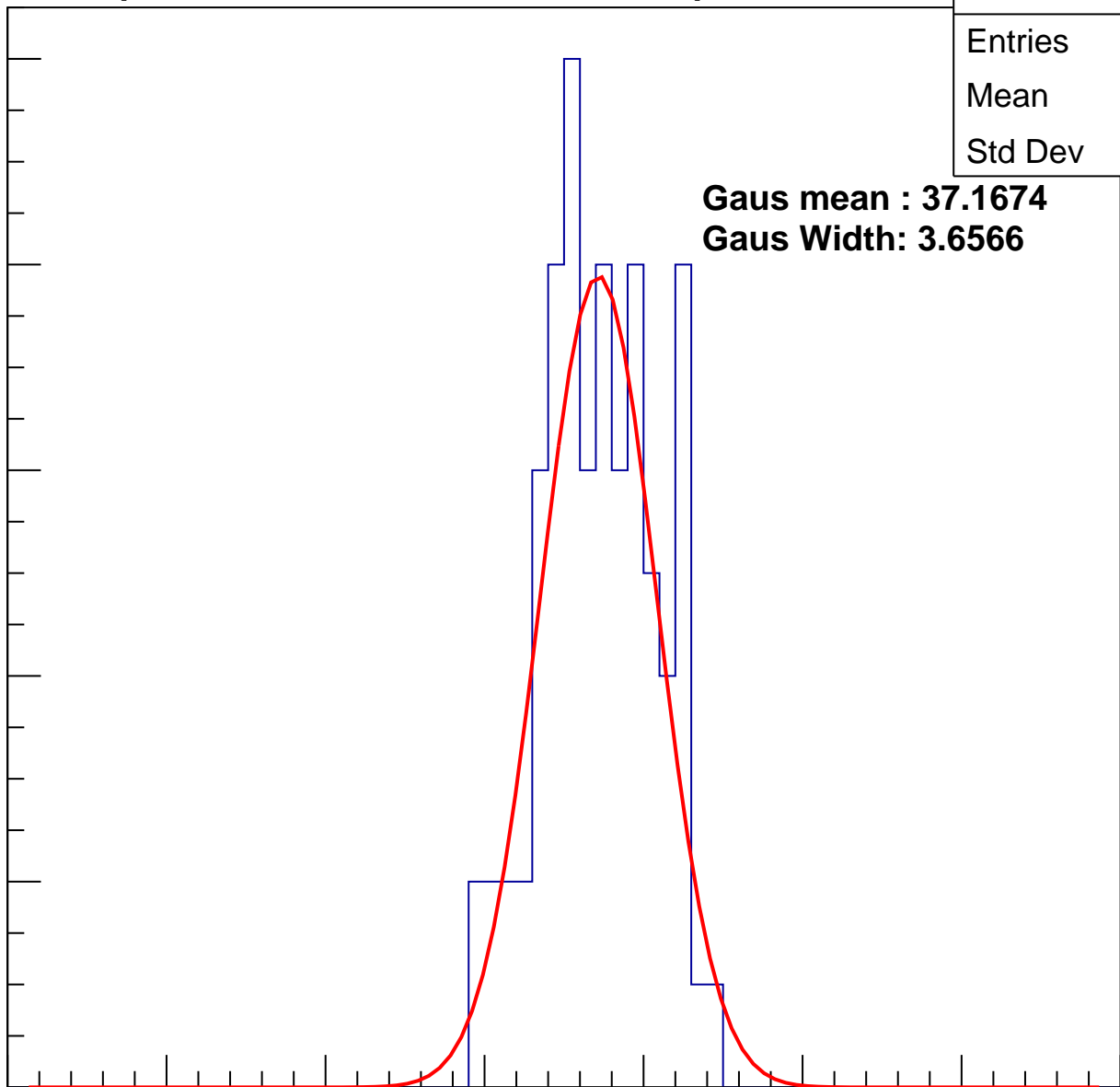
40

50

60

70

ampl



B1L102S, U19-ch7, adc2

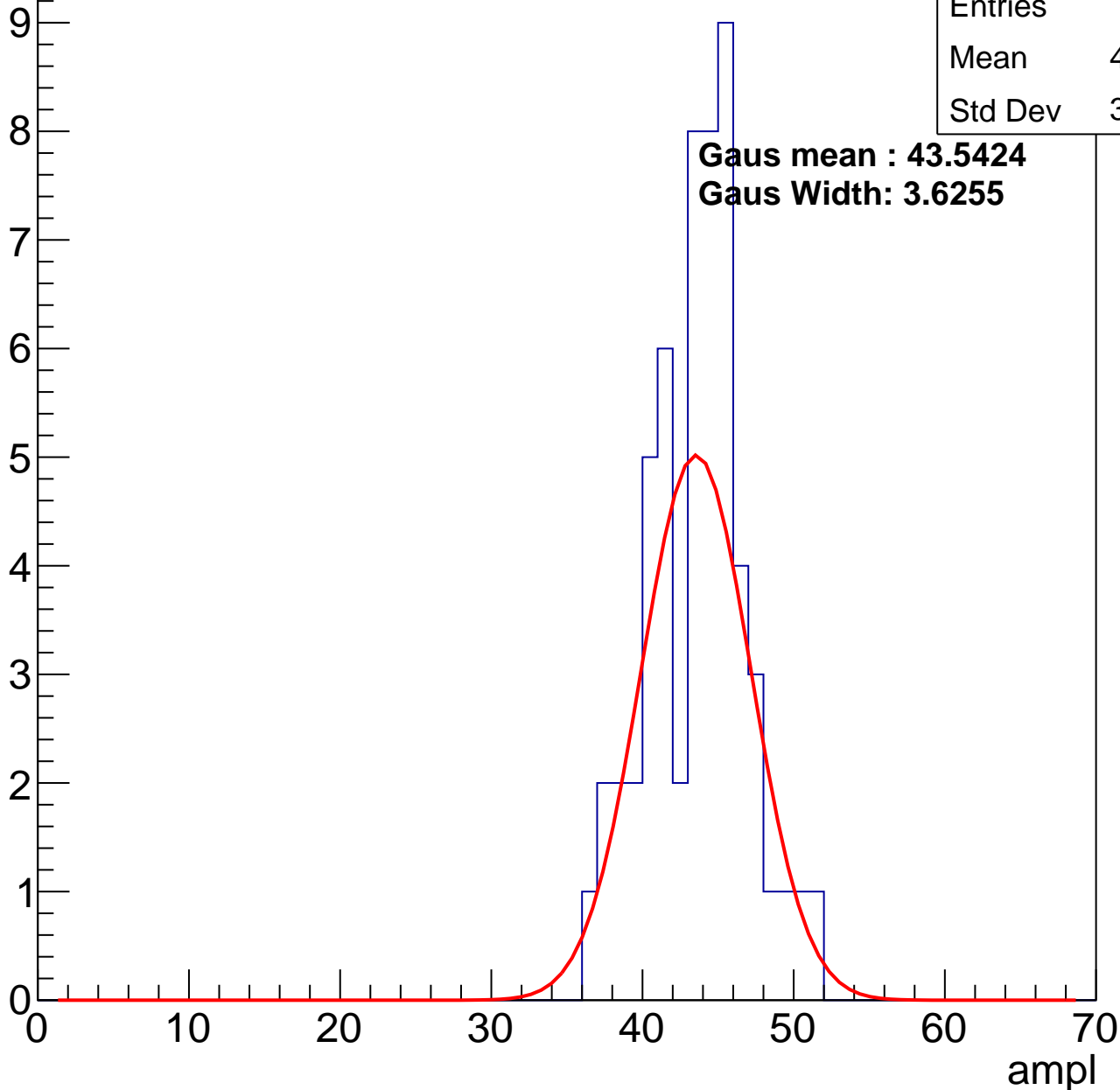
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.18
Std Dev	3.202

Gaus mean : 43.5424

Gaus Width: 3.6255

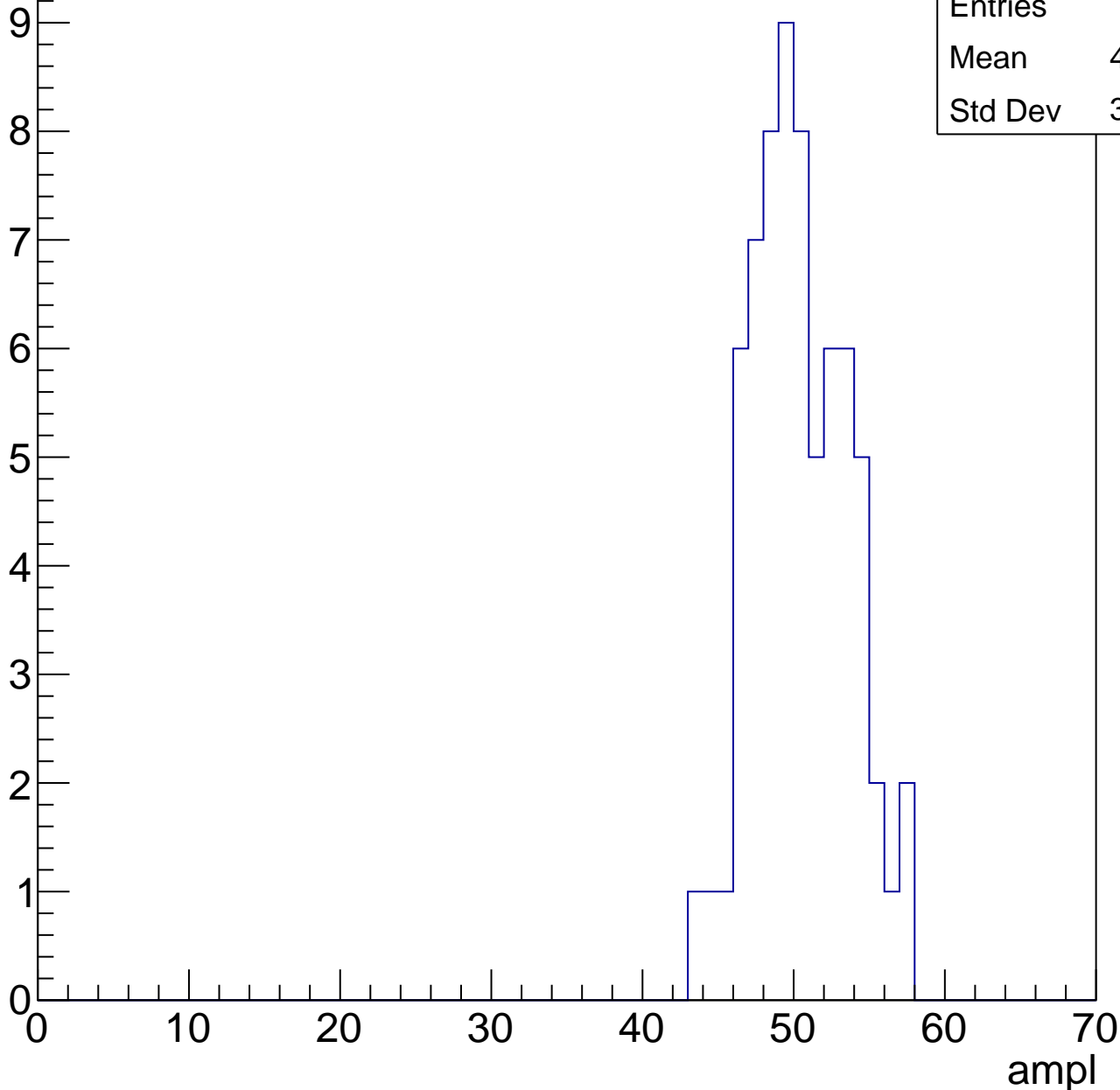


B1L102S, U19-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.96
Std Dev	3.108

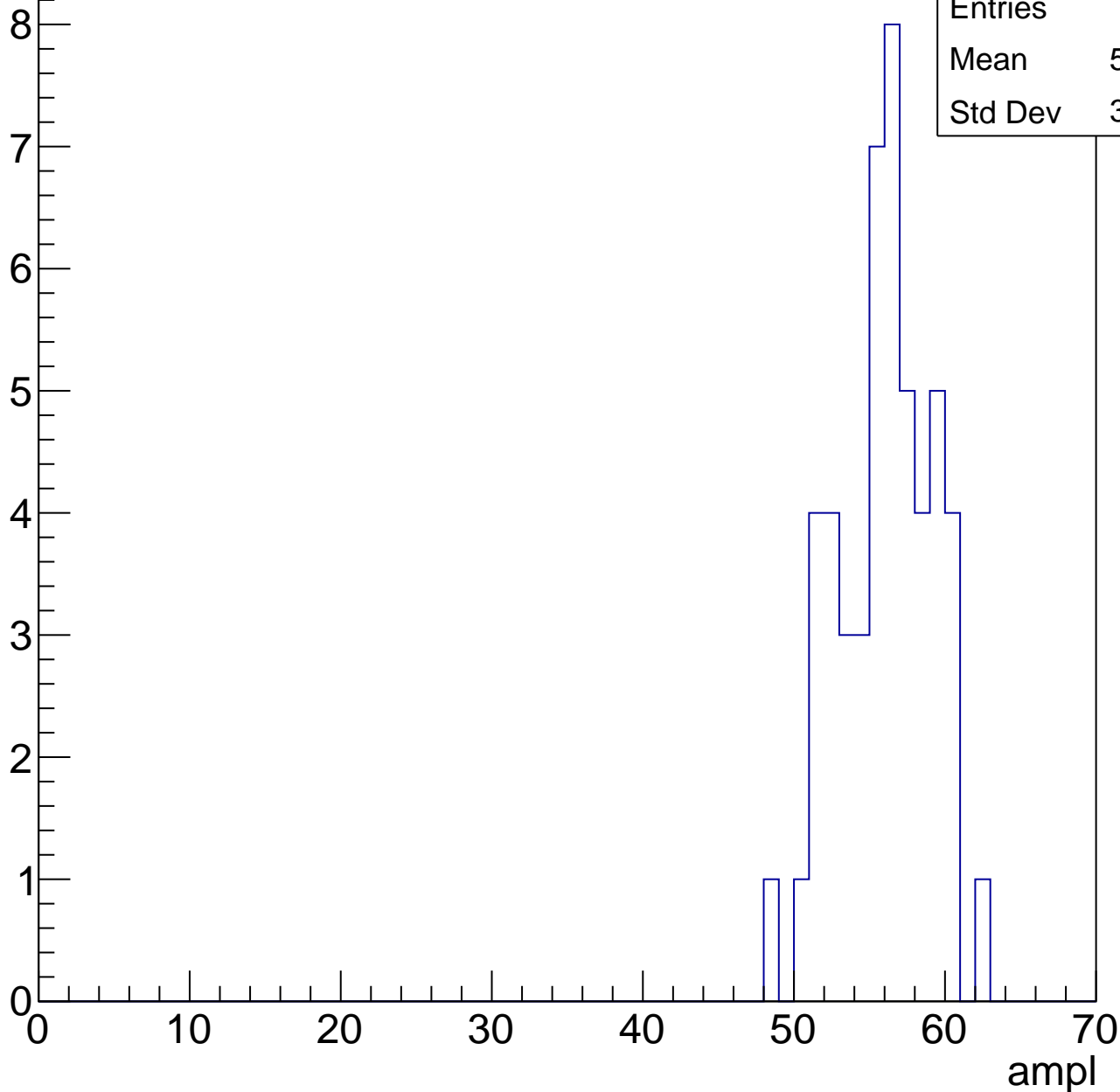


B1L102S, U19-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.56
Std Dev	3.054

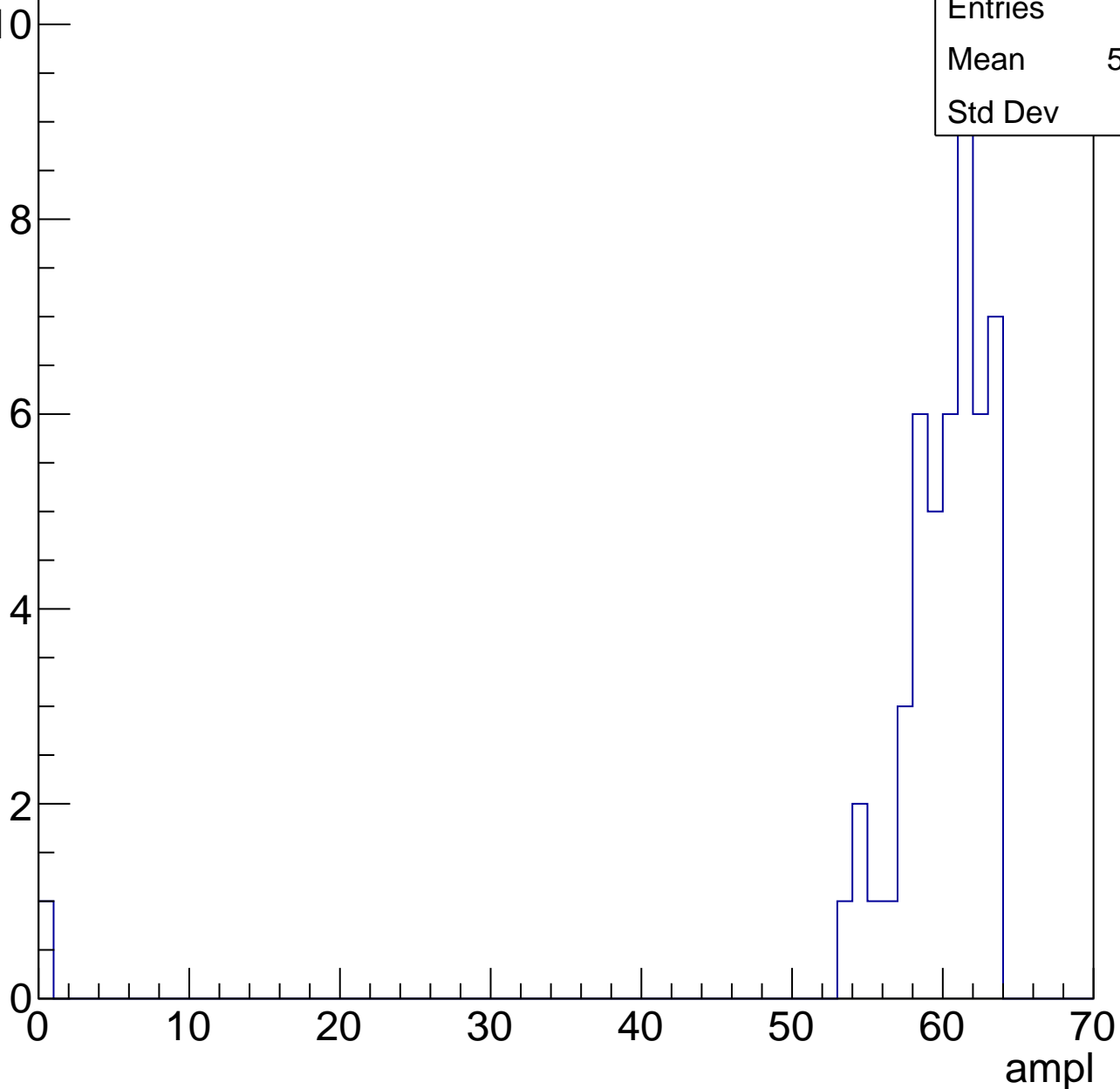


B1L102S, U19-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.55
Std Dev	8.82



B1L102S, U19-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch8, adc0

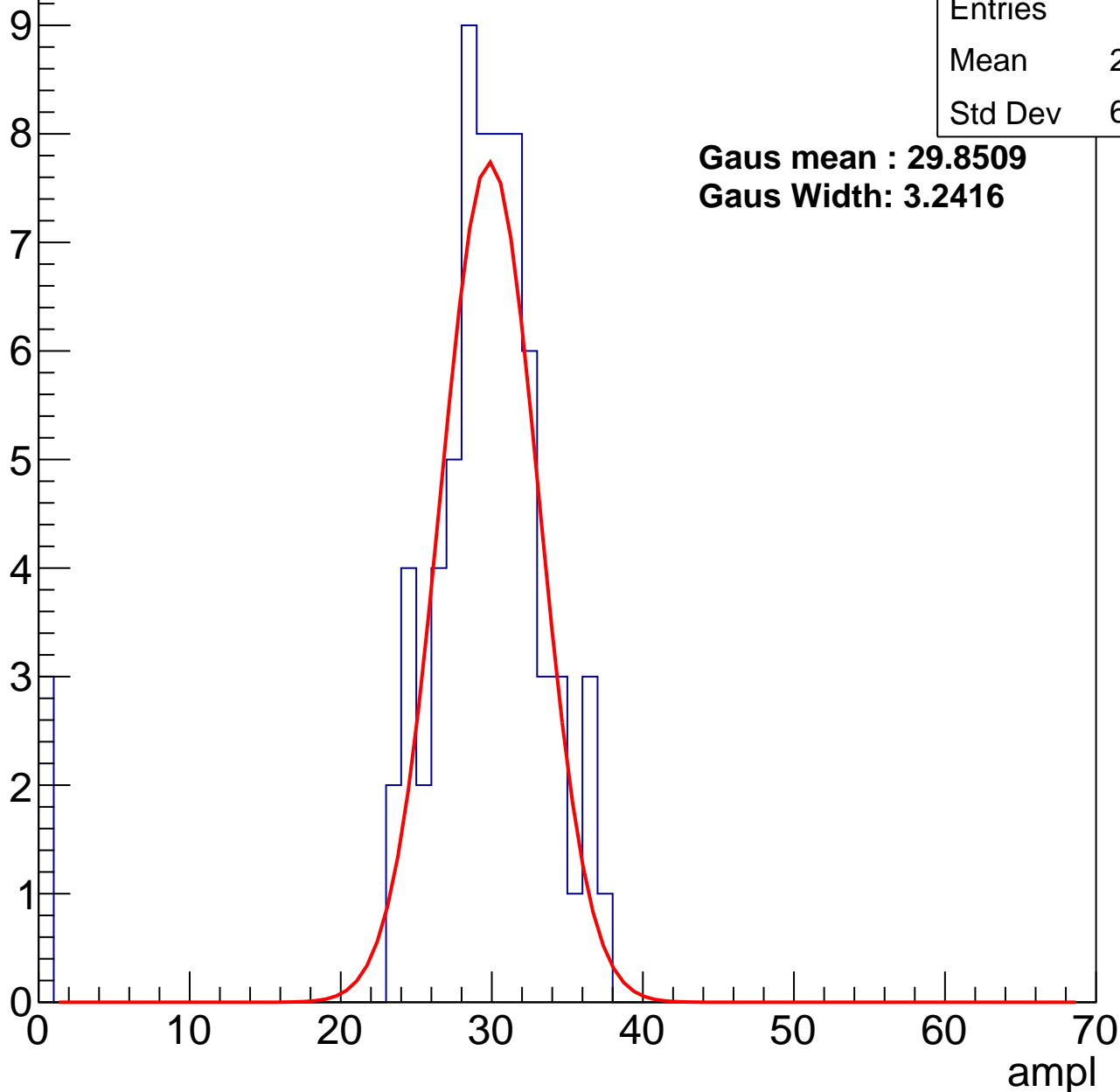
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.23
Std Dev	6.776

Gaus mean : 29.8509

Gaus Width: 3.2416



B1L102S, U19-ch8, adc1

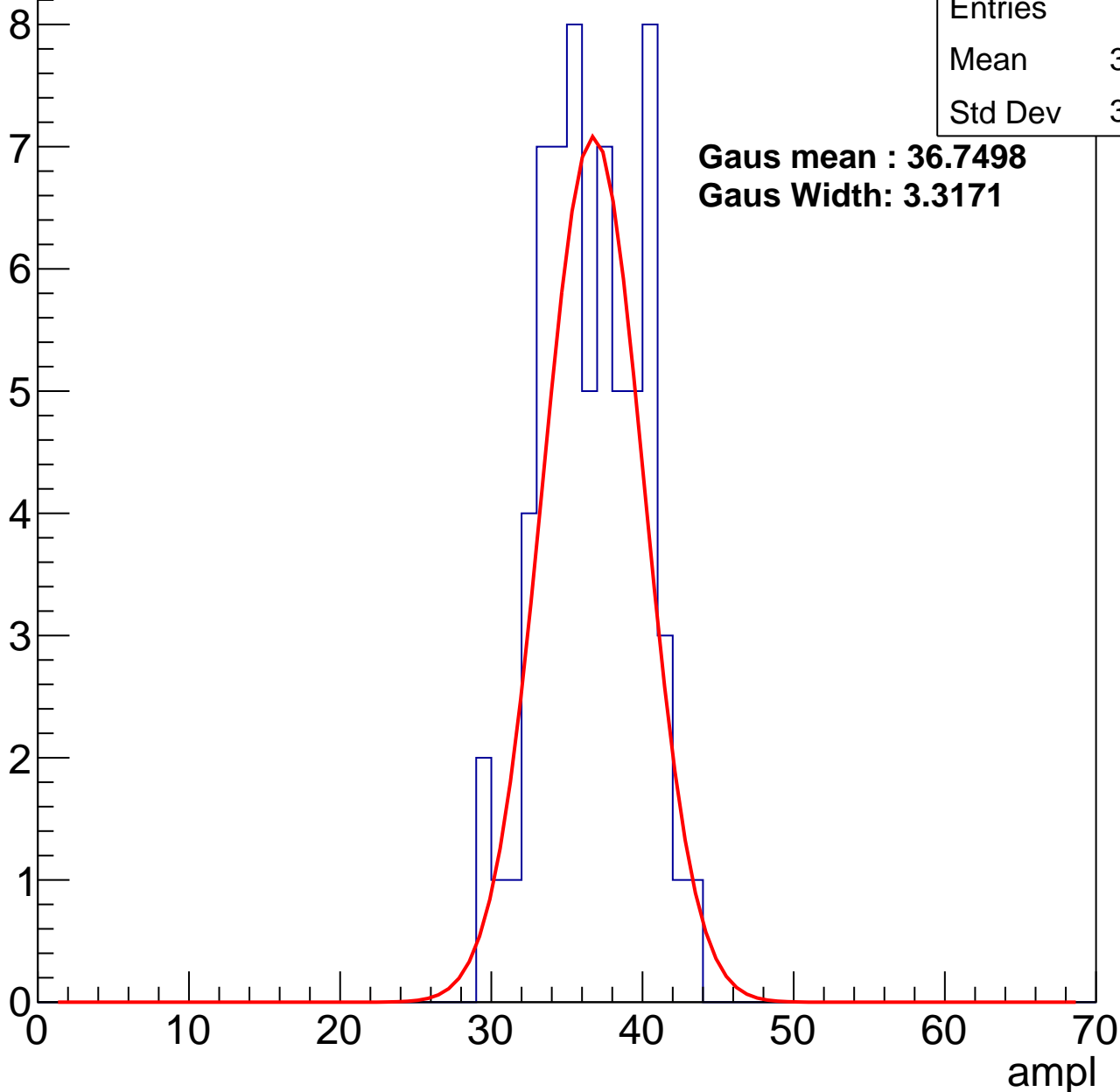
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.12
Std Dev	3.237

Gaus mean : 36.7498

Gaus Width: 3.3171



B1L102S, U19-ch8, adc2

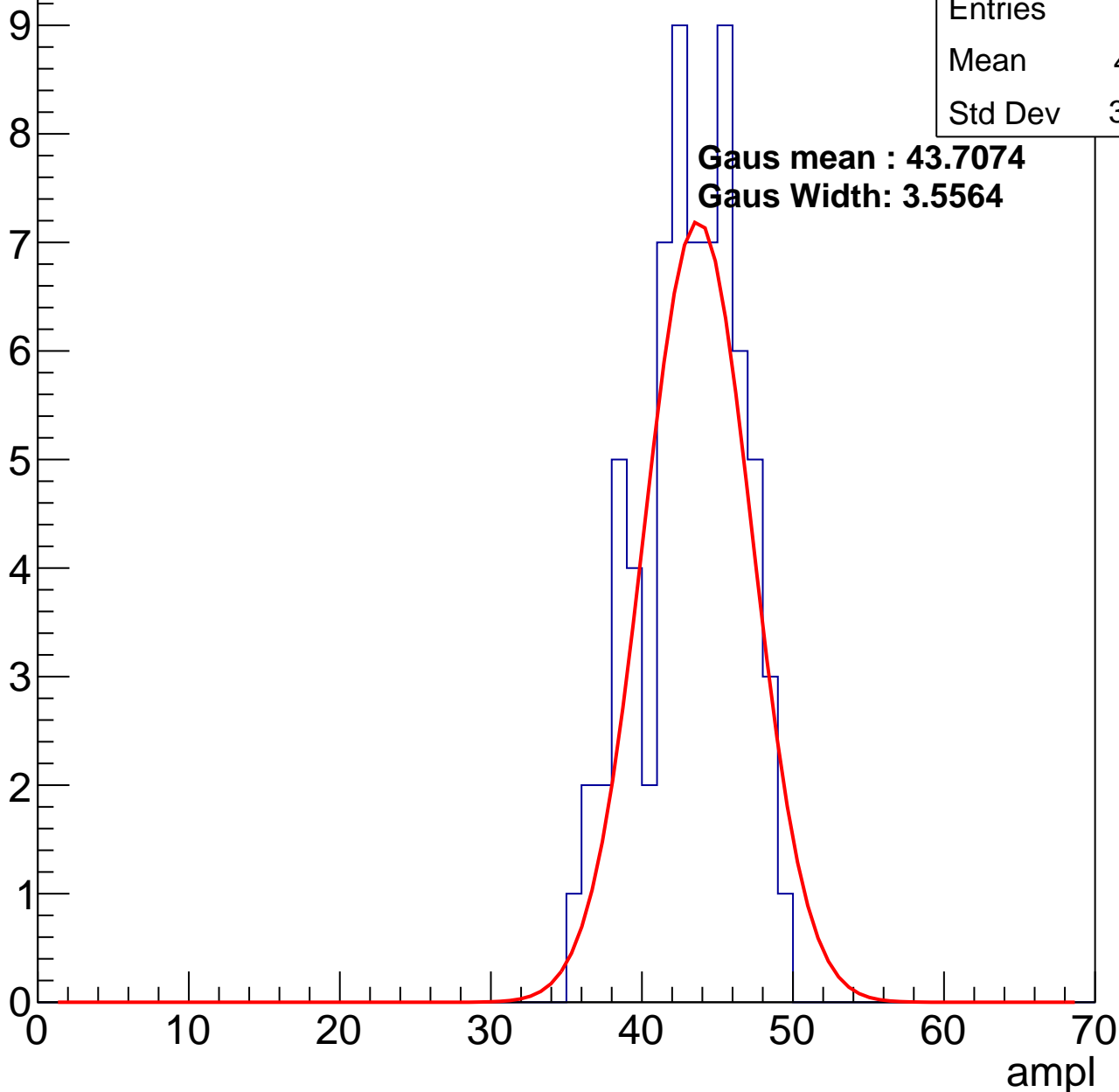
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.71
Std Dev	3.296

Gaus mean : 43.7074

Gaus Width: 3.5564

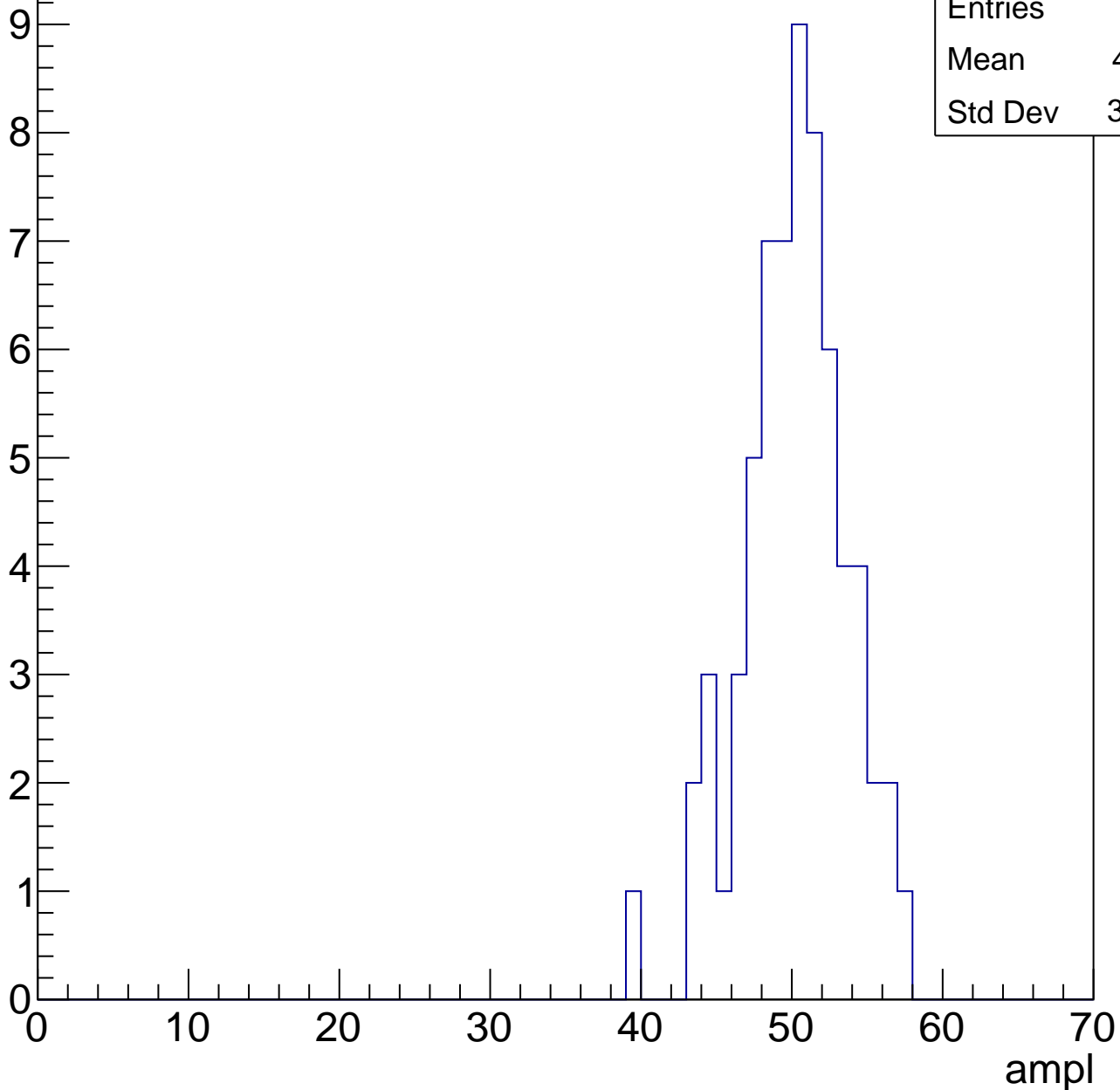


B1L102S, U19-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.71
Std Dev	3.458

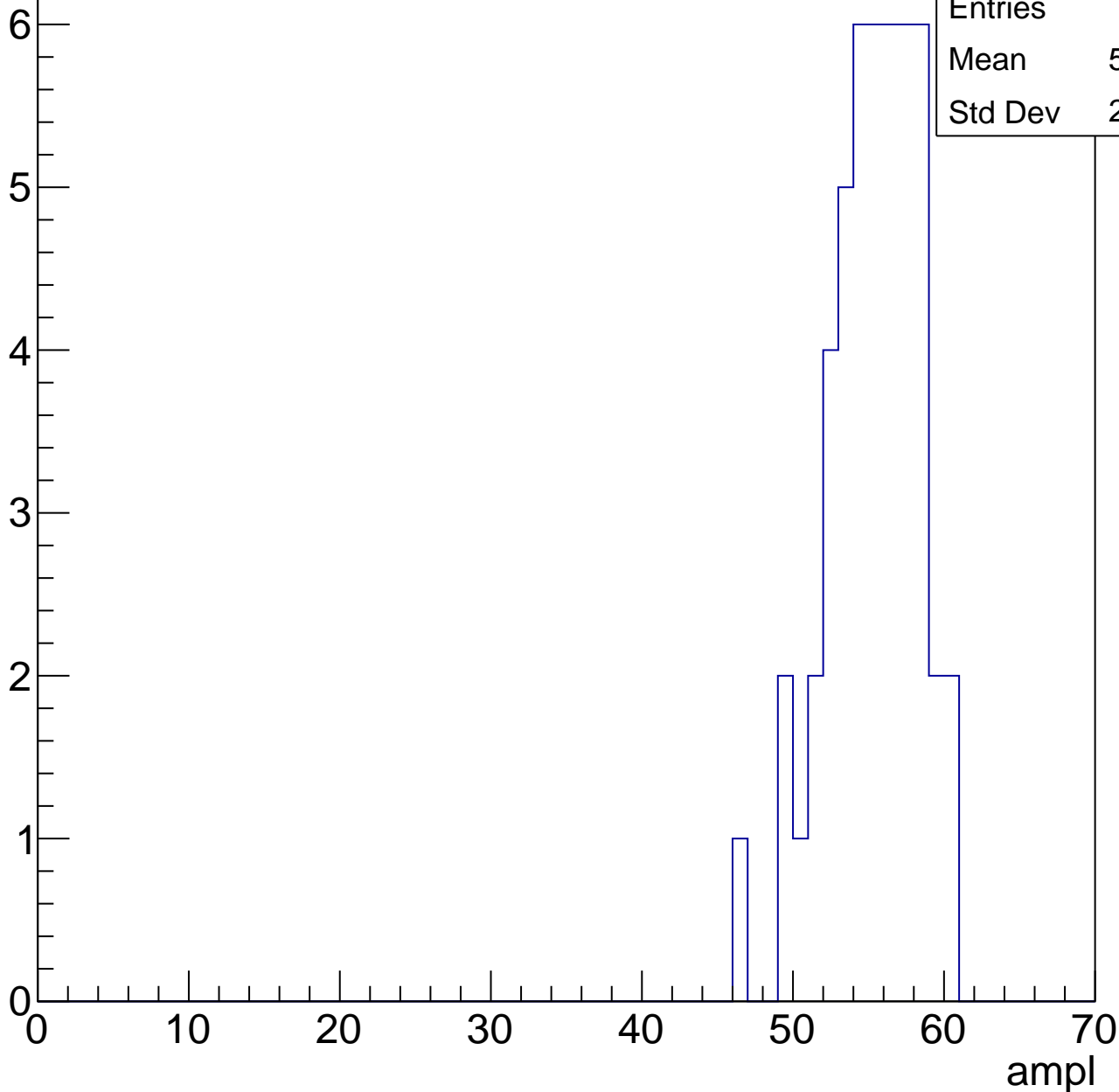


B1L102S, U19-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.84
Std Dev	2.985



B1L102S, U19-ch8, adc5

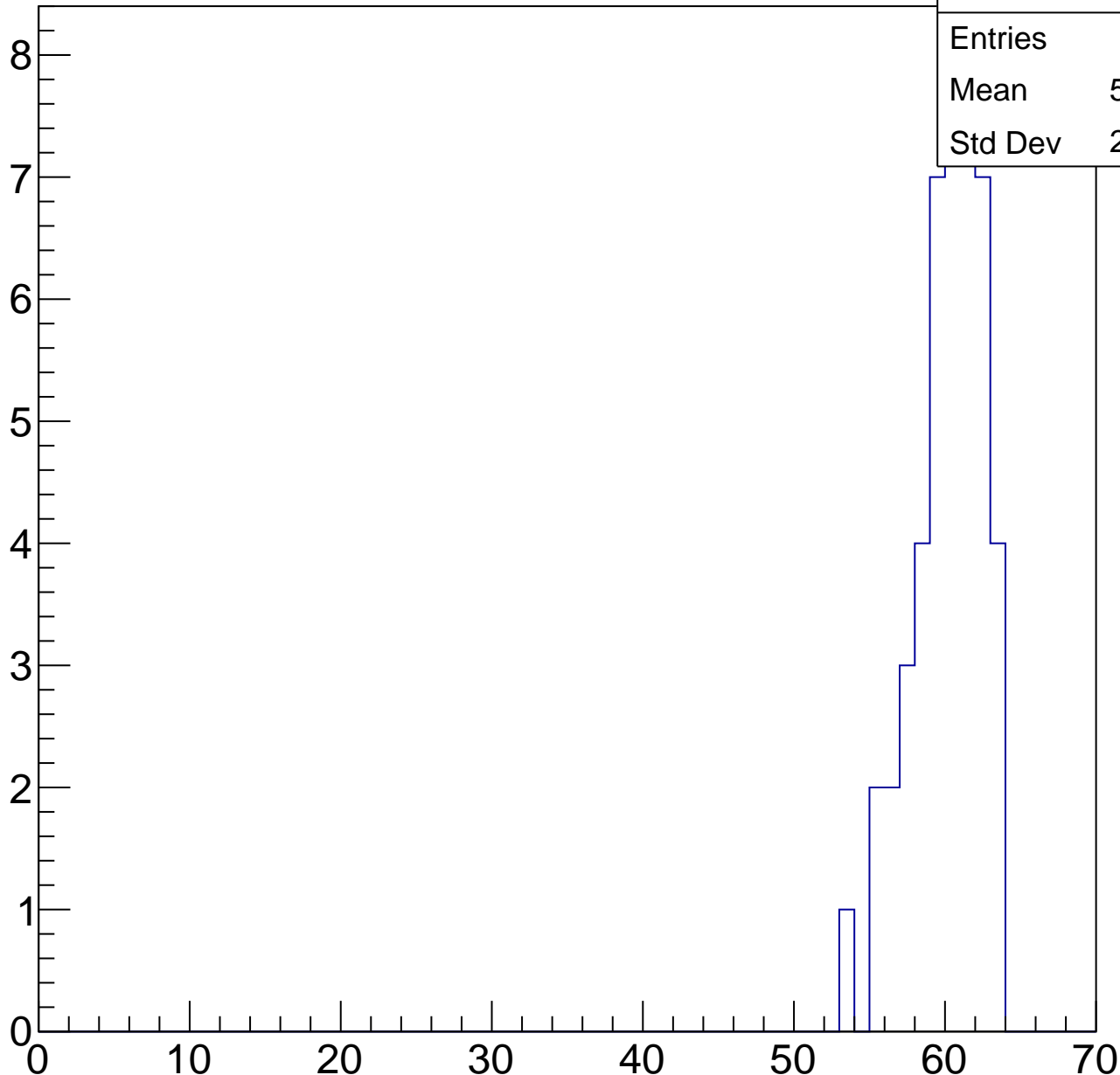
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.67
Std Dev	2.322

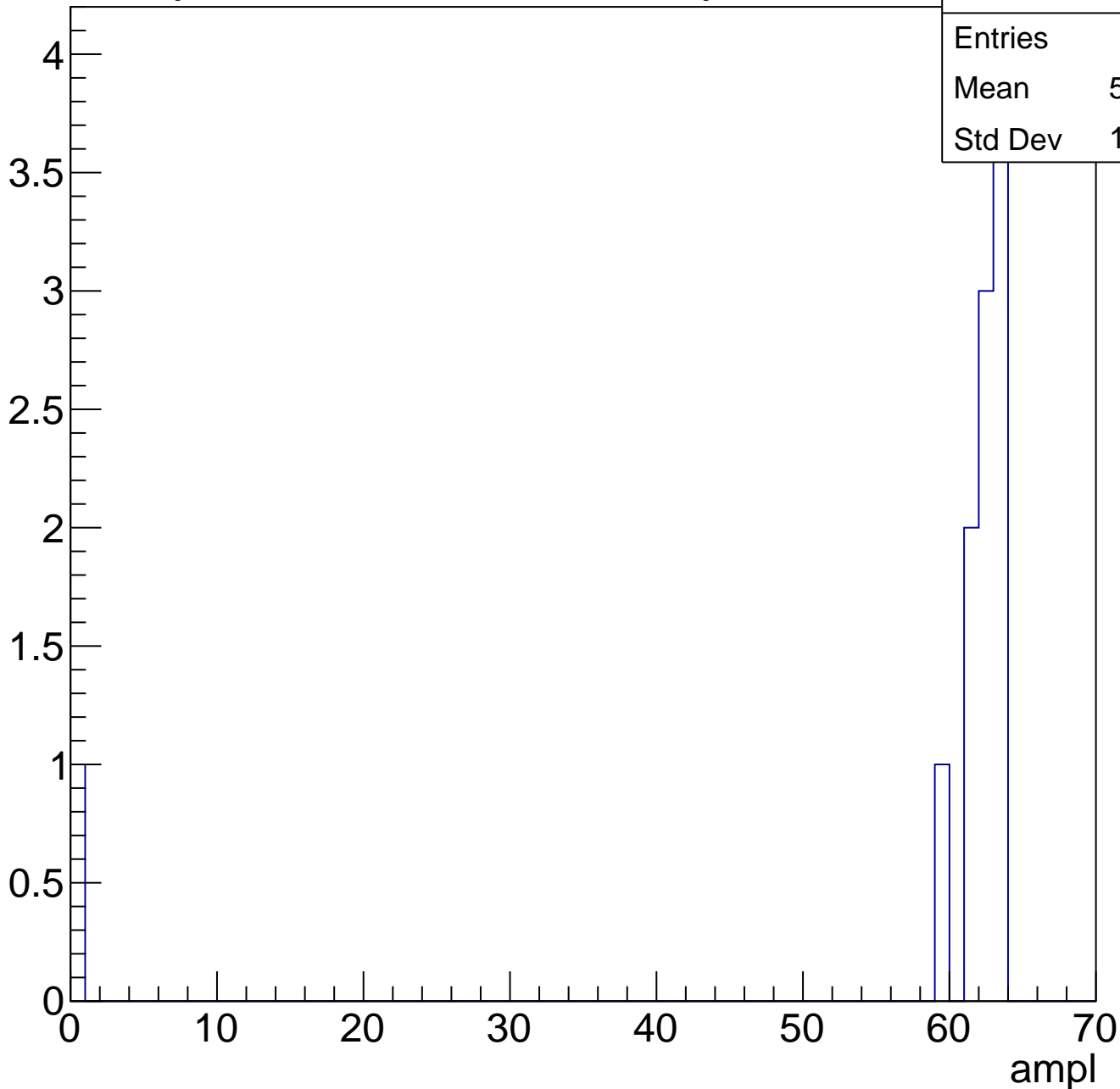
ampl



B1L102S, U19-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

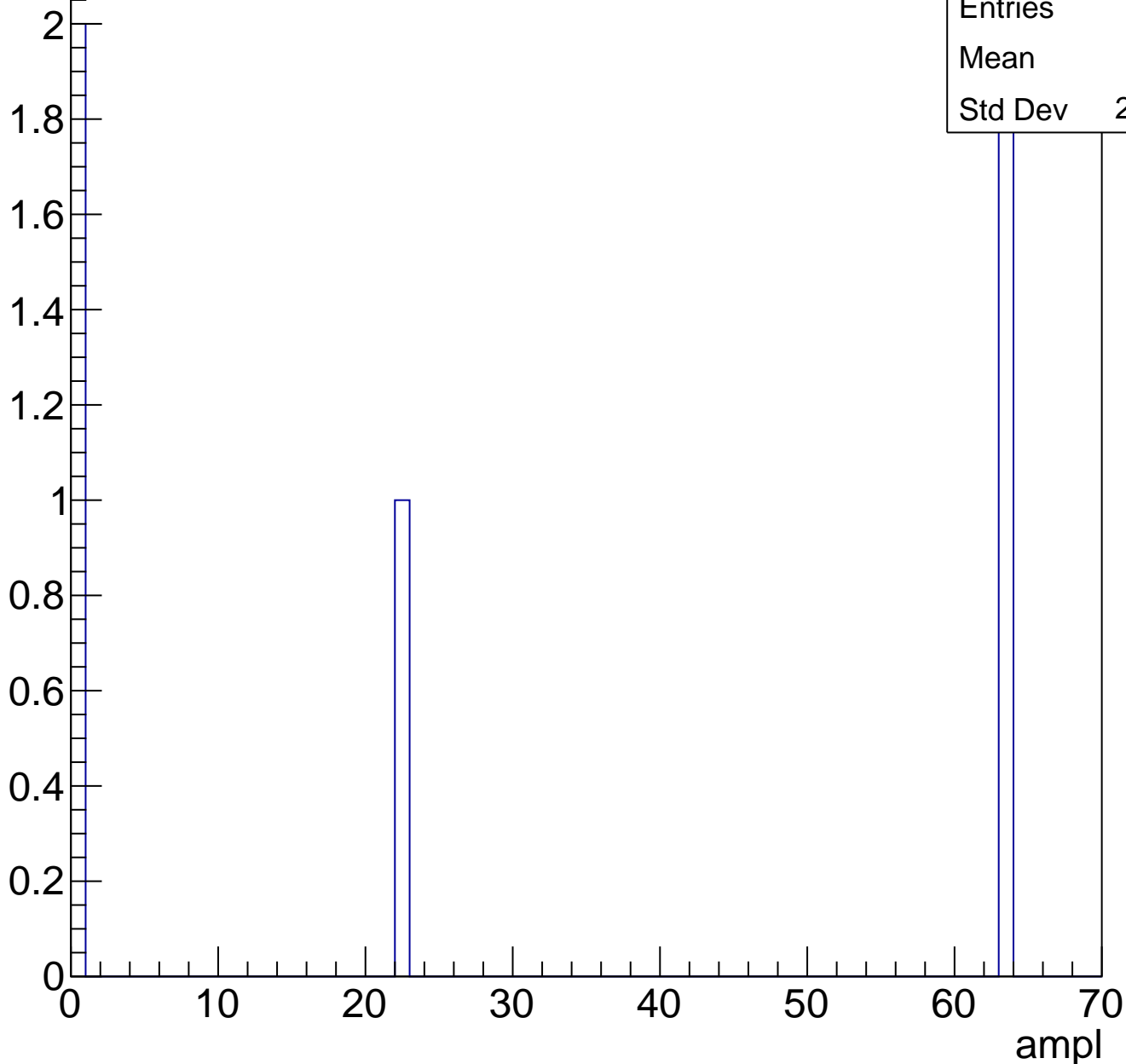
Entry



B1L102S, U19-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch9, adc0

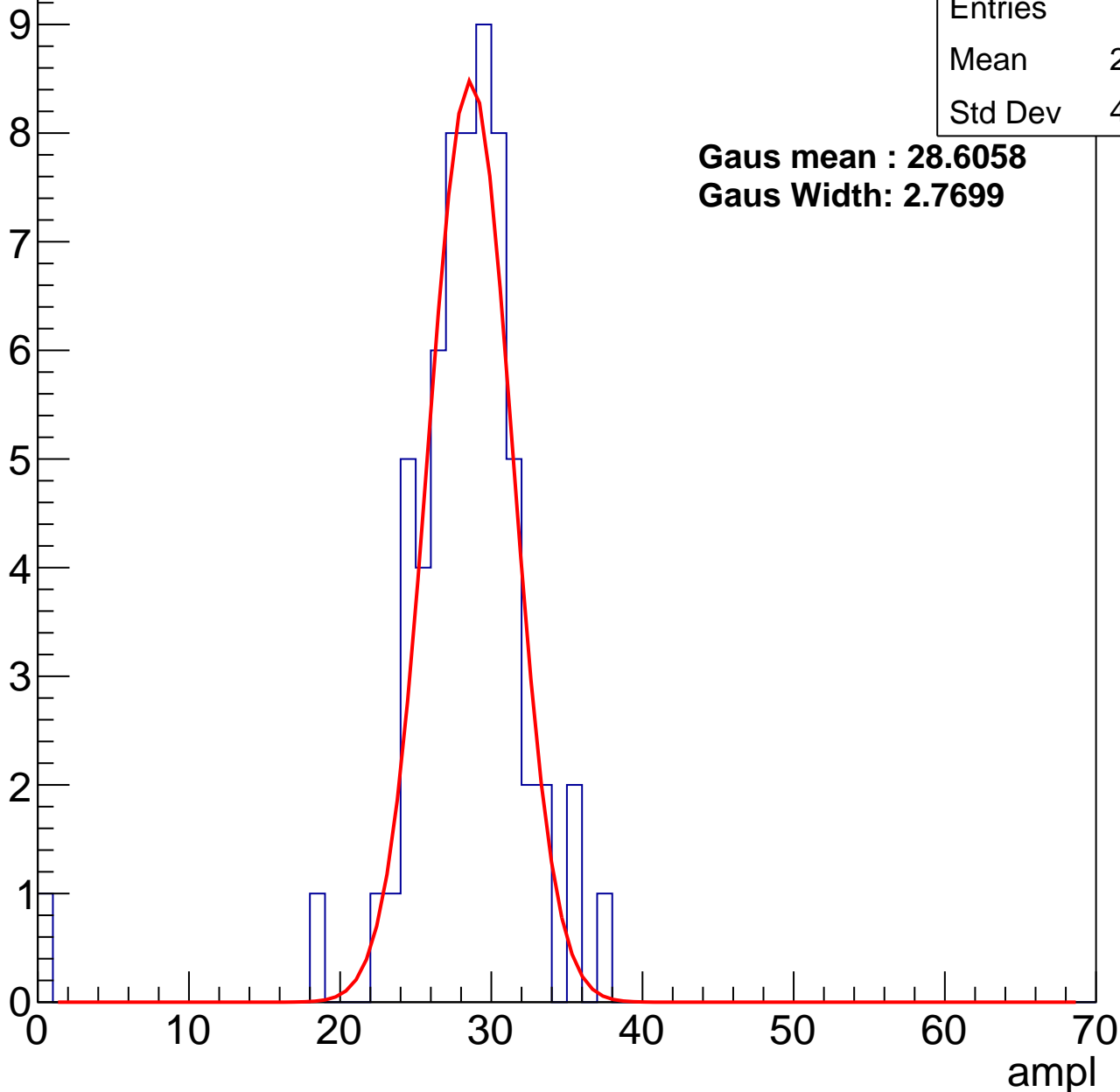
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.69
Std Dev	4.733

Gaus mean : 28.6058

Gaus Width: 2.7699



B1L102S, U19-ch9, adc1

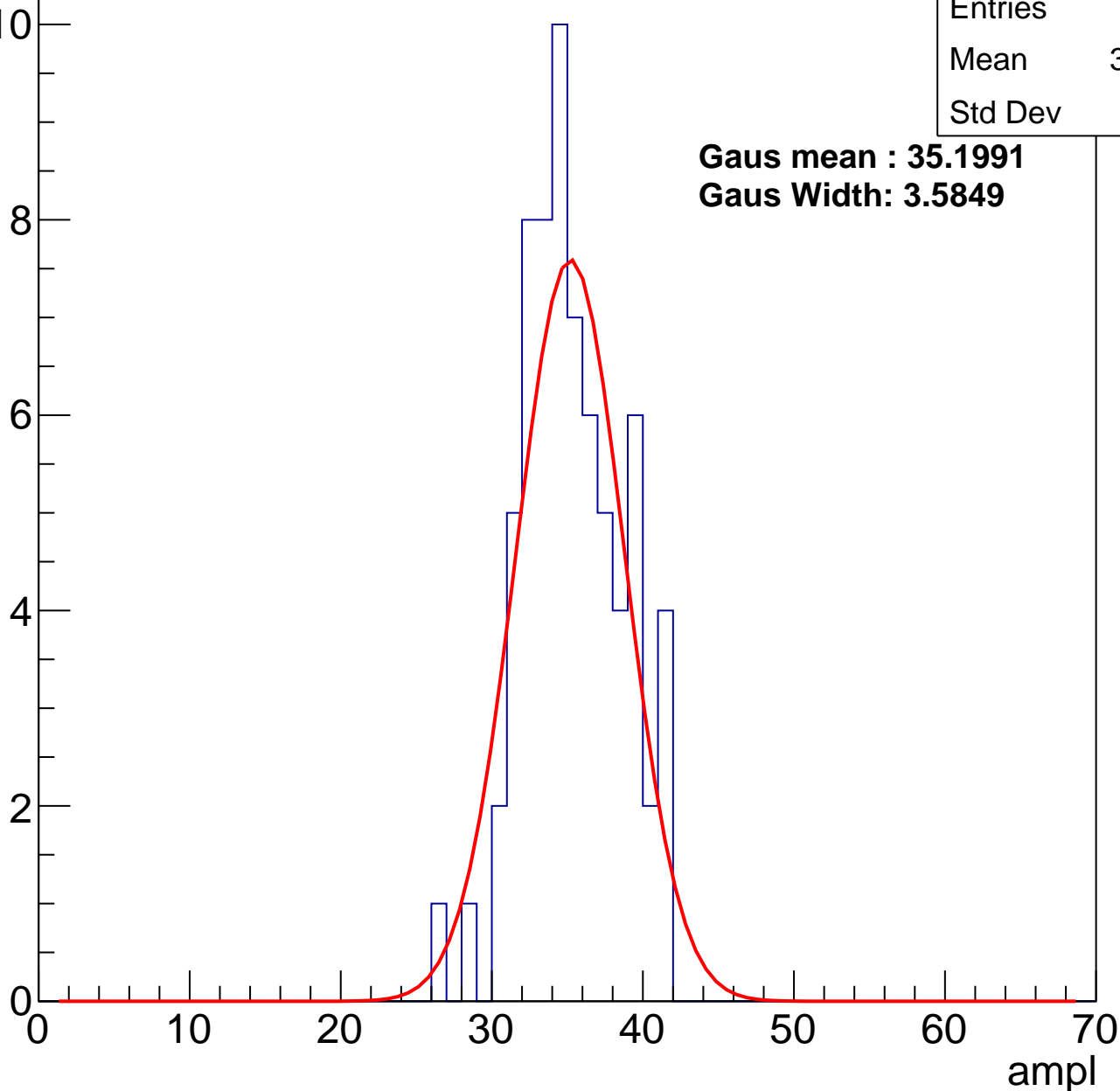
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.86
Std Dev	3.24

Gaus mean : 35.1991

Gaus Width: 3.5849



B1L102S, U19-ch9, adc2

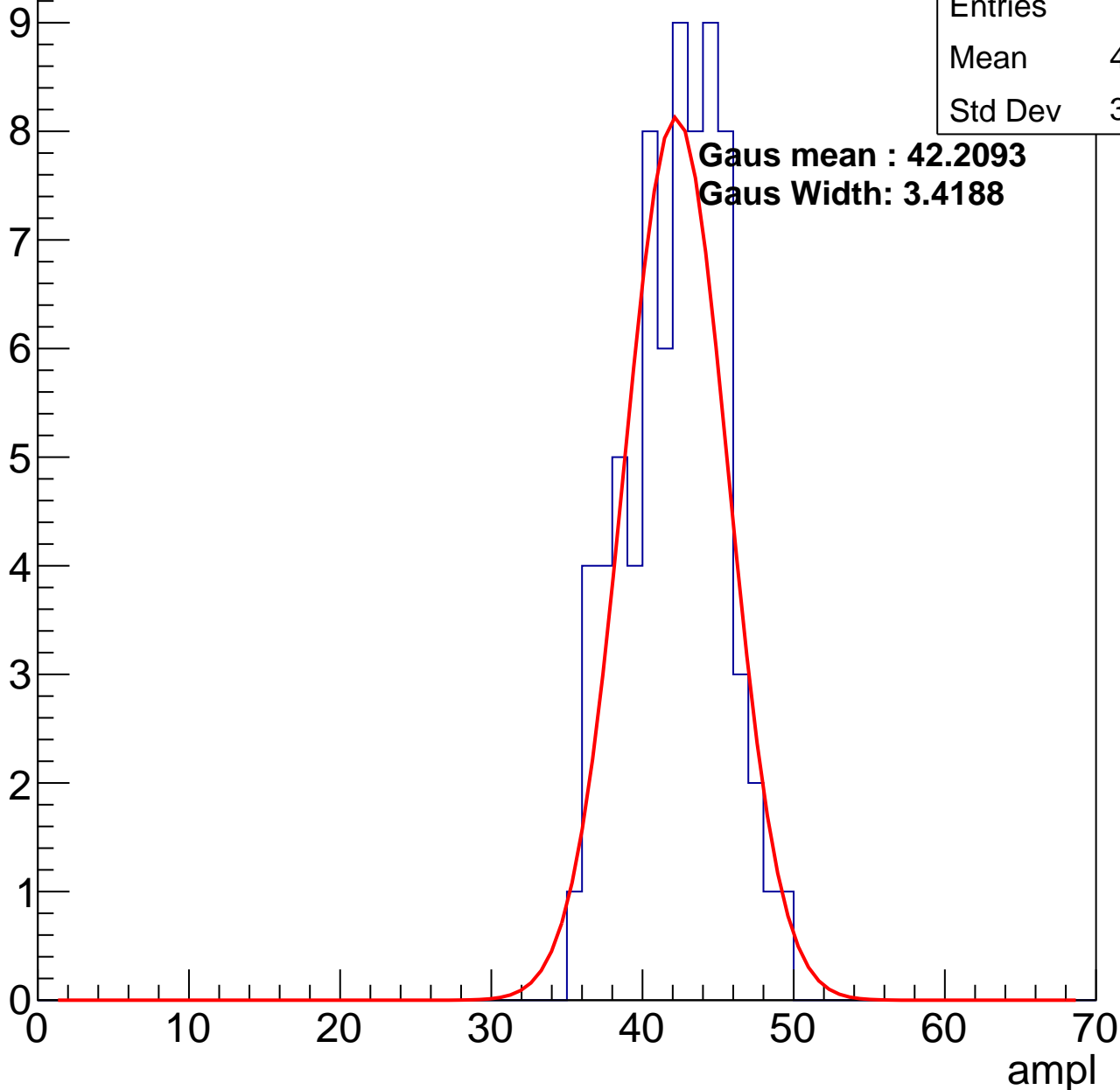
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.73
Std Dev	3.185

Gaus mean : 42.2093

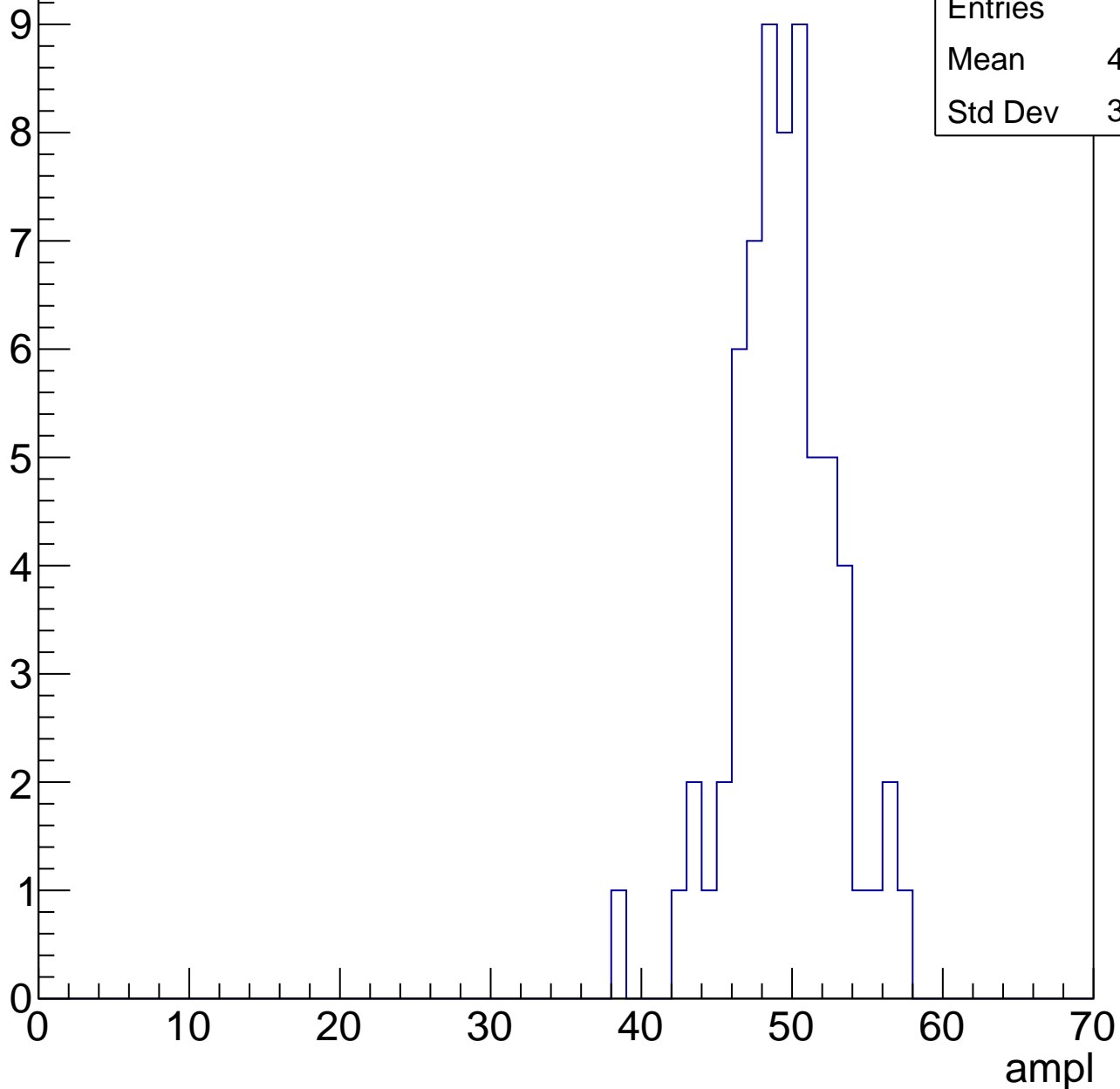
Gaus Width: 3.4188



B1L102S, U19-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

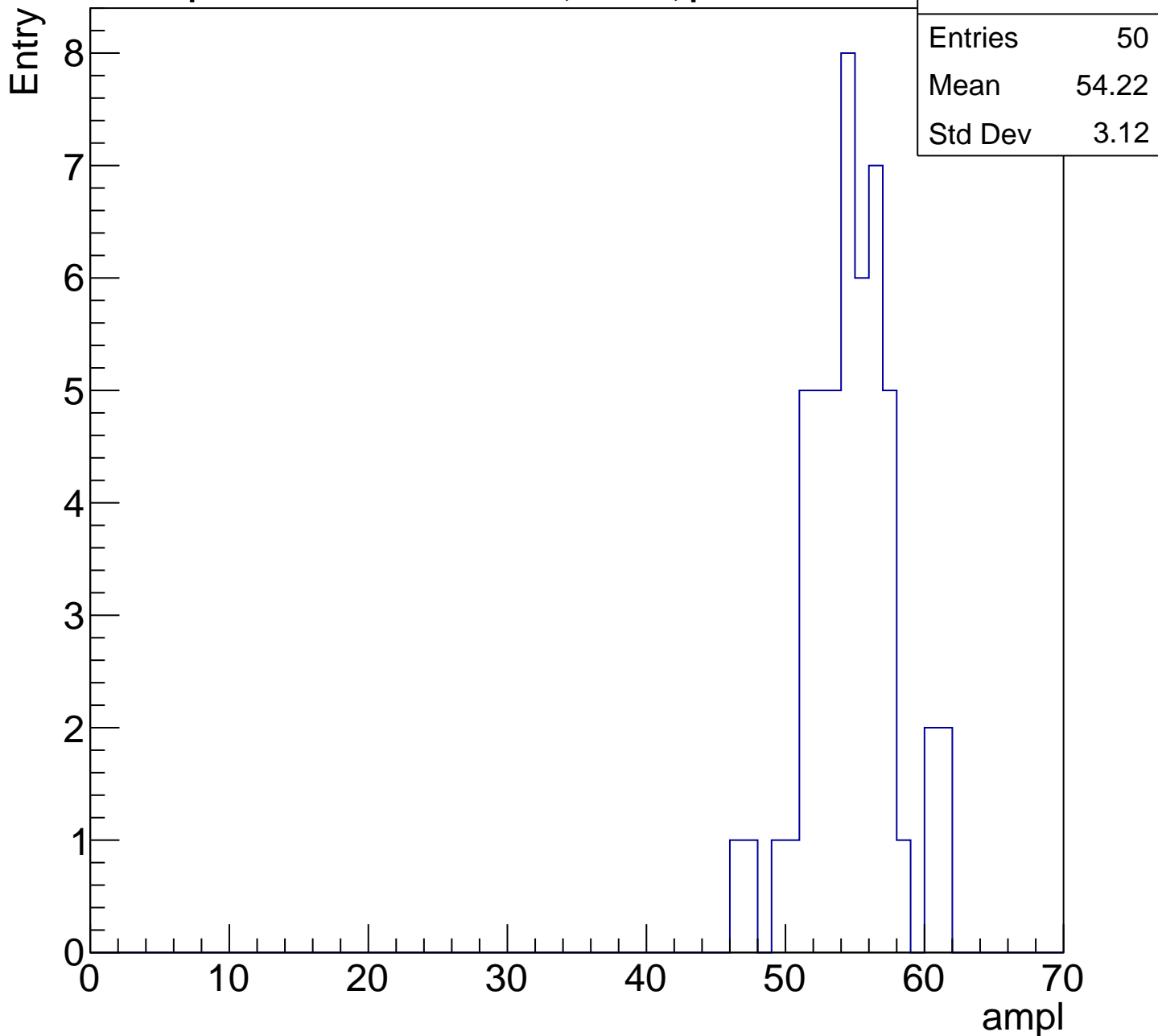
Entry



Entries	65
Mean	48.98
Std Dev	3.399

B1L102S, U19-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U19-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

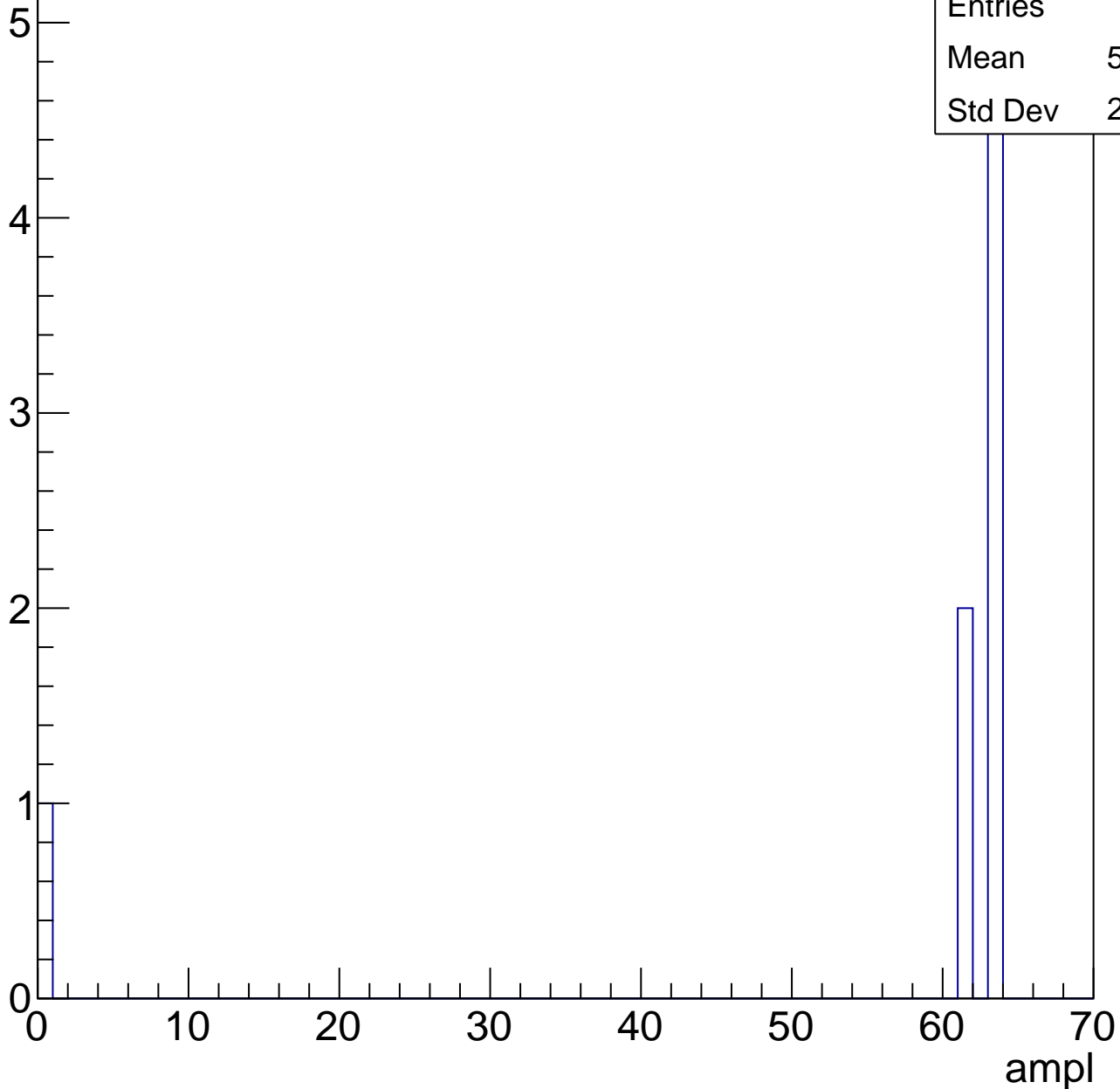
Entries	58
Mean	59.47
Std Dev	2.394

B1L102S, U19-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.62
Std Dev	20.66



B1L102S, U19-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch10, adc0

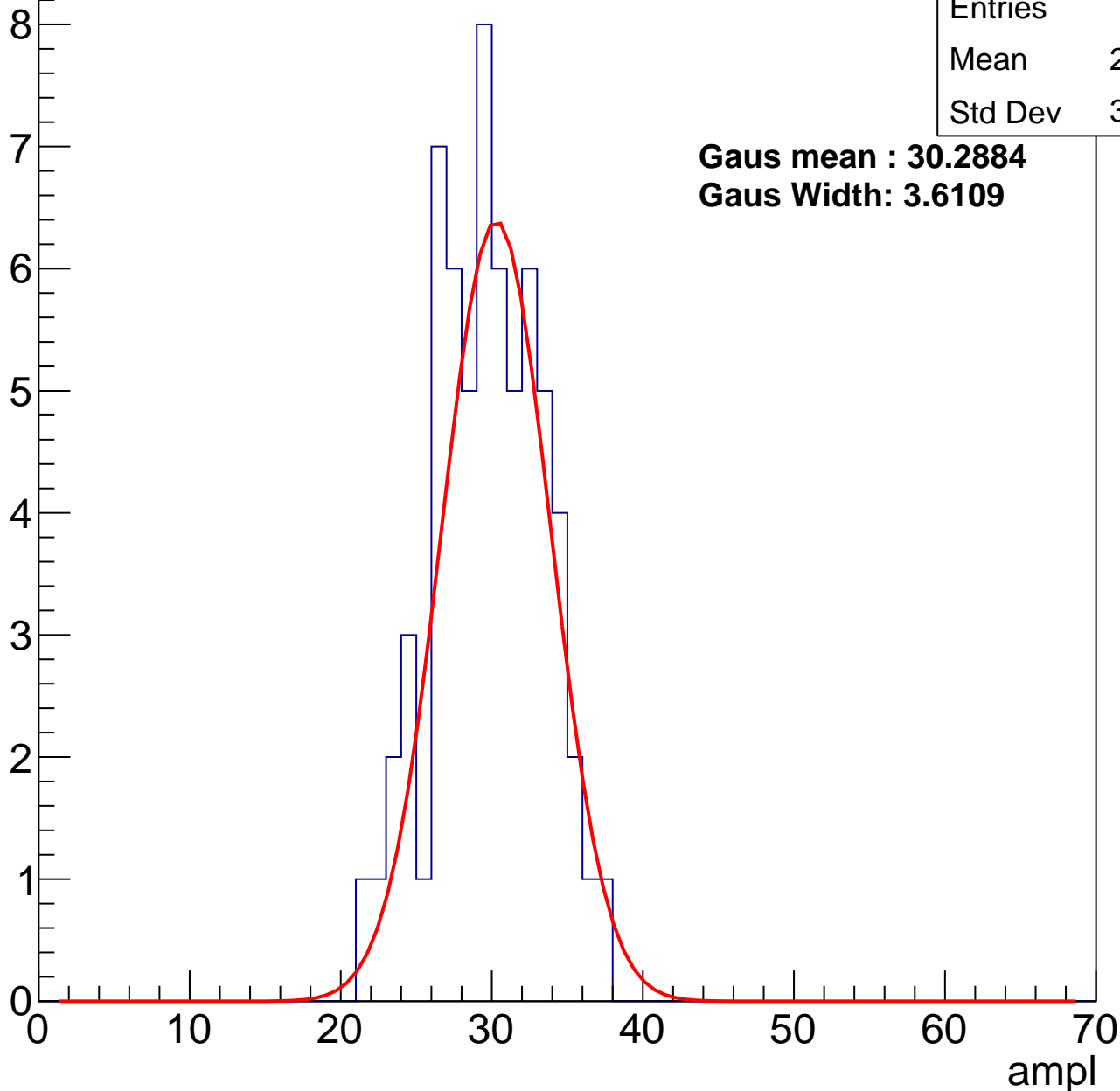
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.27
Std Dev	3.554

Gaus mean : 30.2884

Gaus Width: 3.6109



B1L102S, U19-ch10, adc1

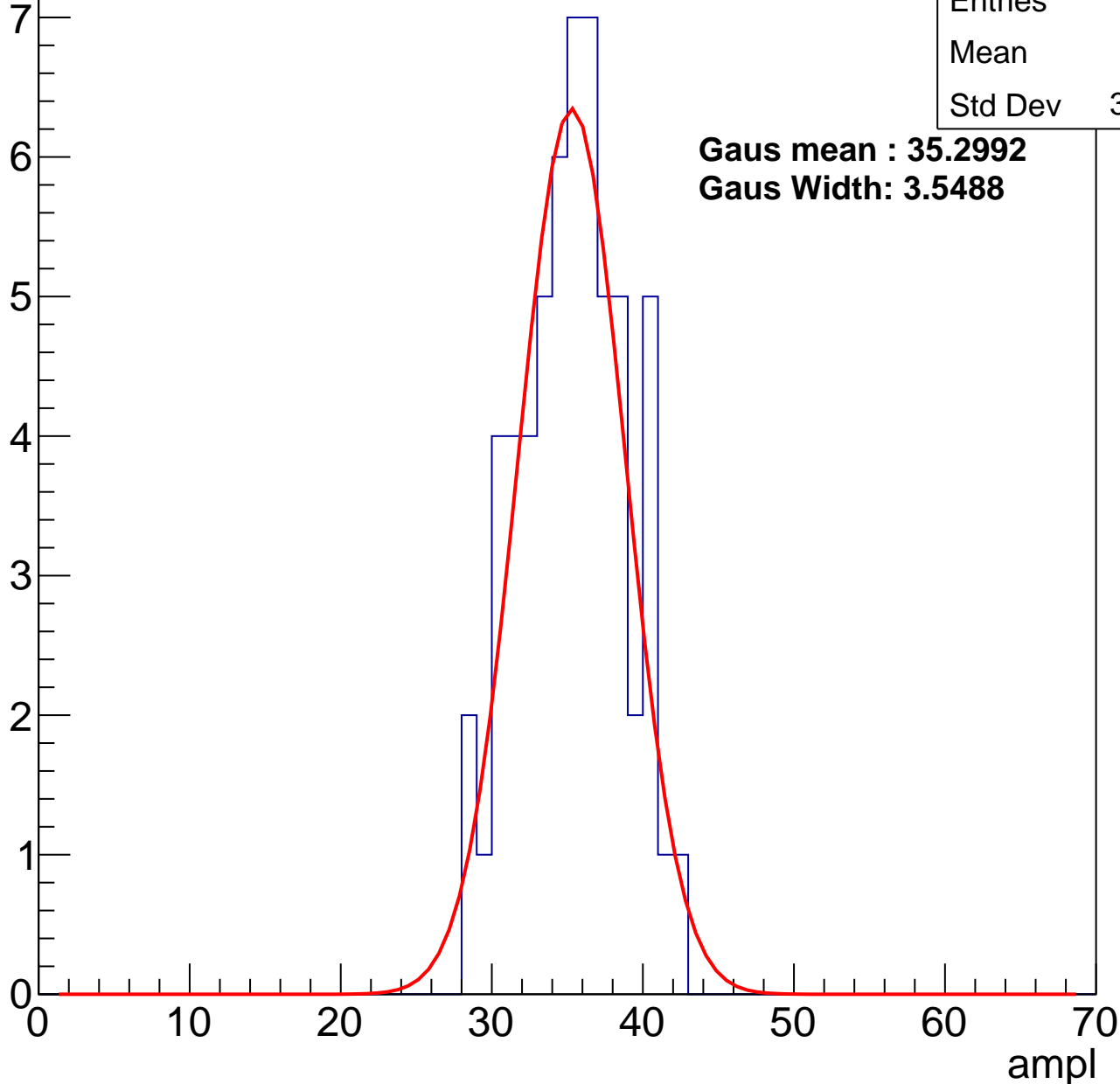
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	34.9
Std Dev	3.378

Gaus mean : 35.2992

Gaus Width: 3.5488



B1L102S, U19-ch10, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42
Std Dev	3.472

Gaus mean : 42.3116

Gaus Width: 3.5280

10

8

6

4

2

0

0

10

20

30

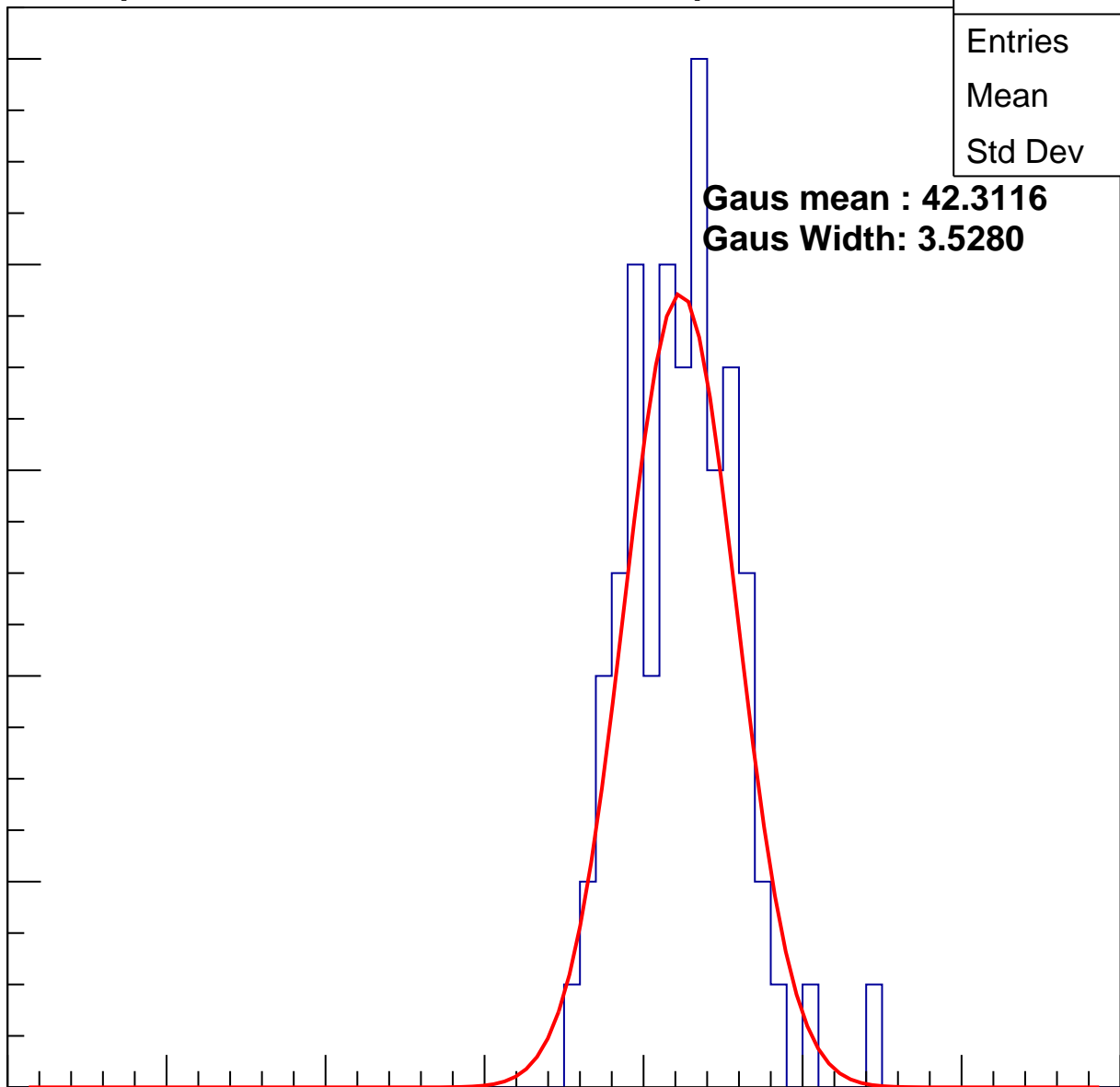
40

50

60

70

ampl

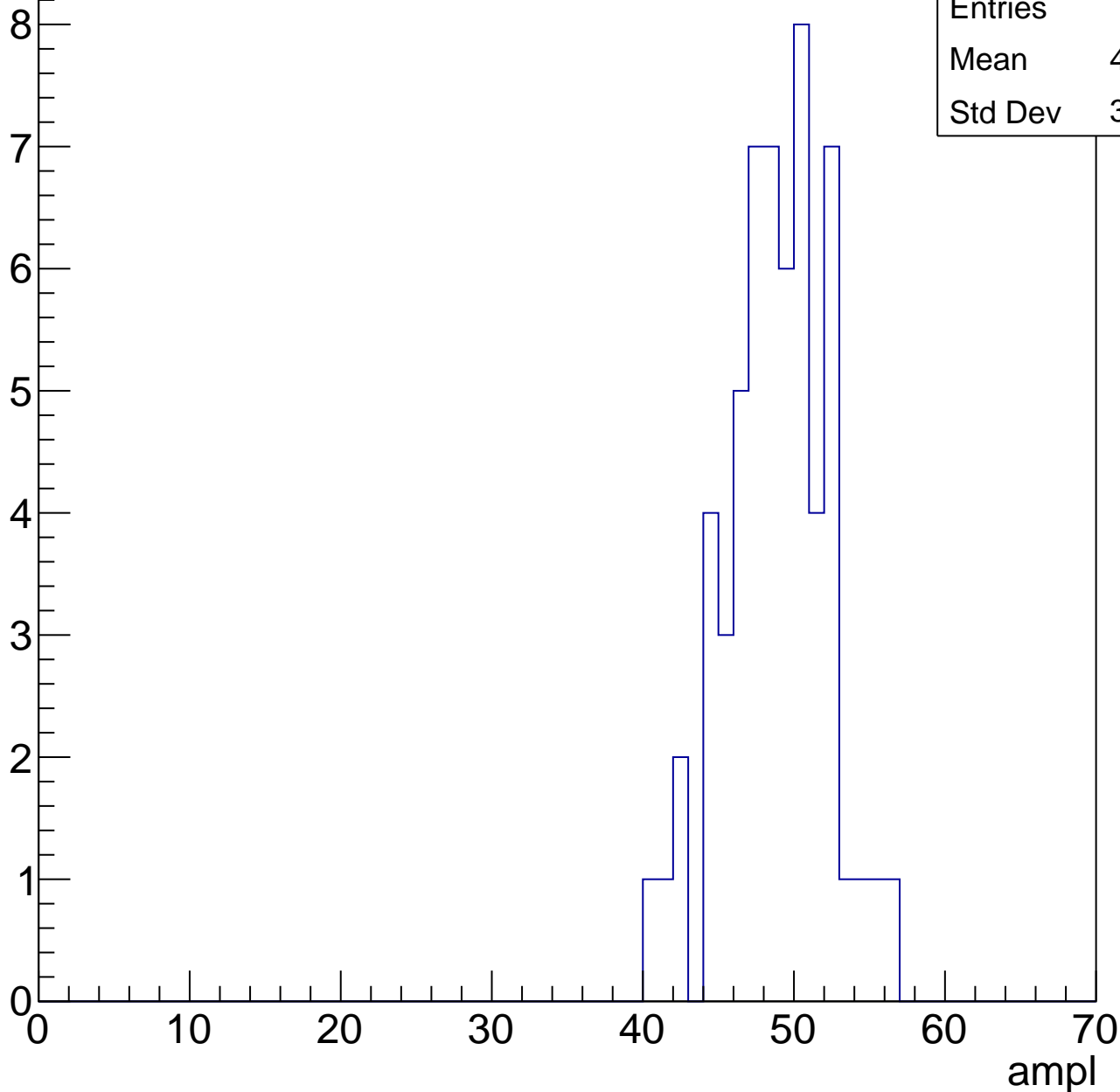


B1L102S, U19-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.32
Std Dev	3.337

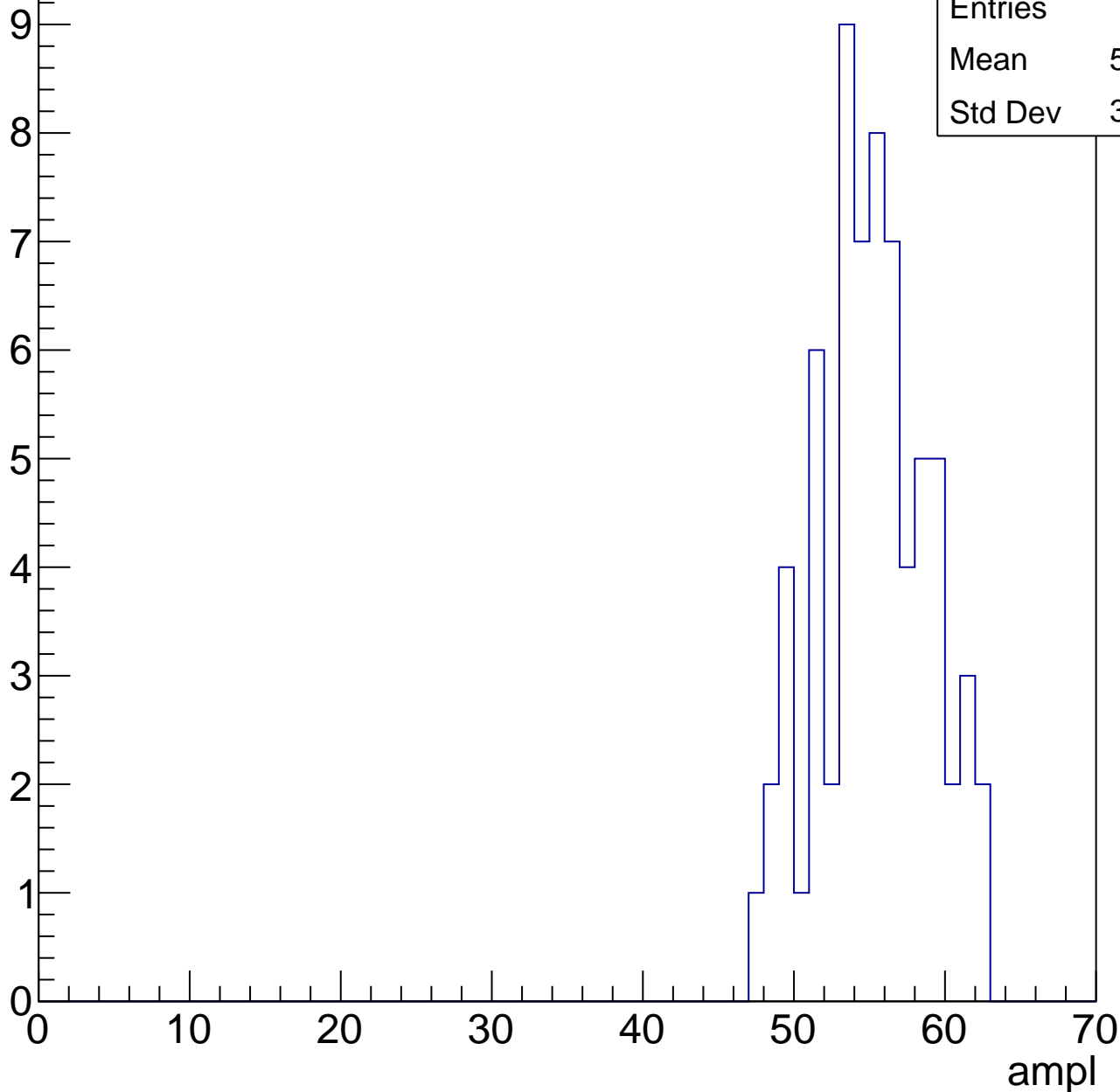


B1L102S, U19-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.79
Std Dev	3.616



B1L102S, U19-ch10, adc5

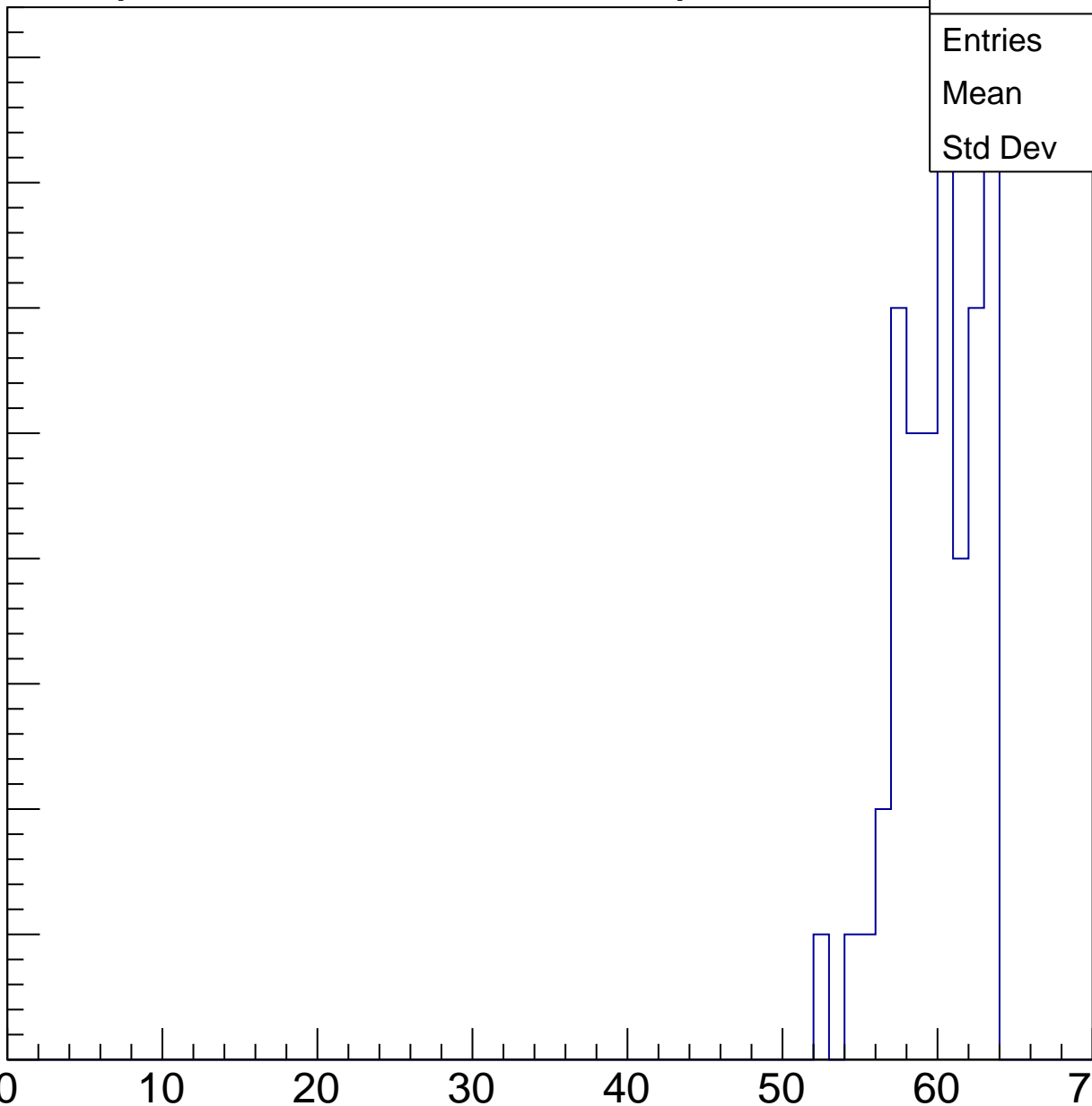
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.57
Std Dev	2.64

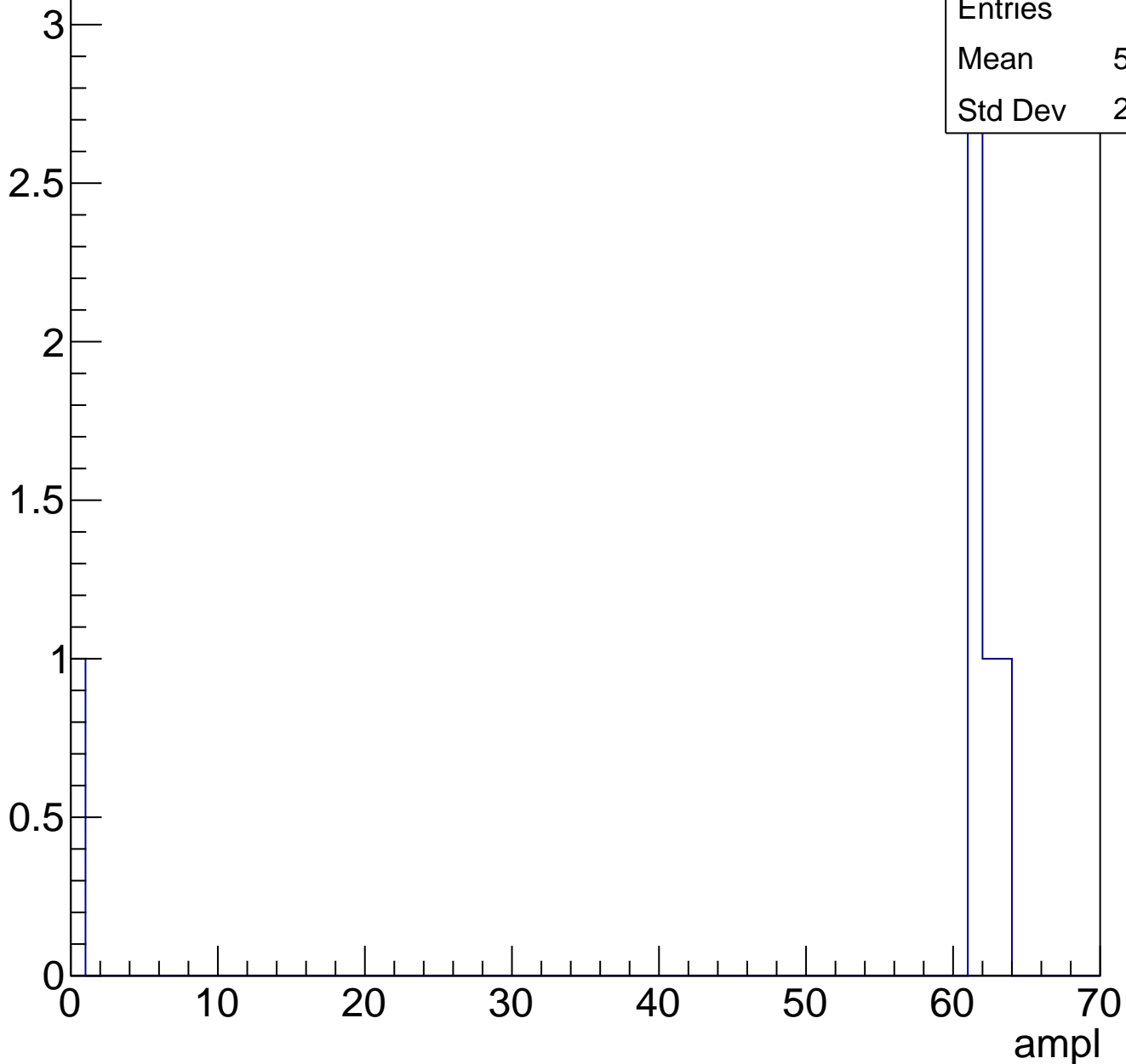
ampl



B1L102S, U19-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

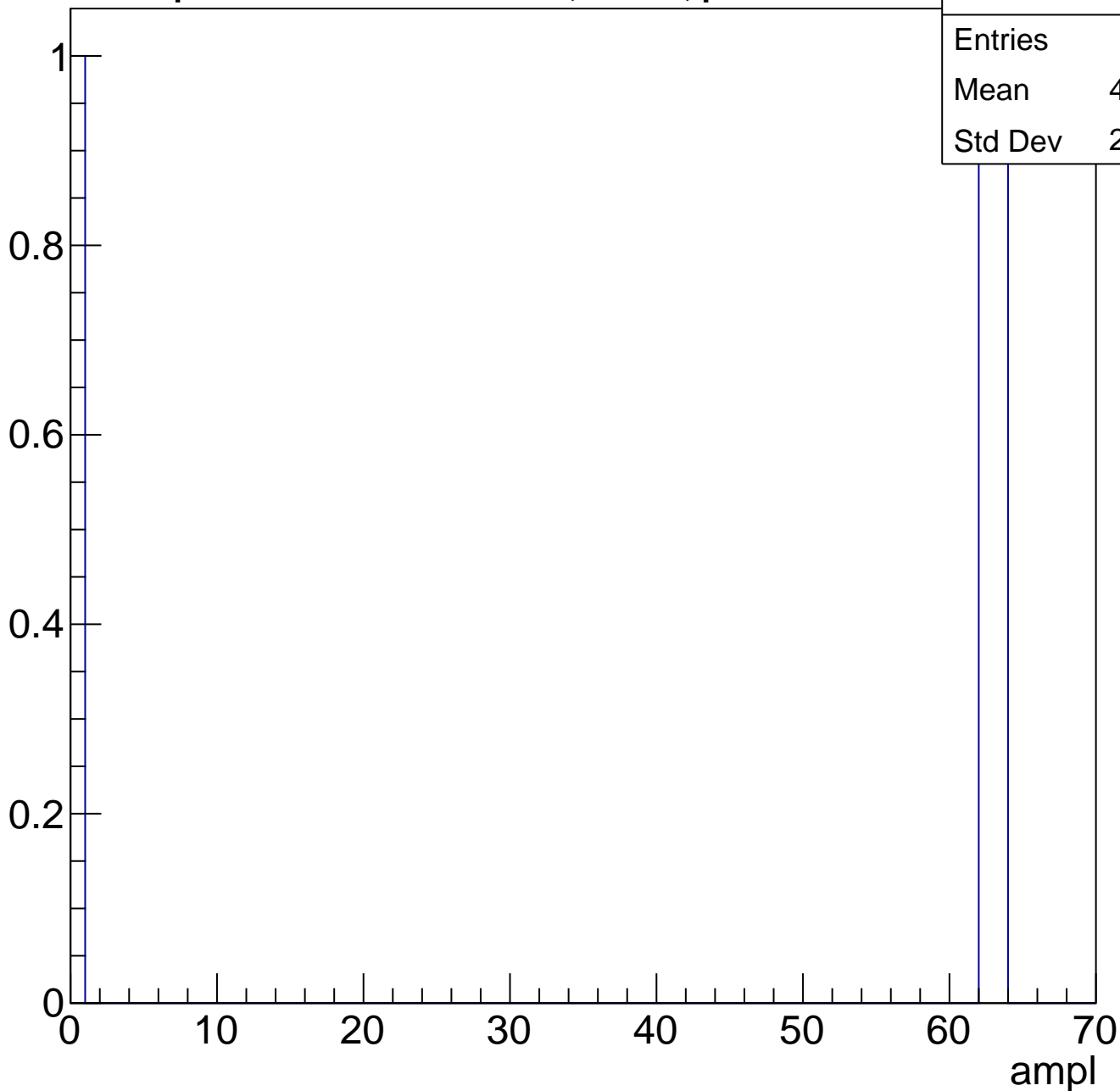
Entry



B1L102S, U19-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch11, adc0

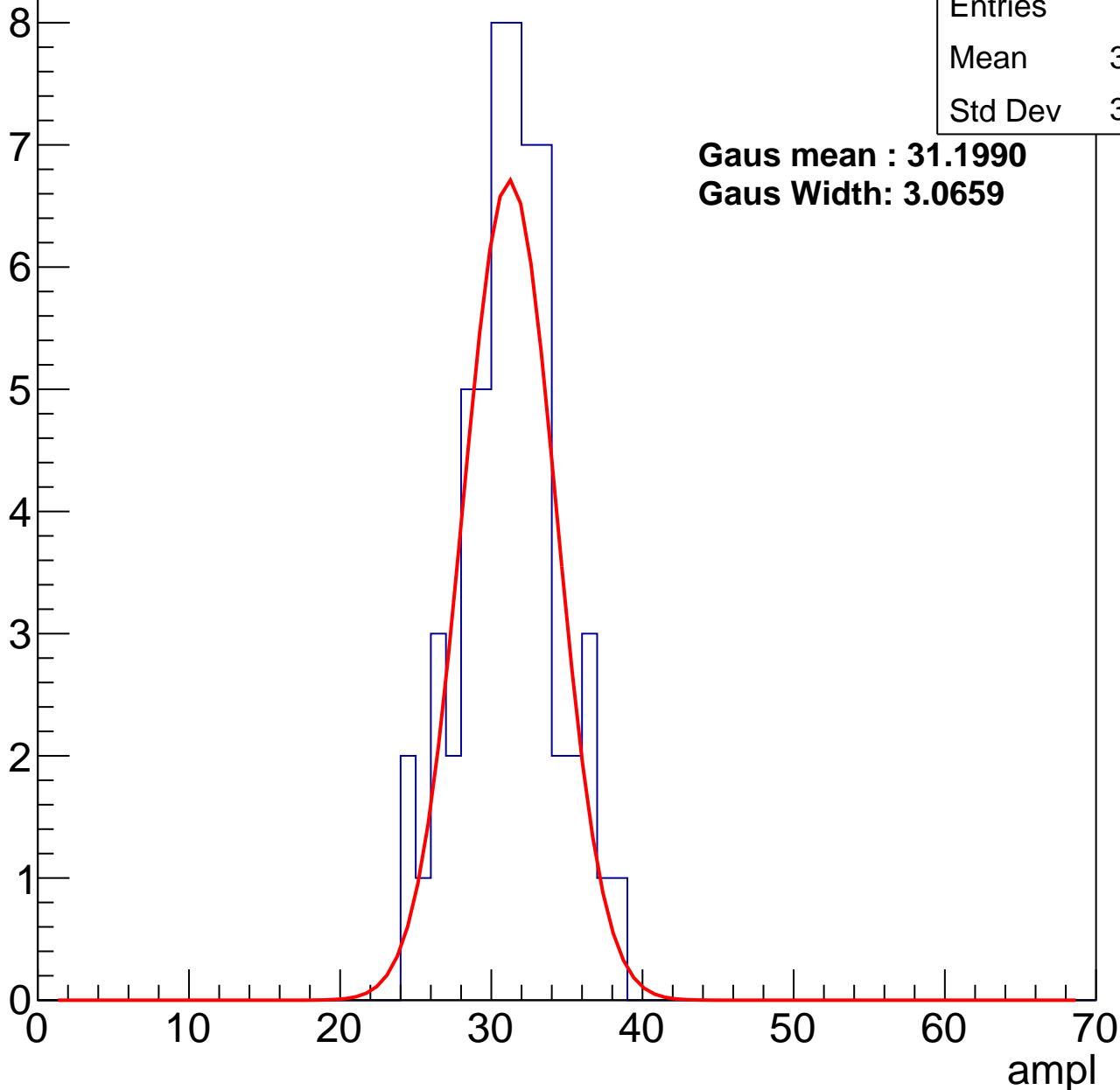
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	30.77
Std Dev	3.123

Gaus mean : 31.1990

Gaus Width: 3.0659



B1L102S, U19-ch11, adc1

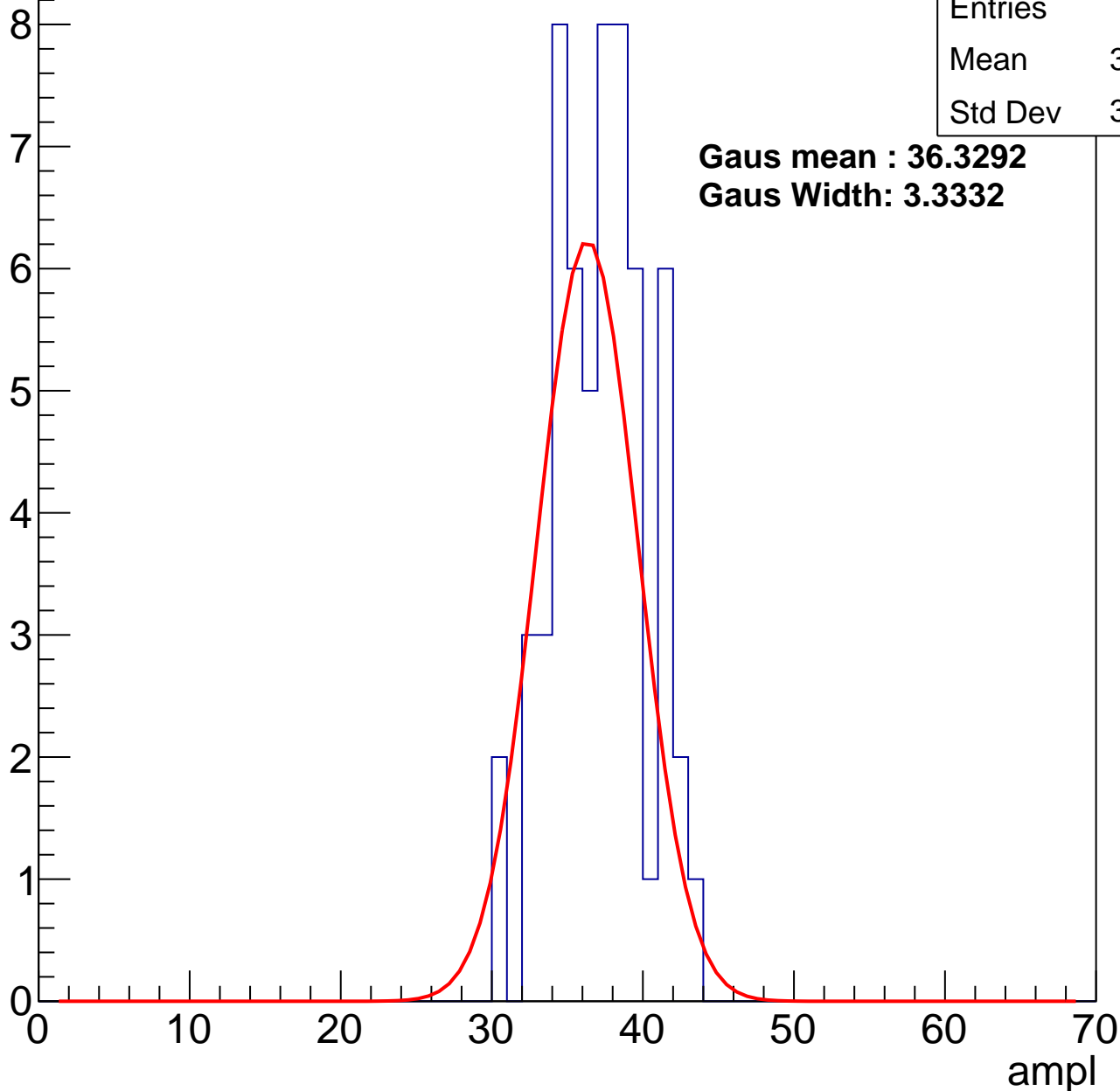
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.68
Std Dev	3.022

Gaus mean : 36.3292

Gaus Width: 3.3332



B1L102S, U19-ch11, adc2

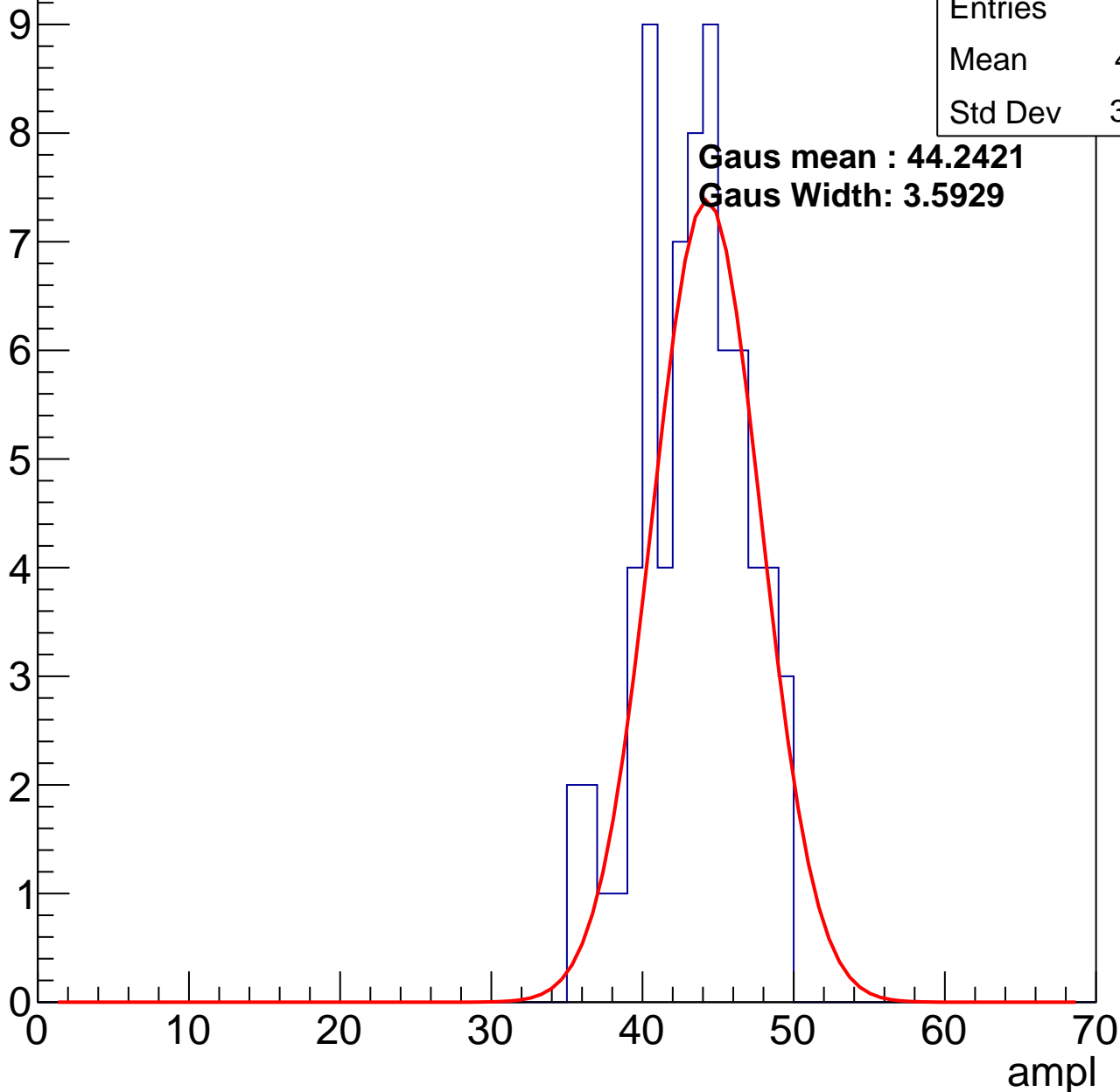
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.91
Std Dev	3.409

Gaus mean : 44.2421

Gaus Width: 3.5929

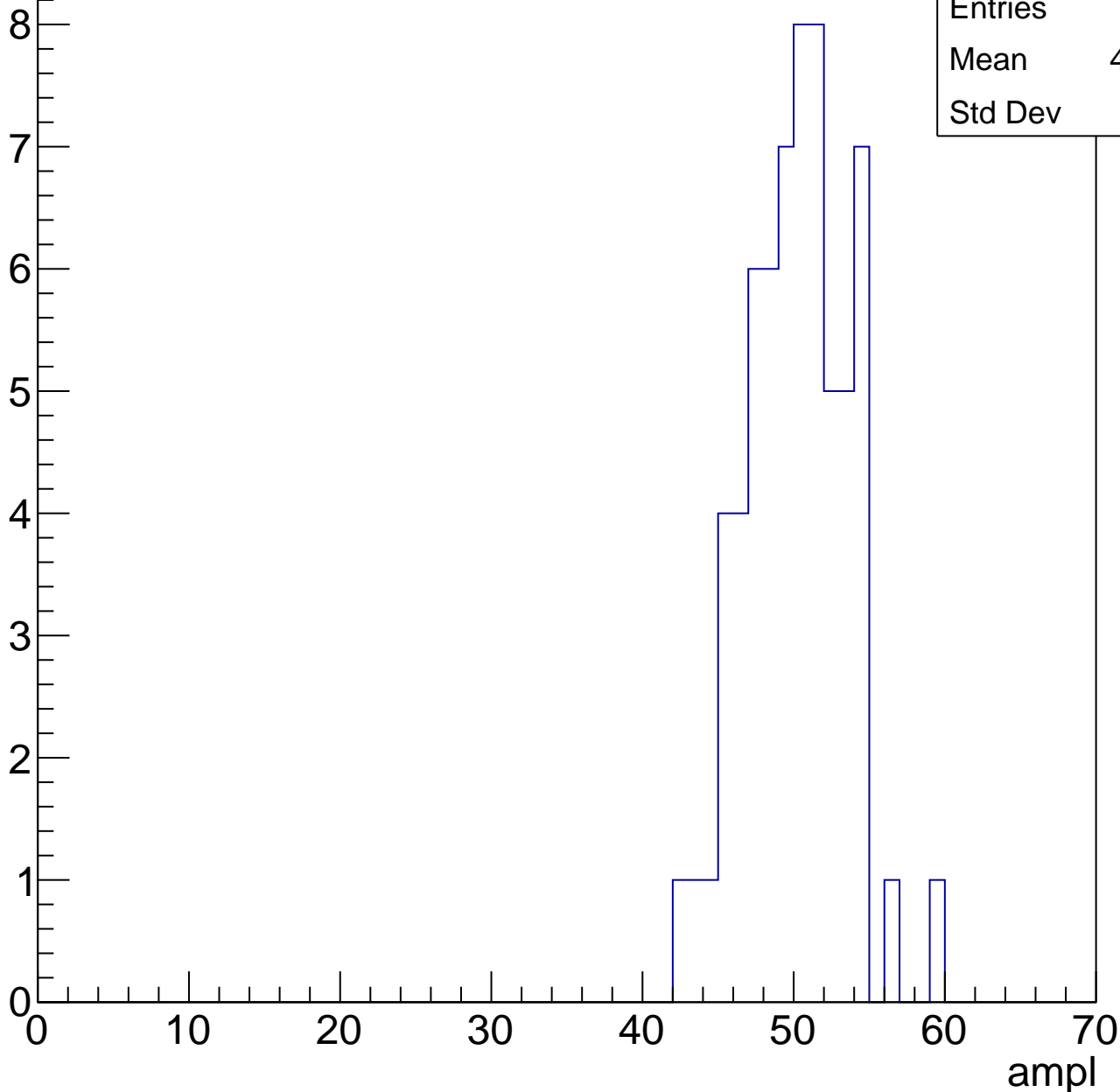


B1L102S, U19-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

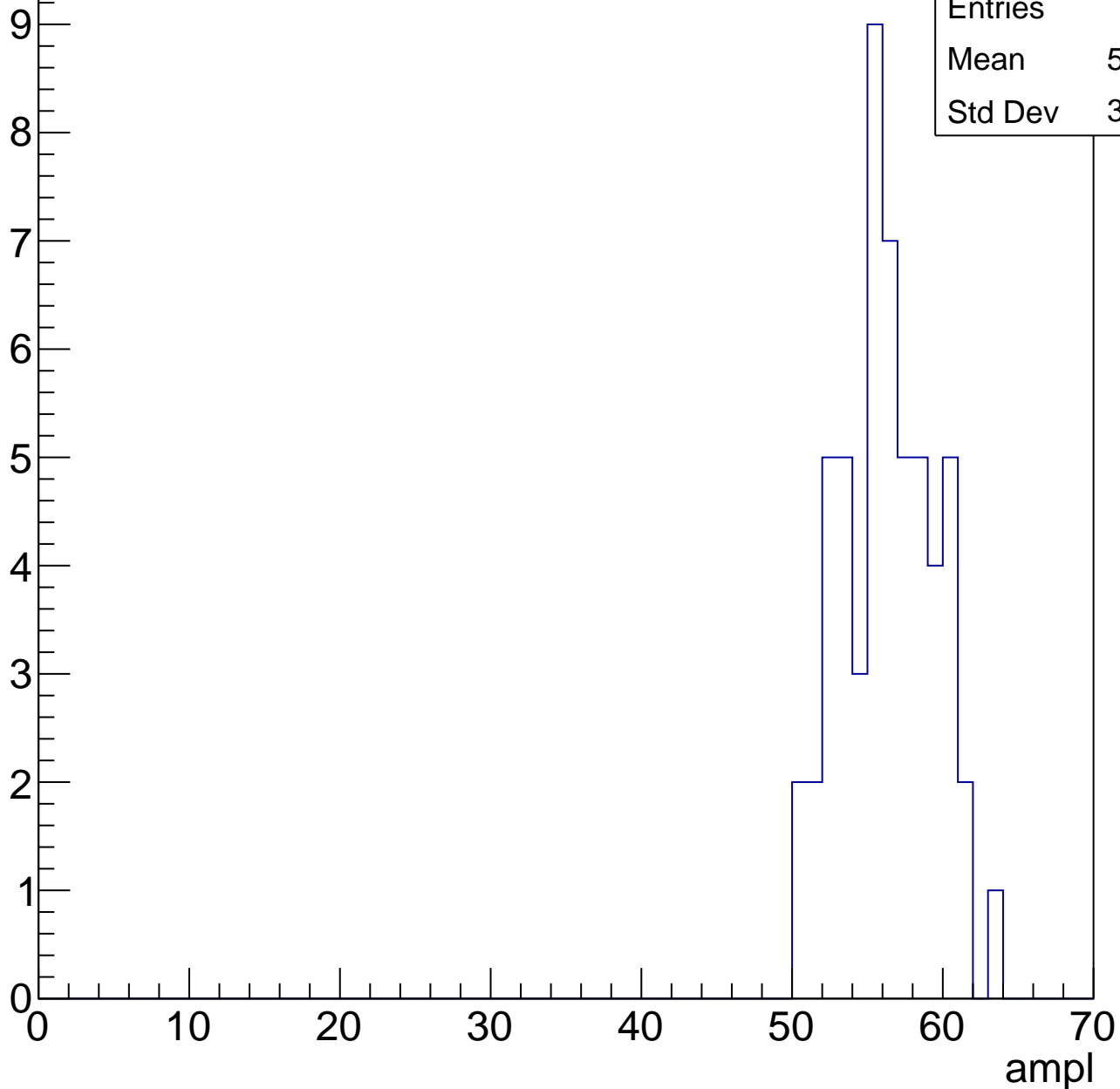
Entries	65
Mean	49.72
Std Dev	3.27



B1L102S, U19-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

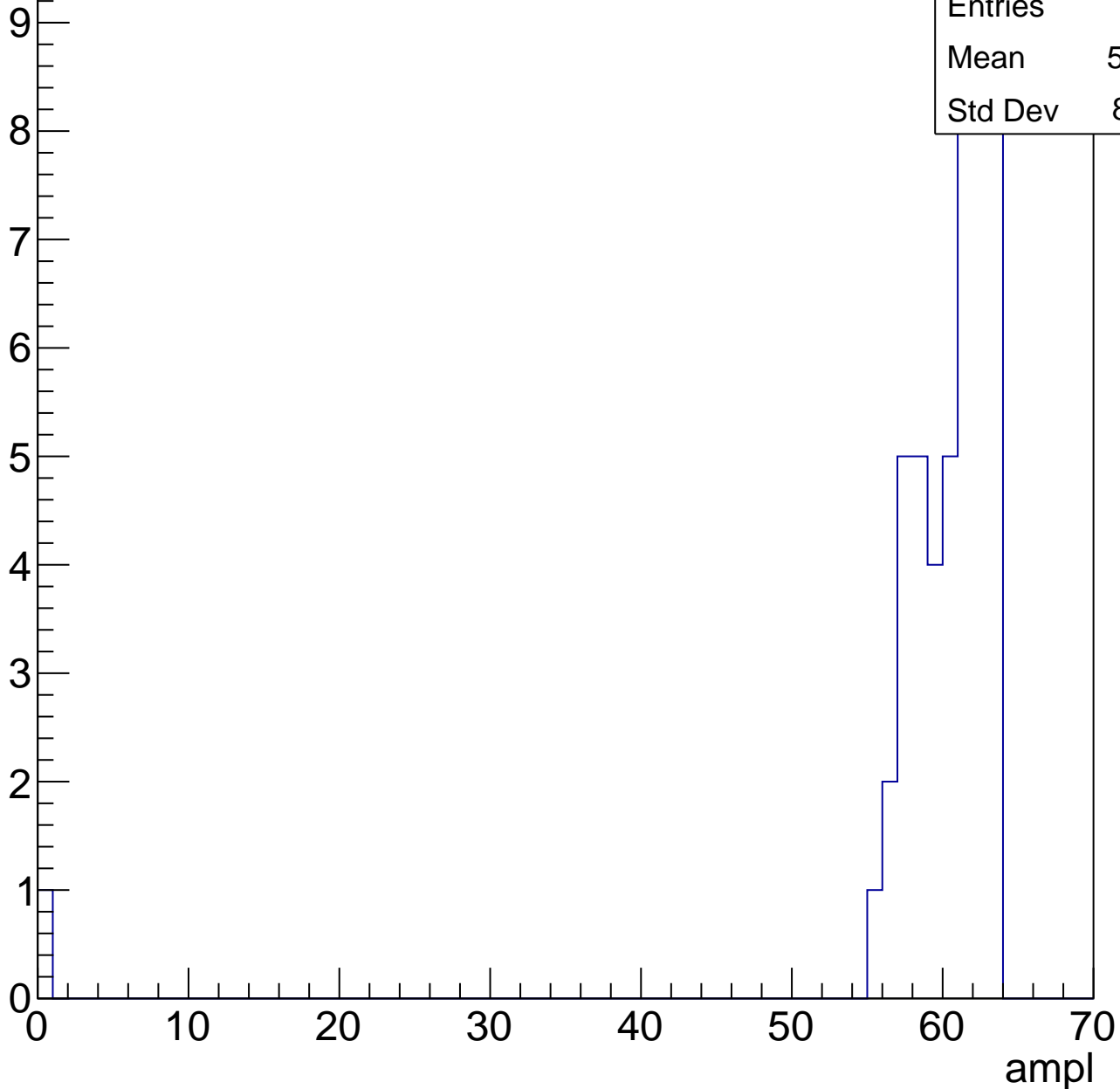


B1L102S, U19-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.92
Std Dev	8.881



B1L102S, U19-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch12, adc0

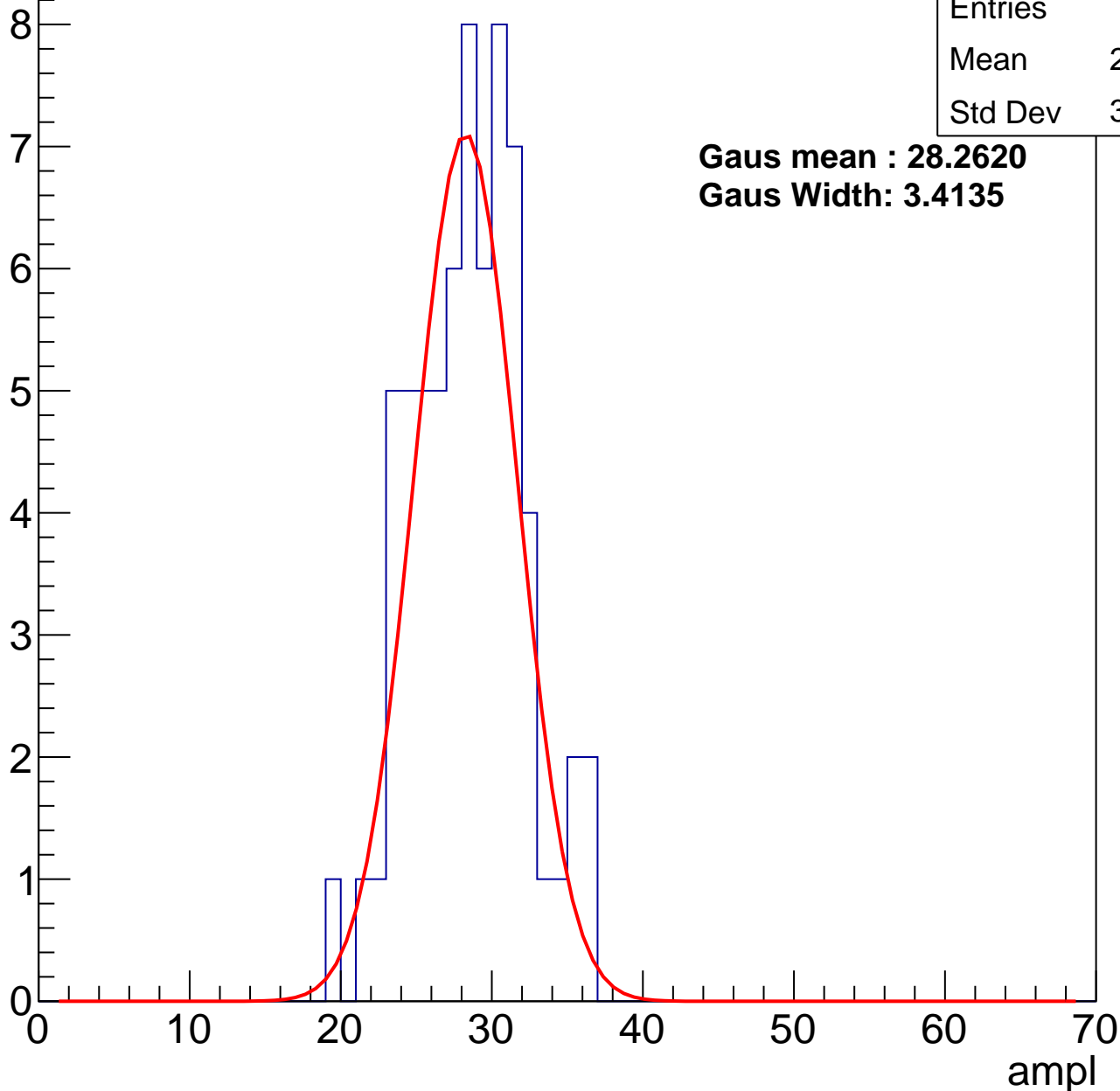
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.03
Std Dev	3.626

Gaus mean : 28.2620

Gaus Width: 3.4135



B1L102S, U19-ch12, adc1

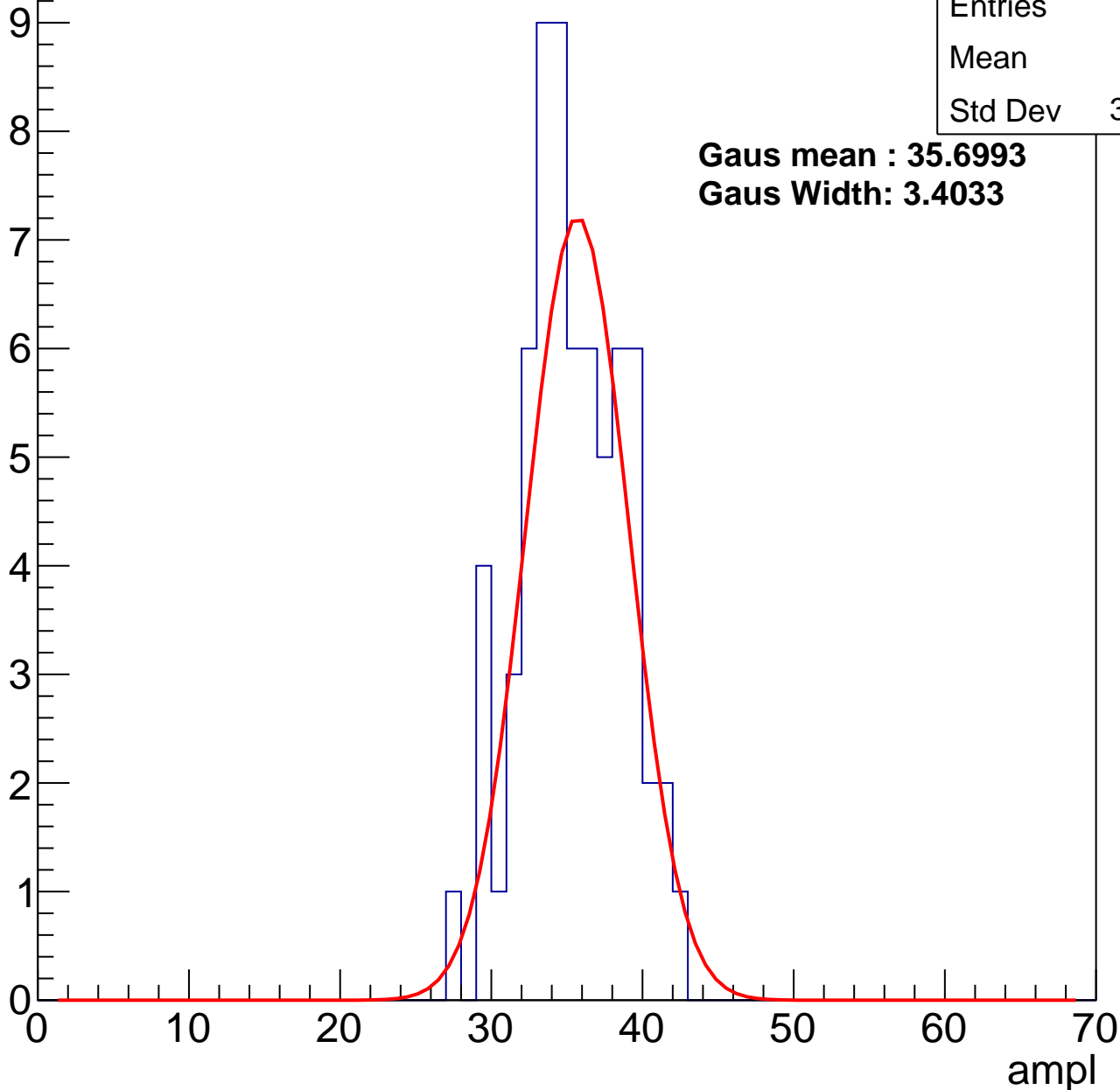
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.9
Std Dev	3.292

Gaus mean : 35.6993

Gaus Width: 3.4033



B1L102S, U19-ch12, adc2

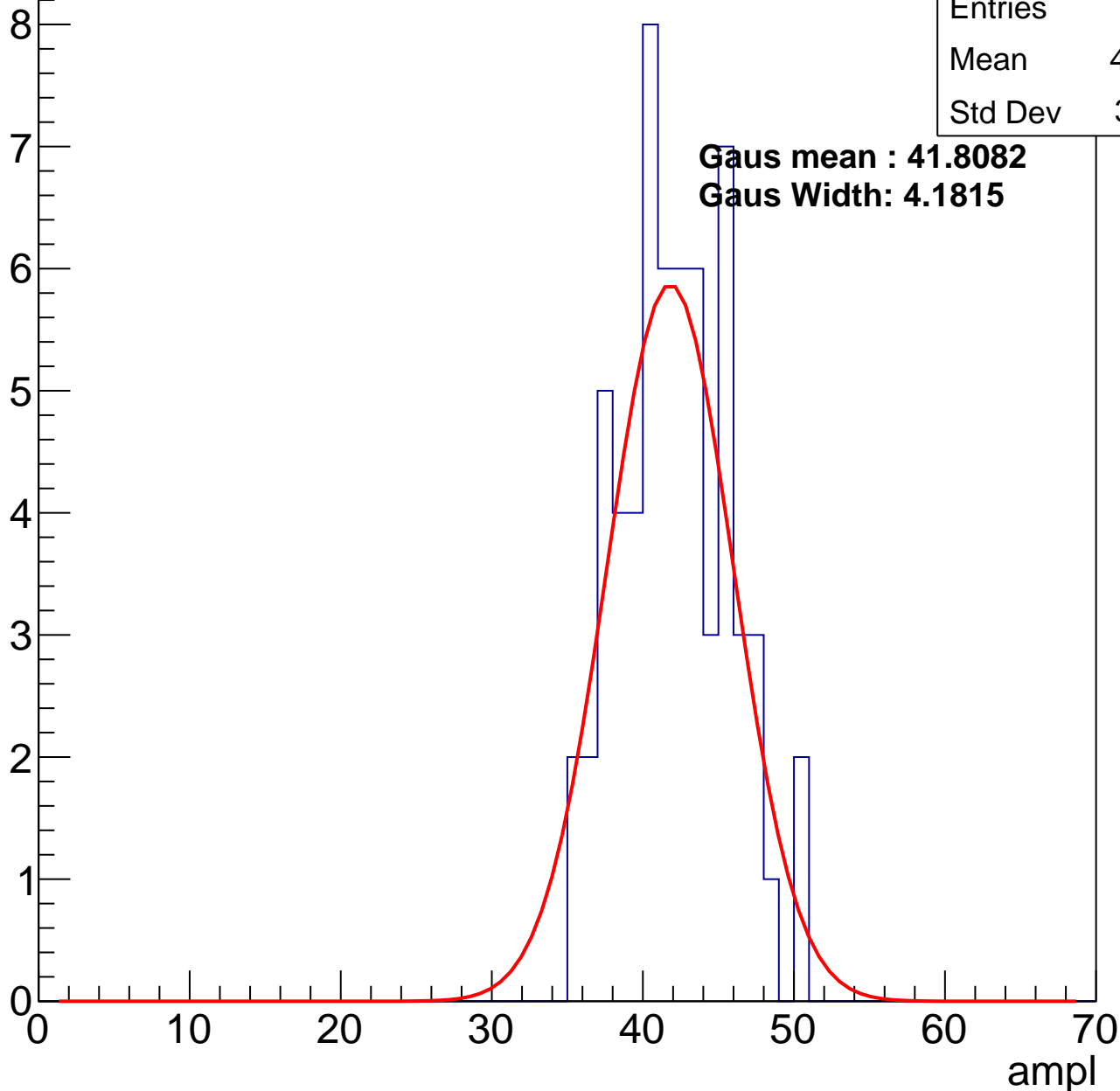
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.69
Std Dev	3.581

Gaus mean : 41.8082

Gaus Width: 4.1815



B1L102S, U19-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

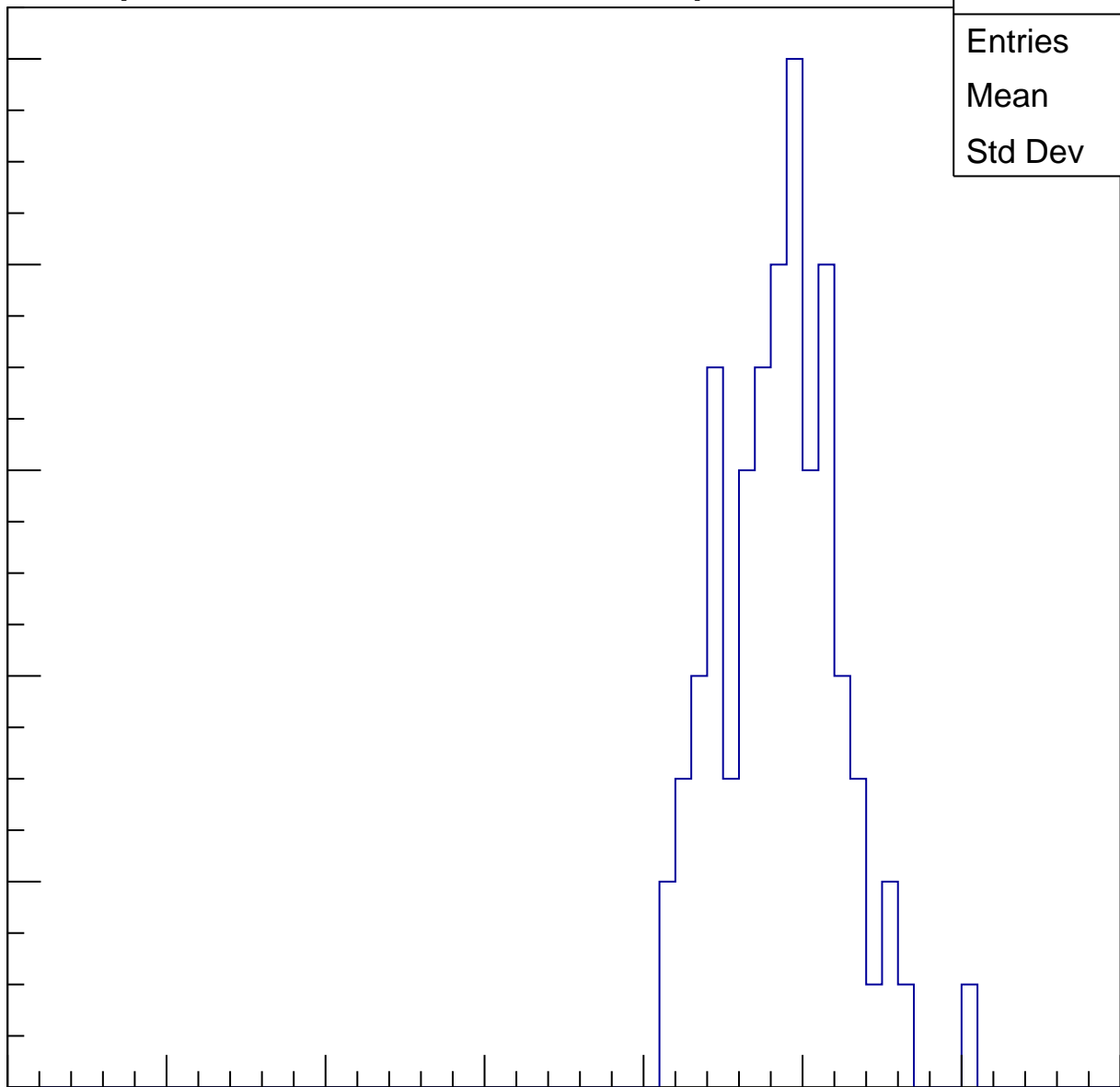
Entries	76
Mean	48.12
Std Dev	3.738

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

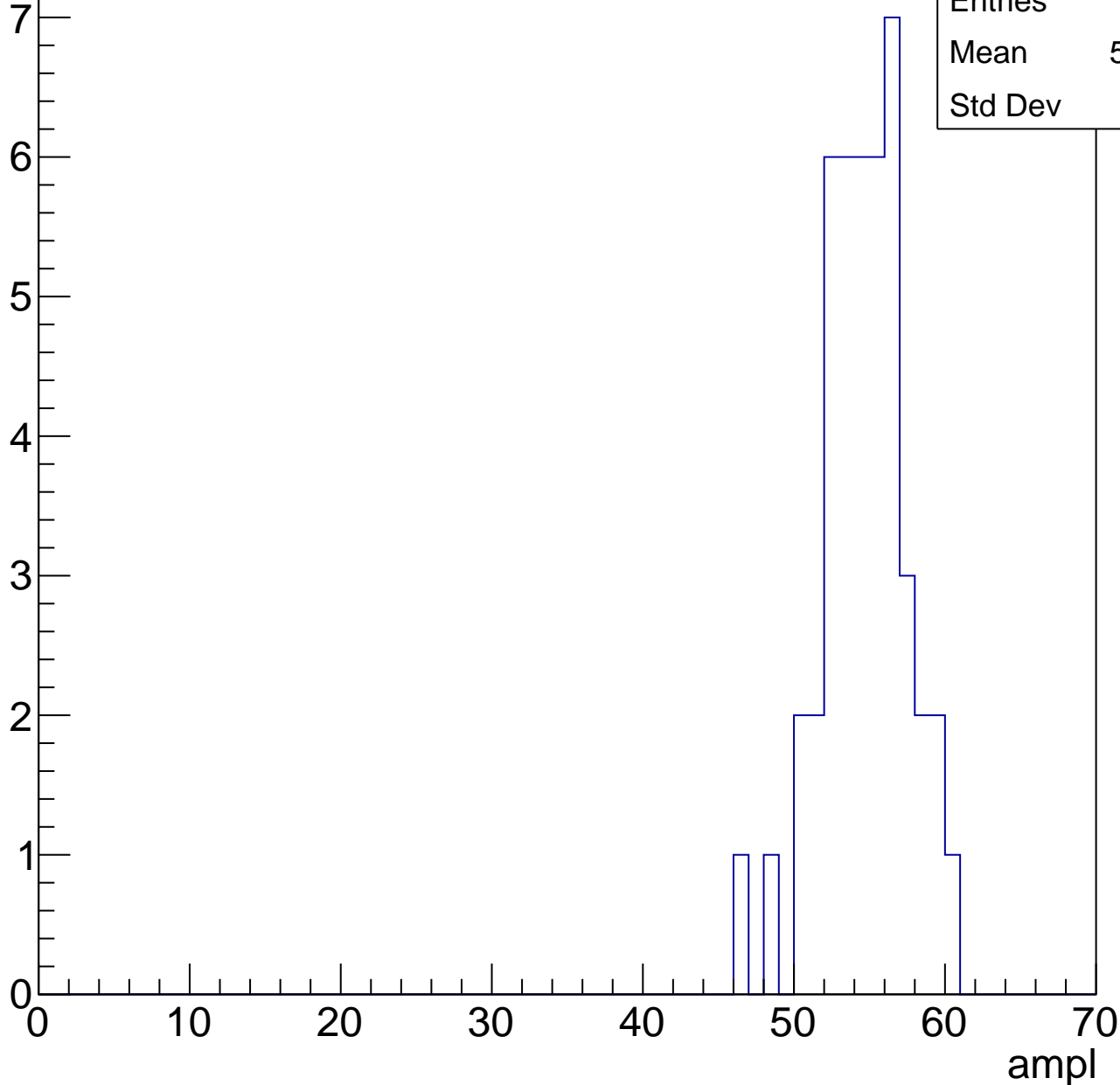


B1L102S, U19-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	54.16
Std Dev	2.82

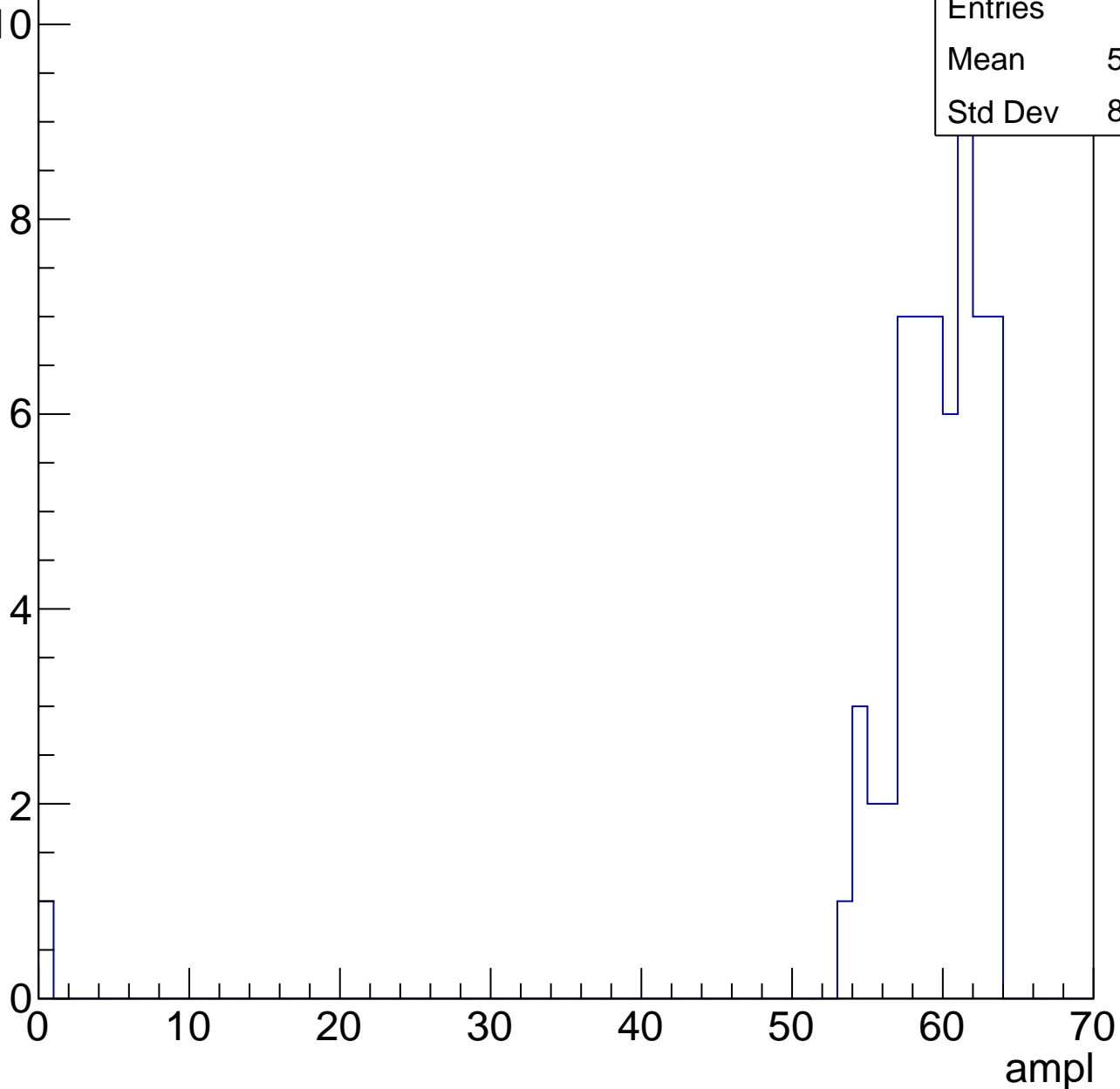


B1L102S, U19-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.33
Std Dev	8.033



B1L102S, U19-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.2

Std Dev

1.72

3

2.5

2

1.5

1

0.5

0

10

20

30

40

50

60

70

ampl

B1L102S, U19-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch13, adc0

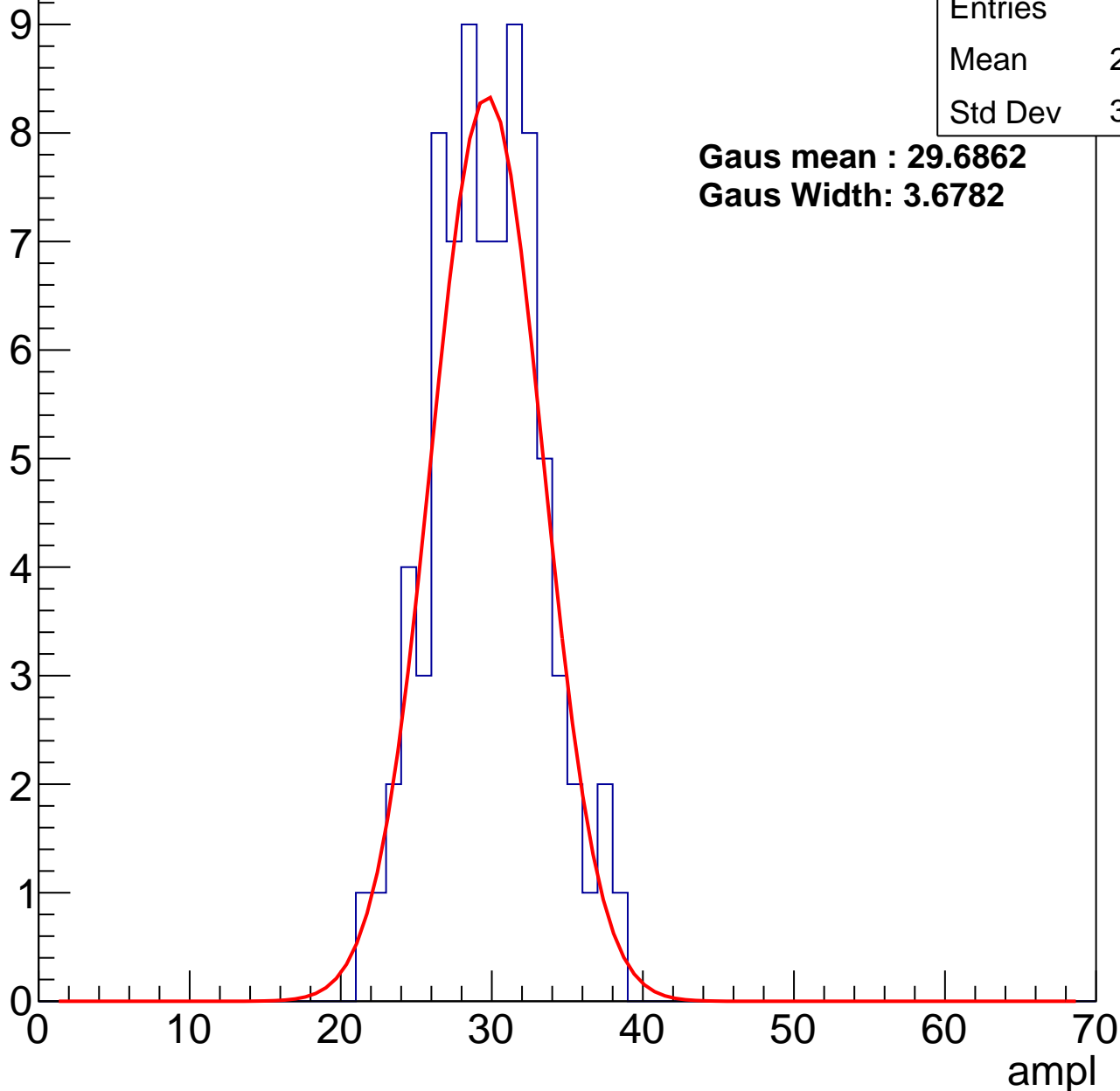
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.27
Std Dev	3.599

Gaus mean : 29.6862

Gaus Width: 3.6782



B1L102S, U19-ch13, adc1

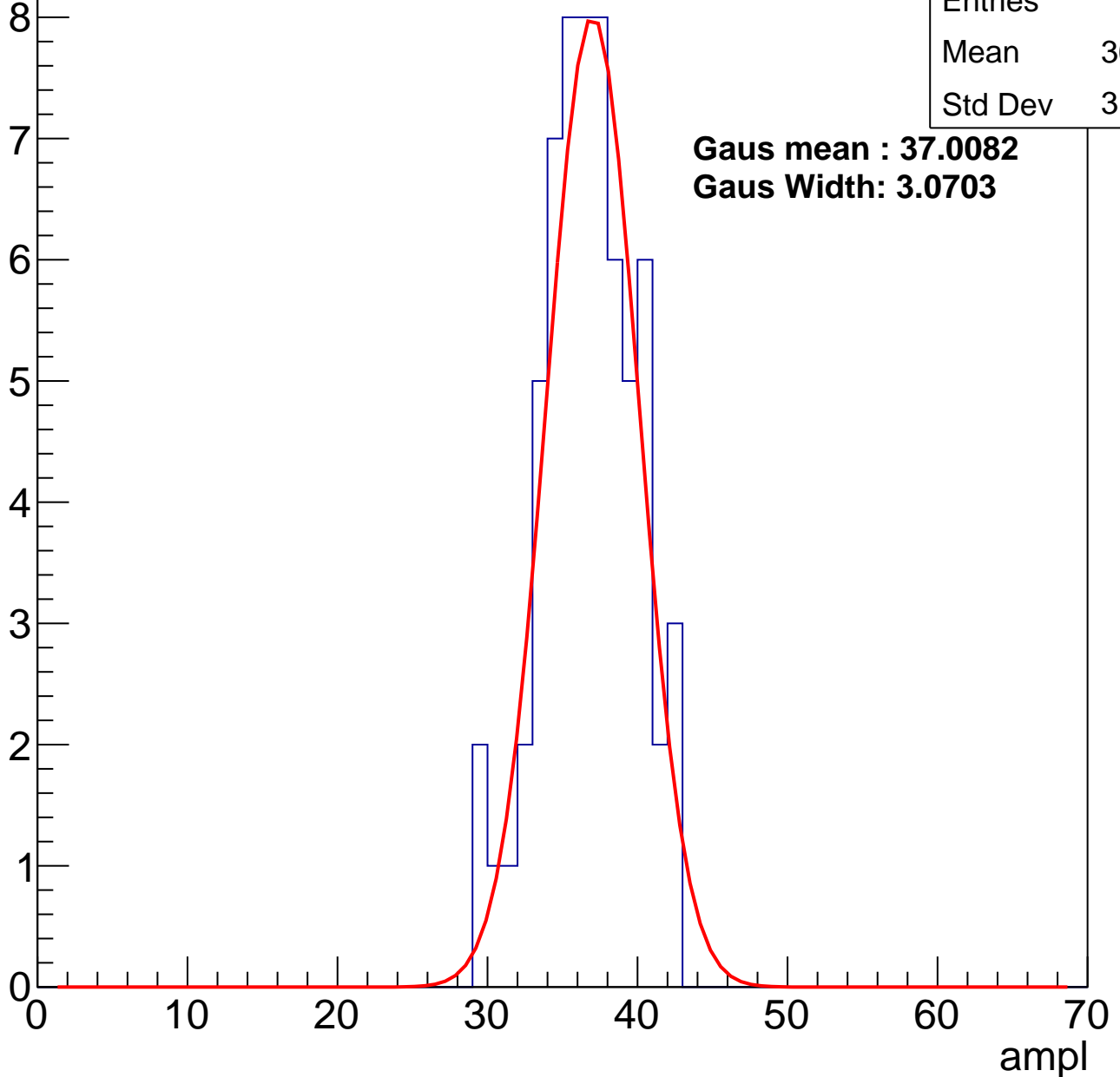
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.27
Std Dev	3.058

Gaus mean : 37.0082

Gaus Width: 3.0703



B1L102S, U19-ch13, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.05
Std Dev	2.825

Gaus mean : 43.3050

Gaus Width: 3.1788

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

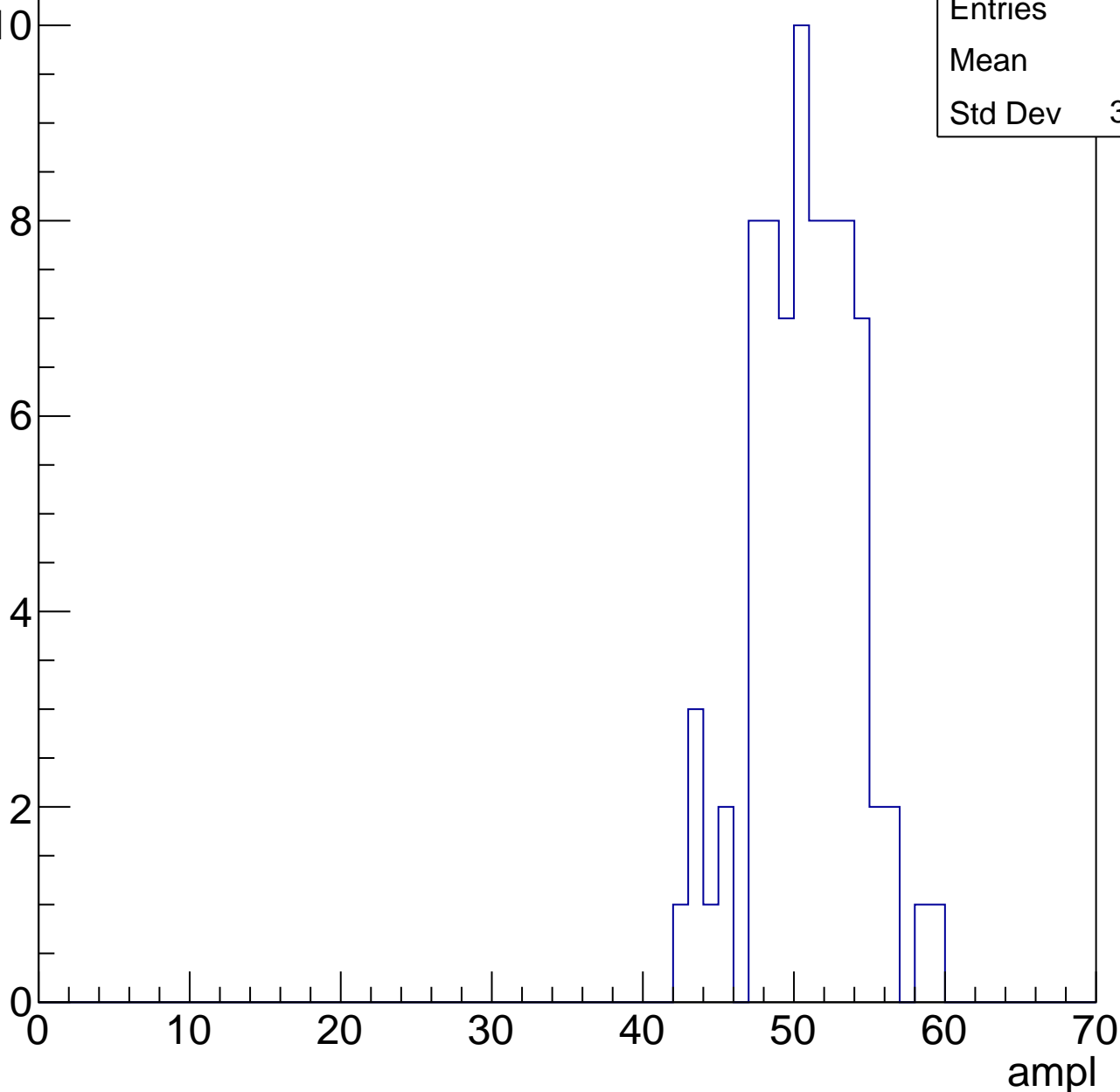


B1L102S, U19-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	50.3
Std Dev	3.404

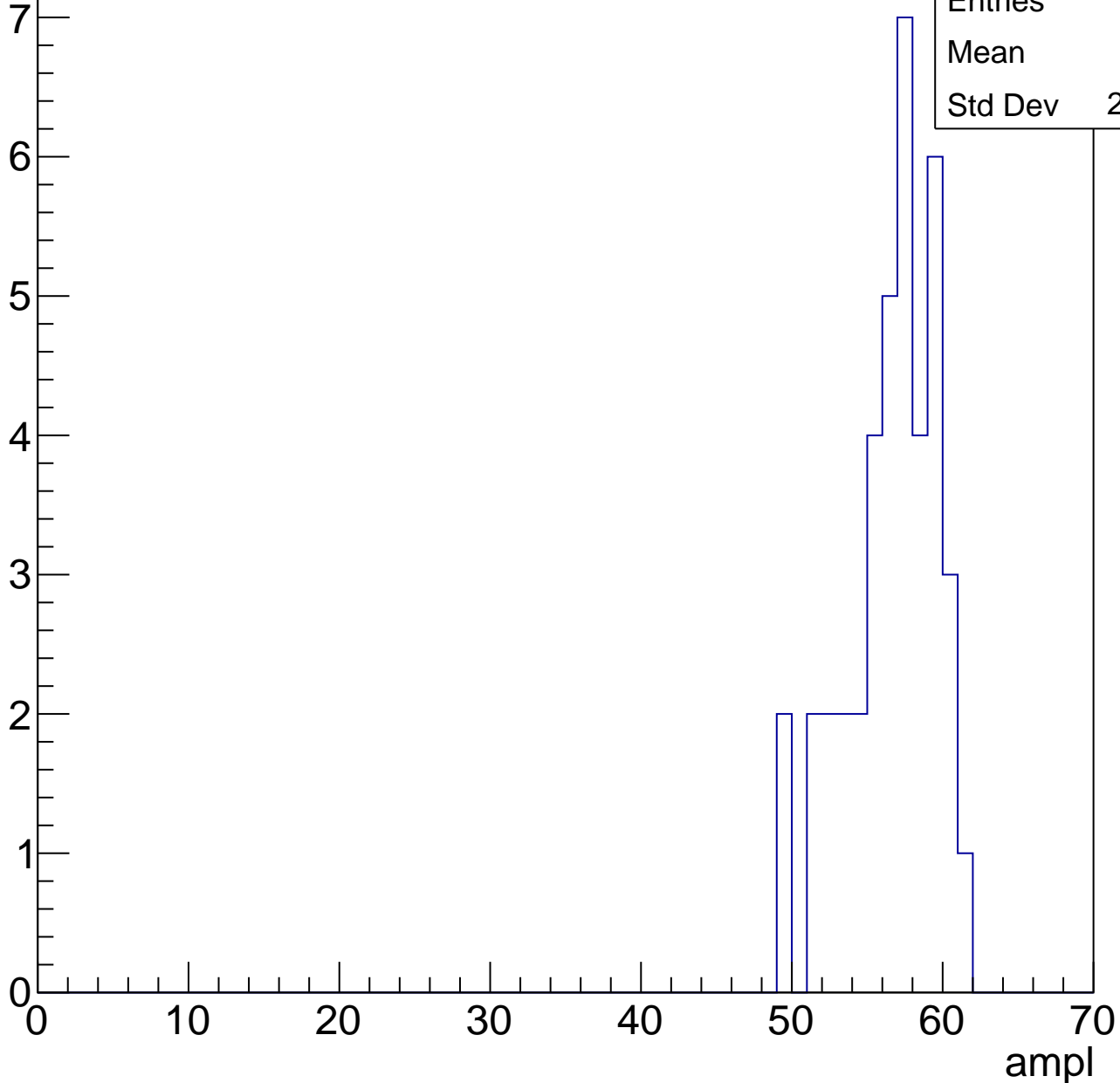


B1L102S, U19-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	56.1
Std Dev	2.998

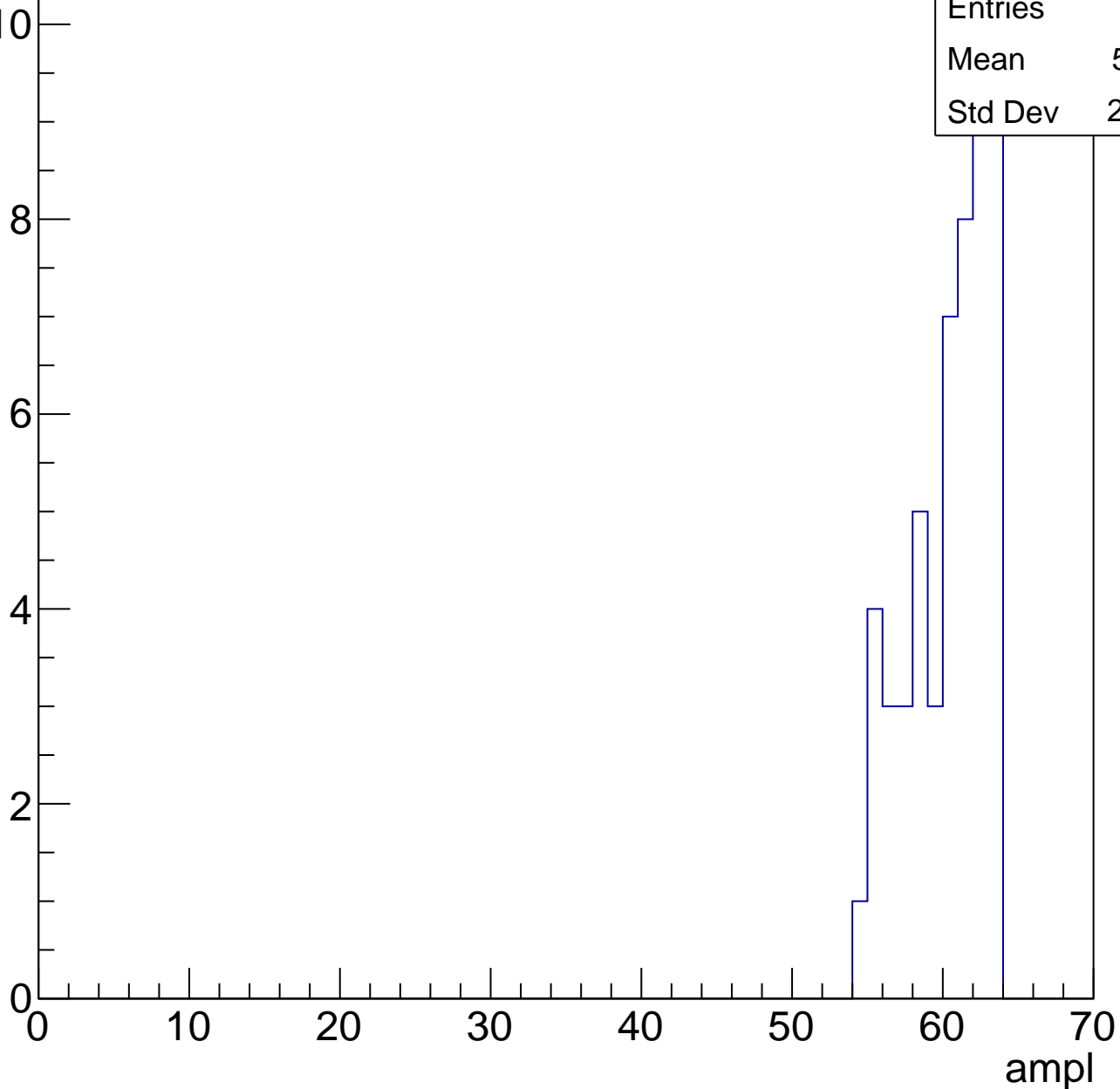


B1L102S, U19-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

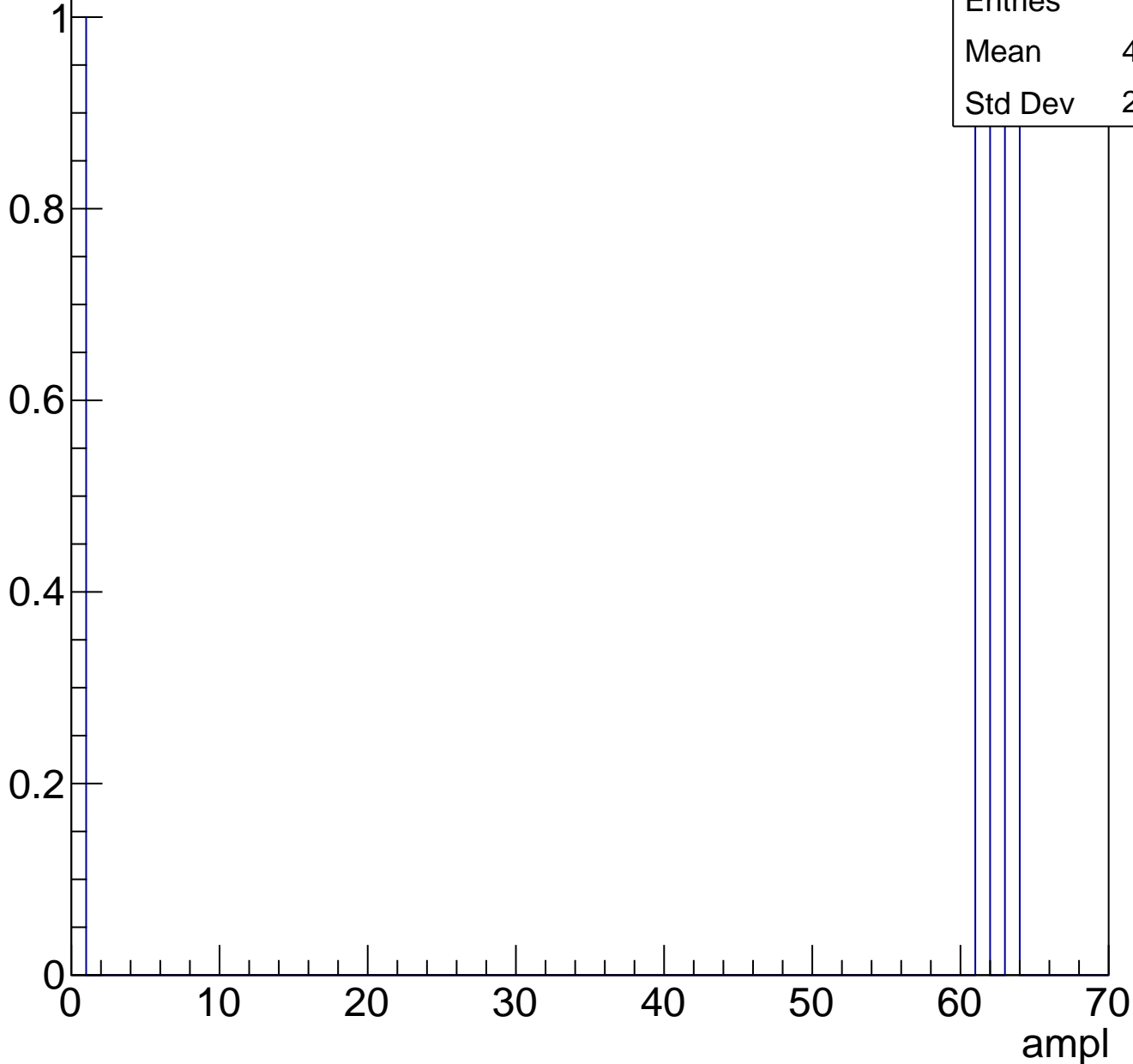
Entries	53
Mean	59.91
Std Dev	2.615



B1L102S, U19-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch14, adc0

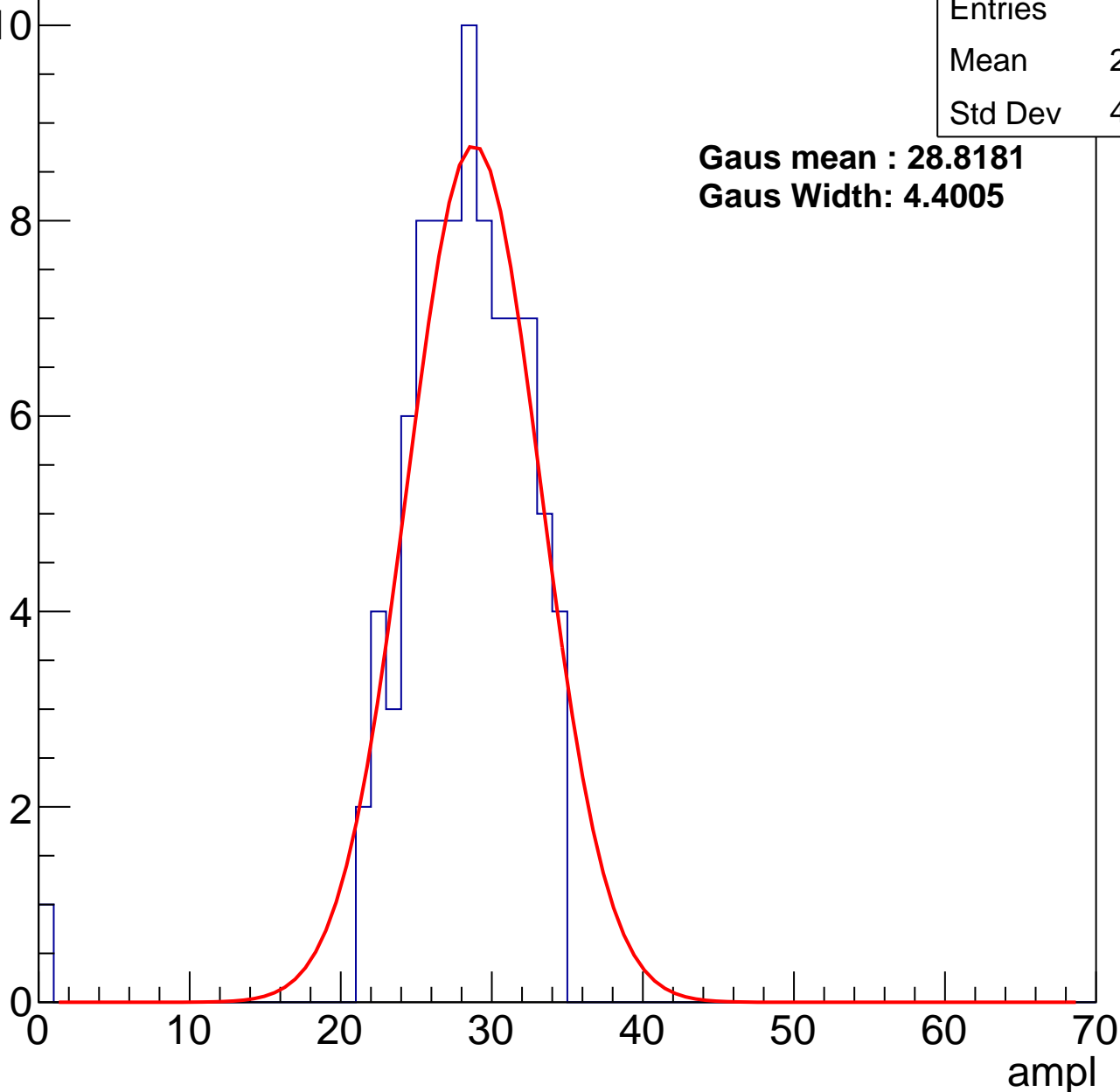
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	27.62
Std Dev	4.493

Gaus mean : 28.8181

Gaus Width: 4.4005



B1L102S, U19-ch14, adc1

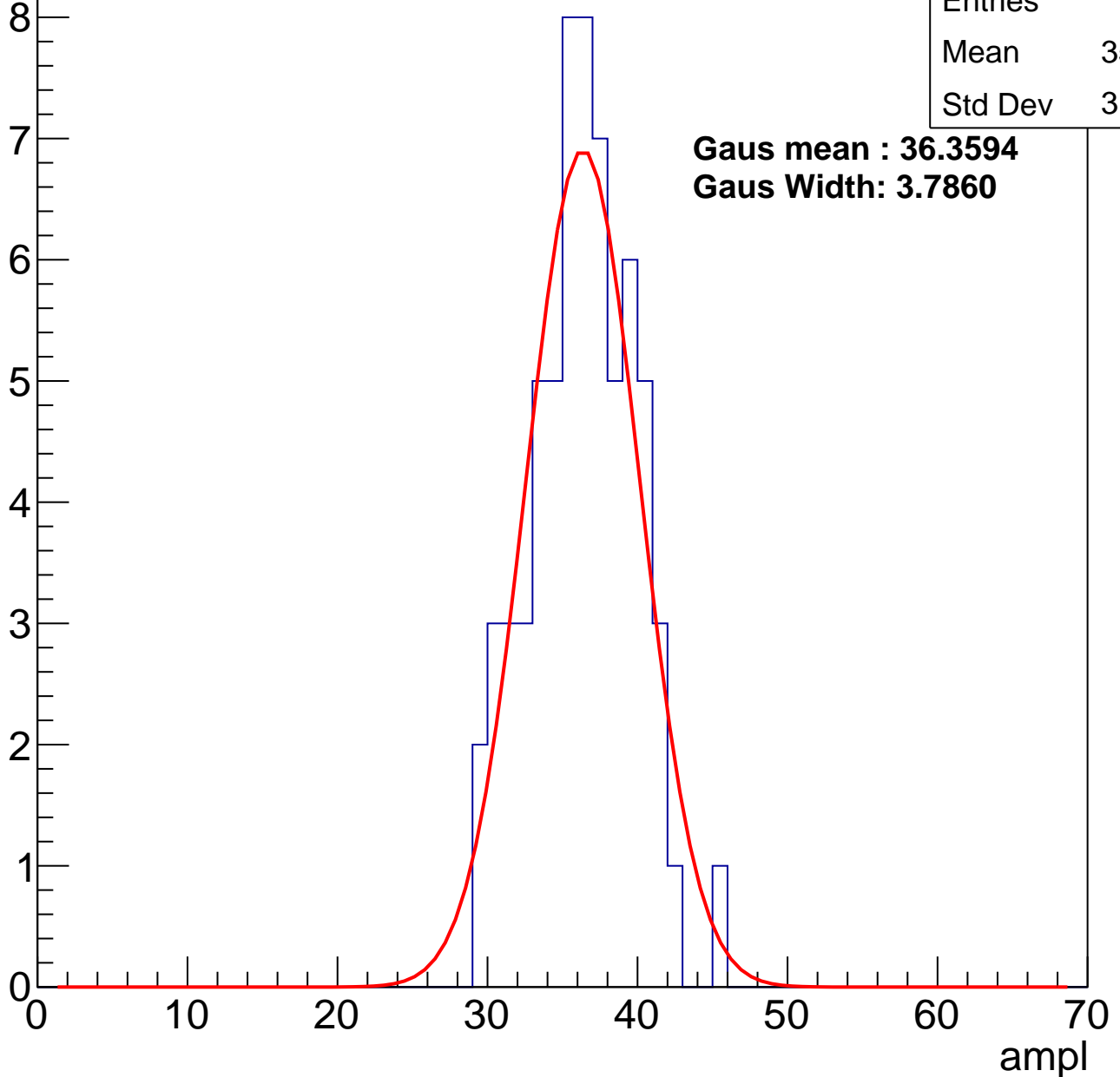
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.89
Std Dev	3.406

Gaus mean : 36.3594

Gaus Width: 3.7860



B1L102S, U19-ch14, adc2

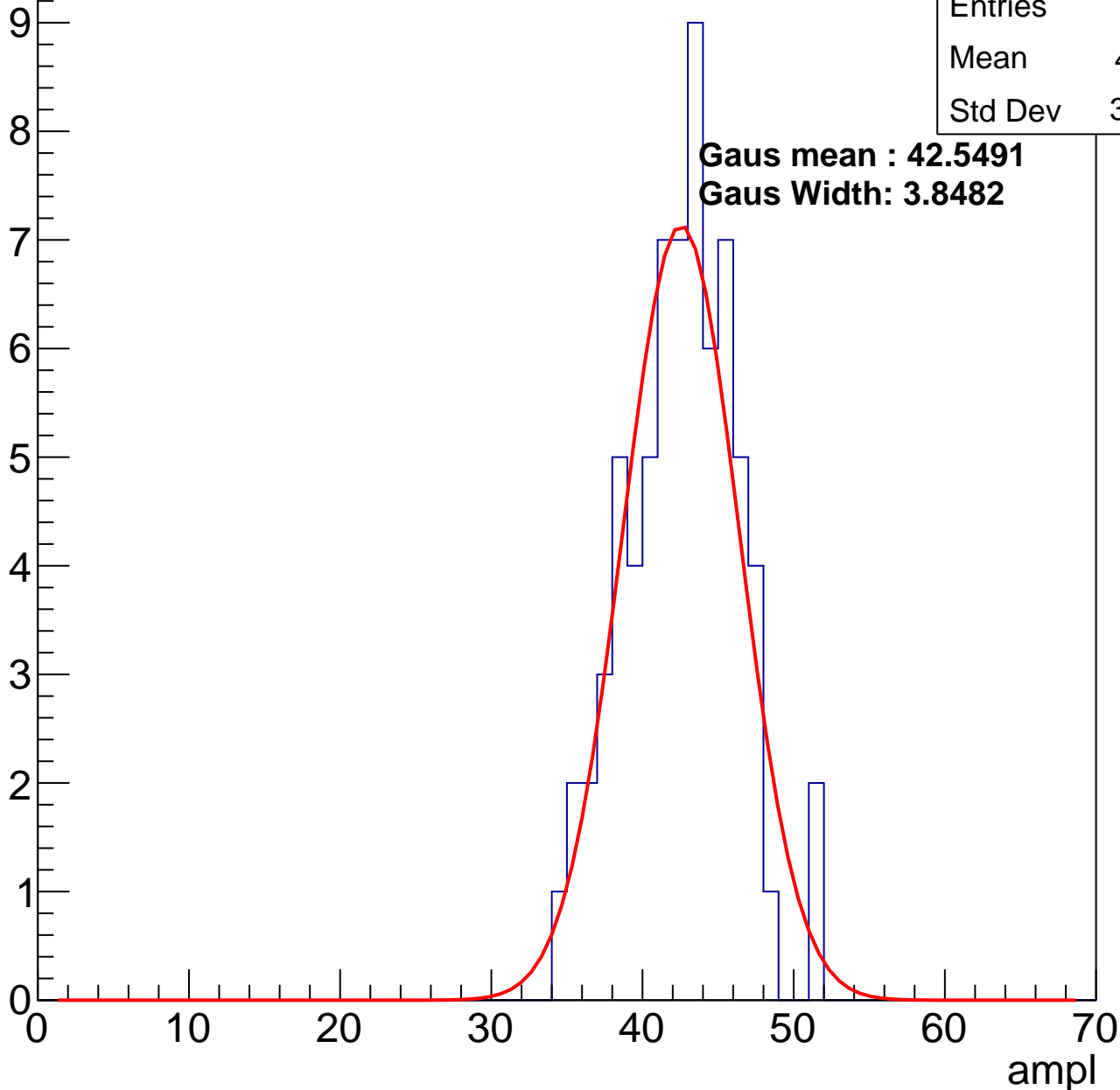
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.11
Std Dev	3.635

Gaus mean : 42.5491

Gaus Width: 3.8482

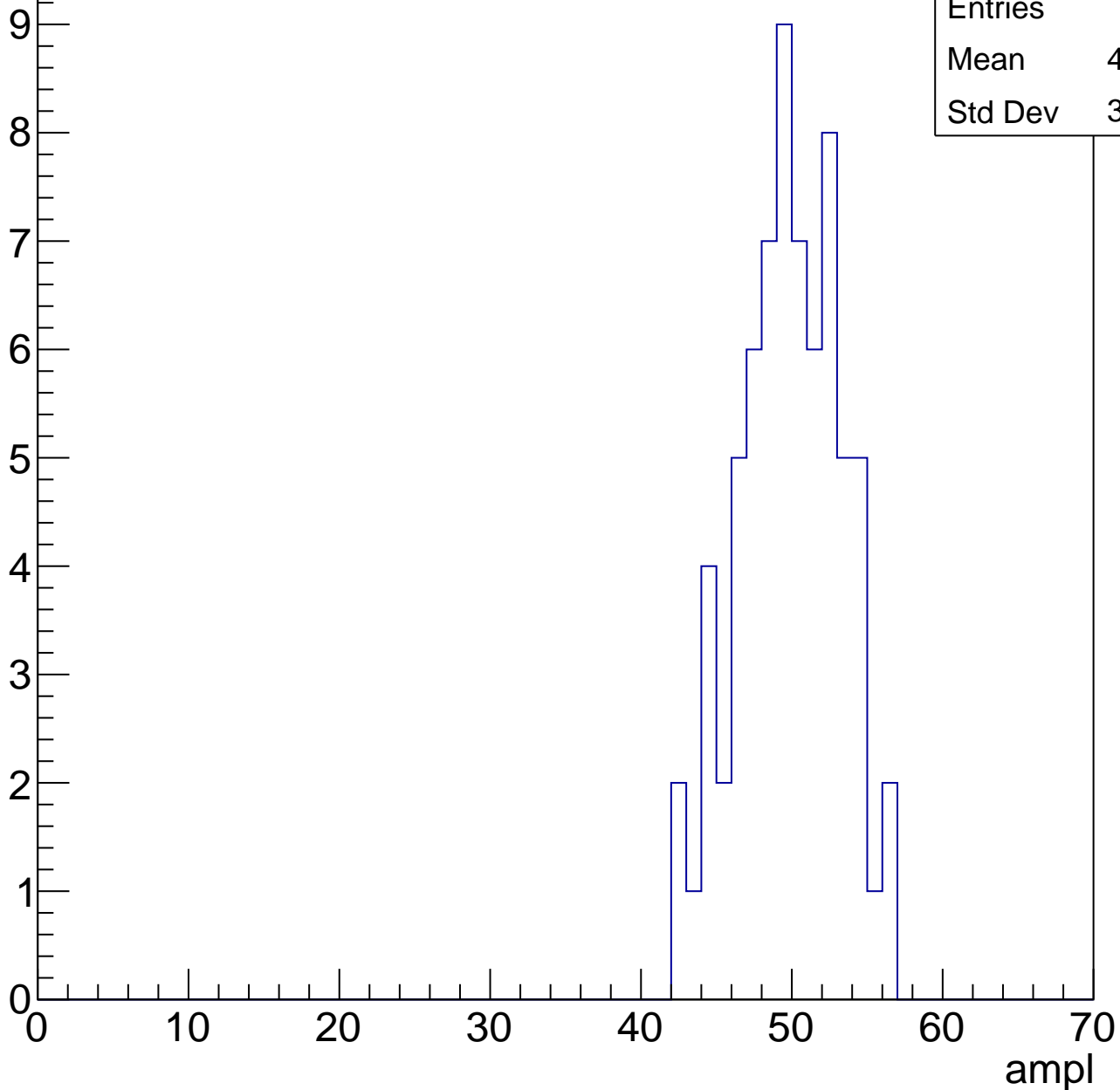


B1L102S, U19-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

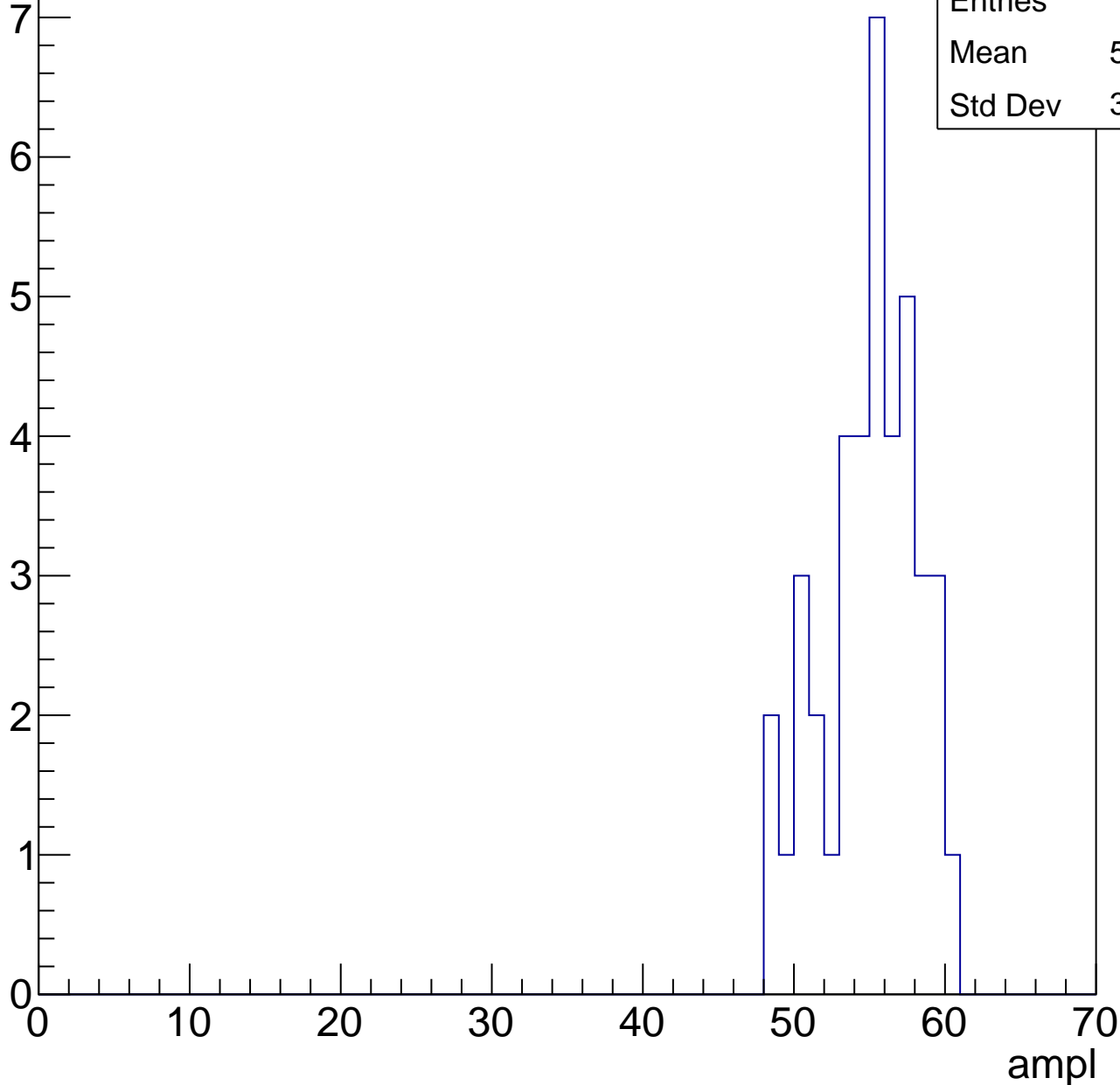
Entries	70
Mean	49.37
Std Dev	3.326



B1L102S, U19-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

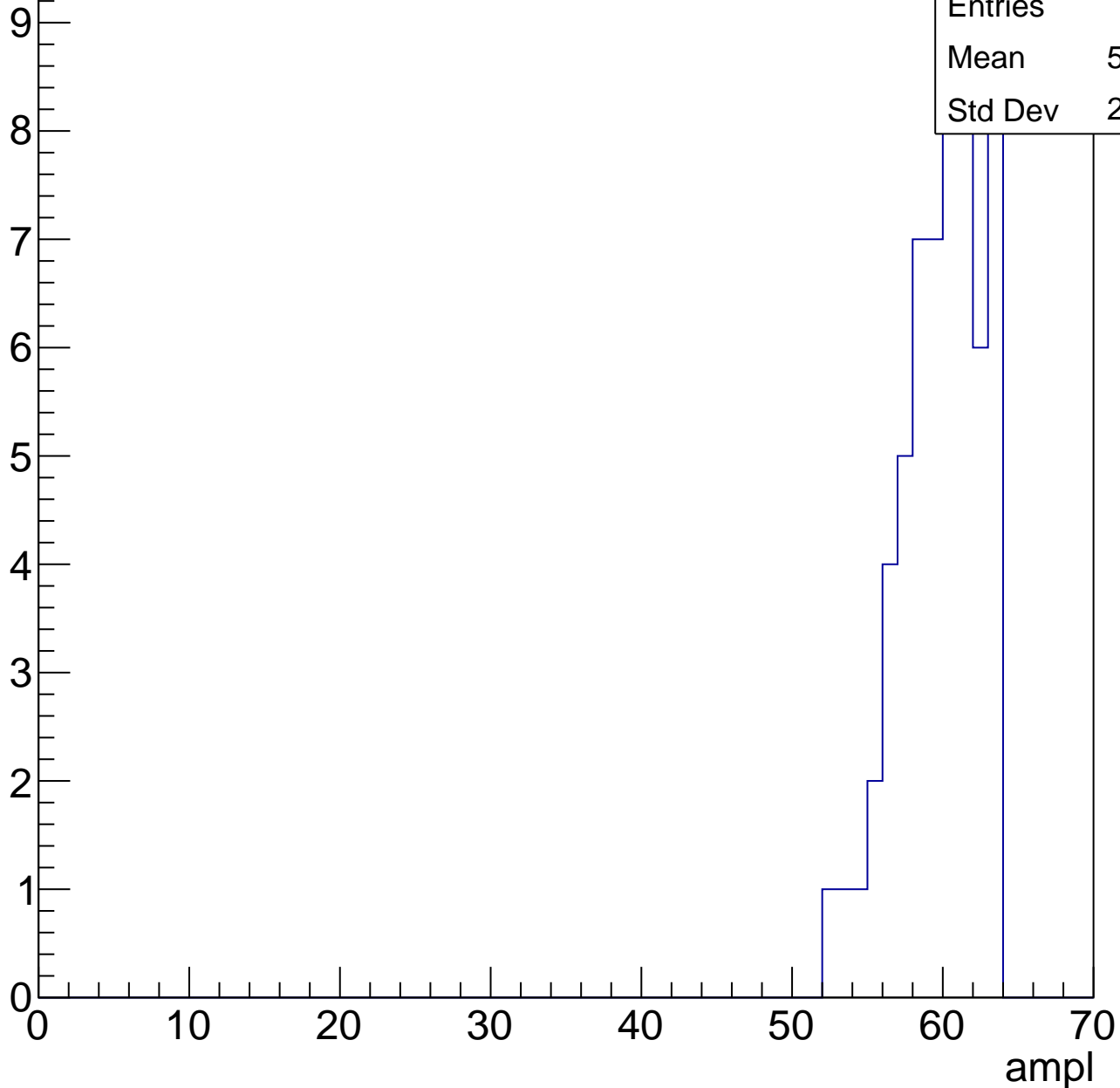


Entries	40
Mean	54.55
Std Dev	3.106

B1L102S, U19-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

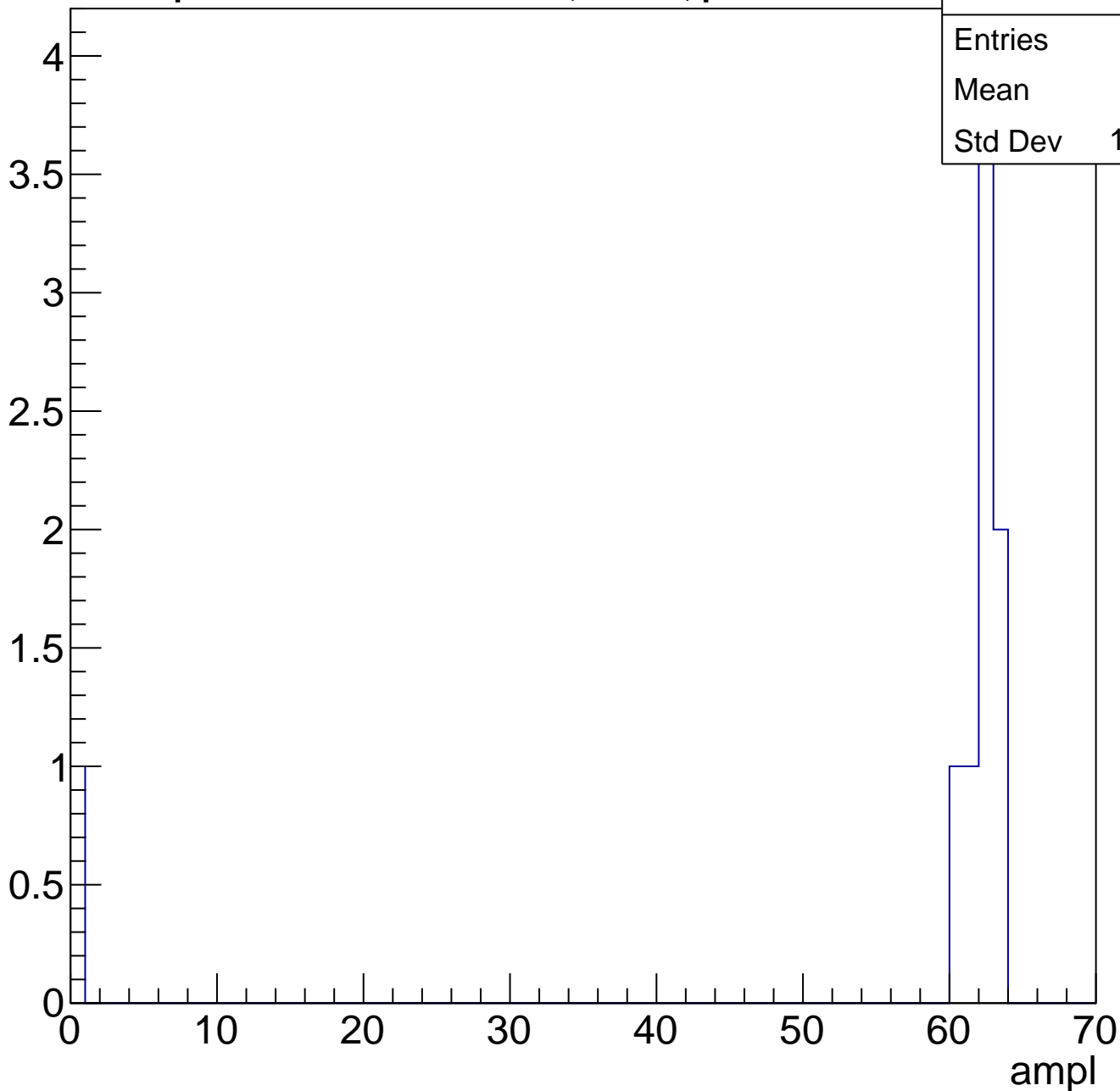
Entry



B1L102S, U19-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch15, adc0

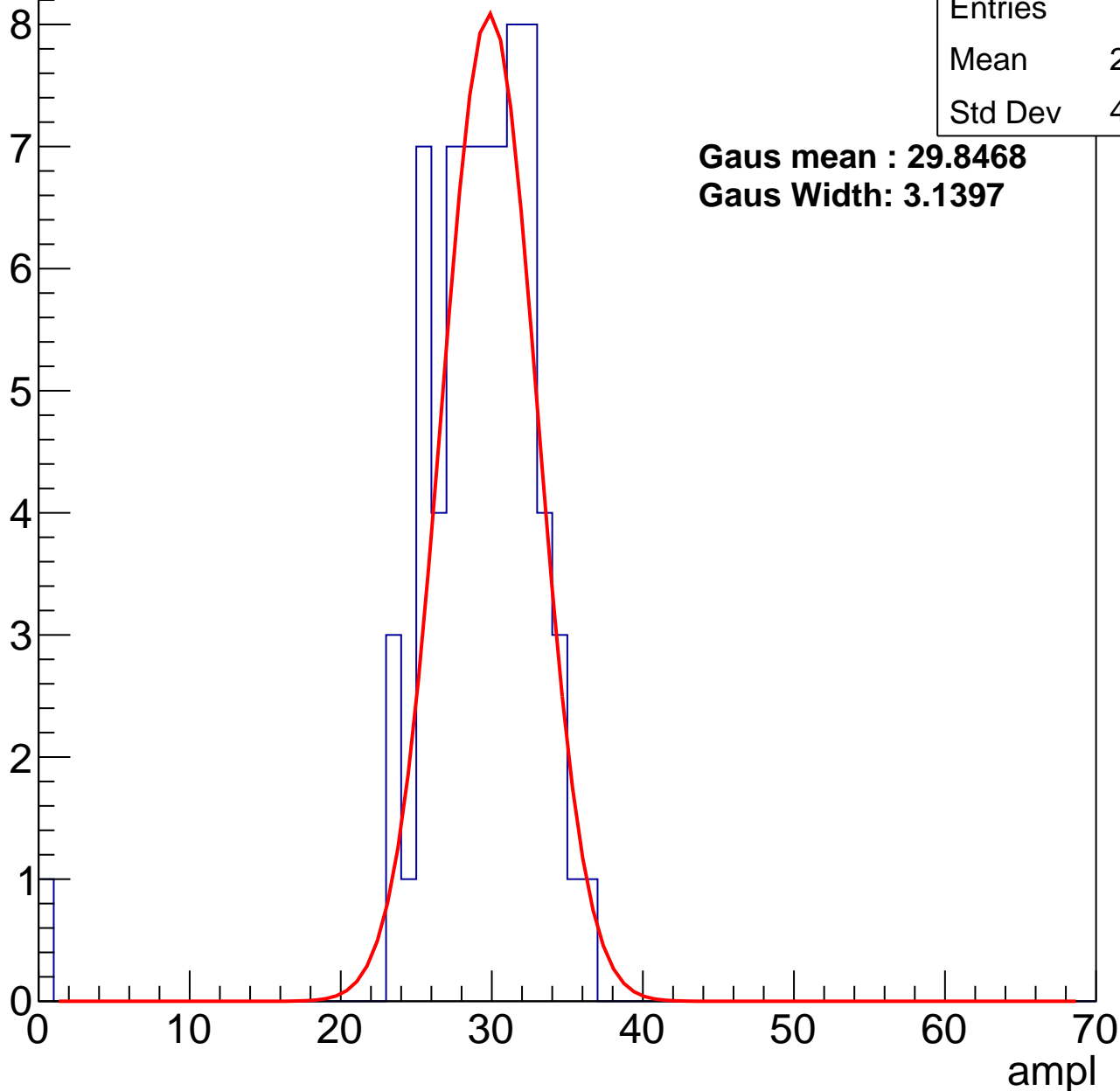
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.68
Std Dev	4.639

Gaus mean : 29.8468

Gaus Width: 3.1397



B1L102S, U19-ch15, adc1

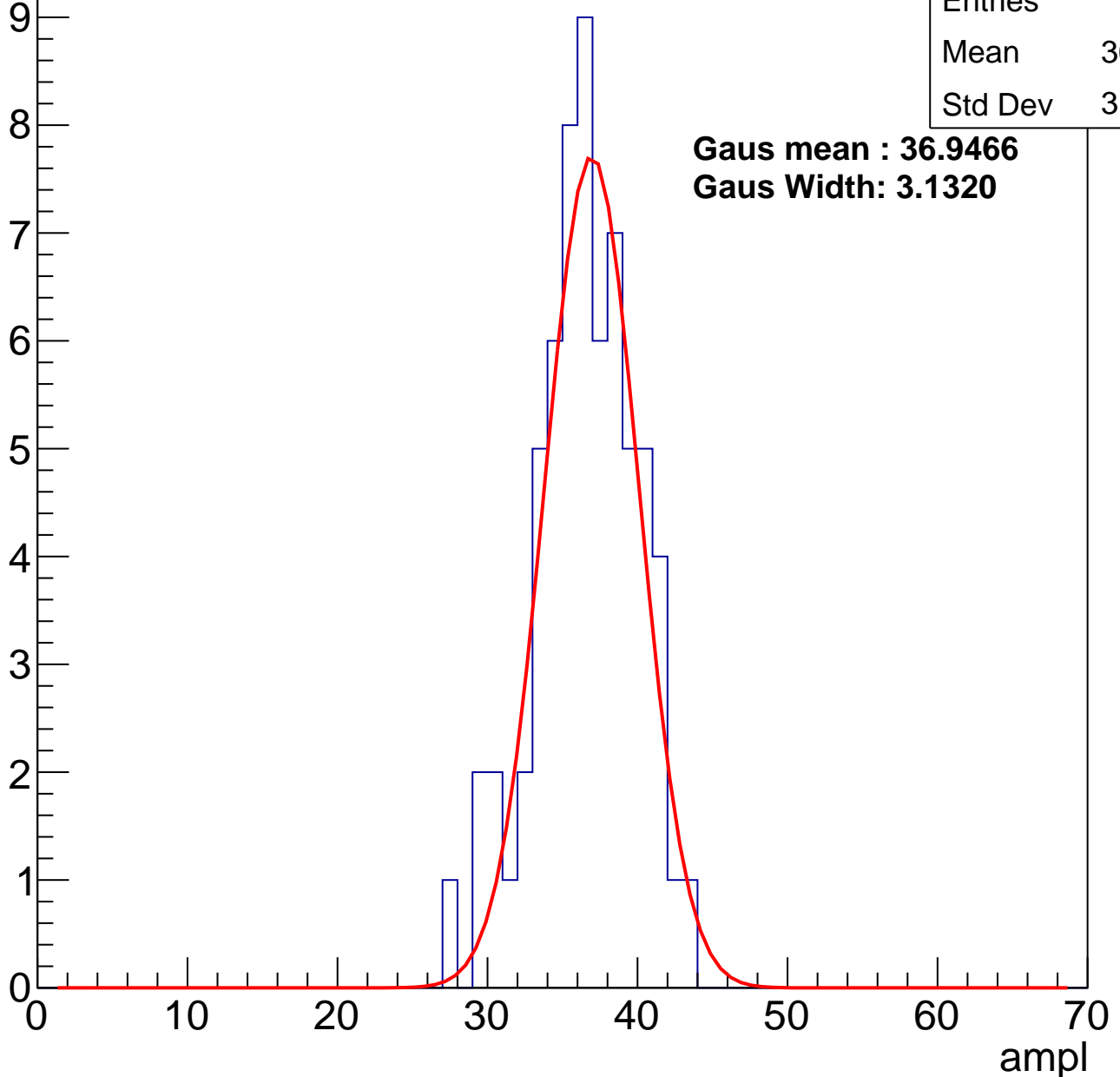
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.08
Std Dev	3.348

Gaus mean : 36.9466

Gaus Width: 3.1320



B1L102S, U19-ch15, adc2

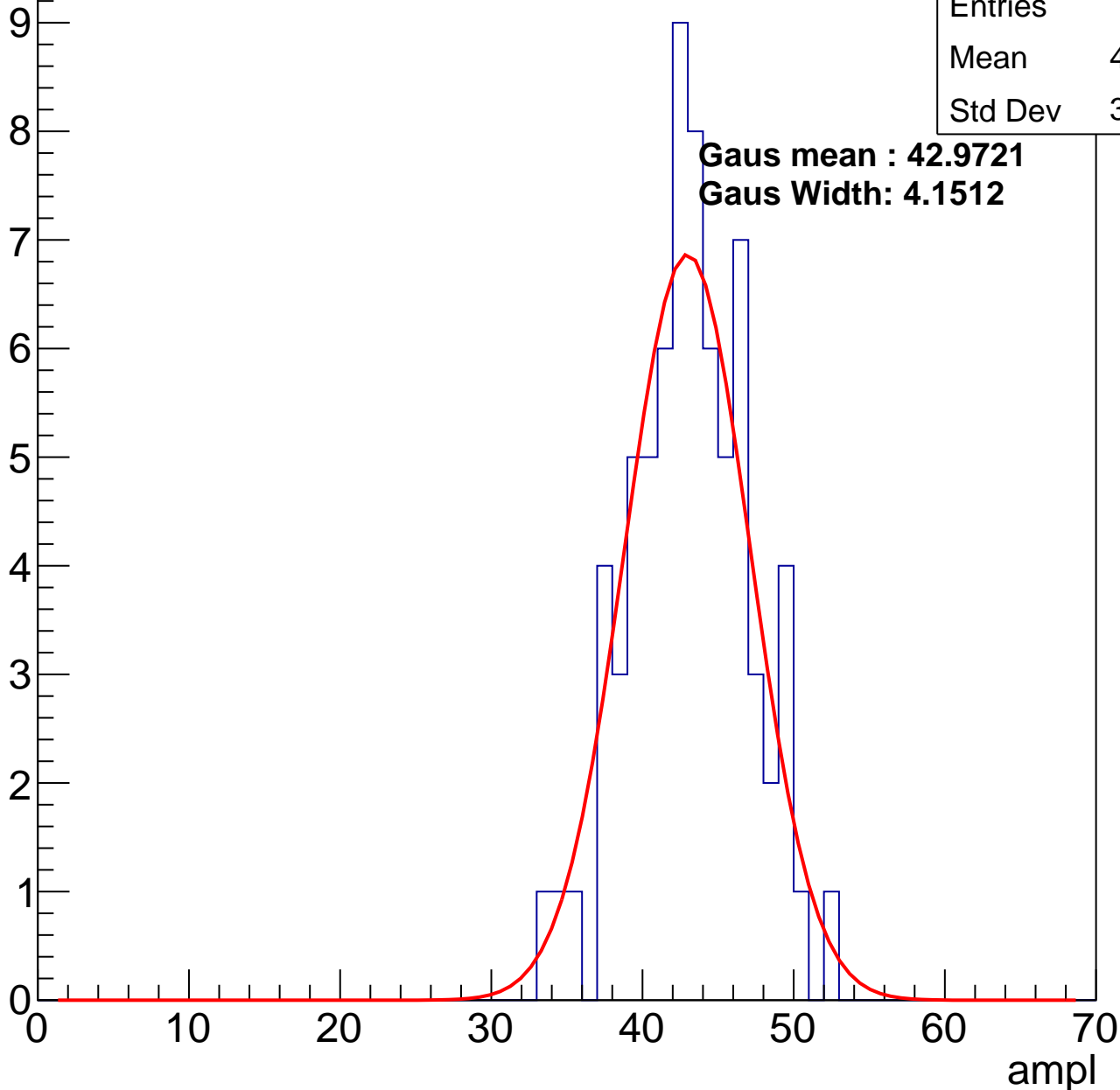
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.68
Std Dev	3.865

Gaus mean : 42.9721

Gaus Width: 4.1512

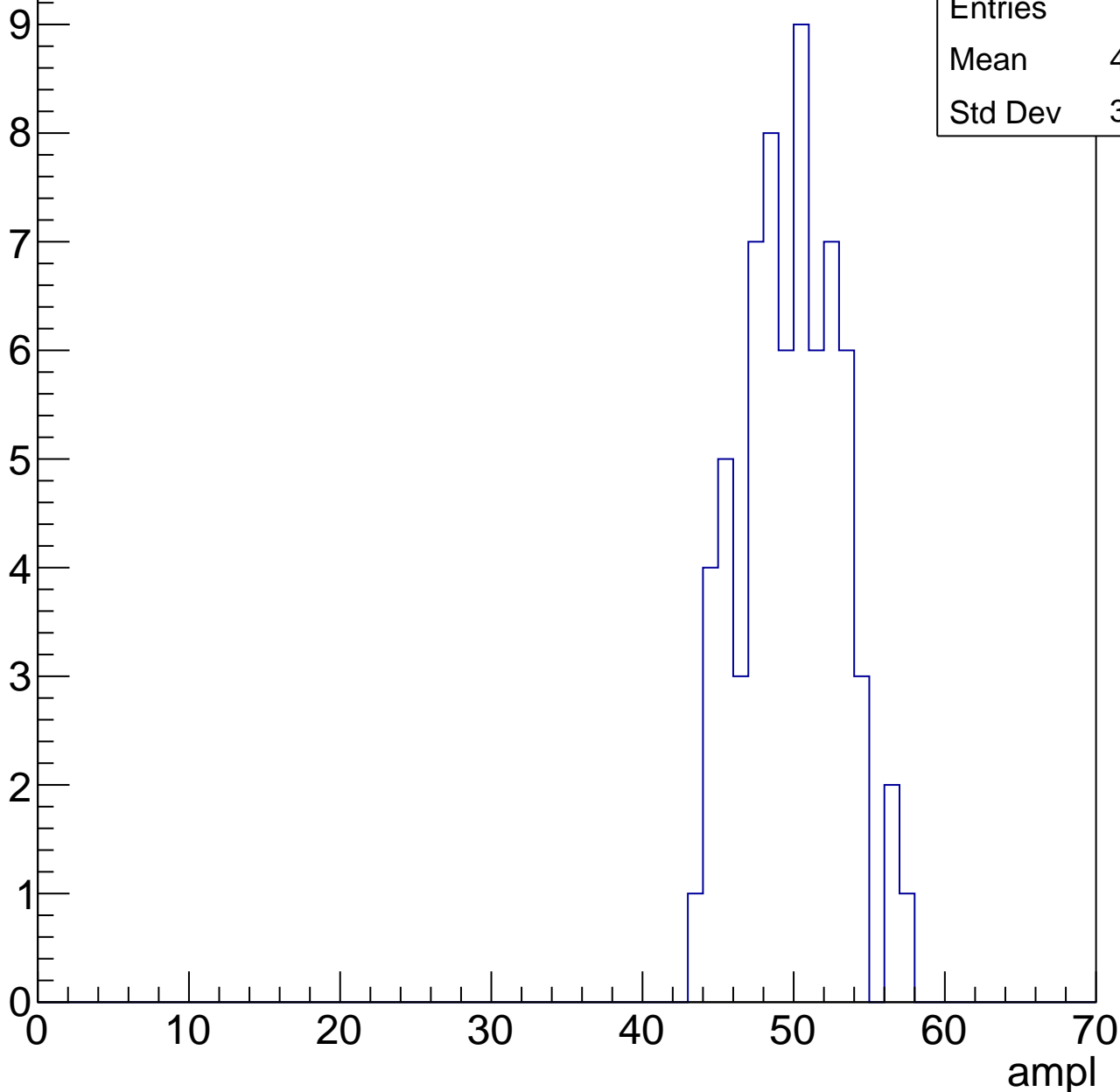


B1L102S, U19-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.38
Std Dev	3.204

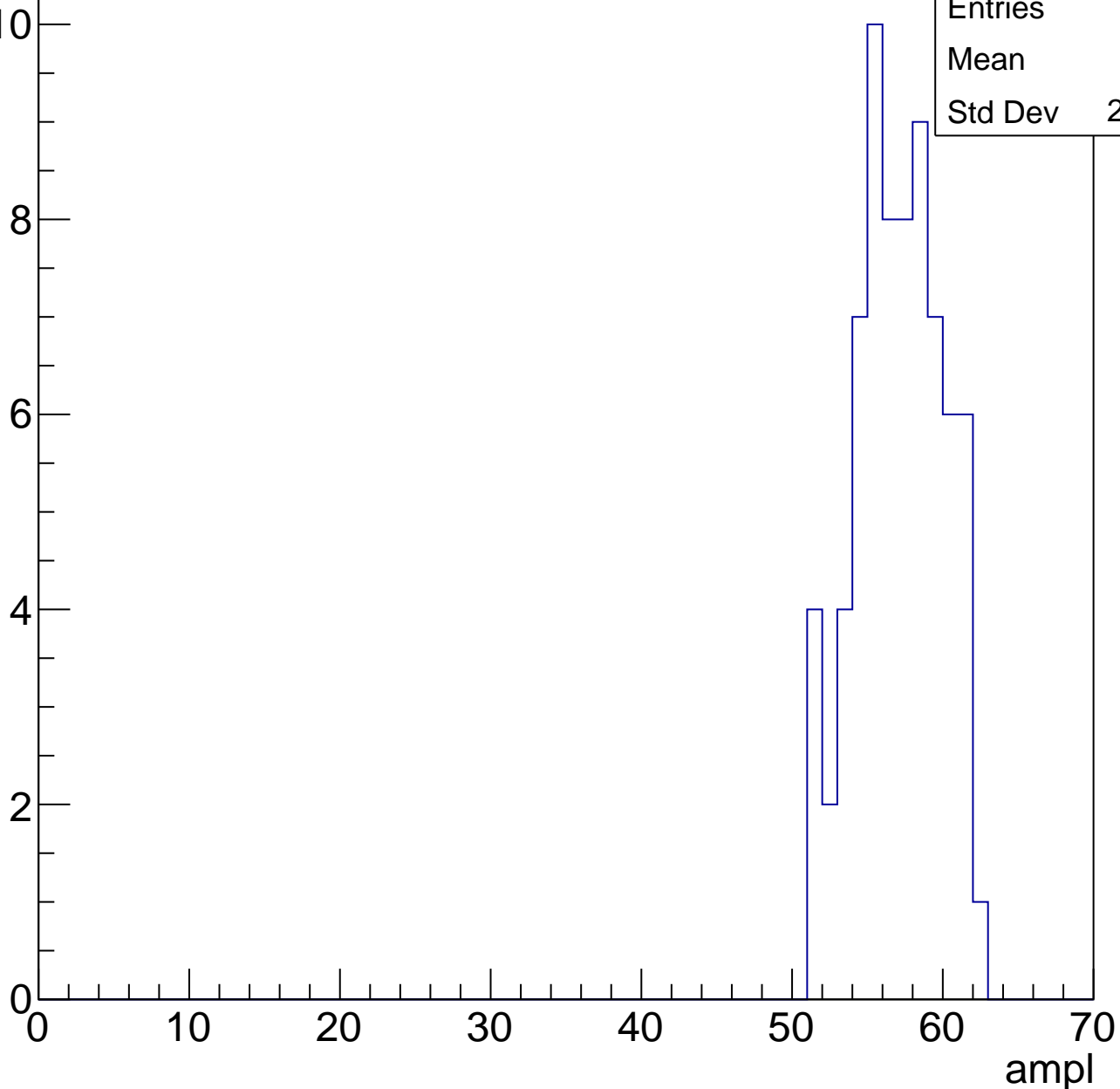


B1L102S, U19-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	56.6
Std Dev	2.812

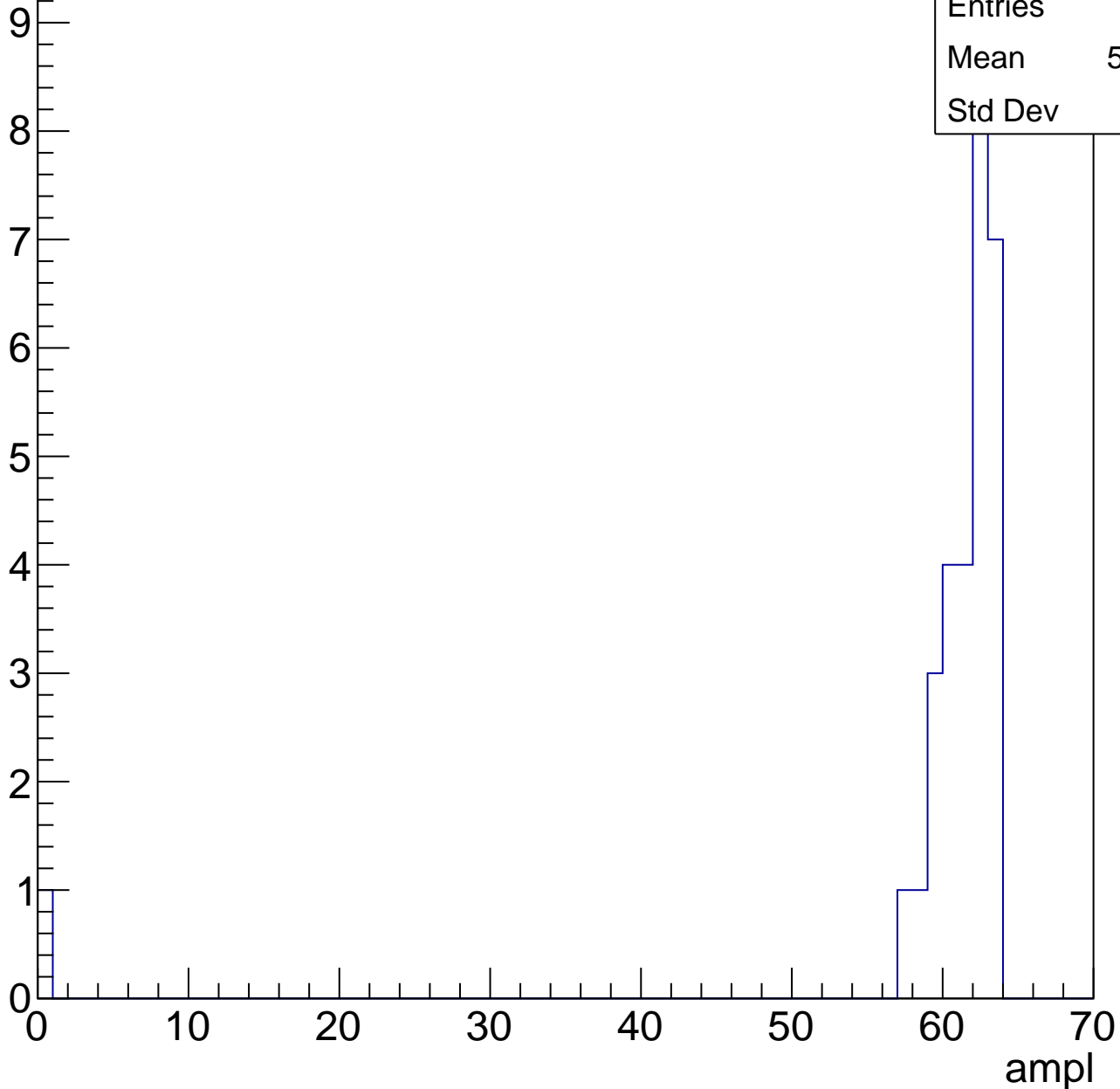


B1L102S, U19-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	59.17
Std Dev	11.1



B1L102S, U19-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch16, adc0

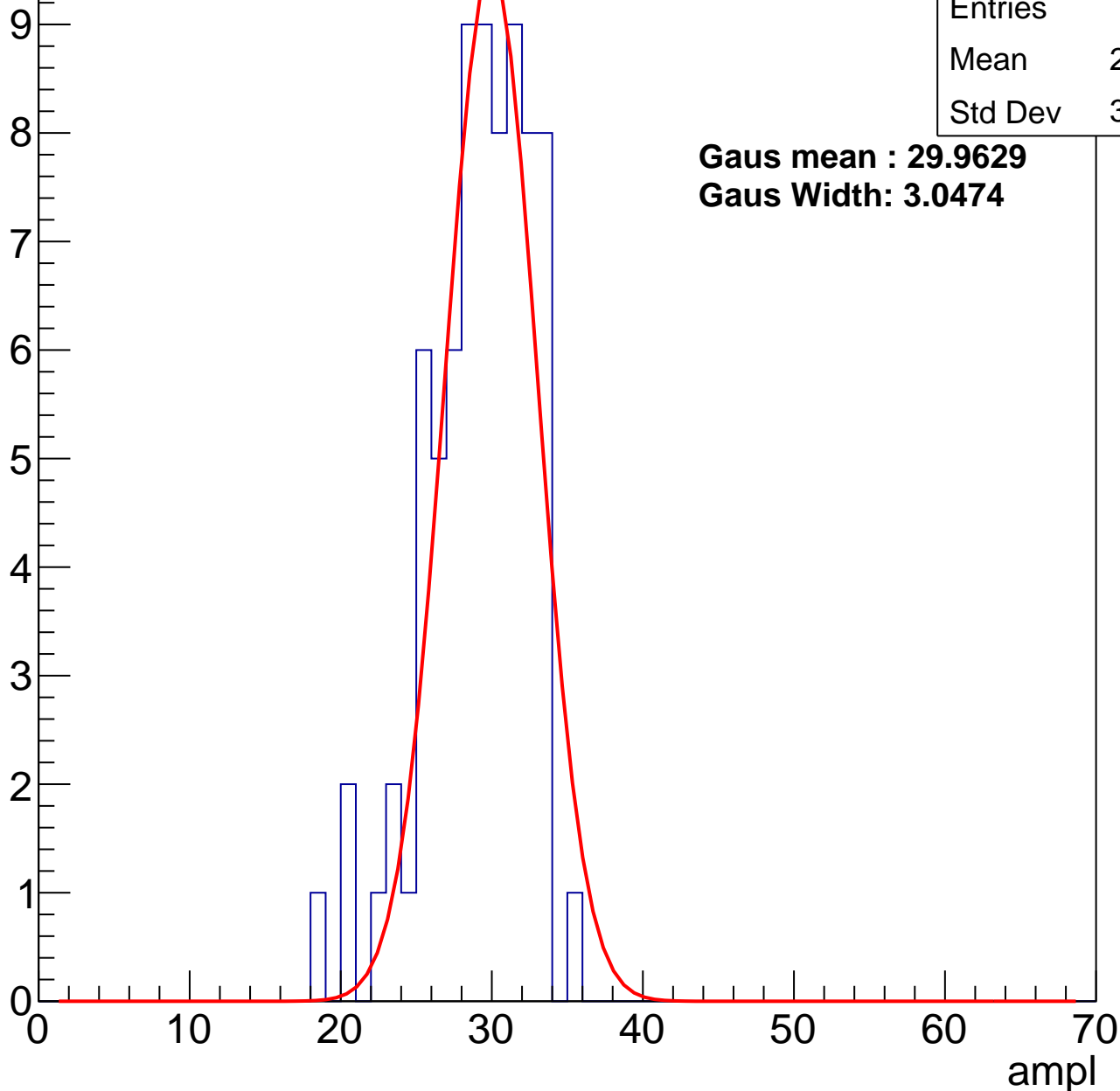
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.67
Std Dev	3.393

Gaus mean : 29.9629

Gaus Width: 3.0474



B1L102S, U19-ch16, adc1

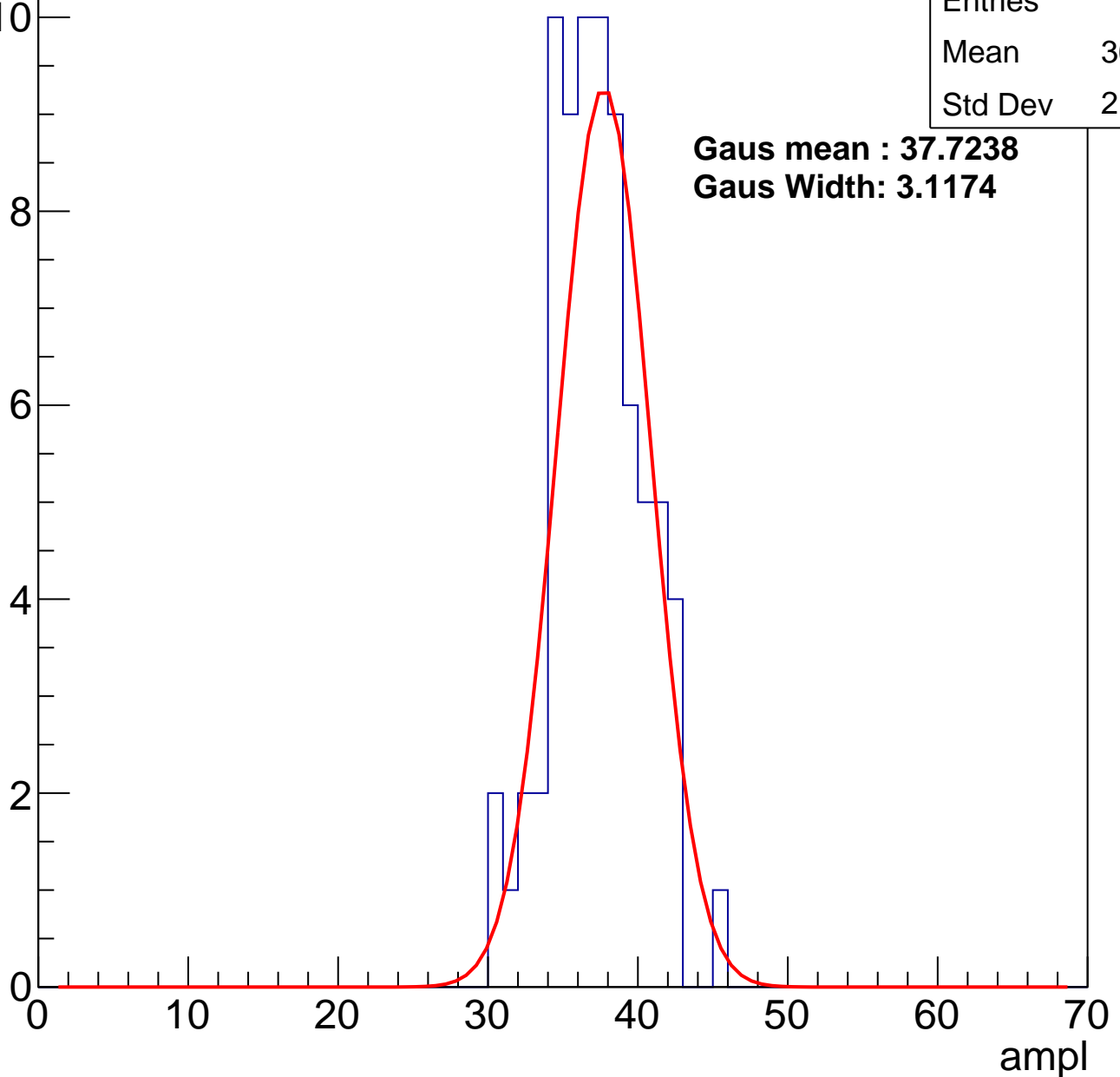
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.84
Std Dev	2.974

Gaus mean : 37.7238

Gaus Width: 3.1174



B1L102S, U19-ch16, adc2

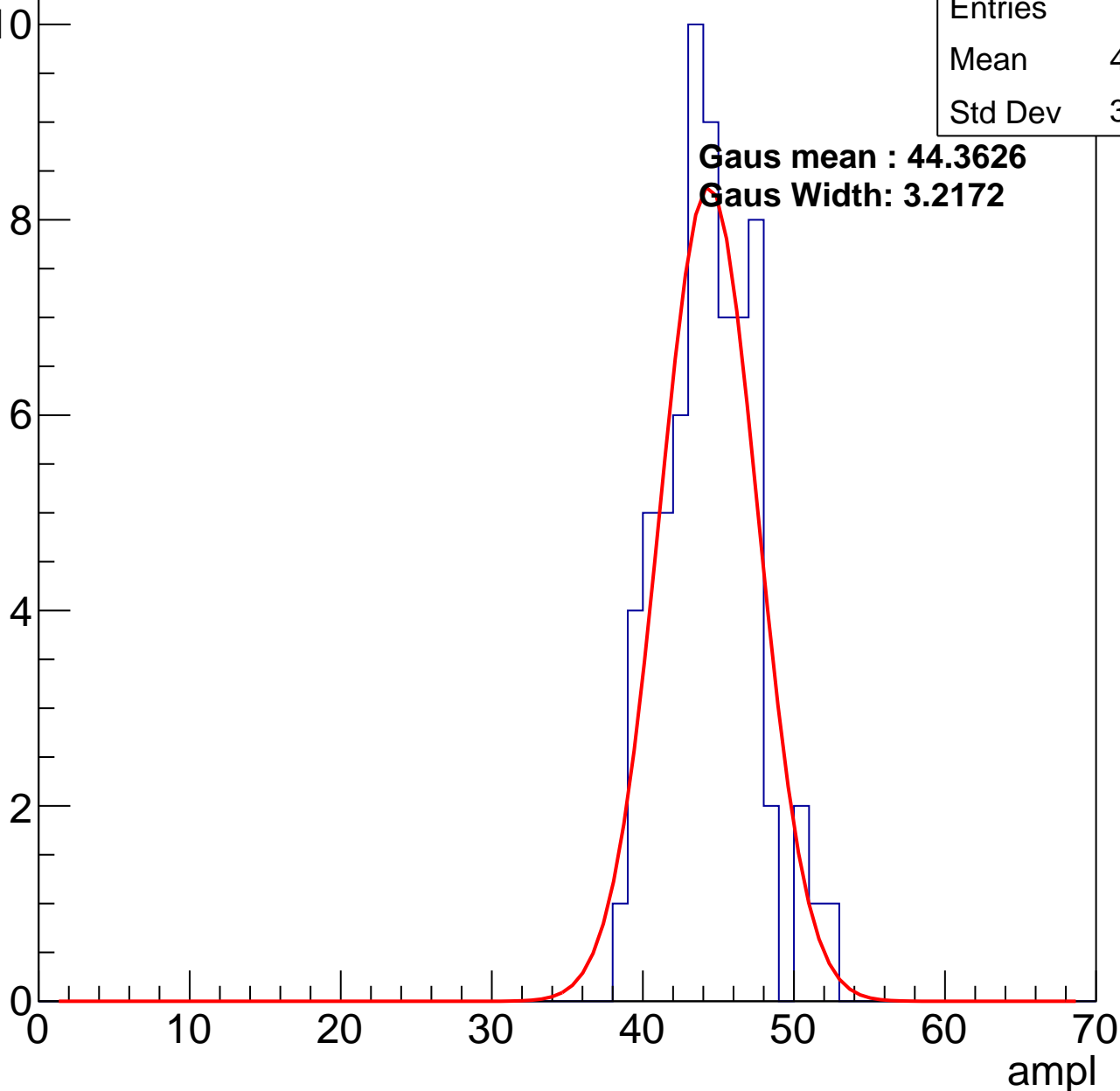
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.96
Std Dev	3.017

Gaus mean : 44.3626

Gaus Width: 3.2172

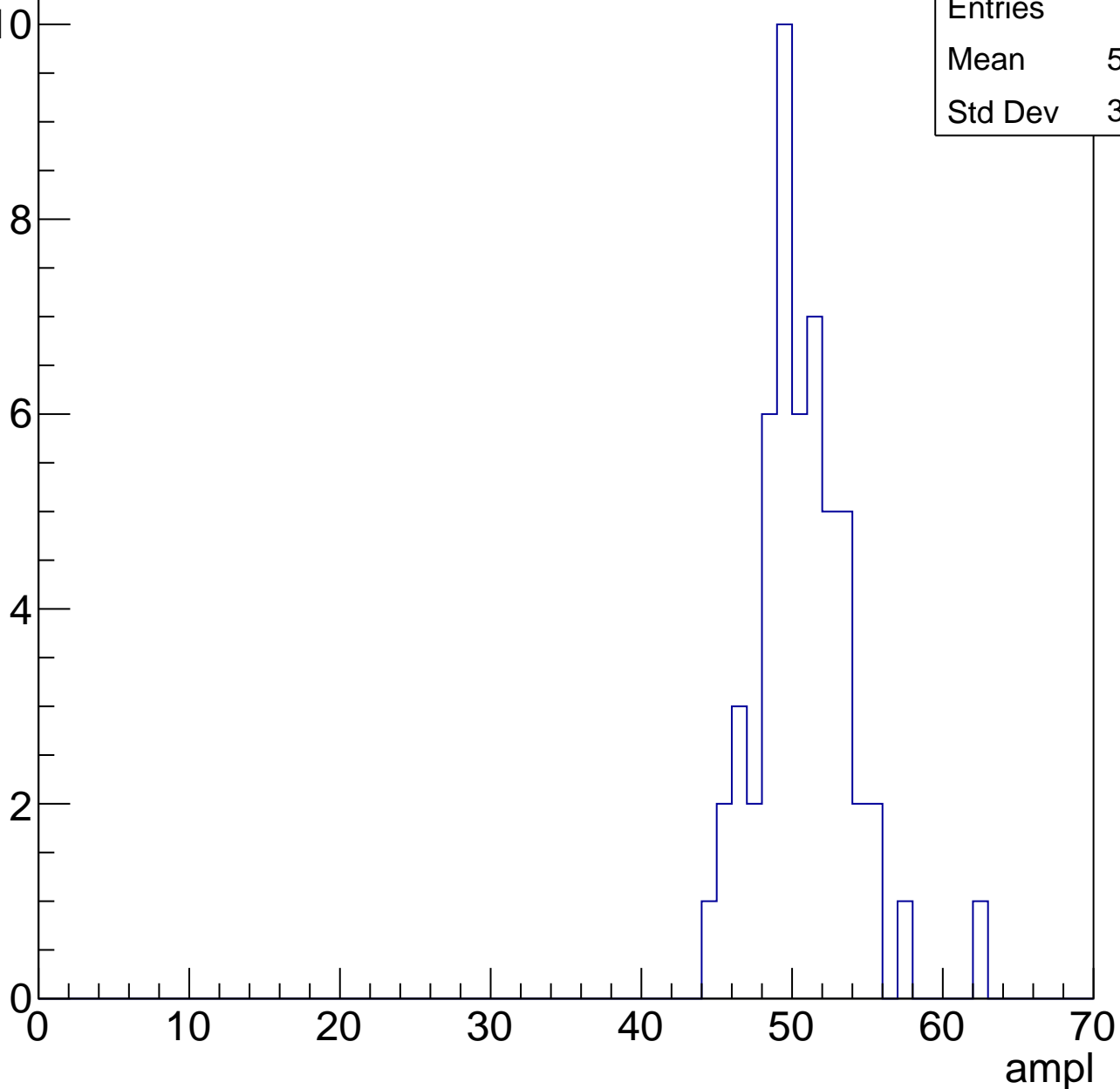


B1L102S, U19-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

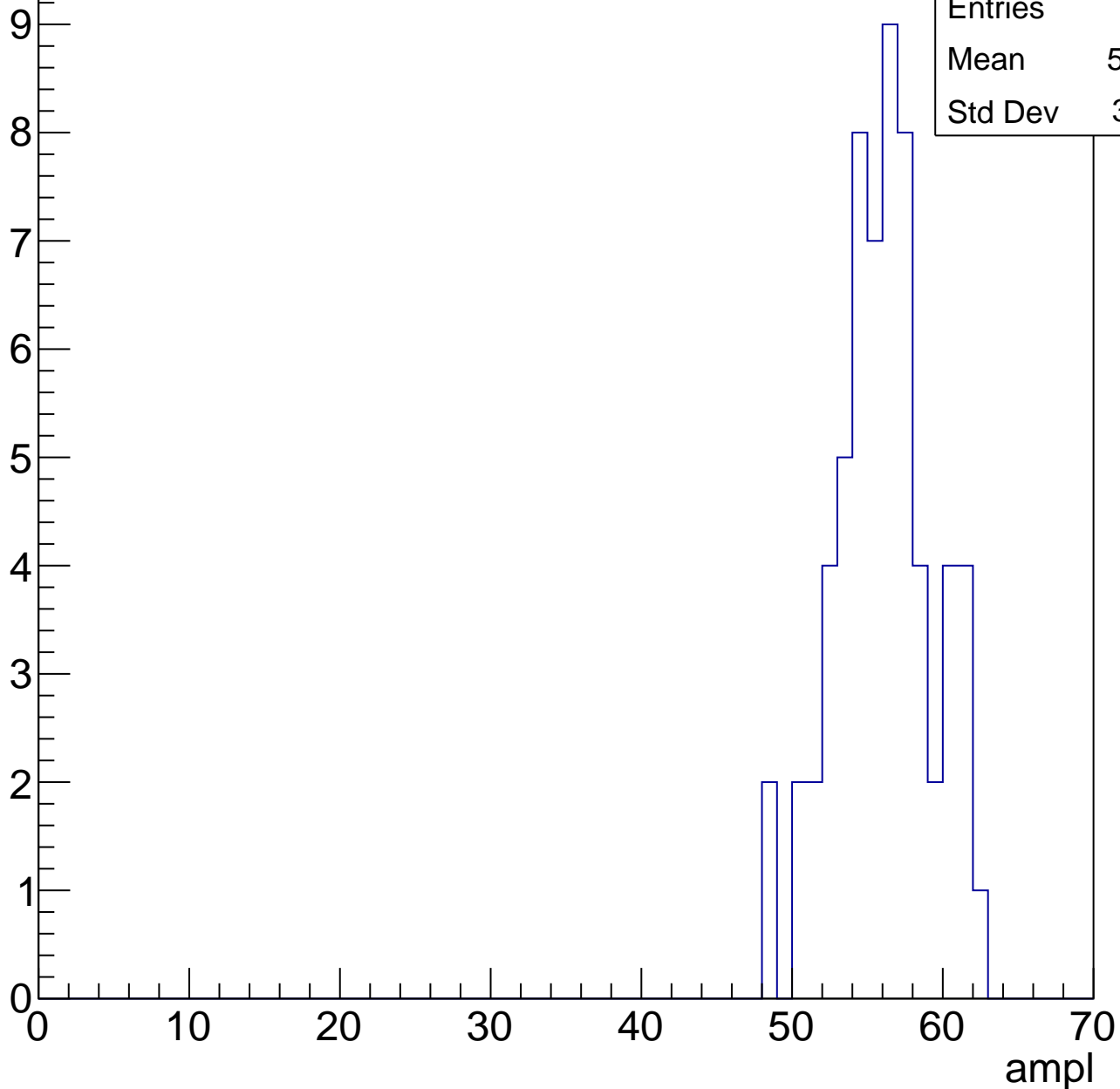
Entries	53
Mean	50.25
Std Dev	3.162



B1L102S, U19-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

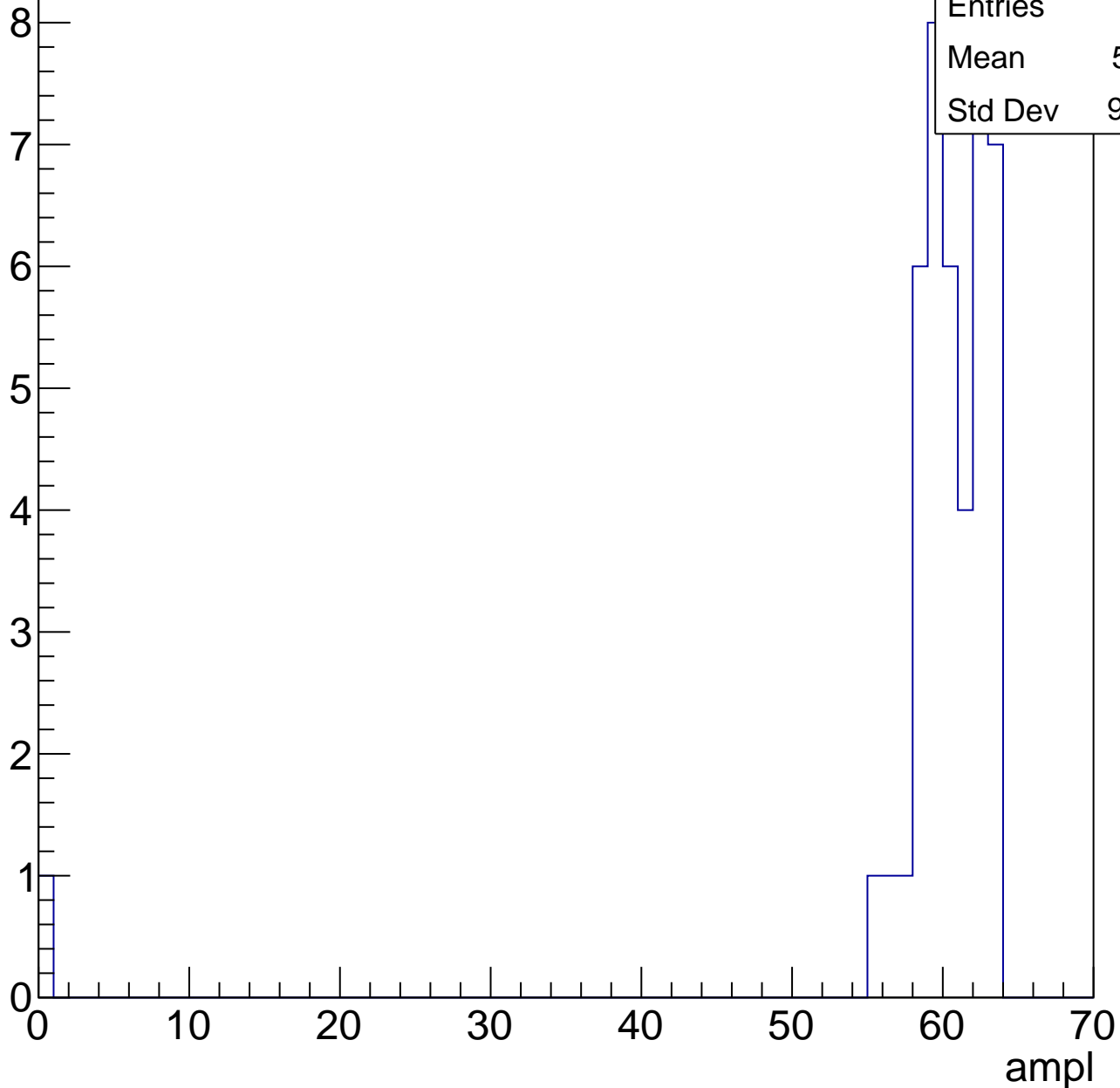


Entries	62
Mean	55.55
Std Dev	3.181

B1L102S, U19-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.2

Std Dev

0.9798

Entries	5
Mean	62.2
Std Dev	0.9798

B1L102S, U19-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch17, adc0

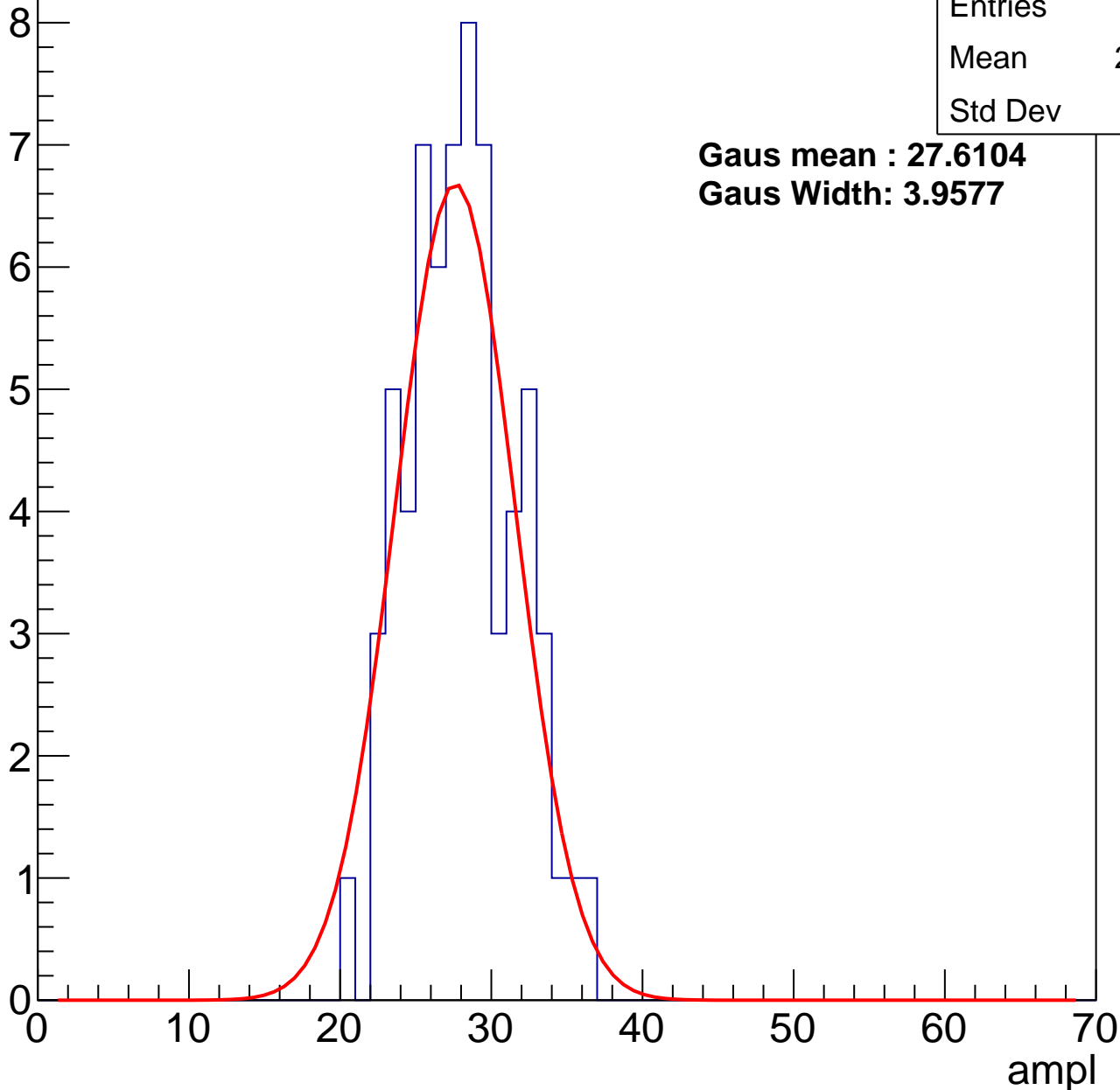
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.61
Std Dev	3.49

Gaus mean : 27.6104

Gaus Width: 3.9577



B1L102S, U19-ch17, adc1

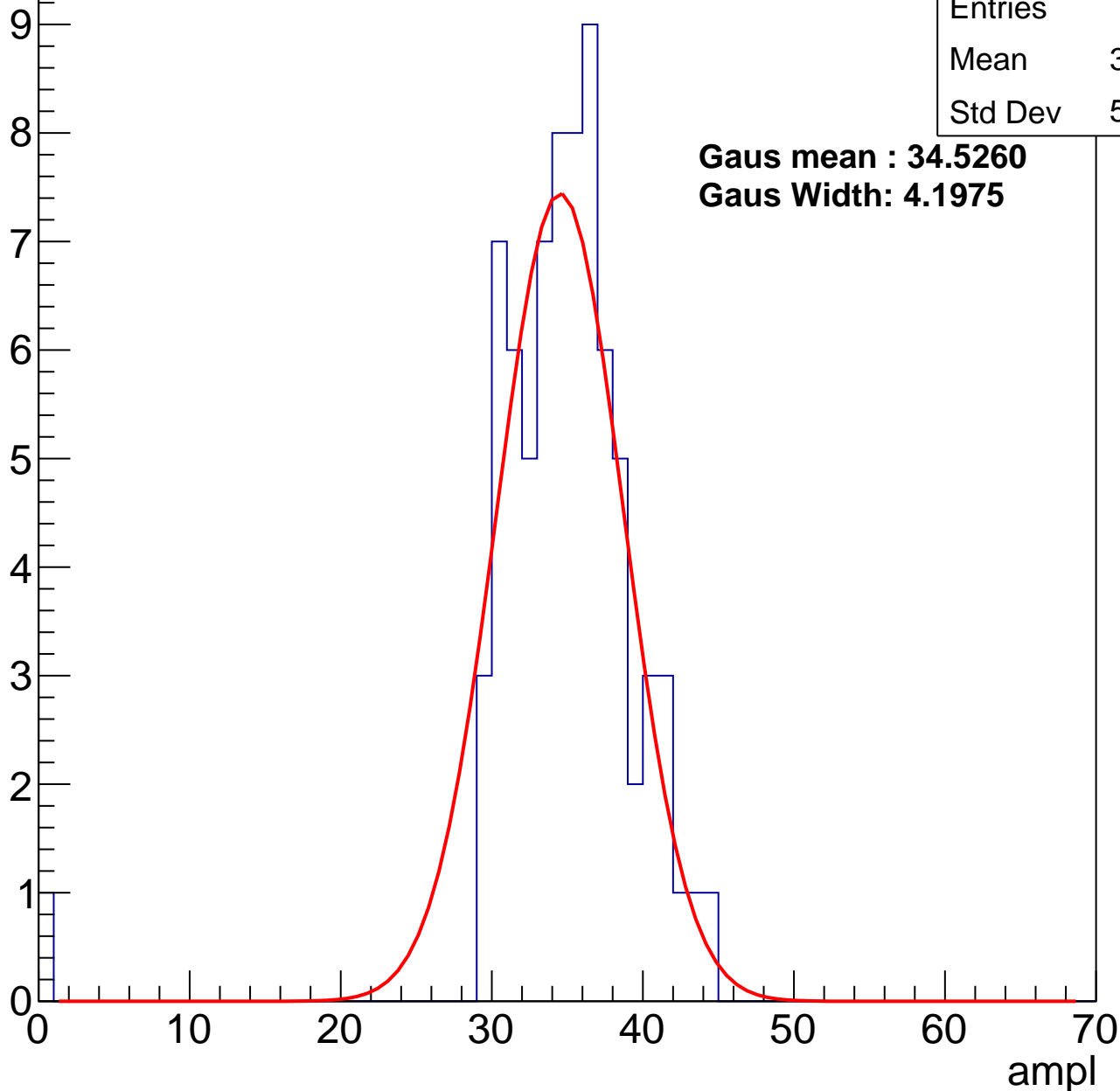
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.37
Std Dev	5.308

Gaus mean : 34.5260

Gaus Width: 4.1975



B1L102S, U19-ch17, adc2

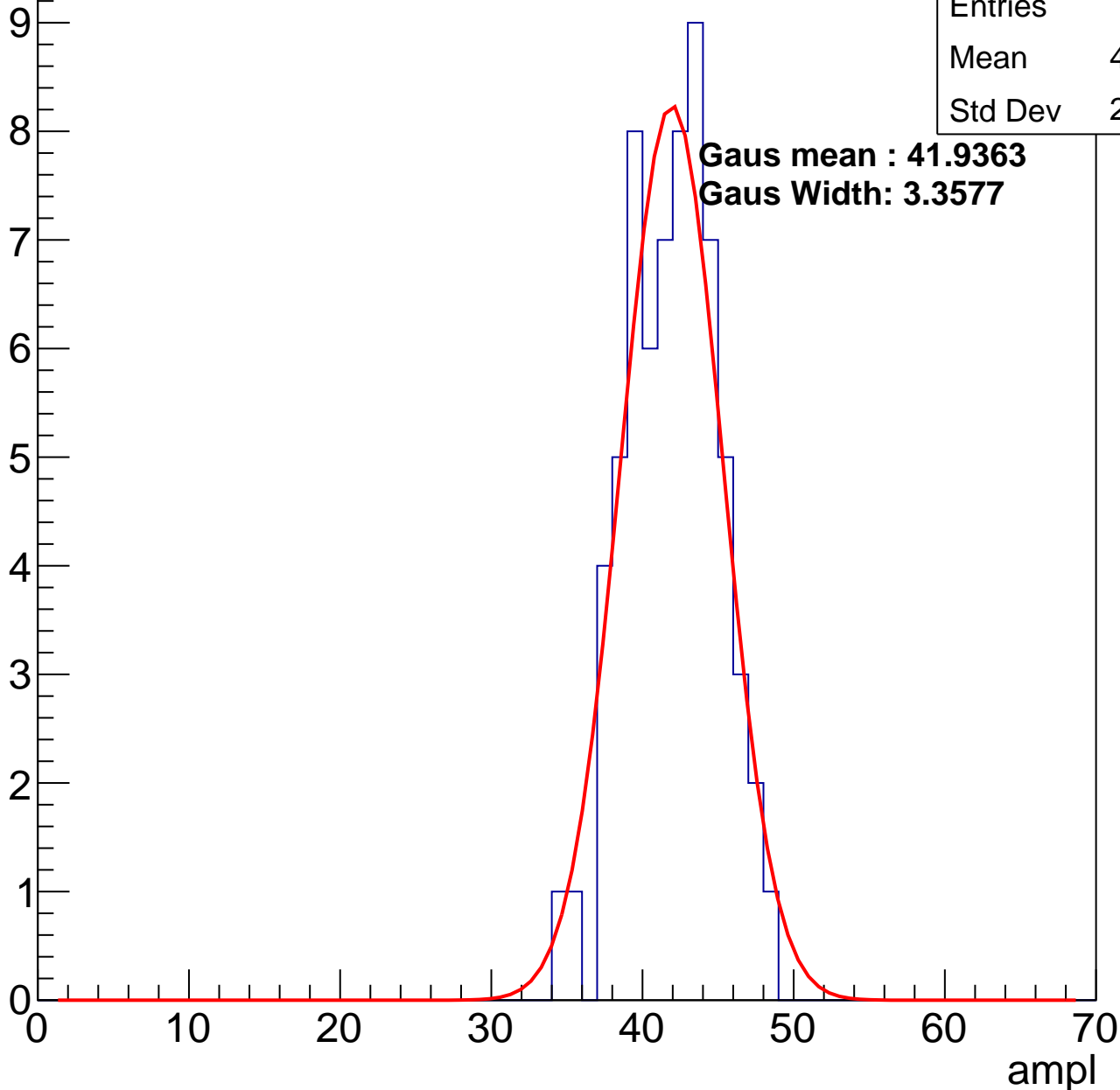
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.52
Std Dev	2.984

Gaus mean : 41.9363

Gaus Width: 3.3577

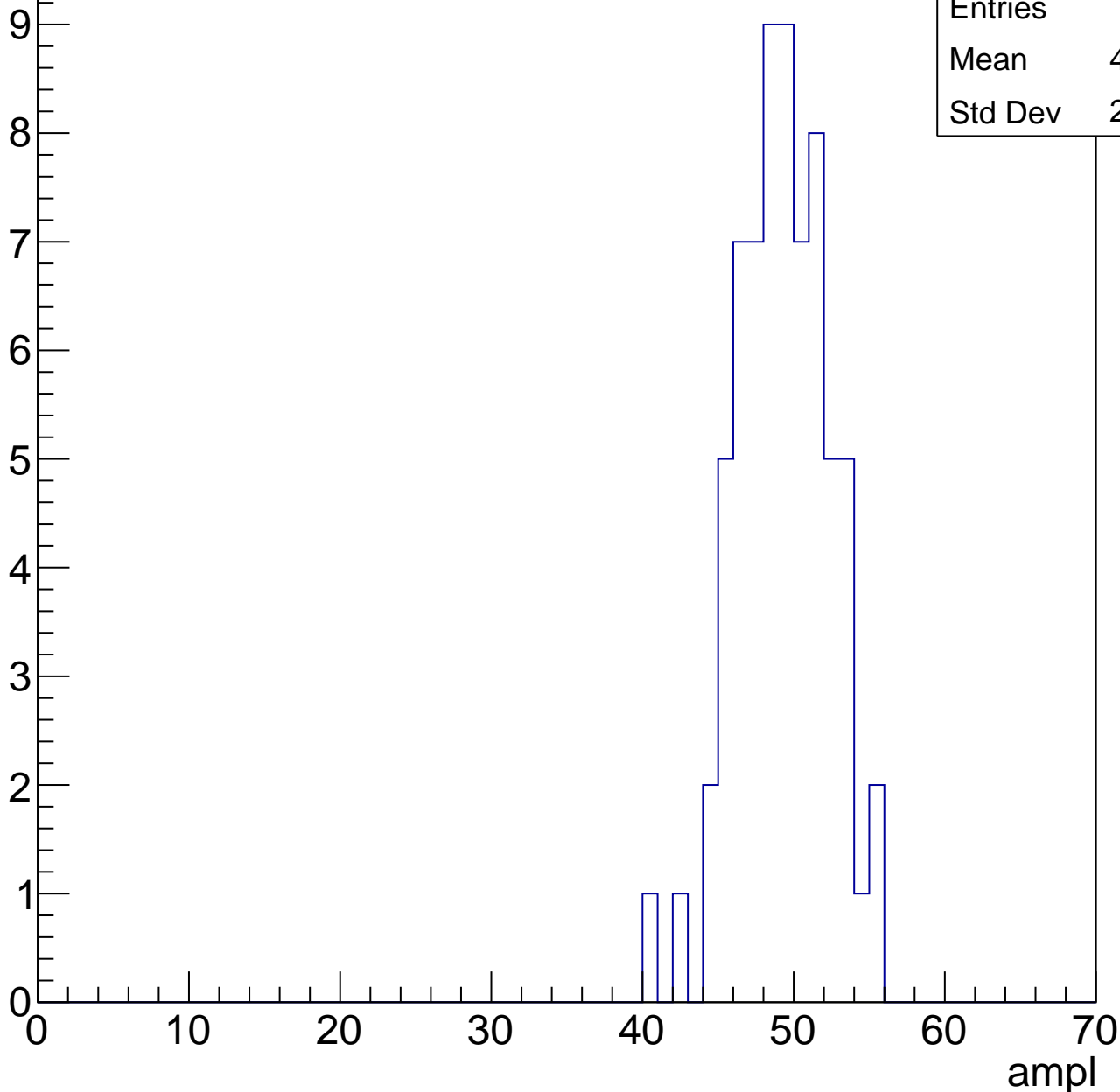


B1L102S, U19-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.78
Std Dev	2.992

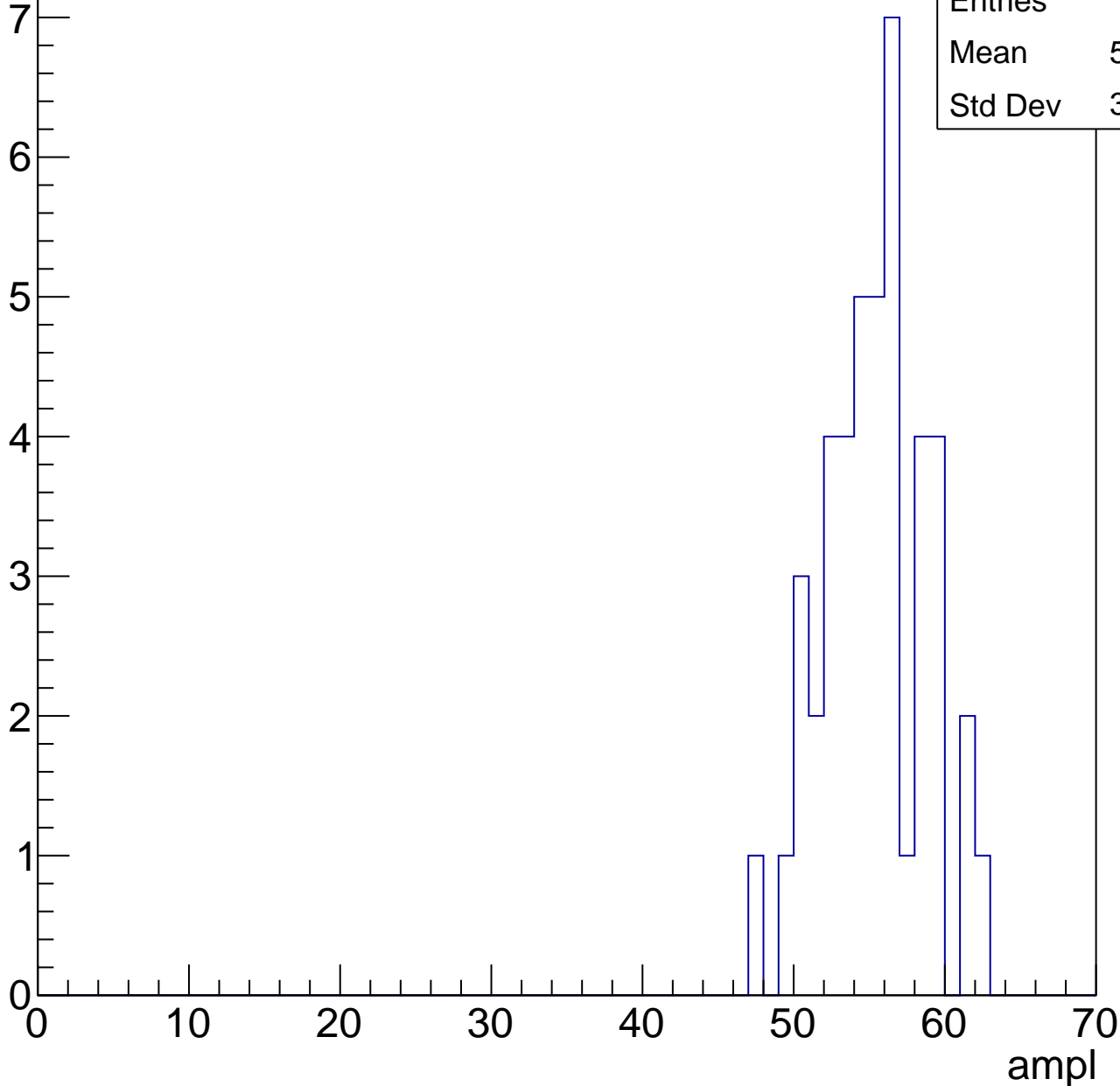


B1L102S, U19-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.86
Std Dev	3.355

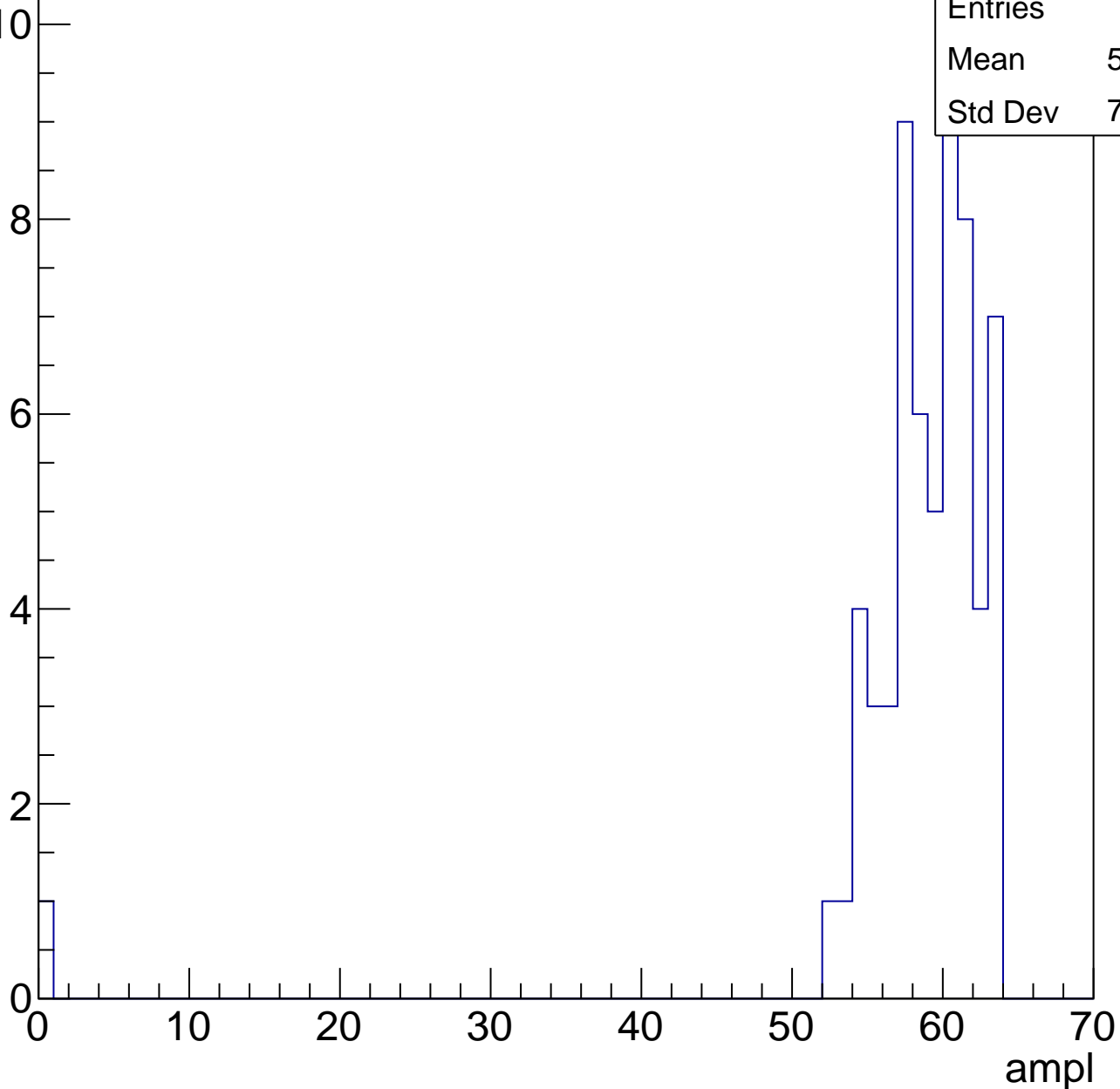


B1L102S, U19-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.85
Std Dev	7.923

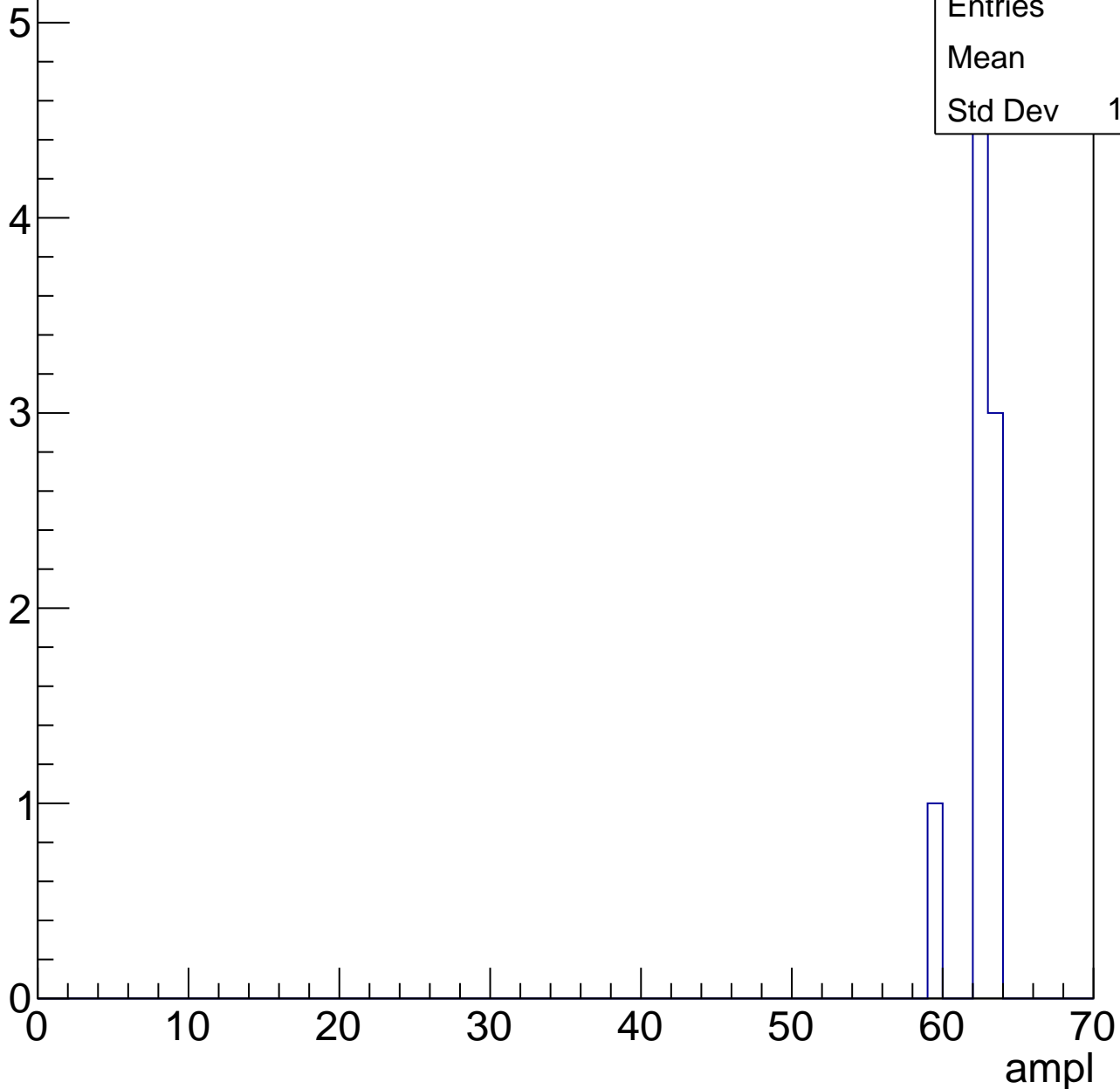


B1L102S, U19-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62
Std Dev	1.155



B1L102S, U19-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch18, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.33
Std Dev	5.653

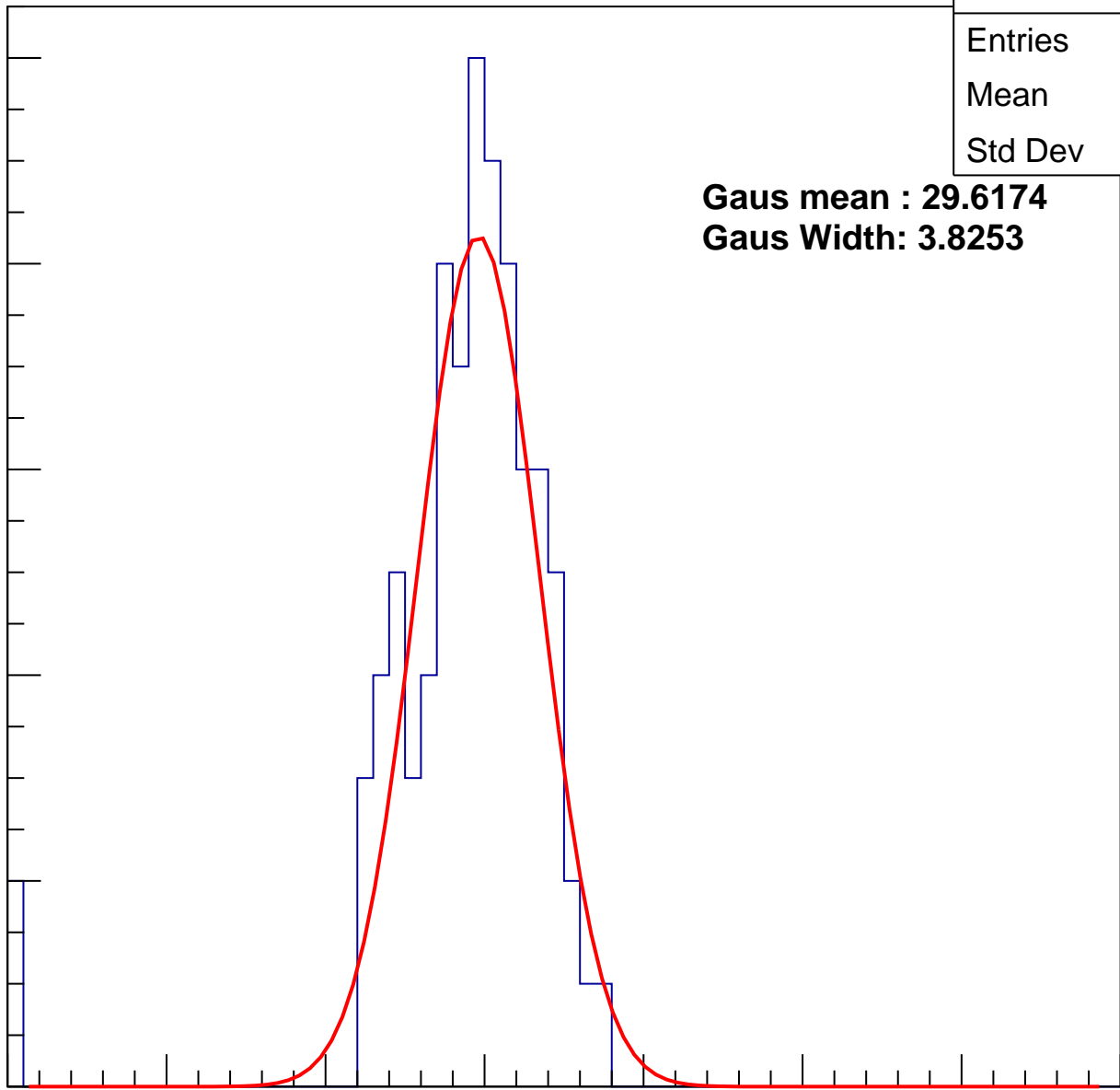
Gaus mean : 29.6174

Gaus Width: 3.8253

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch18, adc1

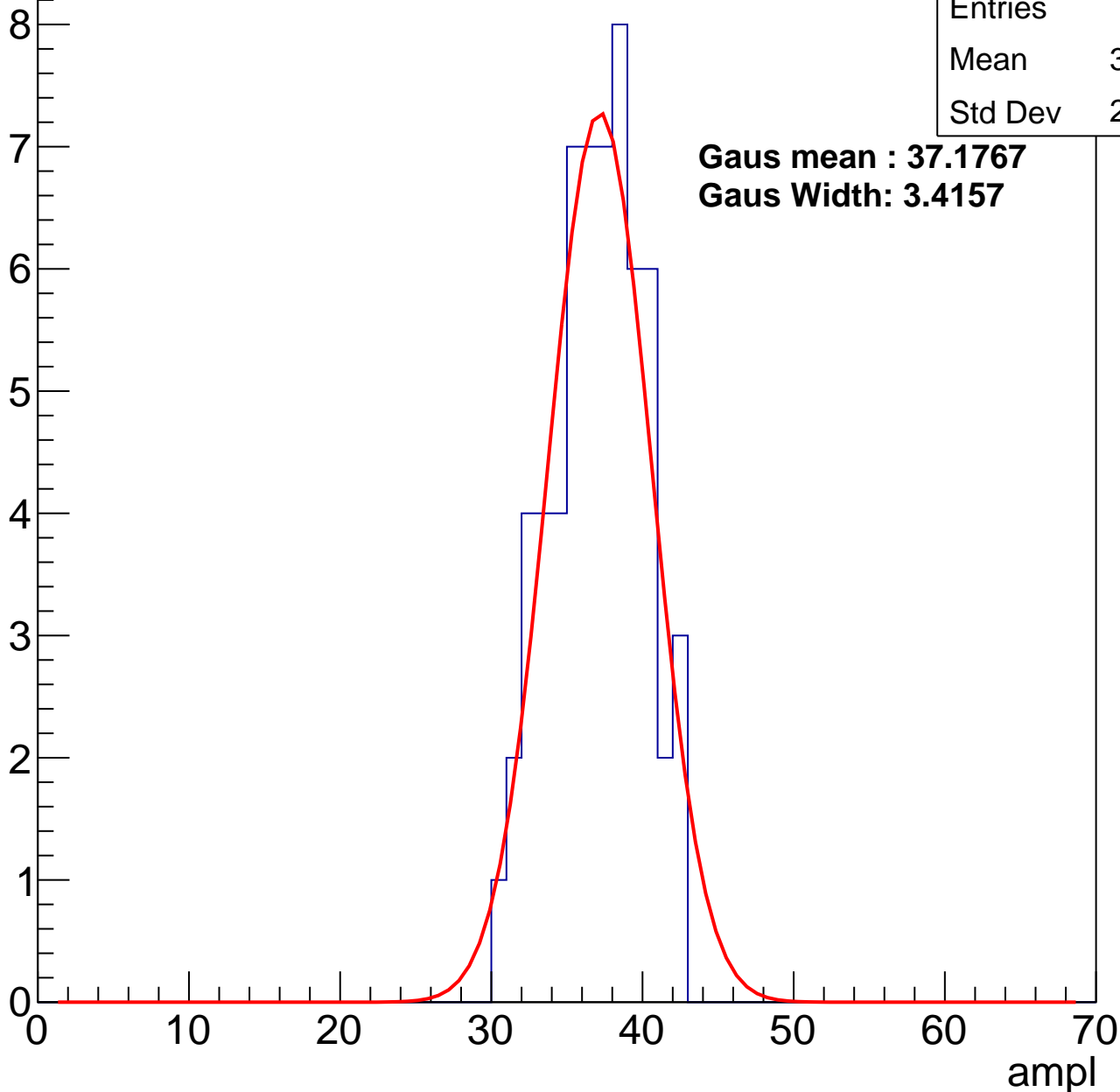
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.56
Std Dev	2.967

Gaus mean : 37.1767

Gaus Width: 3.4157



B1L102S, U19-ch18, adc2

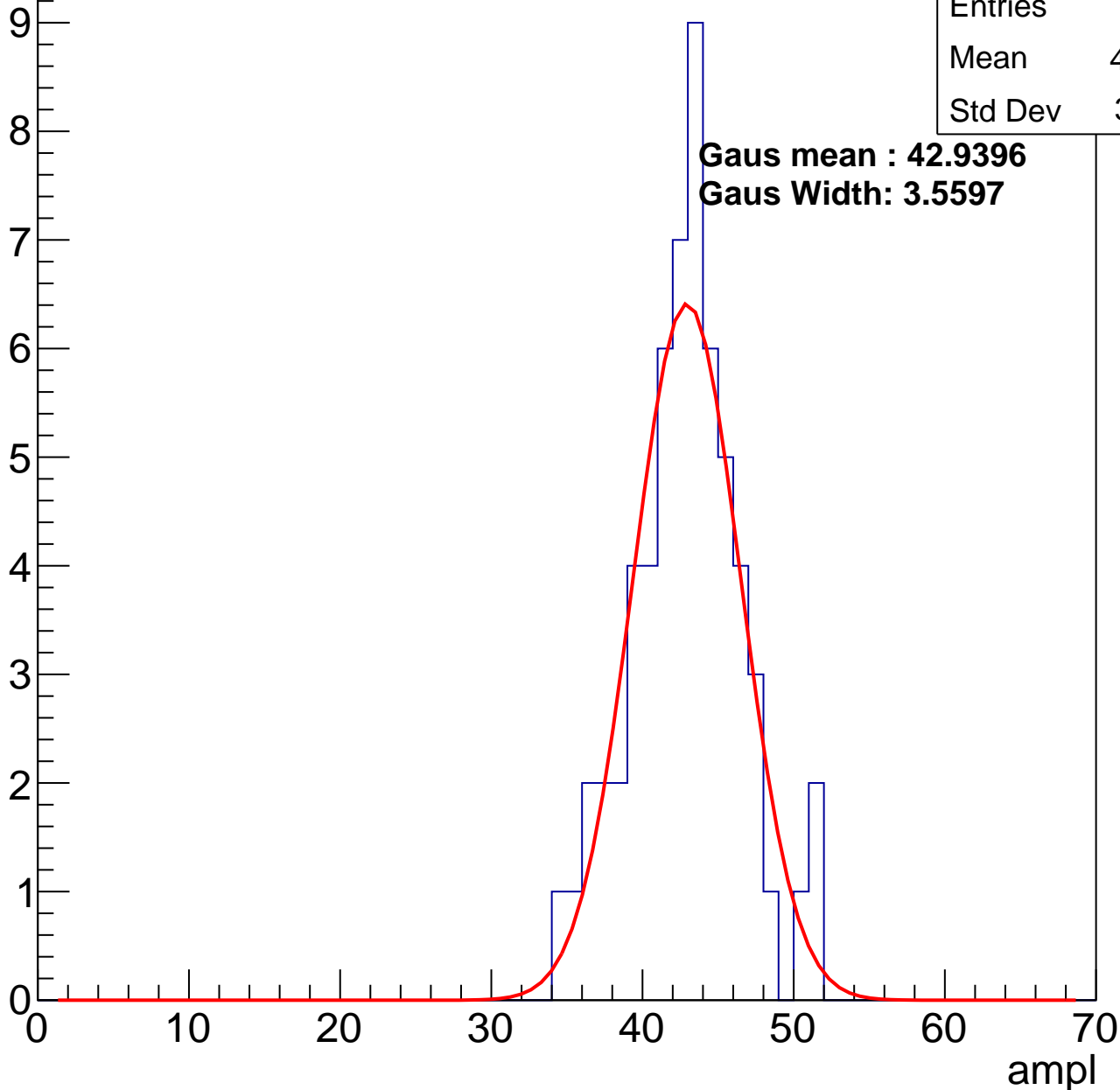
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.47
Std Dev	3.631

Gaus mean : 42.9396

Gaus Width: 3.5597

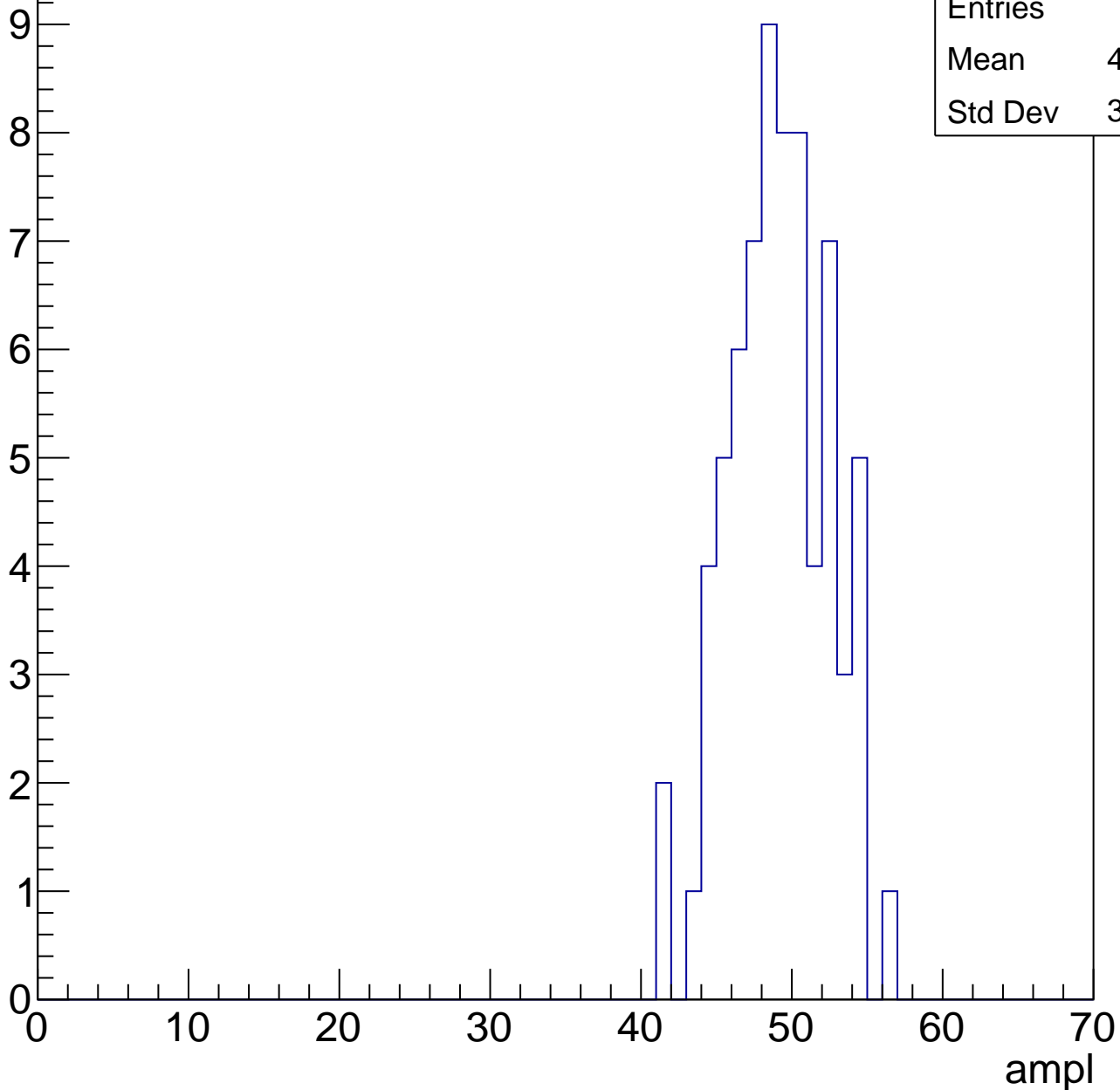


B1L102S, U19-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

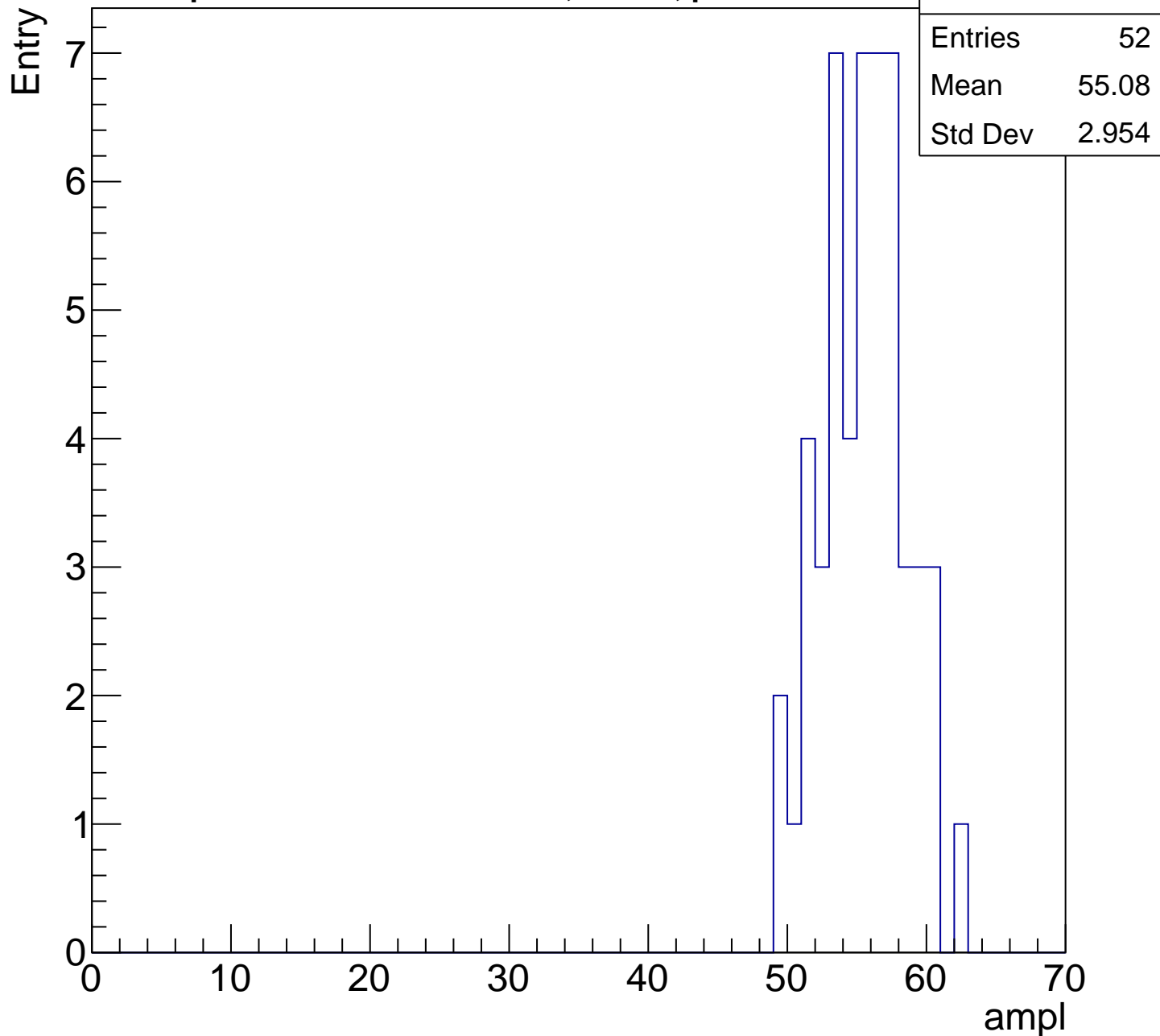
Entry

Entries	70
Mean	48.69
Std Dev	3.245



B1L102S, U19-ch18, adc4

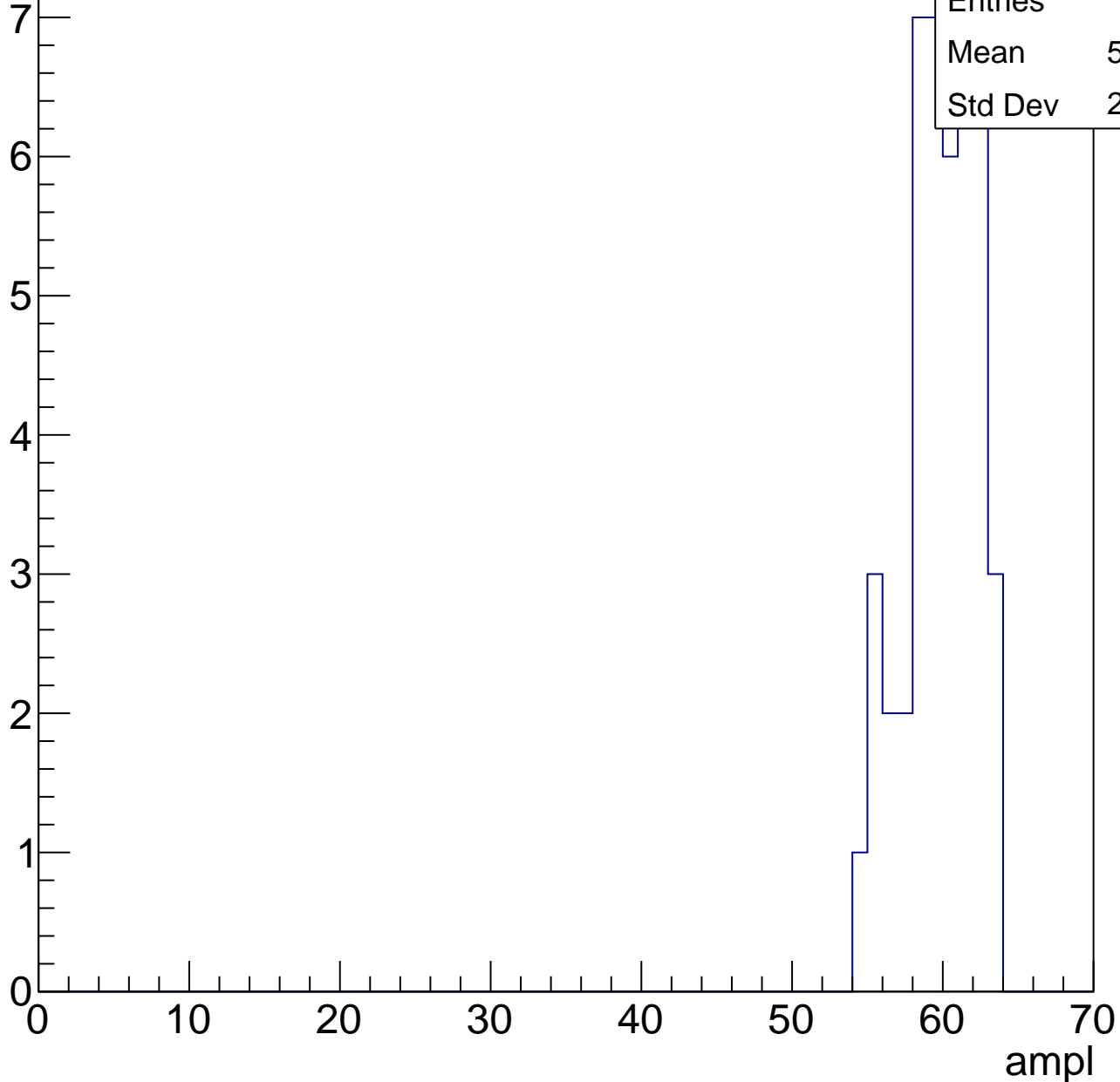
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U19-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

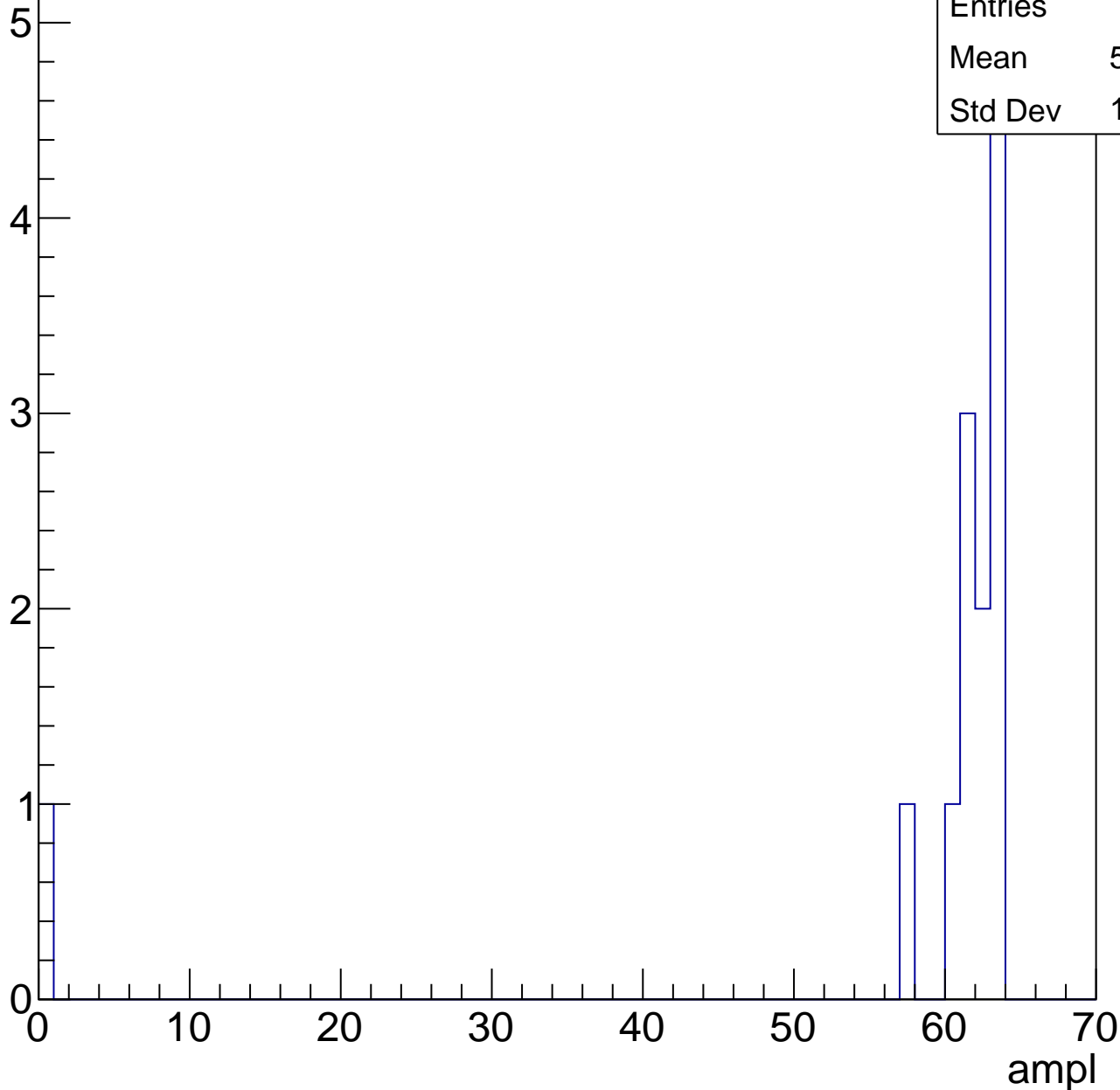


B1L102S, U19-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.85
Std Dev	16.49



B1L102S, U19-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch19, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	30.59
Std Dev	3.57

Gaus mean : 31.0540

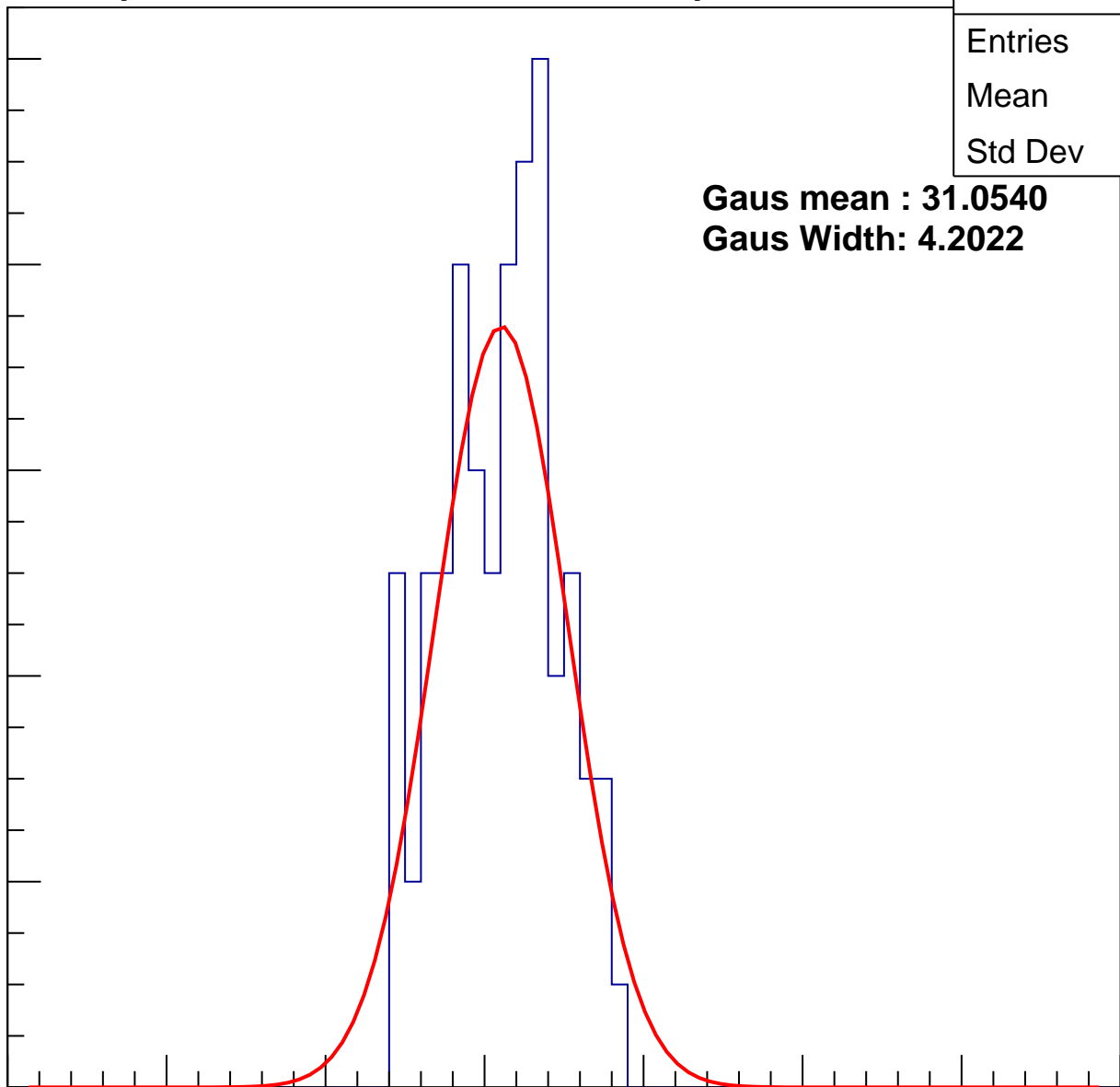
Gaus Width: 4.2022

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch19, adc1

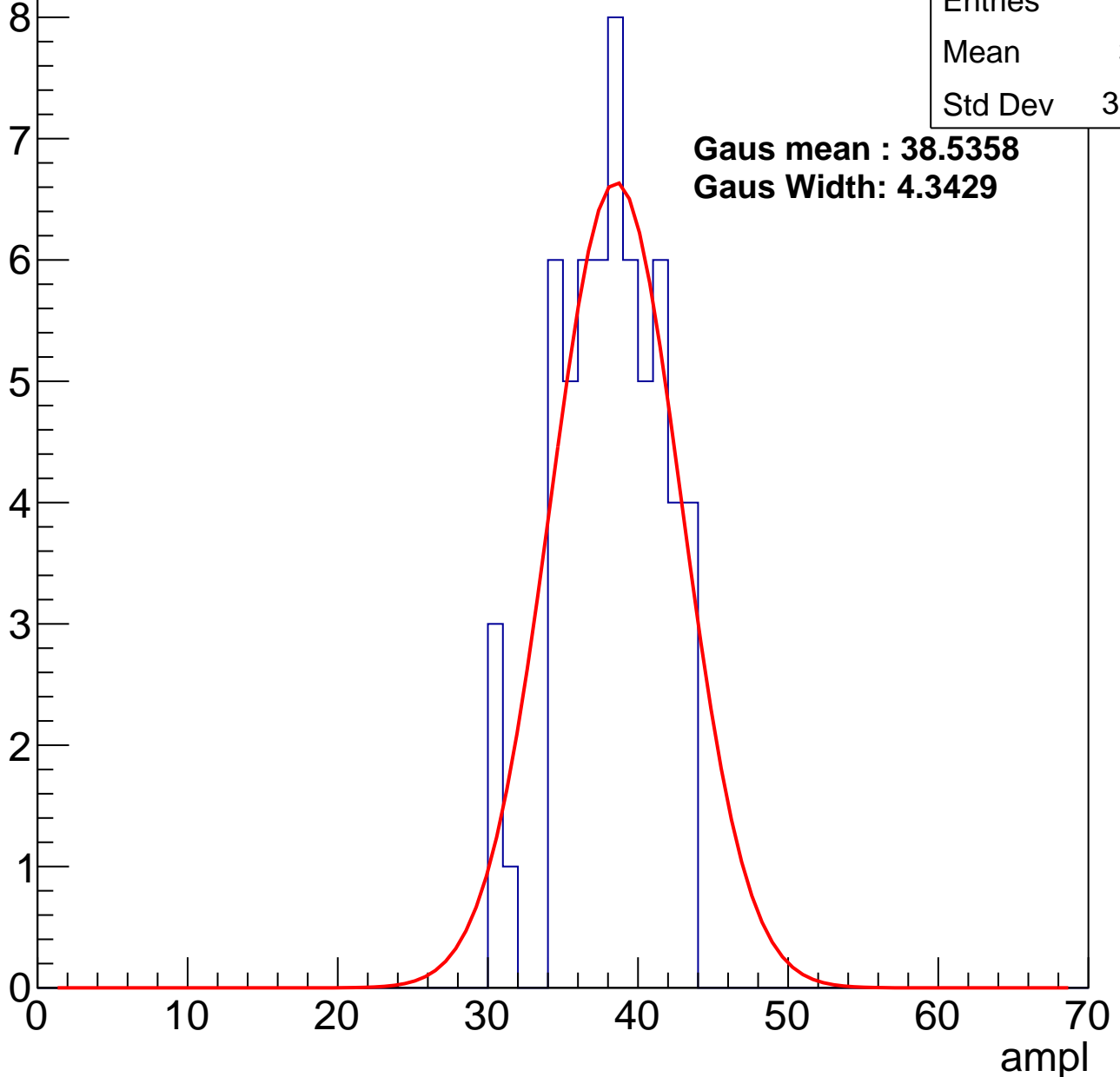
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	37.7
Std Dev	3.293

Gaus mean : 38.5358

Gaus Width: 4.3429



B1L102S, U19-ch19, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	44.88
Std Dev	3.749

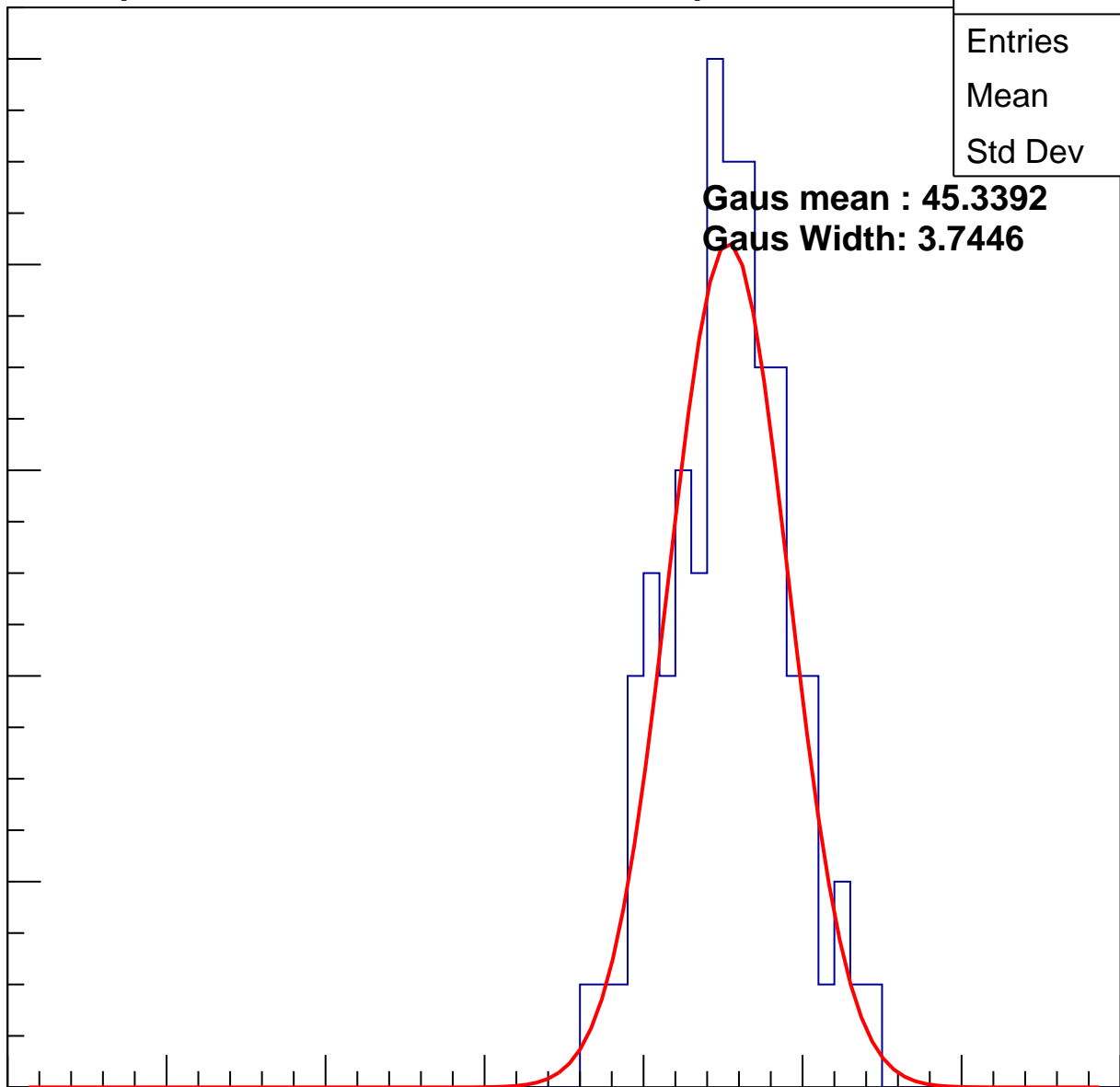
Gaus mean : 45.3392

Gaus Width: 3.7446

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

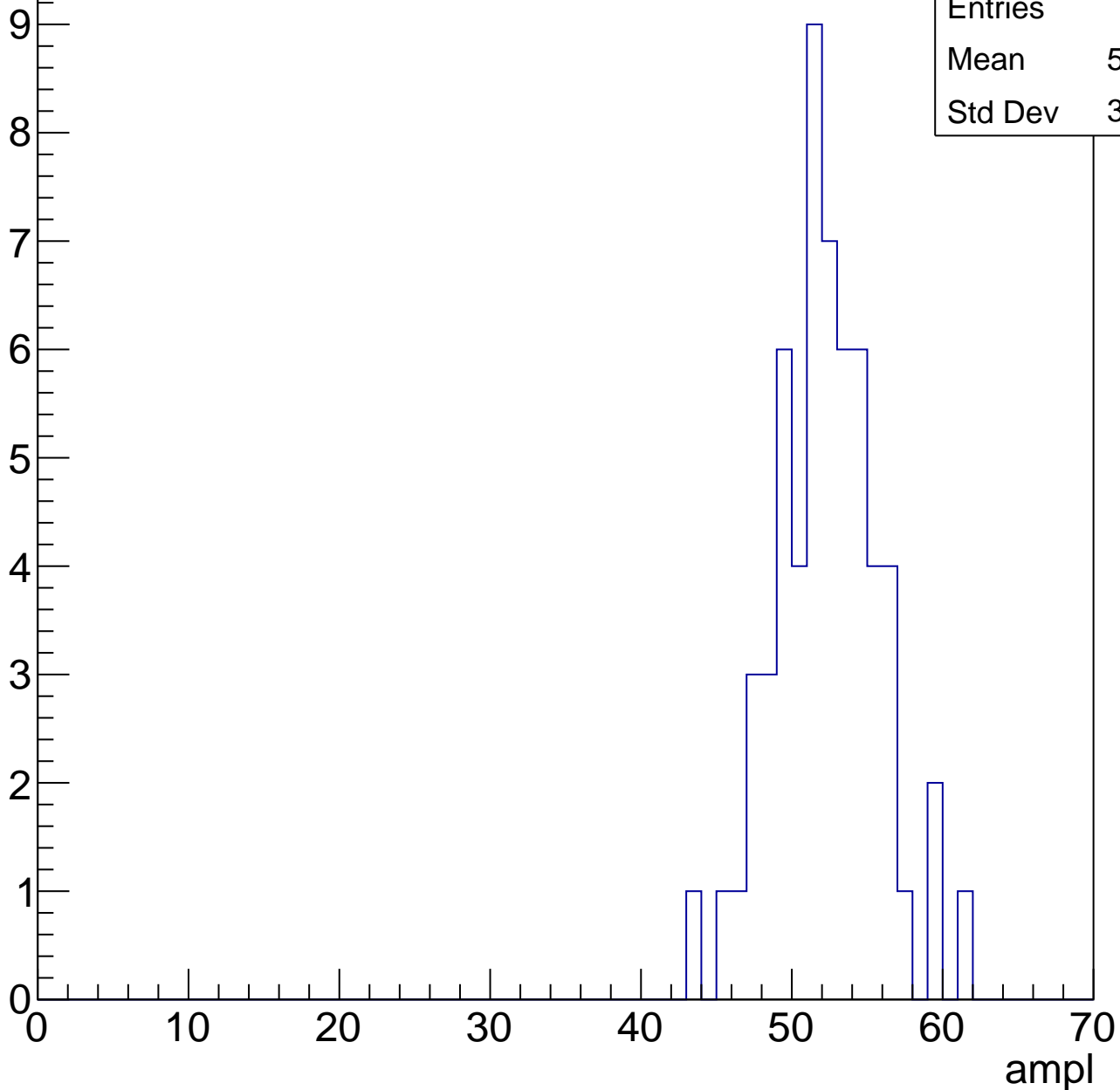


B1L102S, U19-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	51.83
Std Dev	3.445

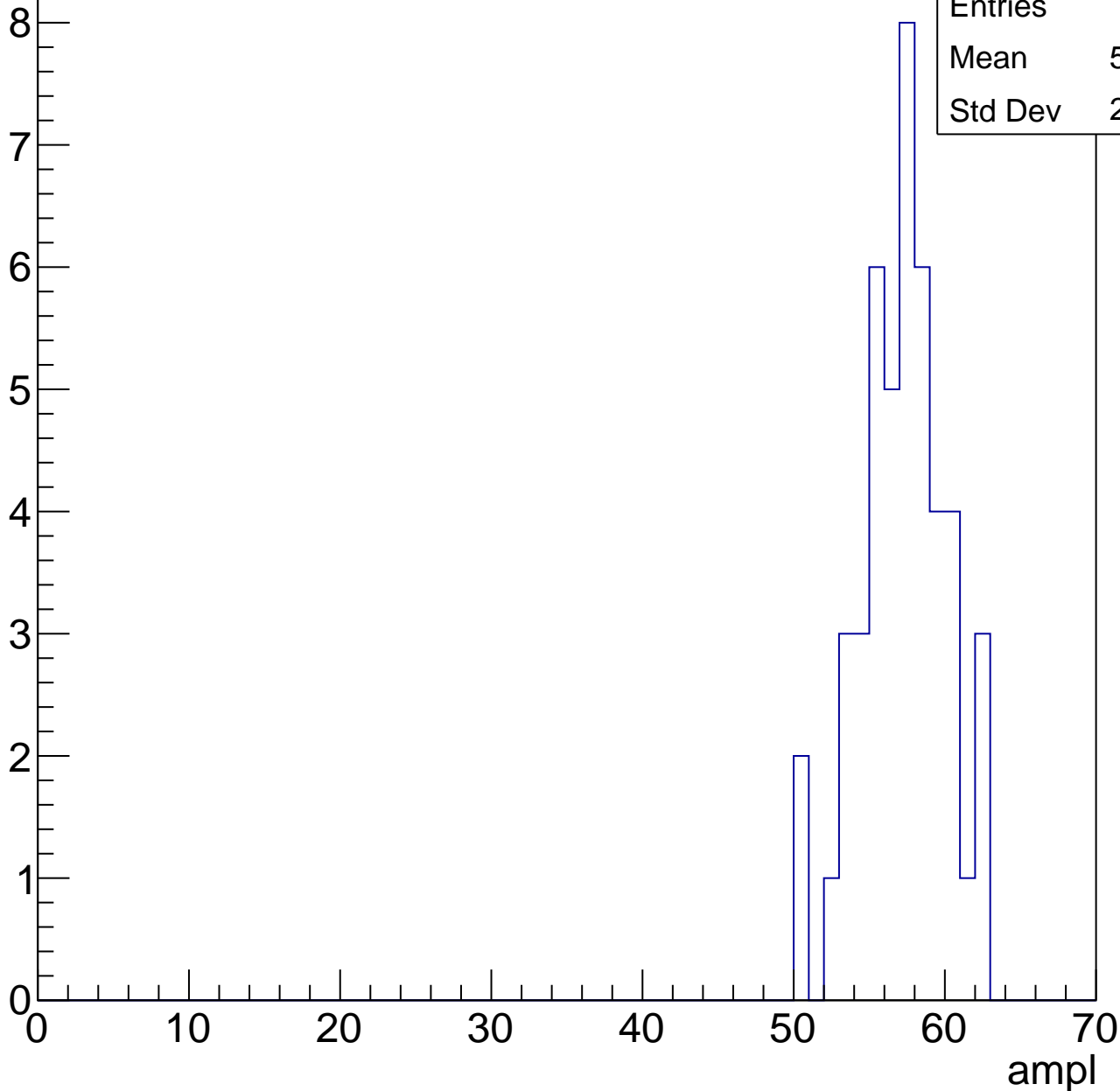


B1L102S, U19-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	56.74
Std Dev	2.847

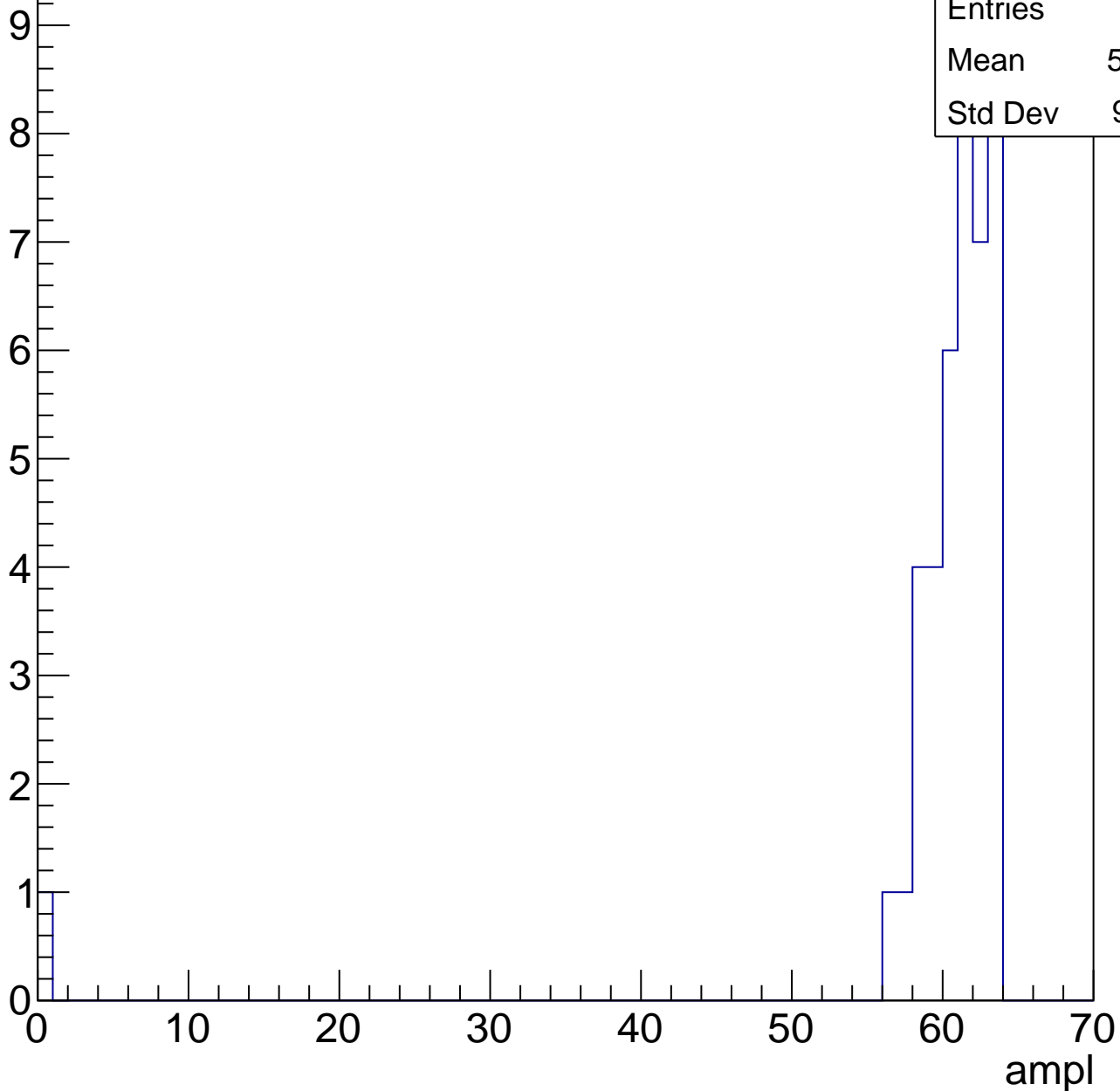


B1L102S, U19-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	59.27
Std Dev	9.551



B1L102S, U19-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch20, adc0

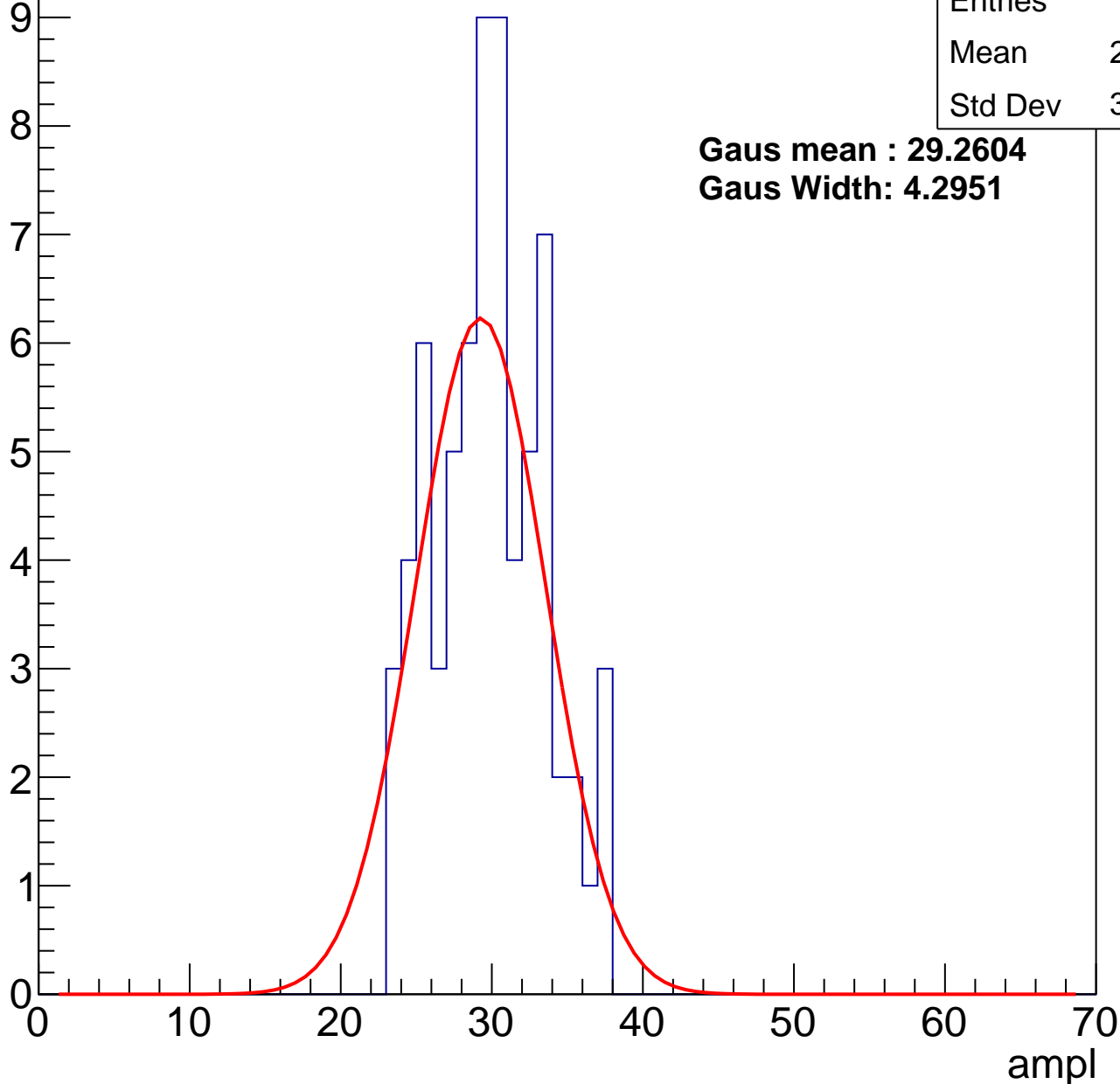
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.38
Std Dev	3.596

Gaus mean : 29.2604

Gaus Width: 4.2951



B1L102S, U19-ch20, adc1

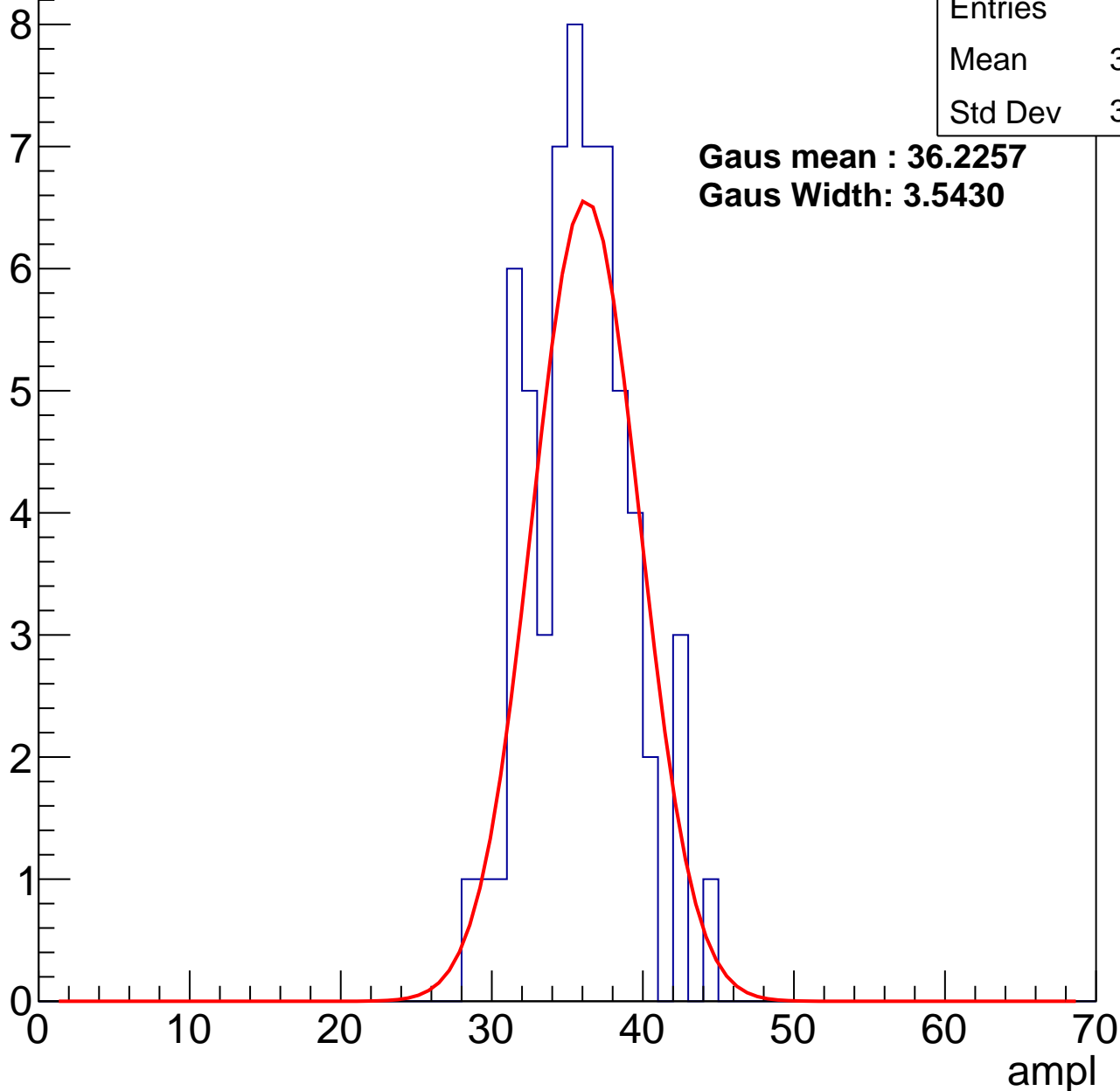
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.36
Std Dev	3.349

Gaus mean : 36.2257

Gaus Width: 3.5430



B1L102S, U19-ch20, adc2

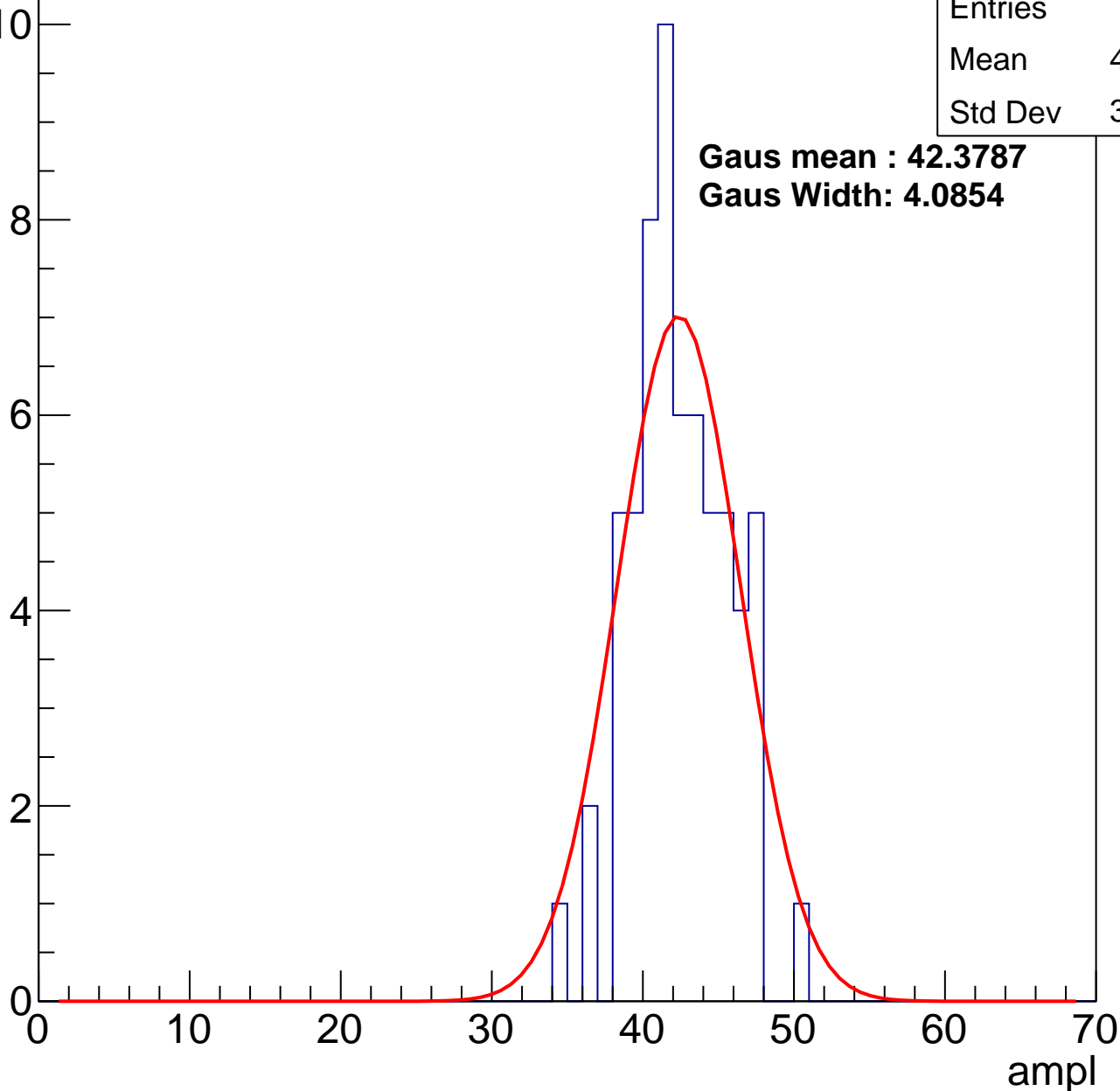
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.98
Std Dev	3.155

Gaus mean : 42.3787

Gaus Width: 4.0854

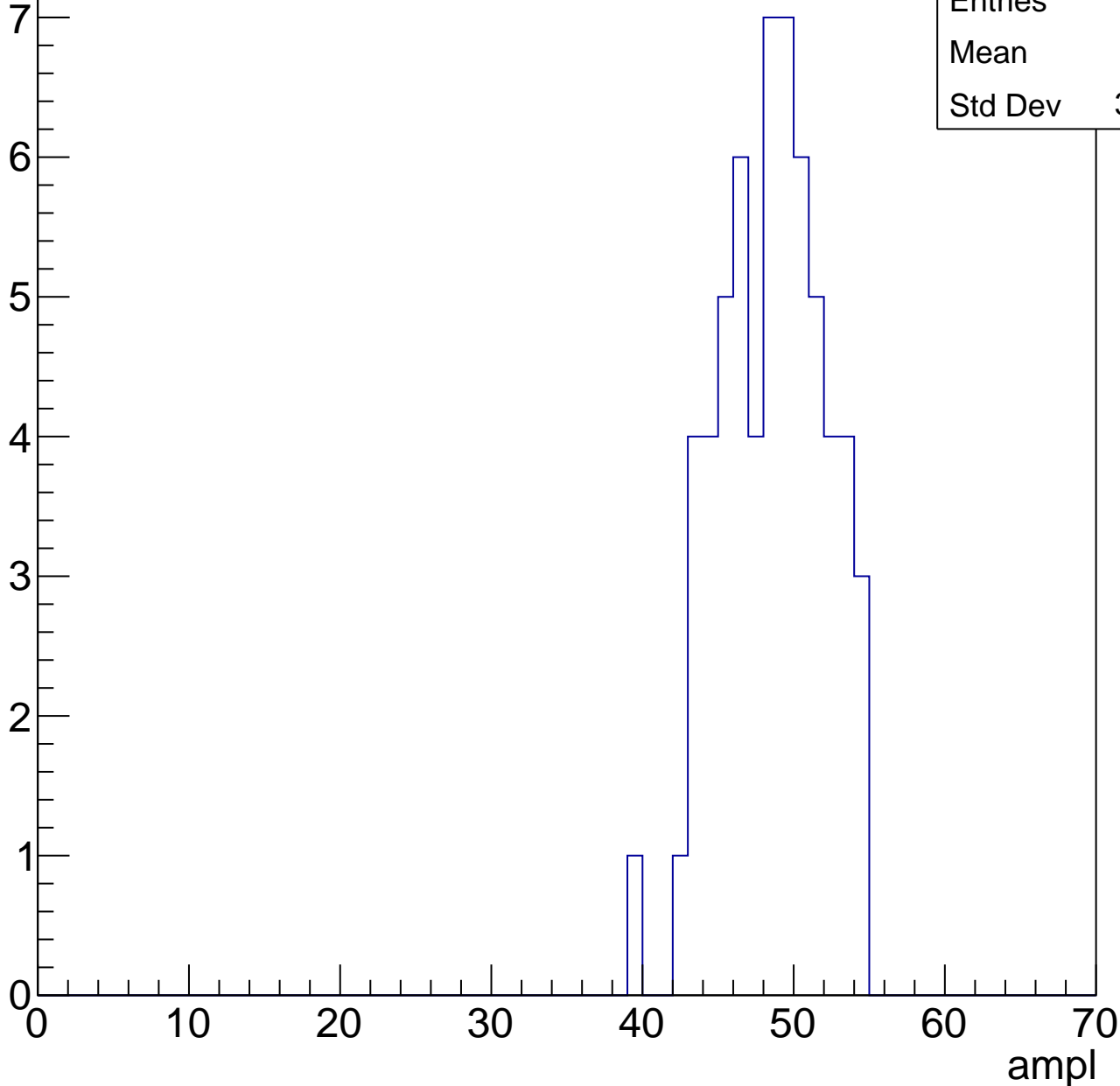


B1L102S, U19-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.1
Std Dev	3.391

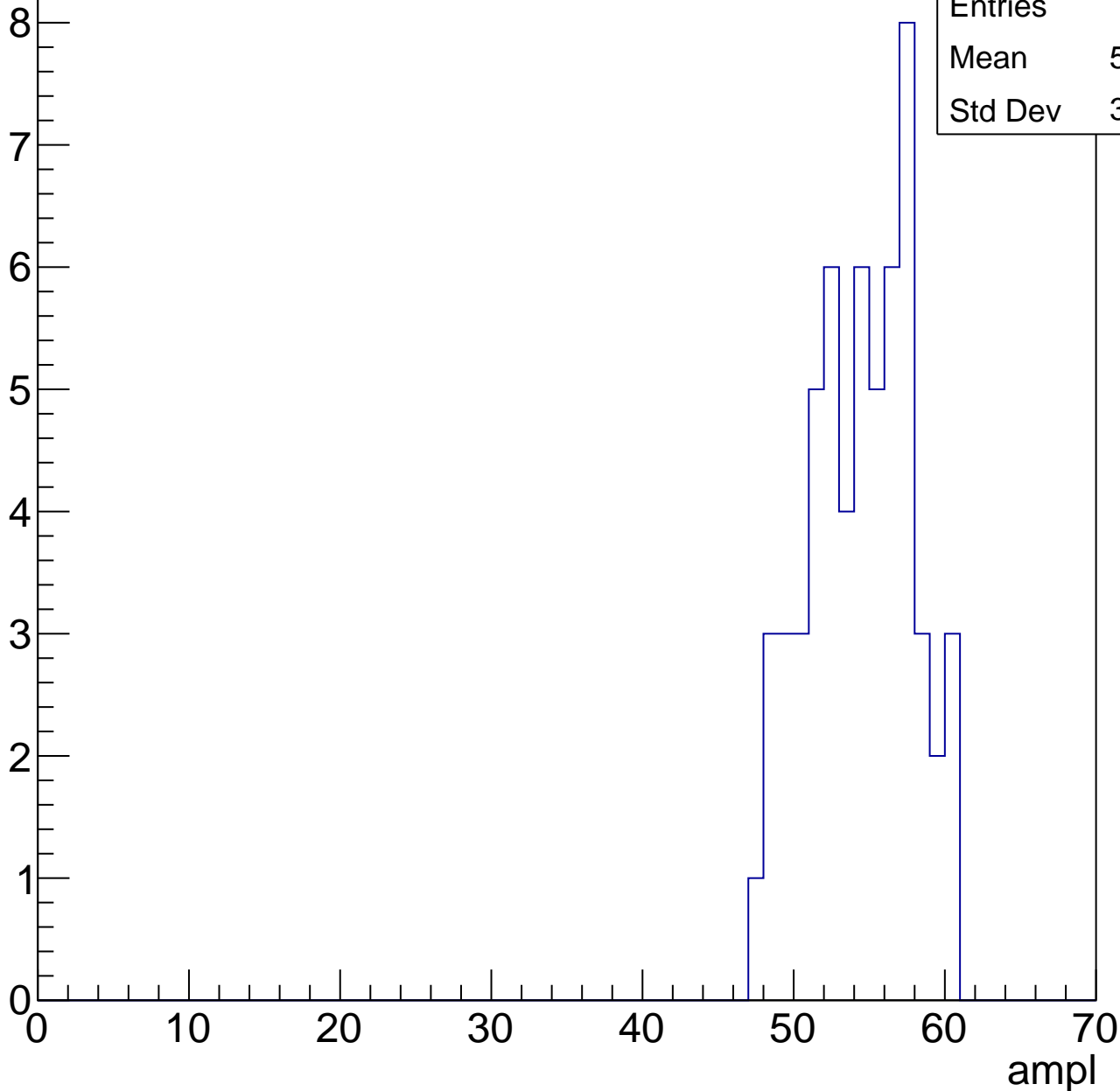


B1L102S, U19-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.97
Std Dev	3.373

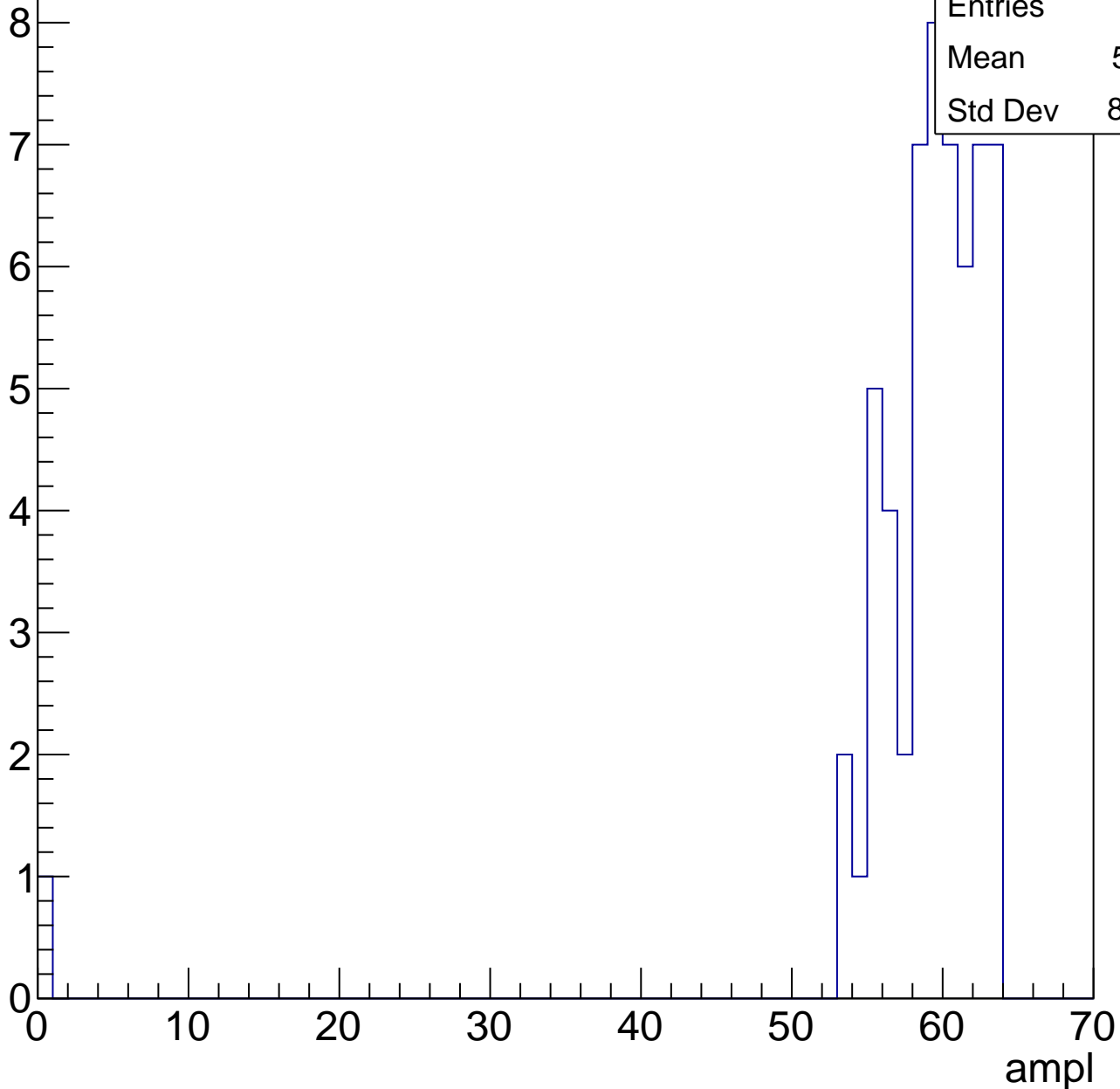


B1L102S, U19-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

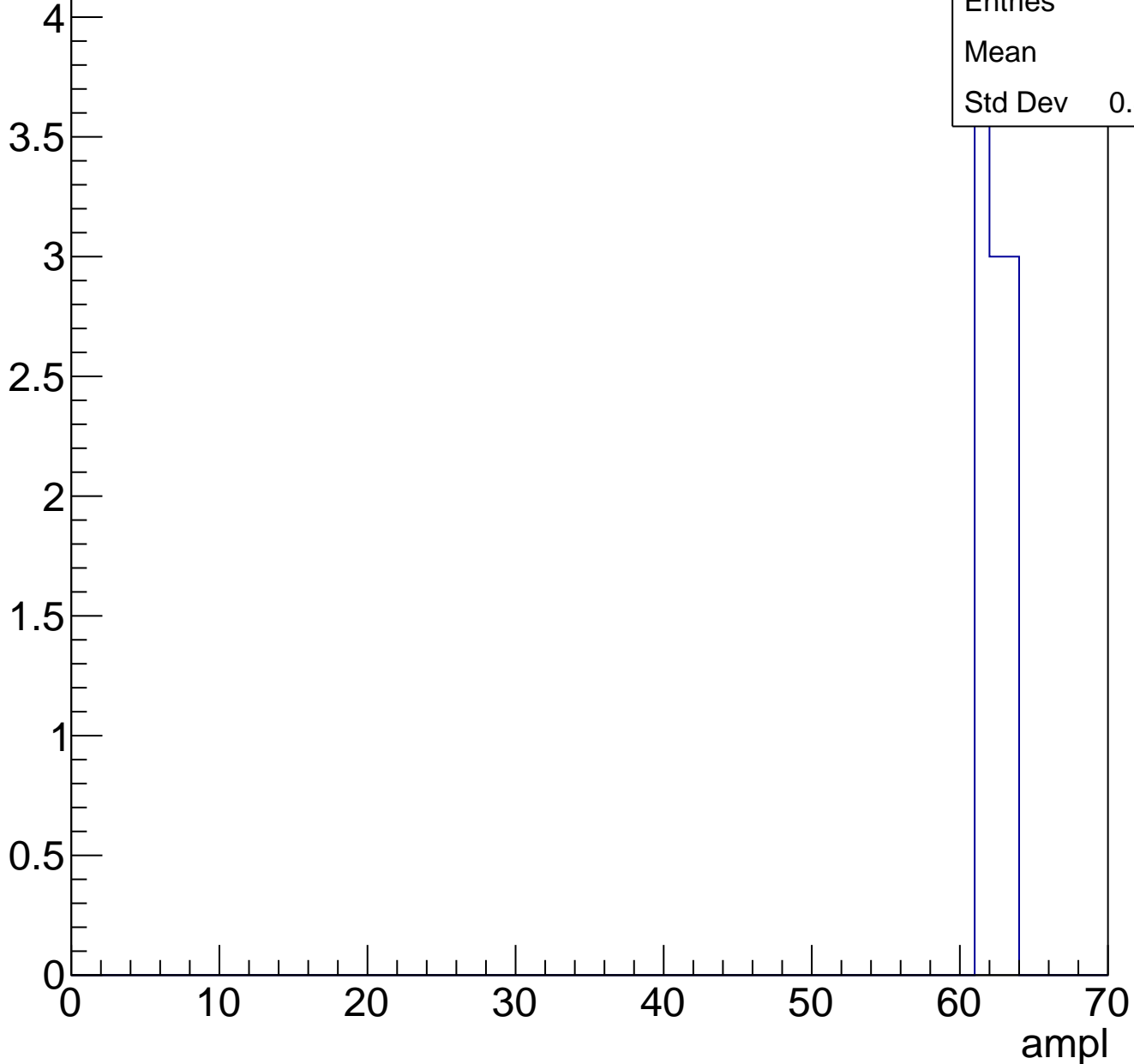
Entries	57
Mean	58.11
Std Dev	8.239



B1L102S, U19-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

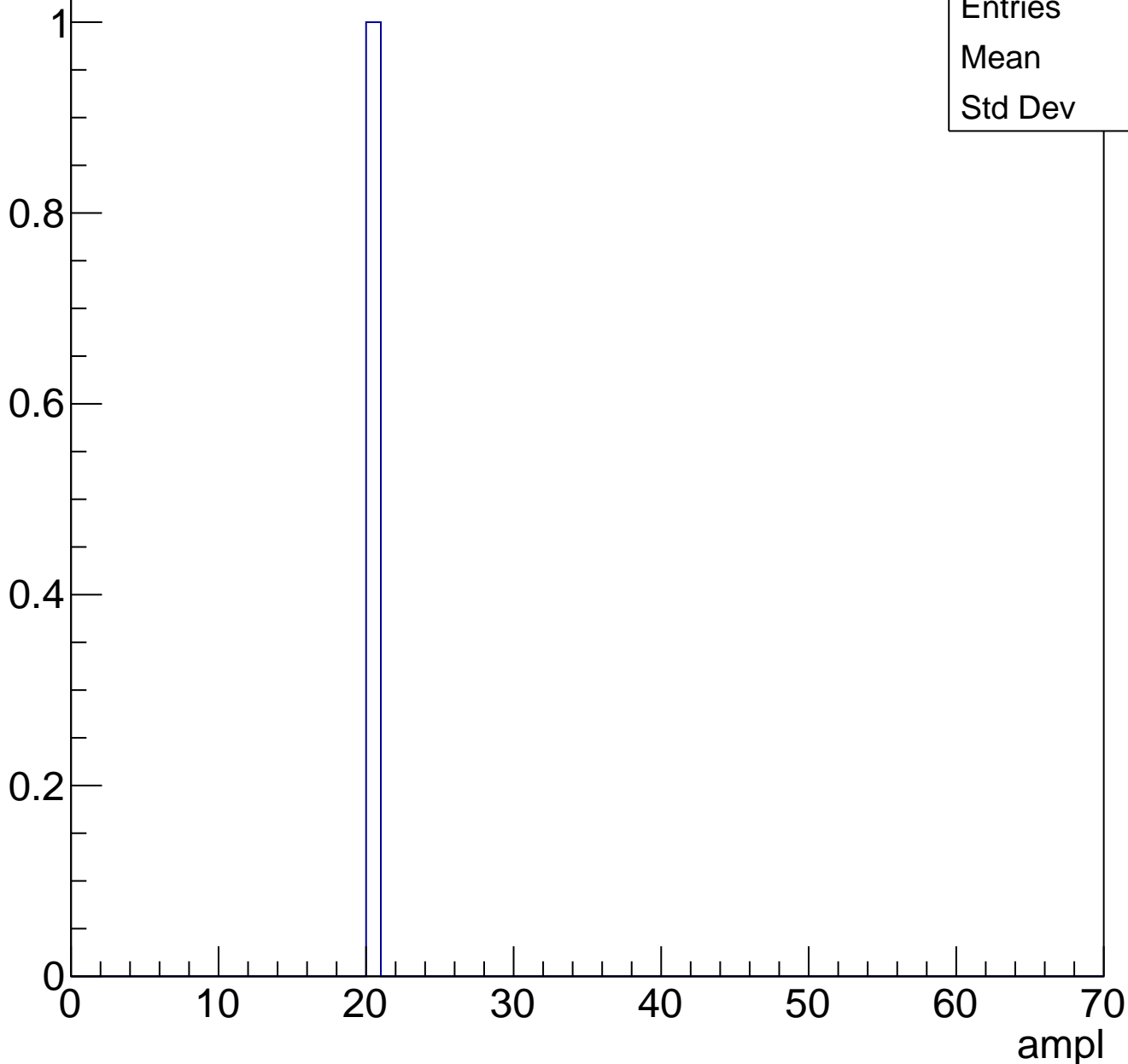
Entry



B1L102S, U19-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U19-ch21, adc0

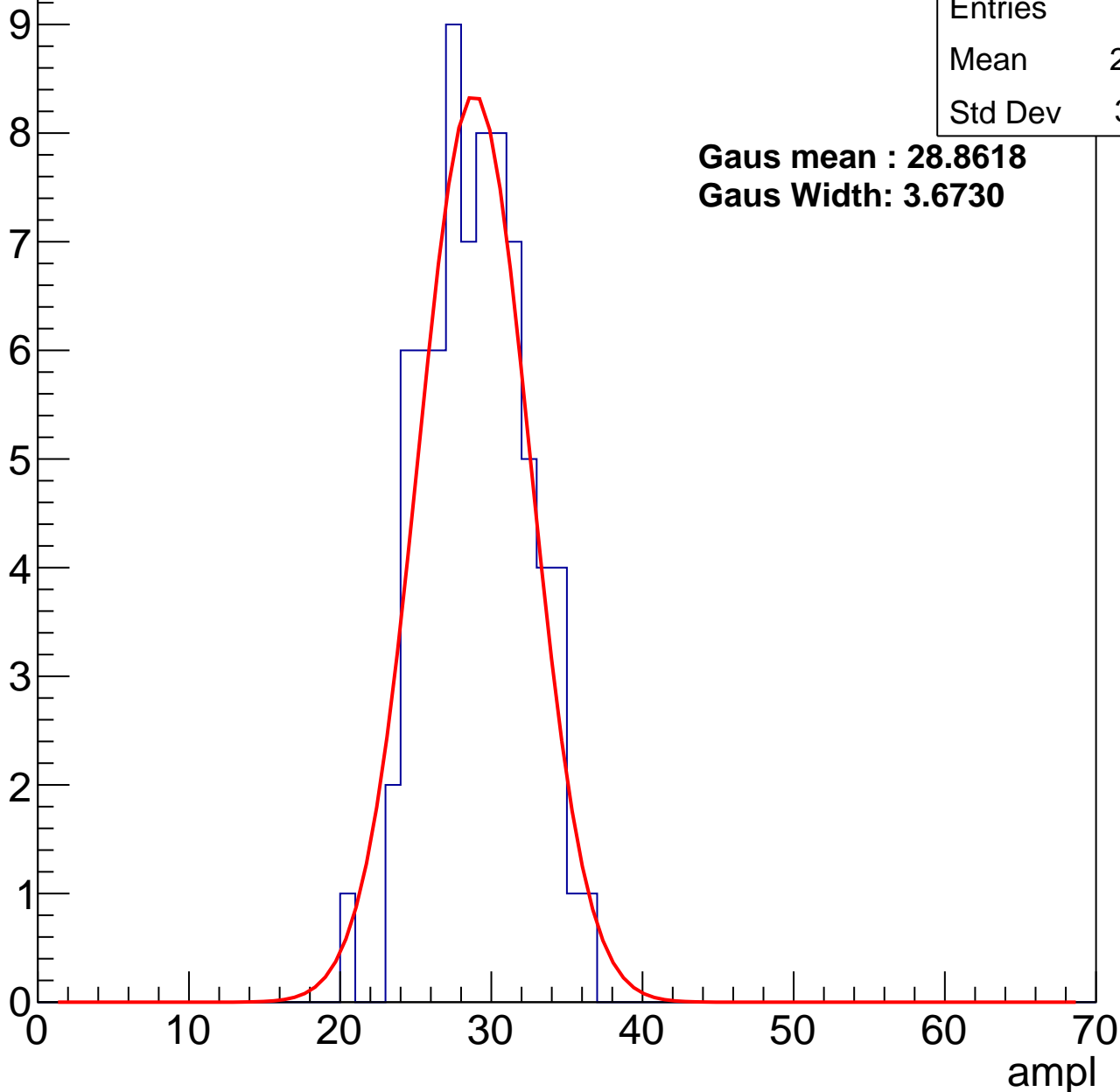
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.57
Std Dev	3.291

Gaus mean : 28.8618

Gaus Width: 3.6730



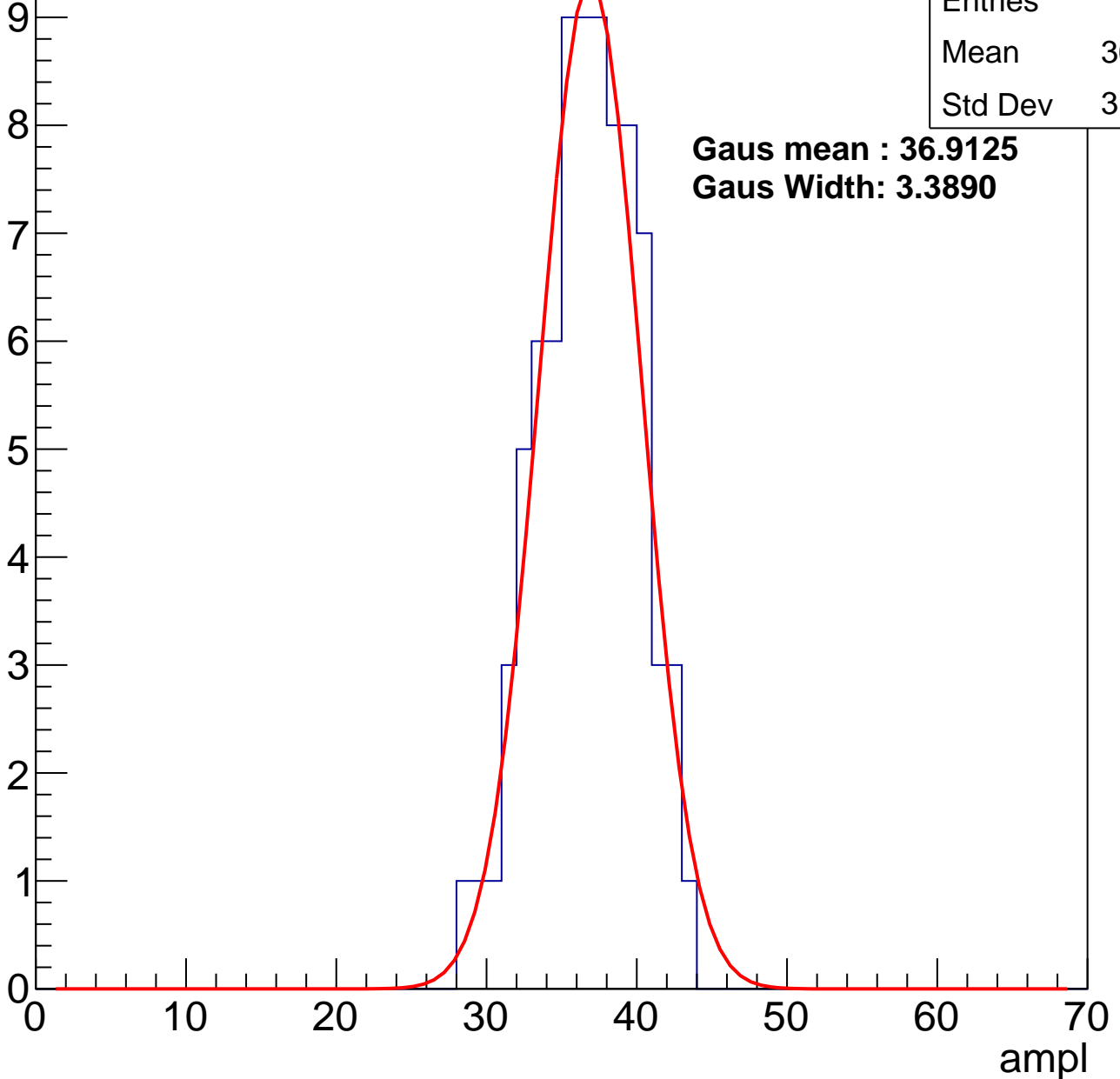
B1L102S, U19-ch21, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.27
Std Dev	3.244

Gaus mean : 36.9125
Gaus Width: 3.3890



B1L102S, U19-ch21, adc2

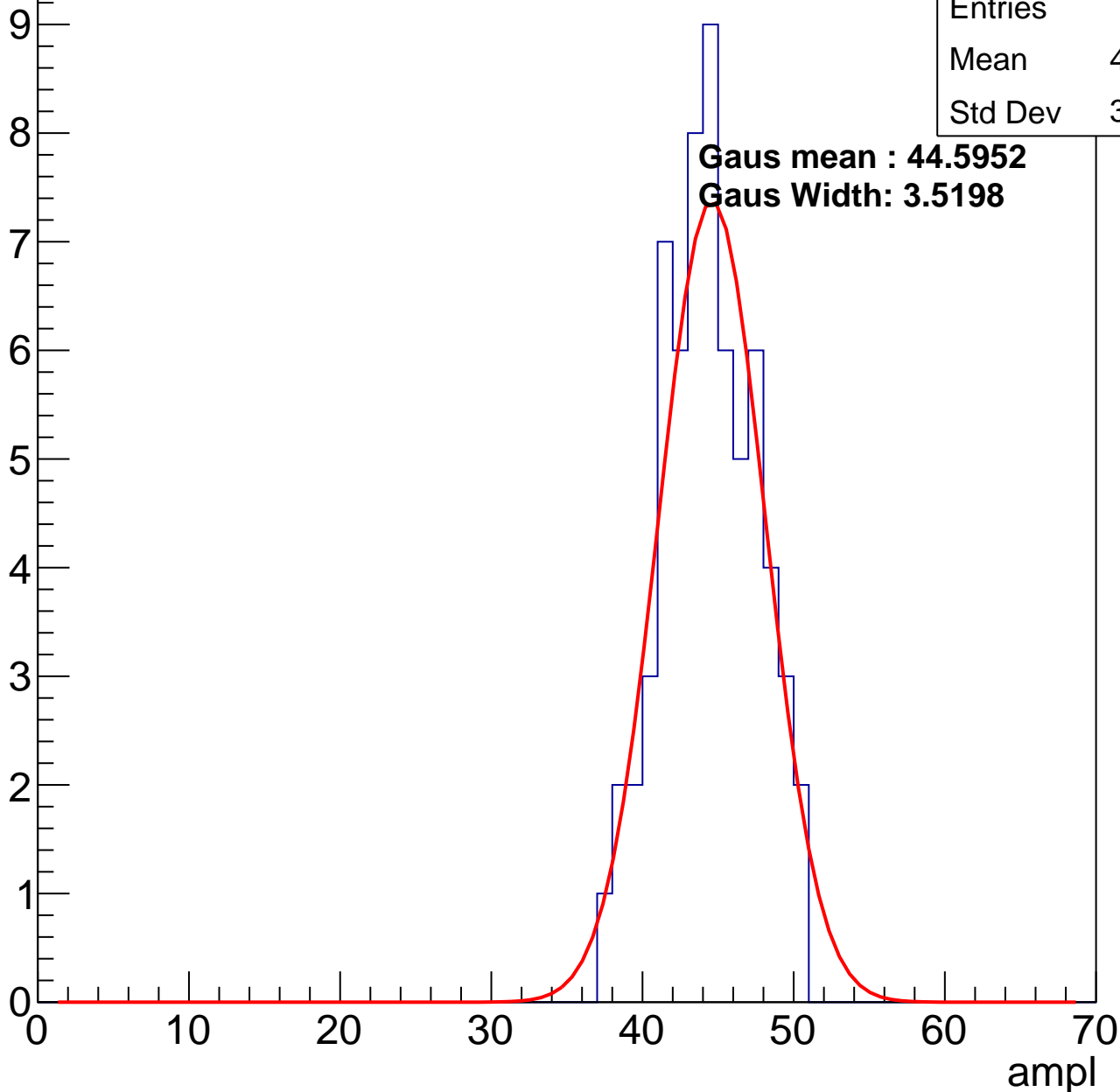
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.92
Std Dev	3.074

Gaus mean : 44.5952

Gaus Width: 3.5198

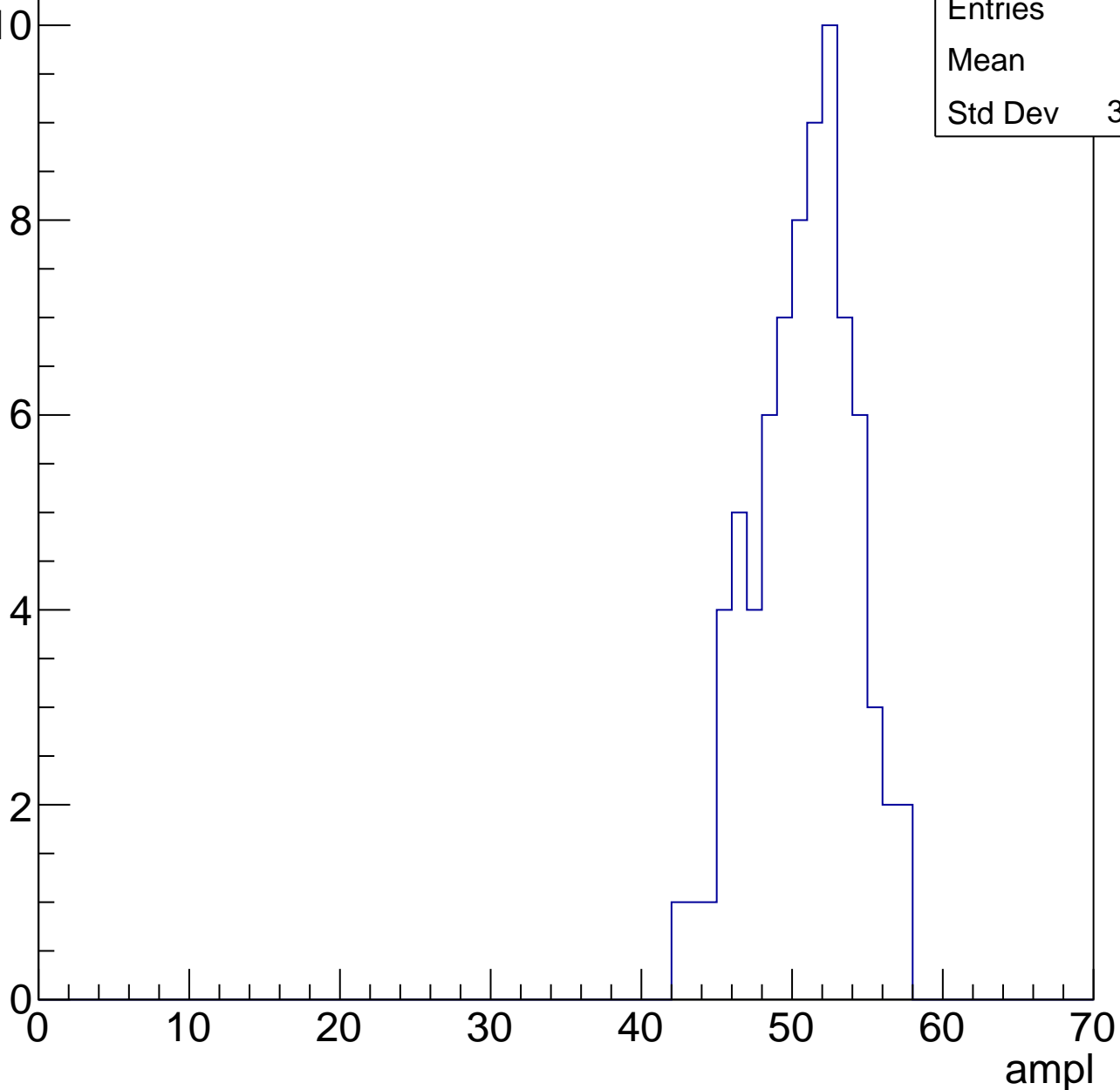


B1L102S, U19-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	50.3
Std Dev	3.329

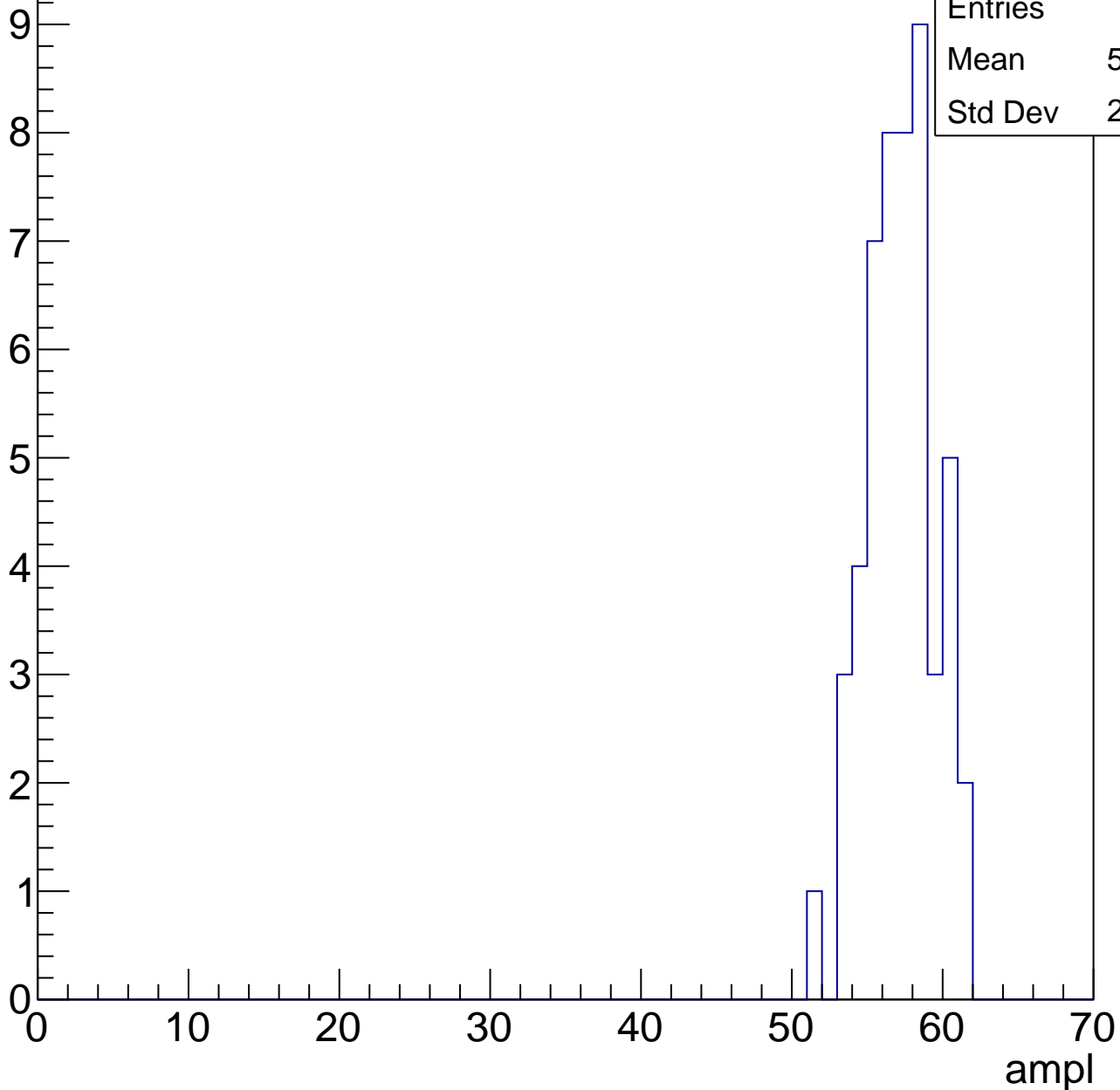


B1L102S, U19-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.72
Std Dev	2.236

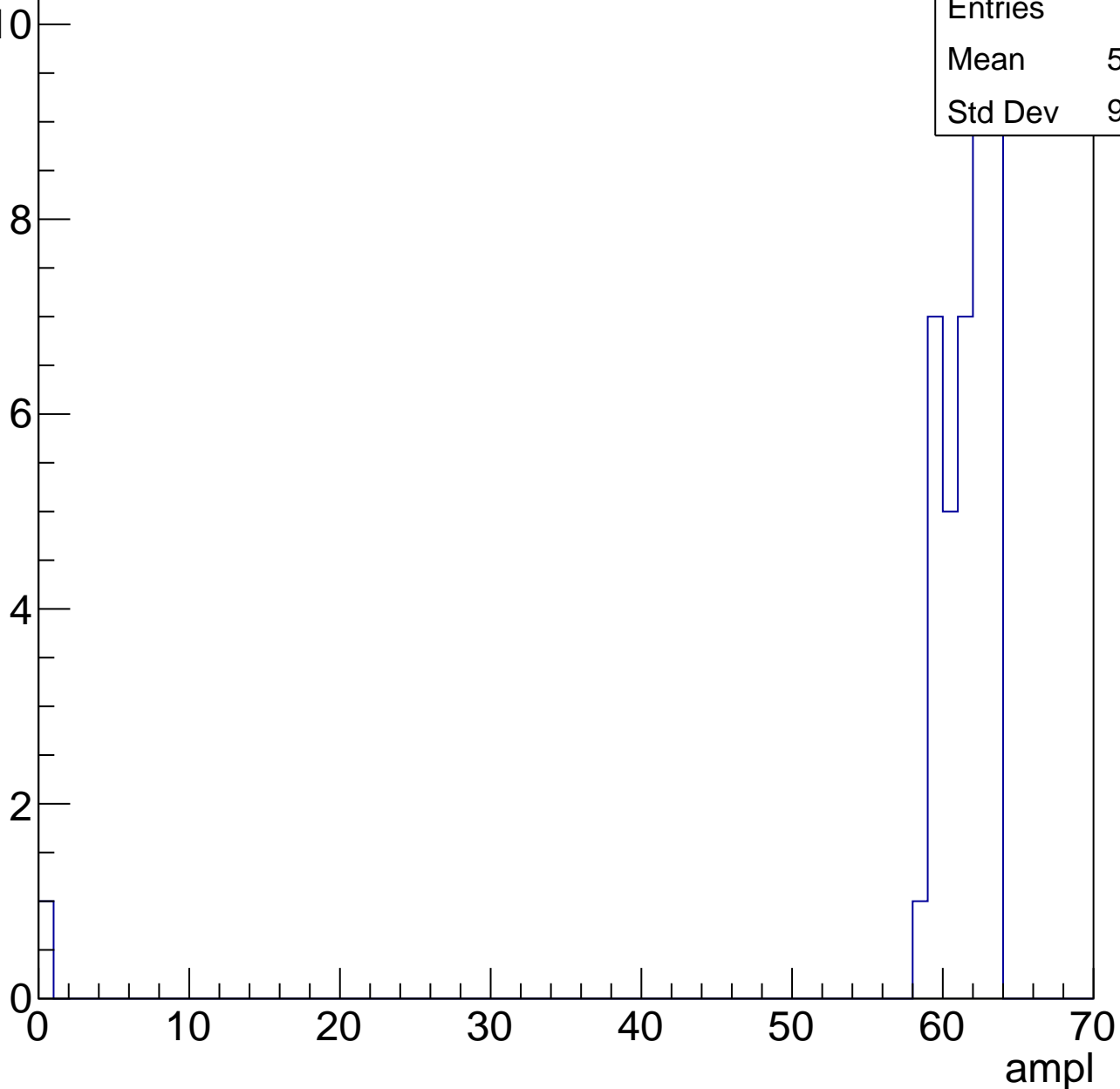


B1L102S, U19-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

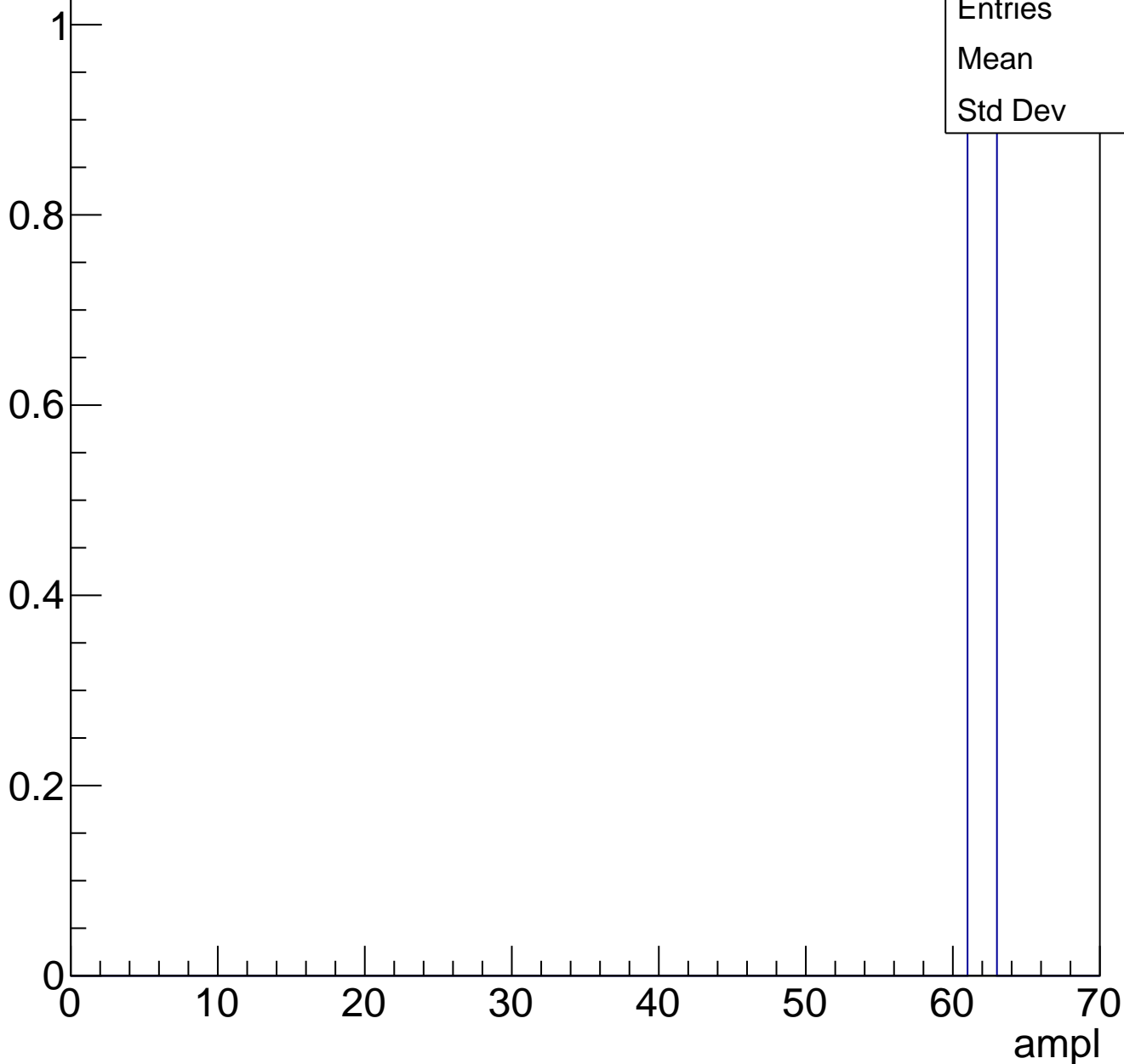
Entries	40
Mean	59.65
Std Dev	9.668



B1L102S, U19-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch22, adc0

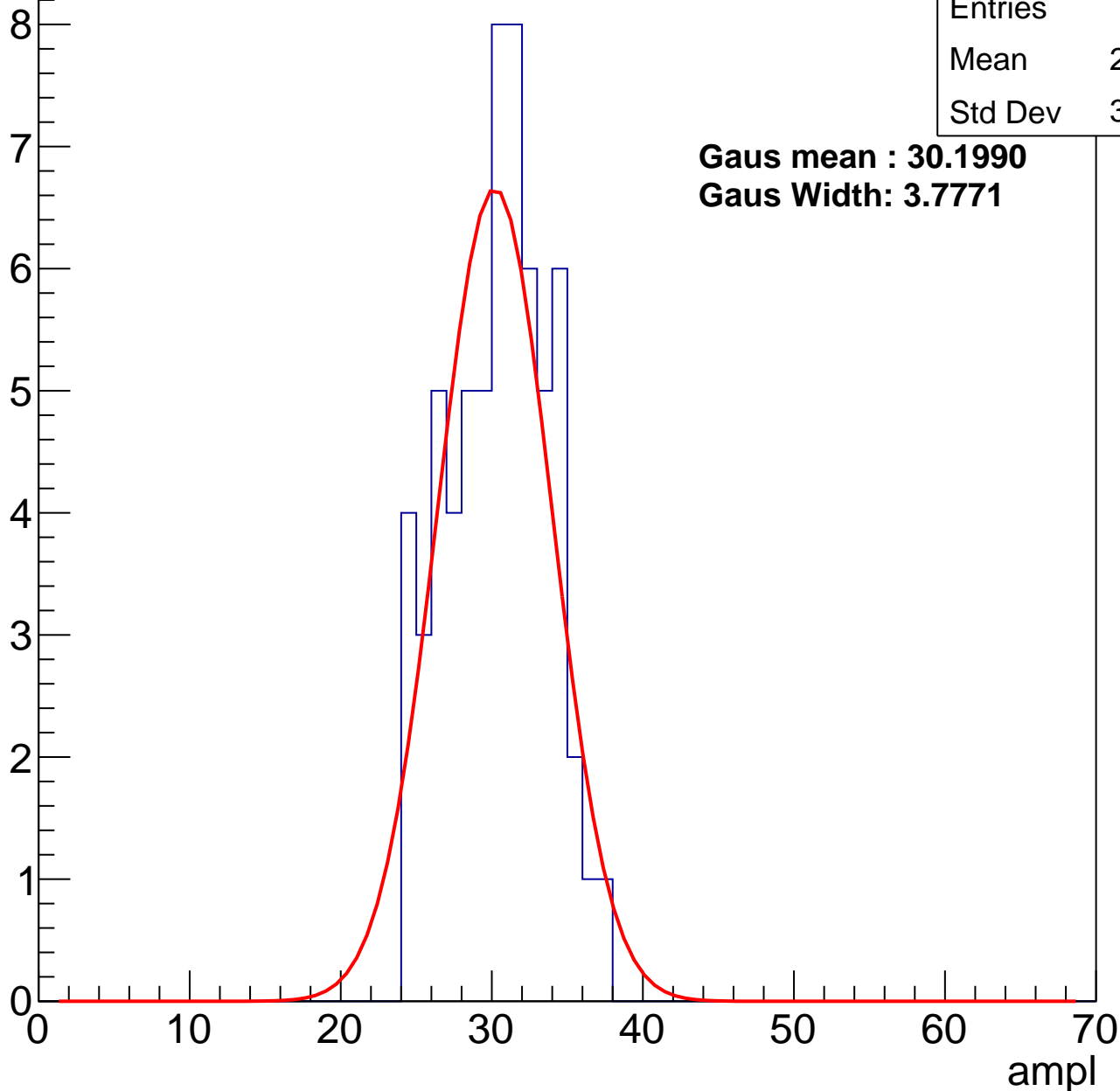
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.94
Std Dev	3.256

Gaus mean : 30.1990

Gaus Width: 3.7771



B1L102S, U19-ch22, adc1

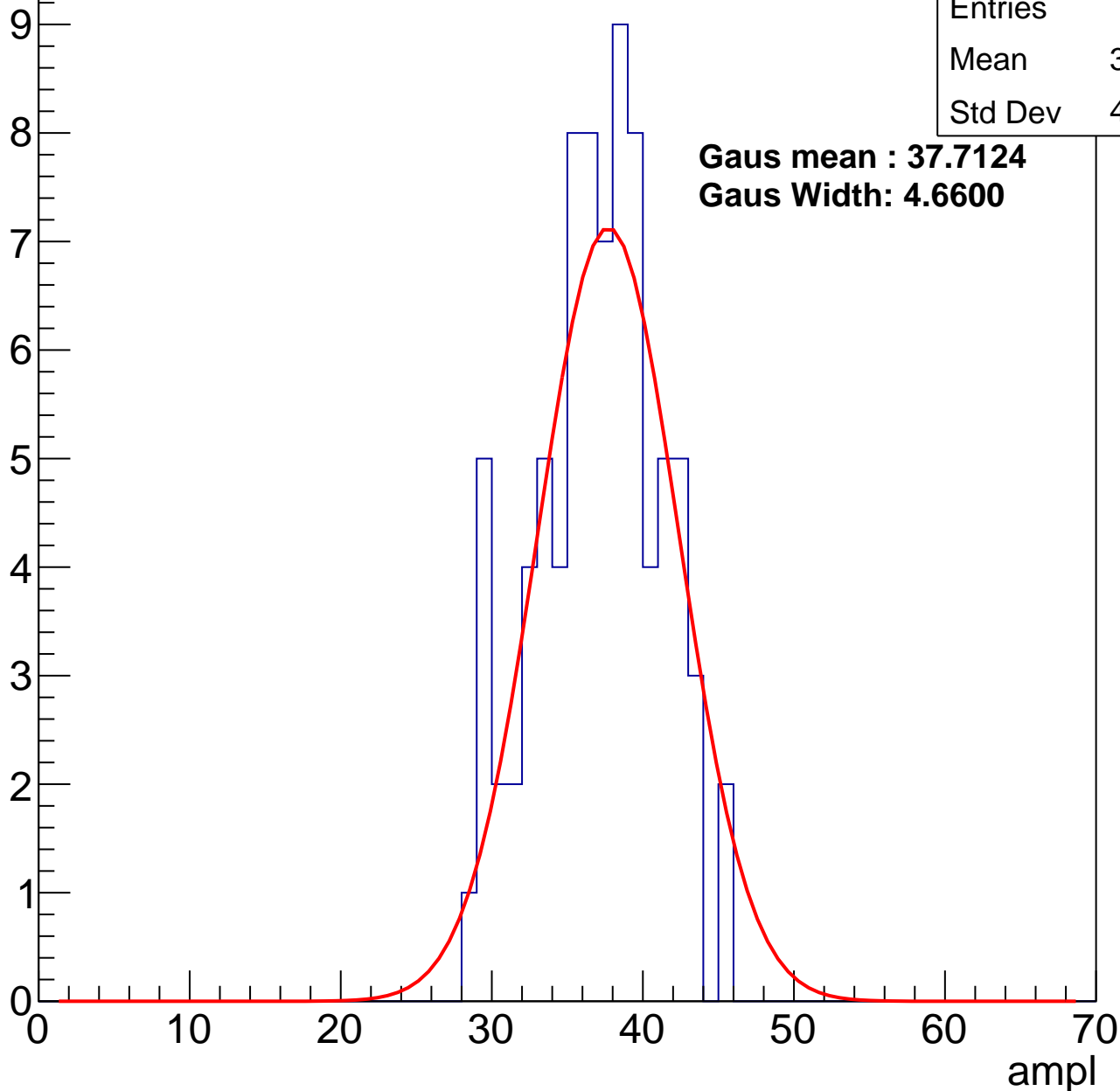
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	36.57
Std Dev	4.027

Gaus mean : 37.7124

Gaus Width: 4.6600



B1L102S, U19-ch22, adc2

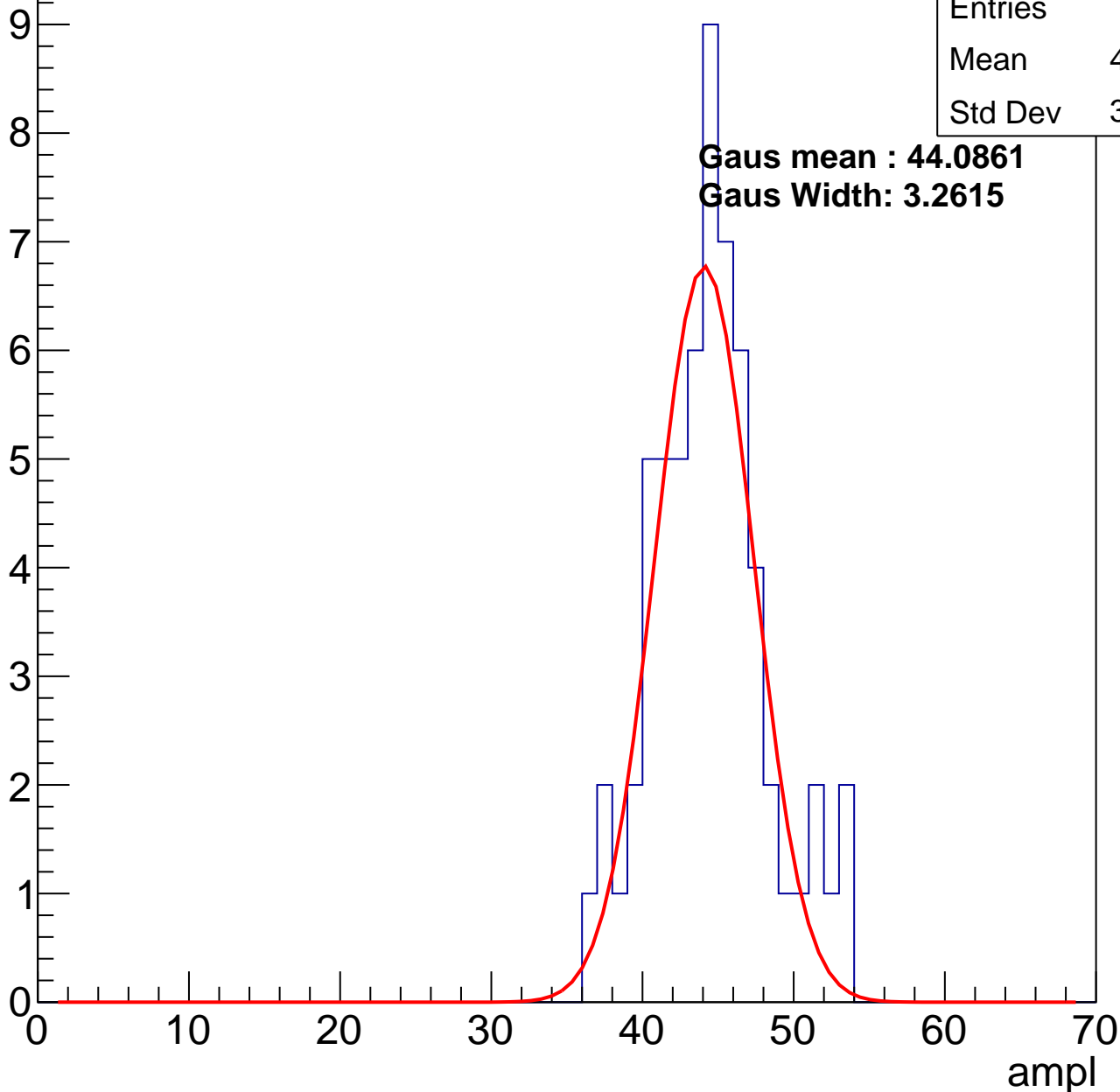
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.02
Std Dev	3.774

Gaus mean : 44.0861

Gaus Width: 3.2615

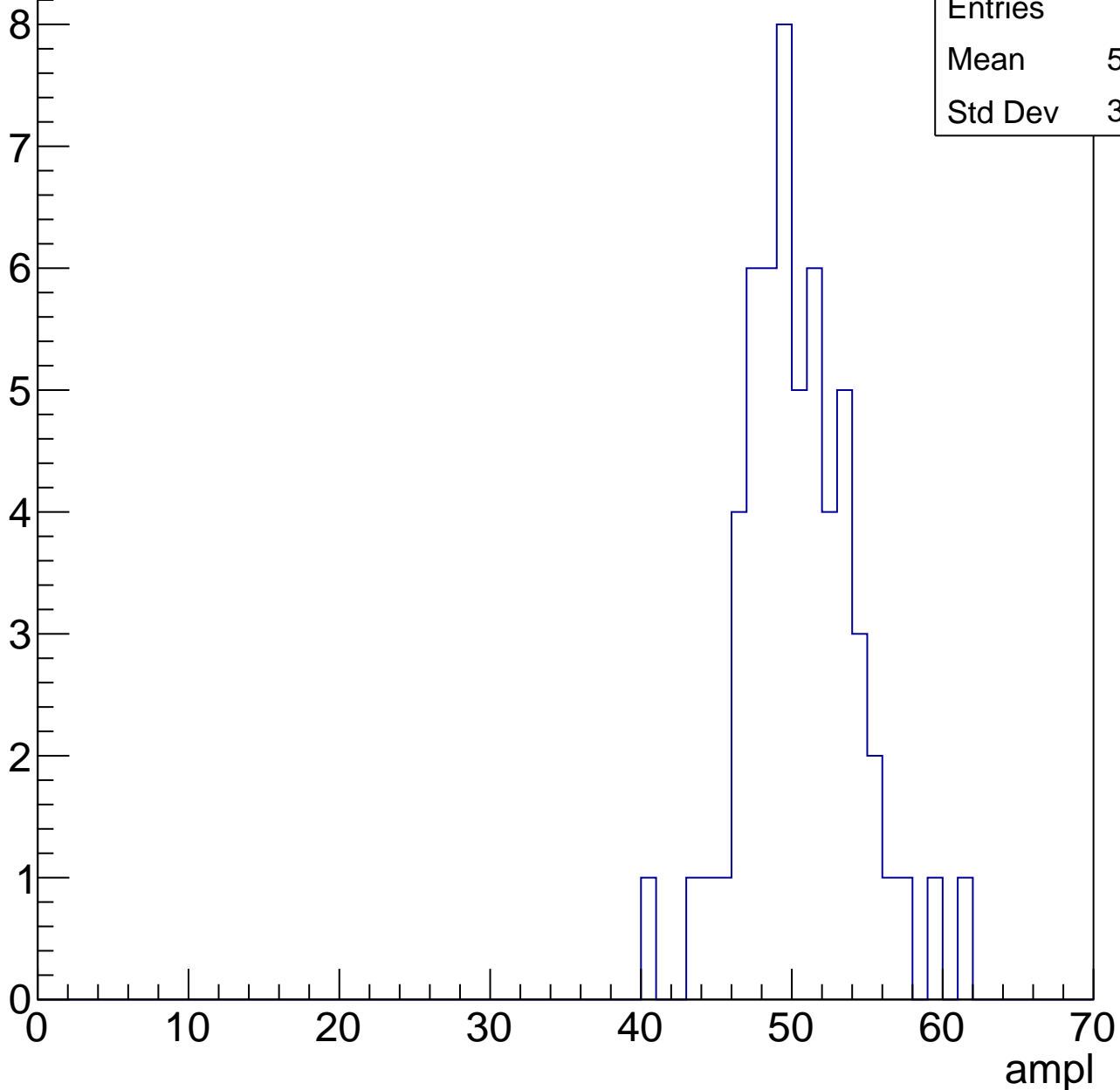


B1L102S, U19-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

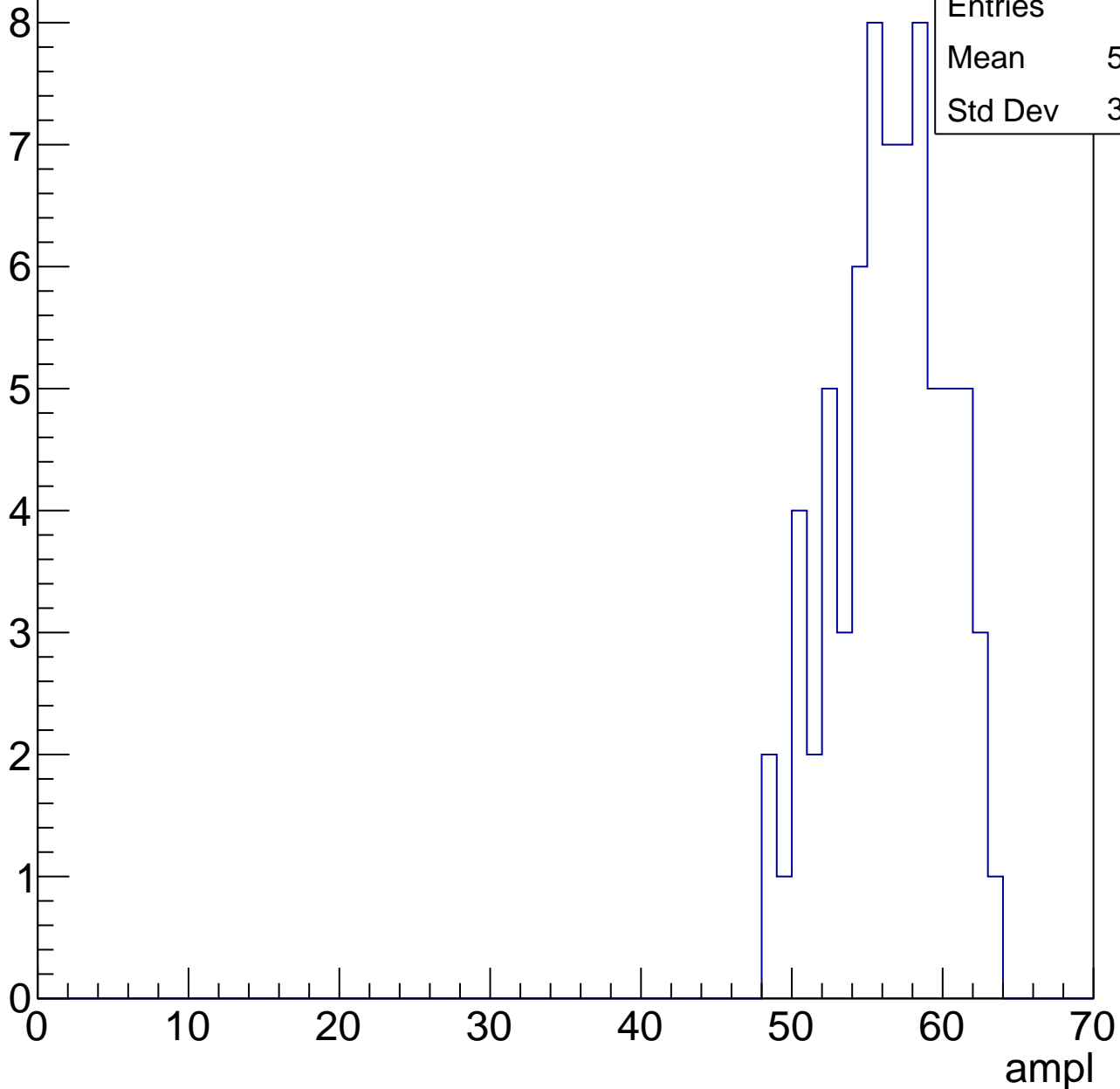
Entries	57
Mean	50.04
Std Dev	3.774



B1L102S, U19-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

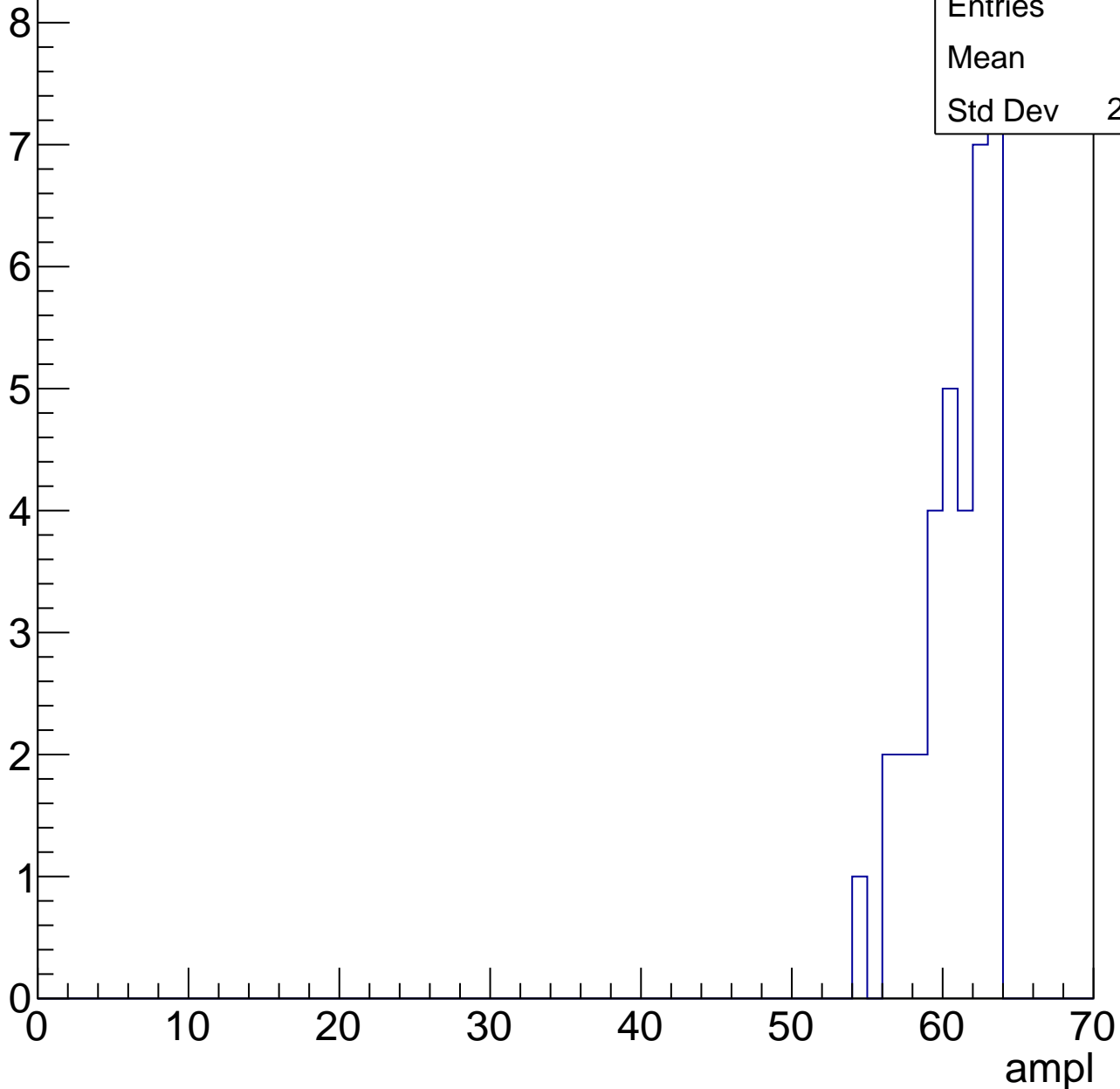


B1L102S, U19-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

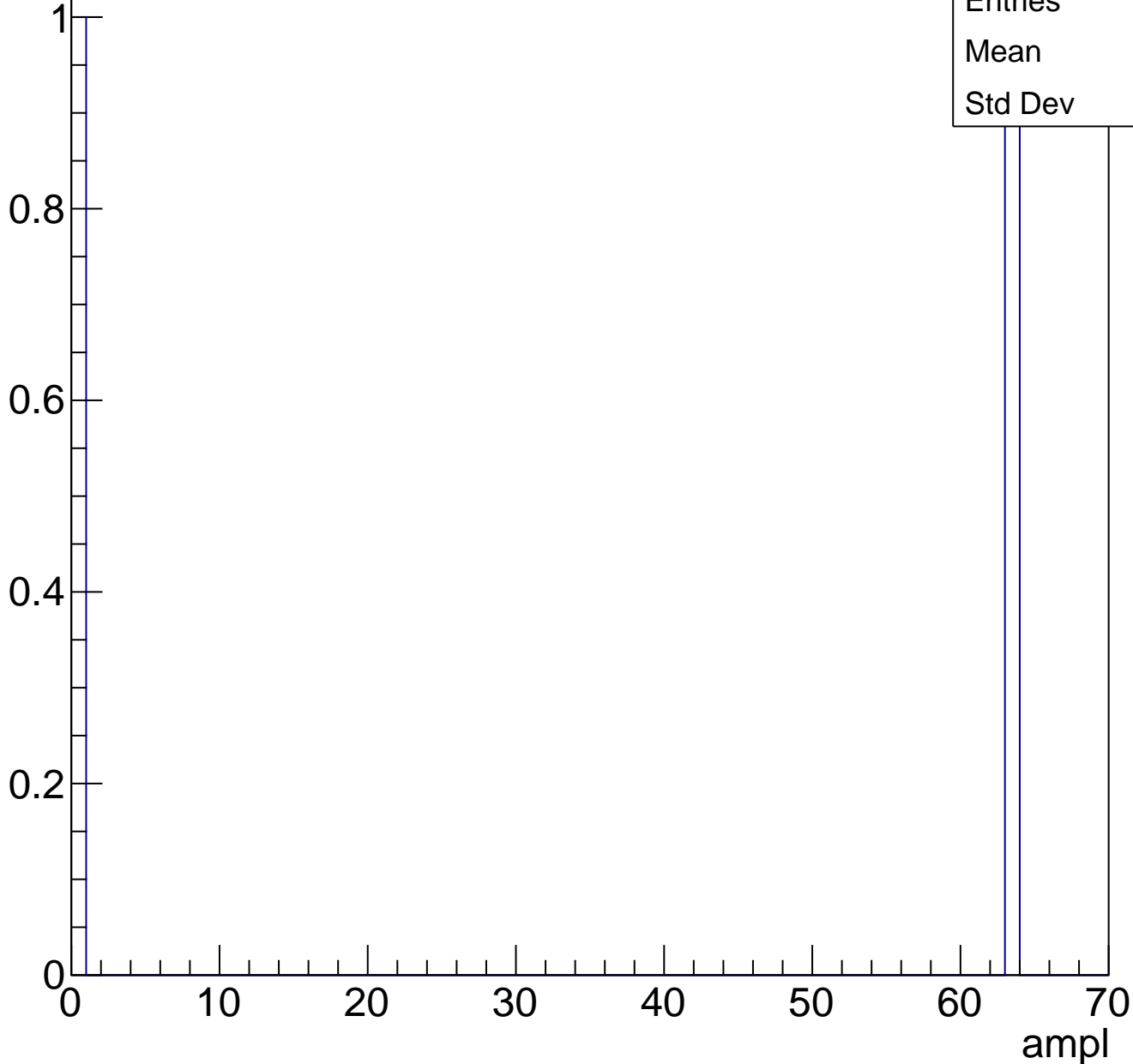
Entries	35
Mean	60.4
Std Dev	2.369



B1L102S, U19-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch23, adc0

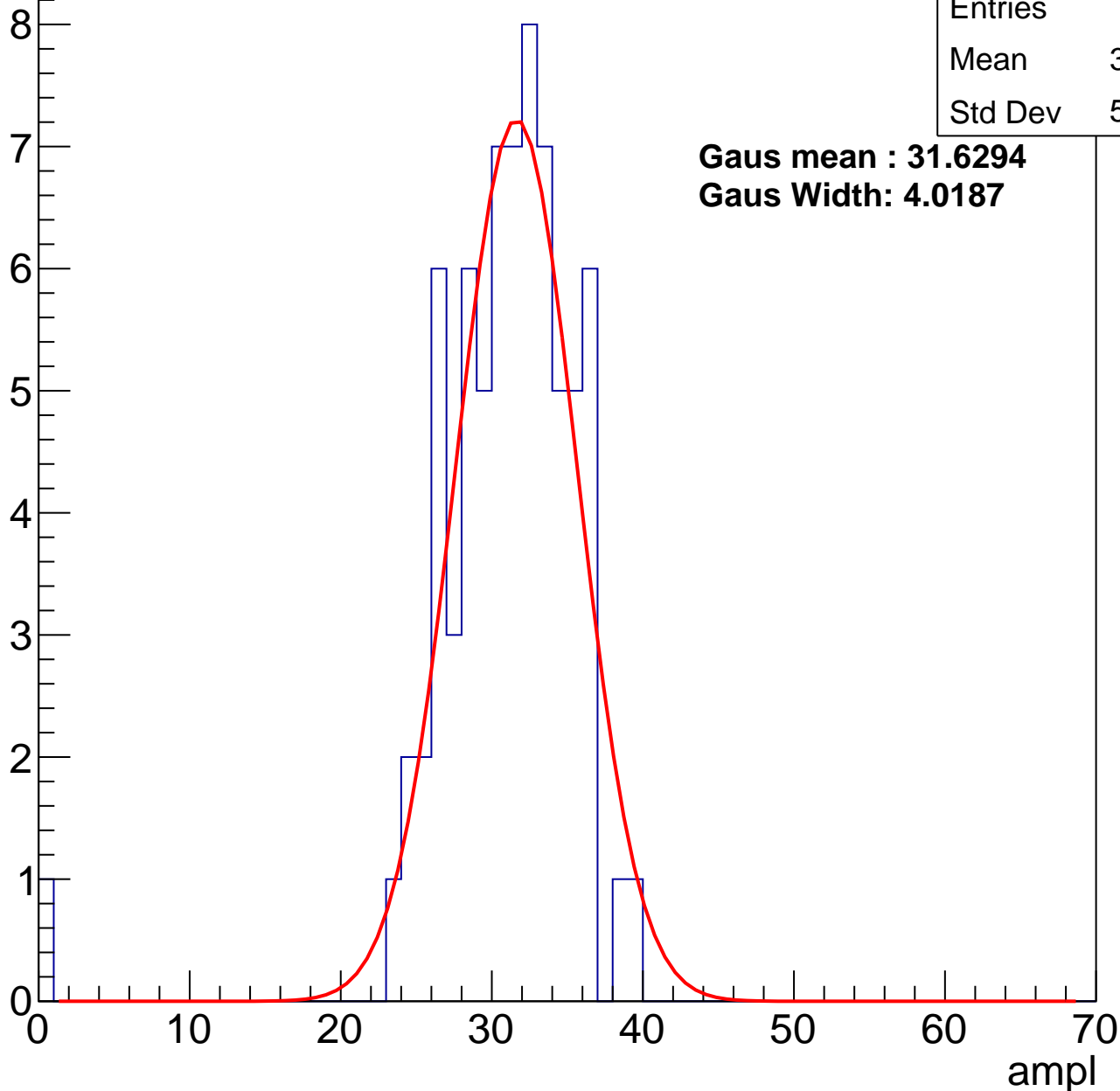
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.45
Std Dev	5.069

Gaus mean : 31.6294

Gaus Width: 4.0187



B1L102S, U19-ch23, adc1

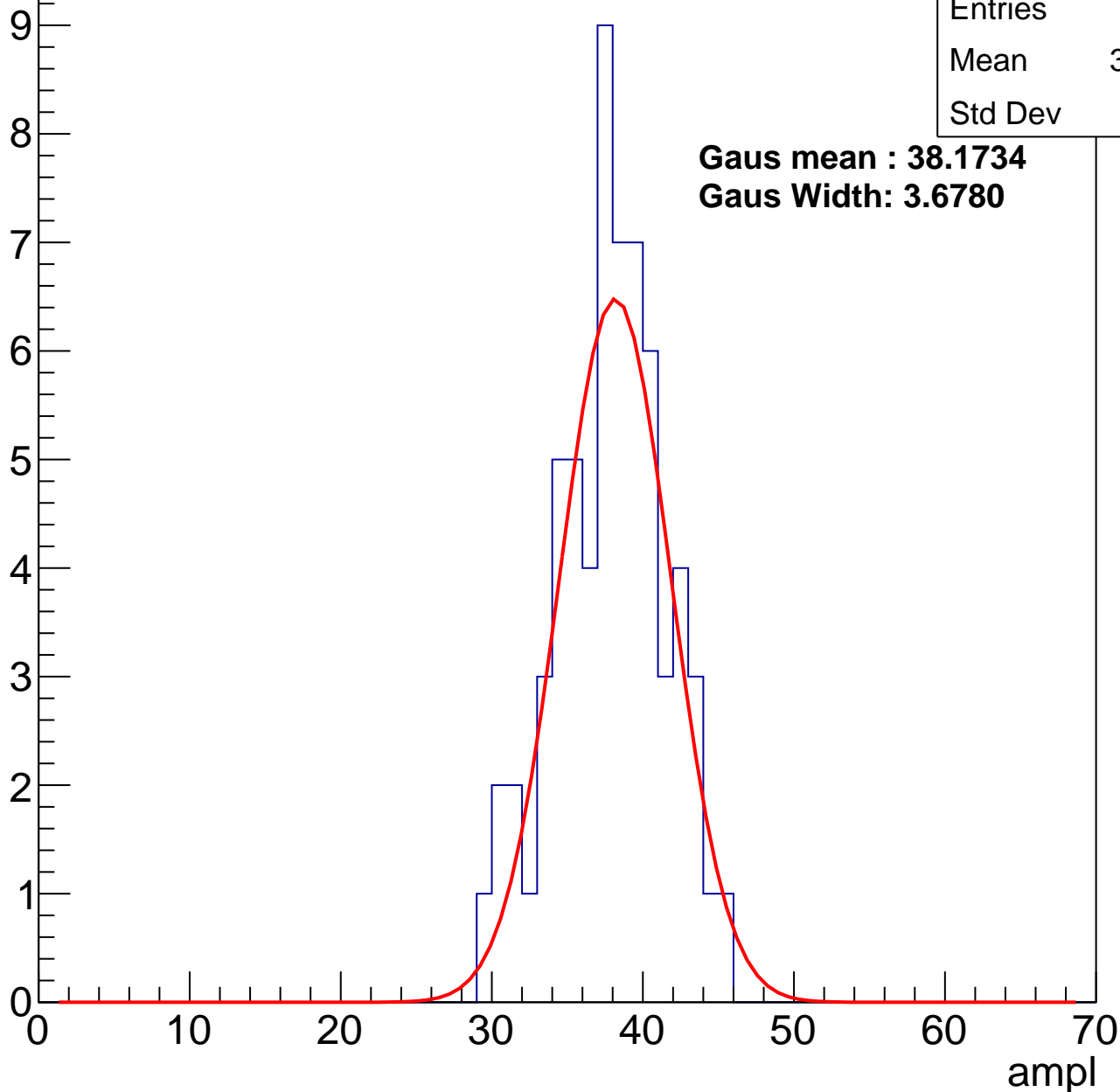
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.38
Std Dev	3.59

Gaus mean : 38.1734

Gaus Width: 3.6780



B1L102S, U19-ch23, adc2

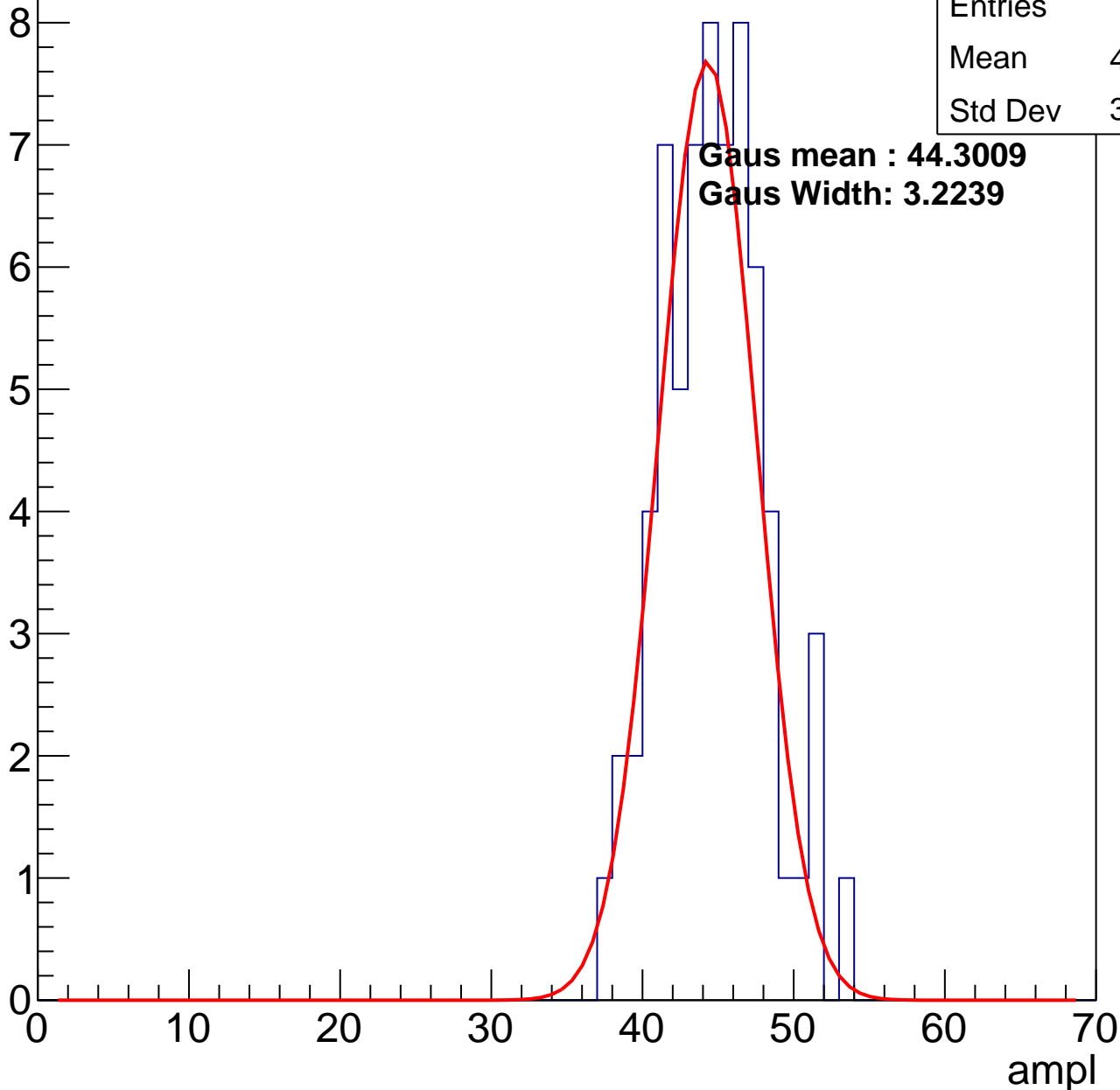
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.22
Std Dev	3.385

Gaus mean : 44.3009

Gaus Width: 3.2239

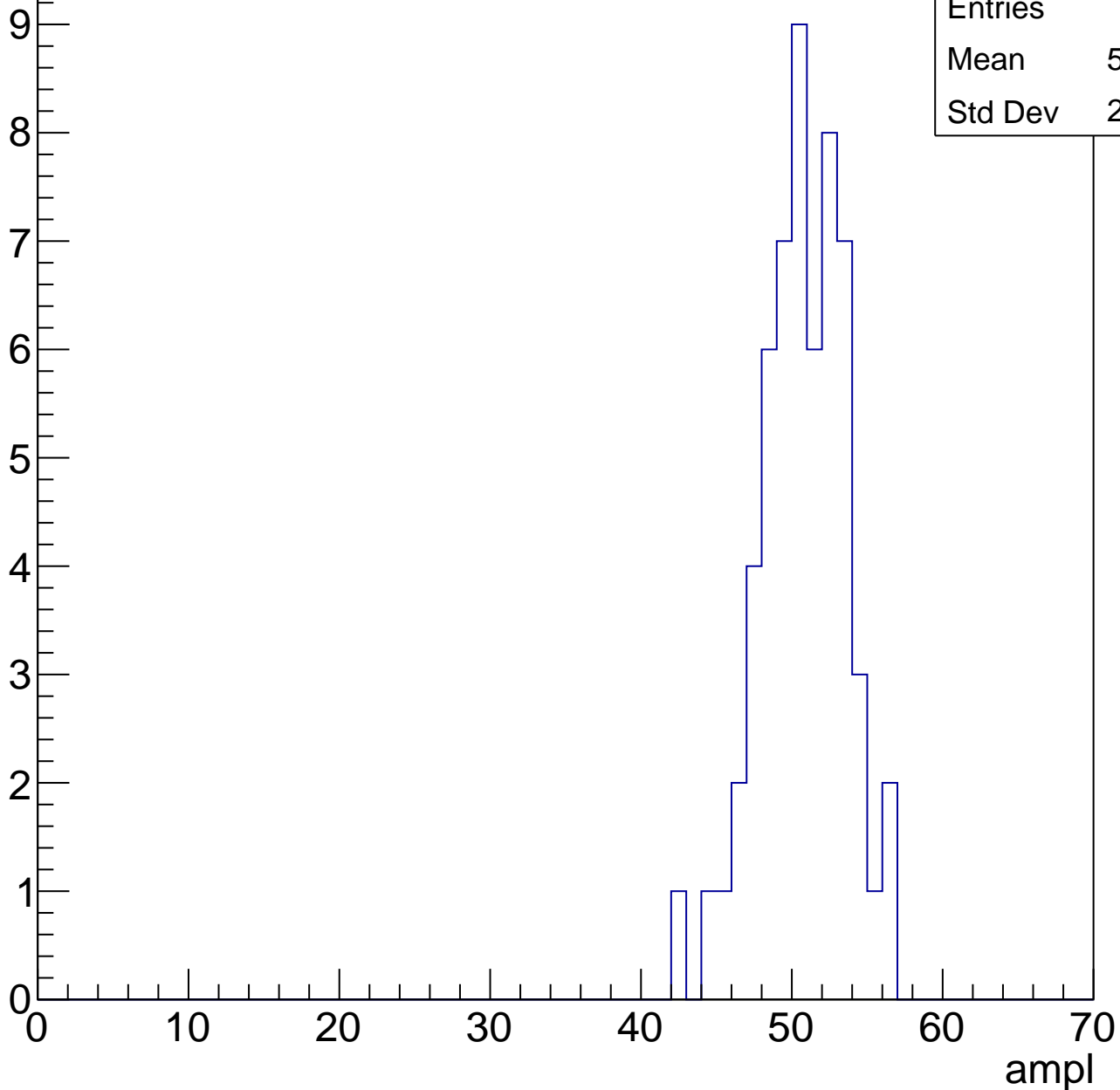


B1L102S, U19-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

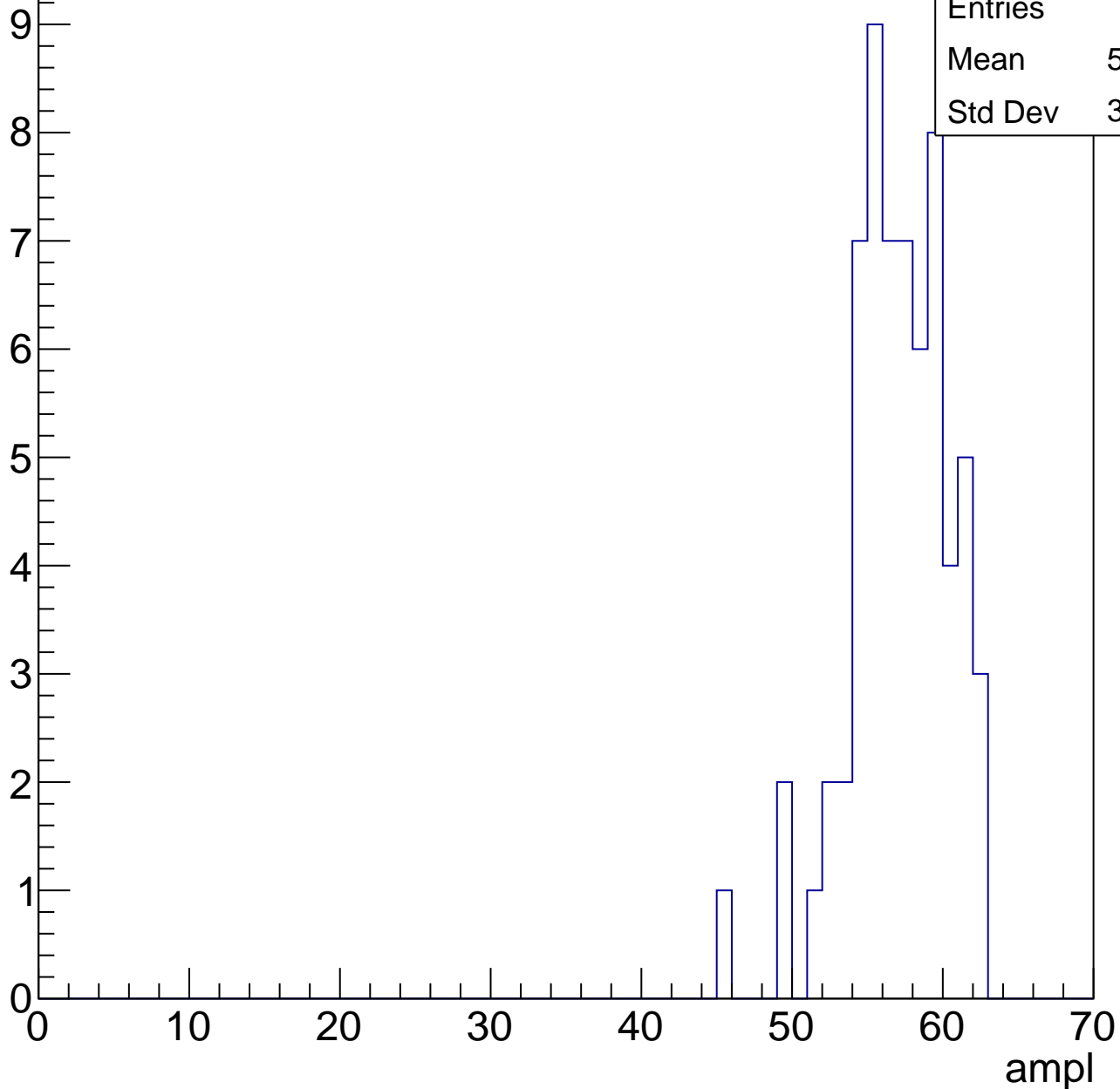
Entries	58
Mean	50.24
Std Dev	2.836



B1L102S, U19-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



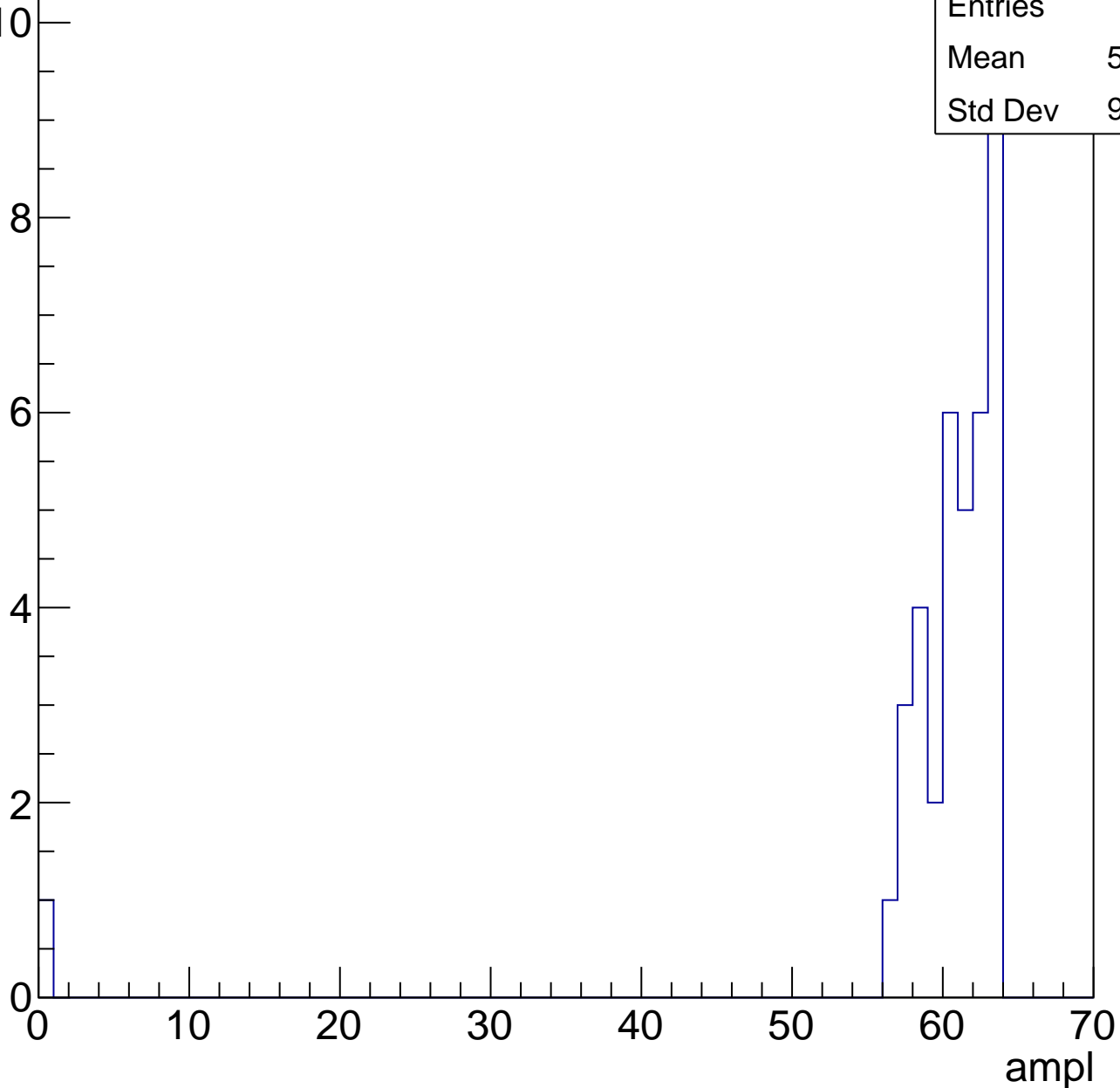
Entries	64
Mean	56.55
Std Dev	3.335

B1L102S, U19-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.05
Std Dev	9.929



B1L102S, U19-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch24, adc0

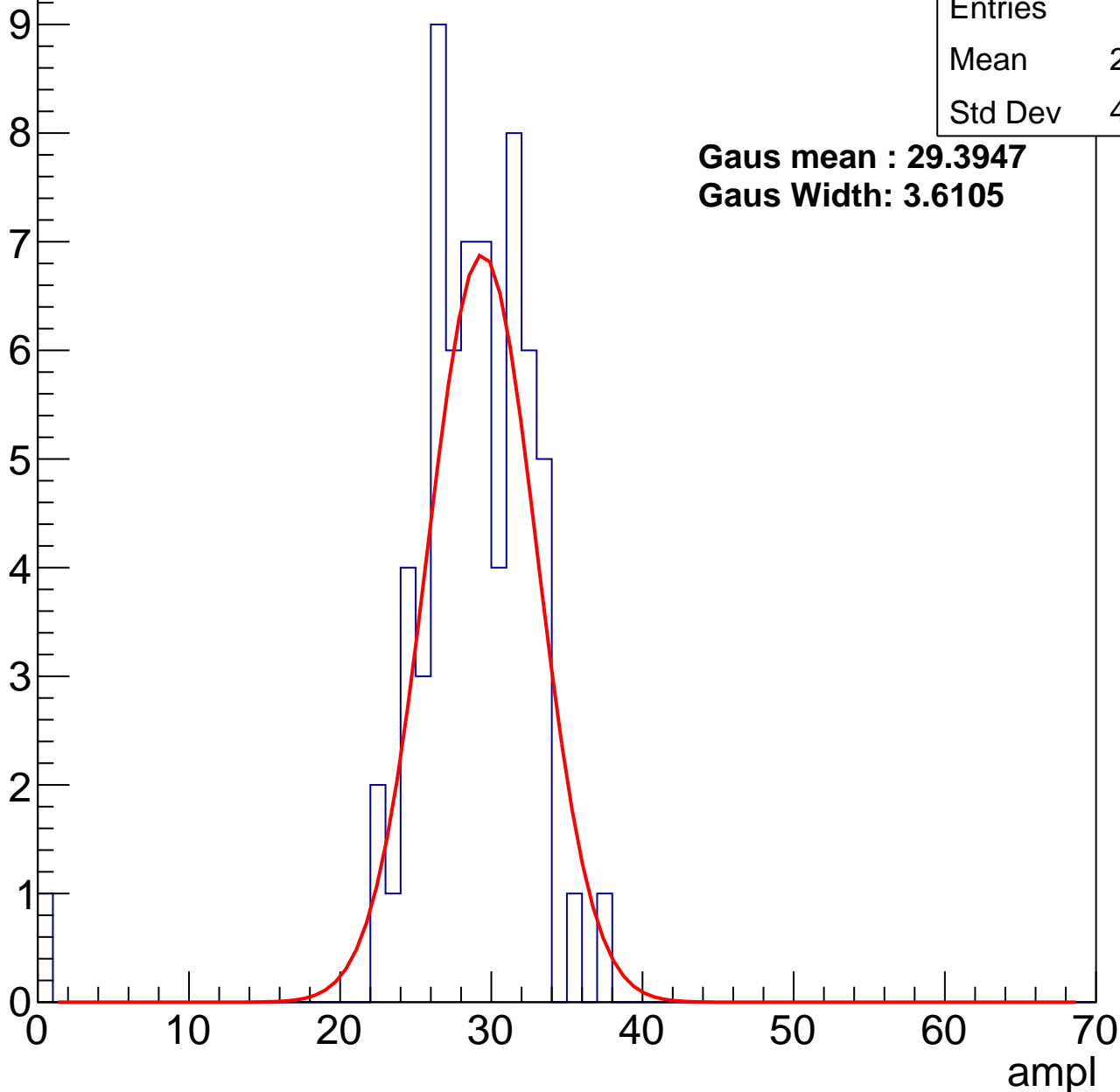
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.15
Std Dev	4.737

Gaus mean : 29.3947

Gaus Width: 3.6105



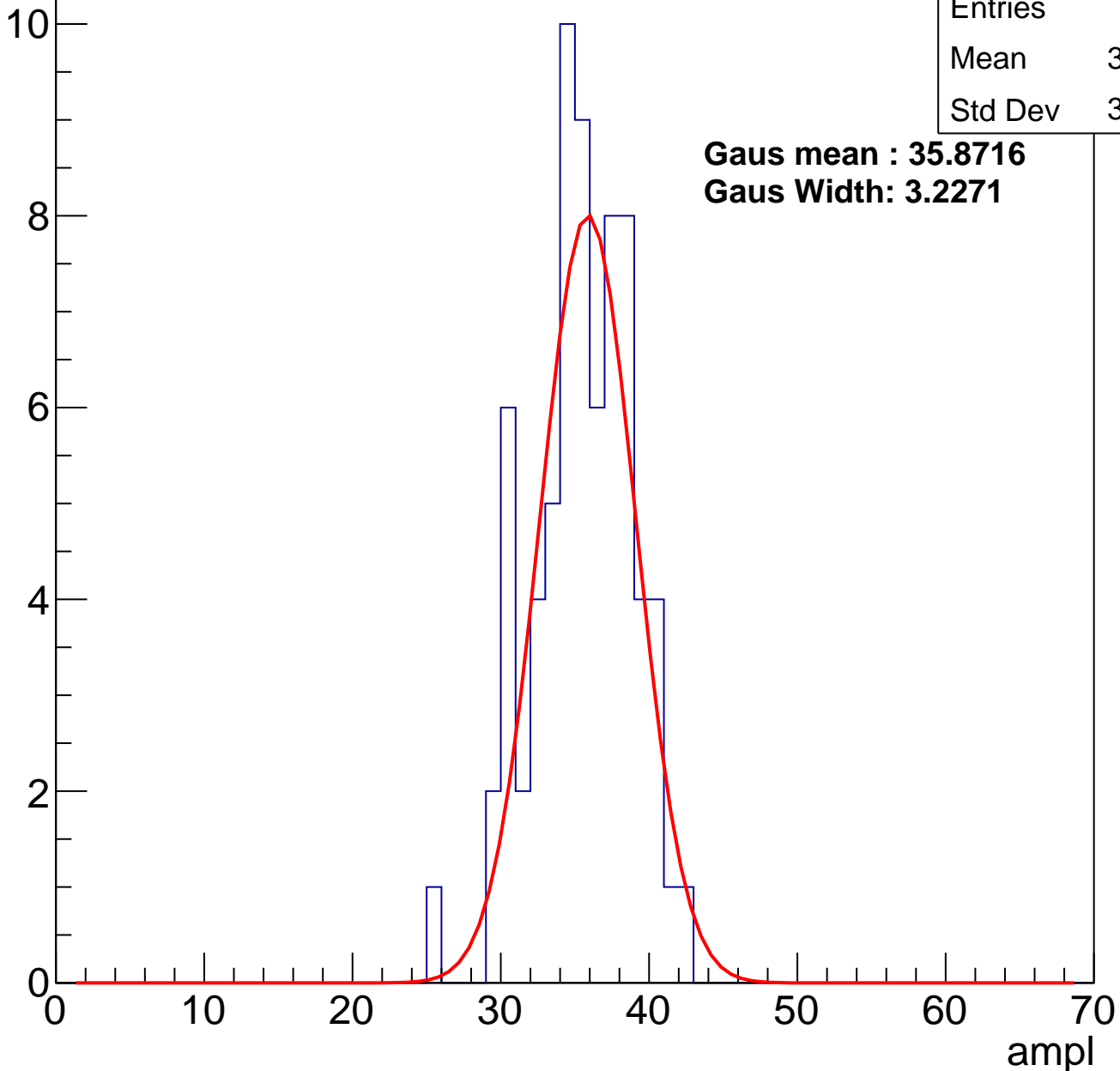
B1L102S, U19-ch24, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.04
Std Dev	3.312

Gaus mean : 35.8716
Gaus Width: 3.2271

Entry



B1L102S, U19-ch24, adc2

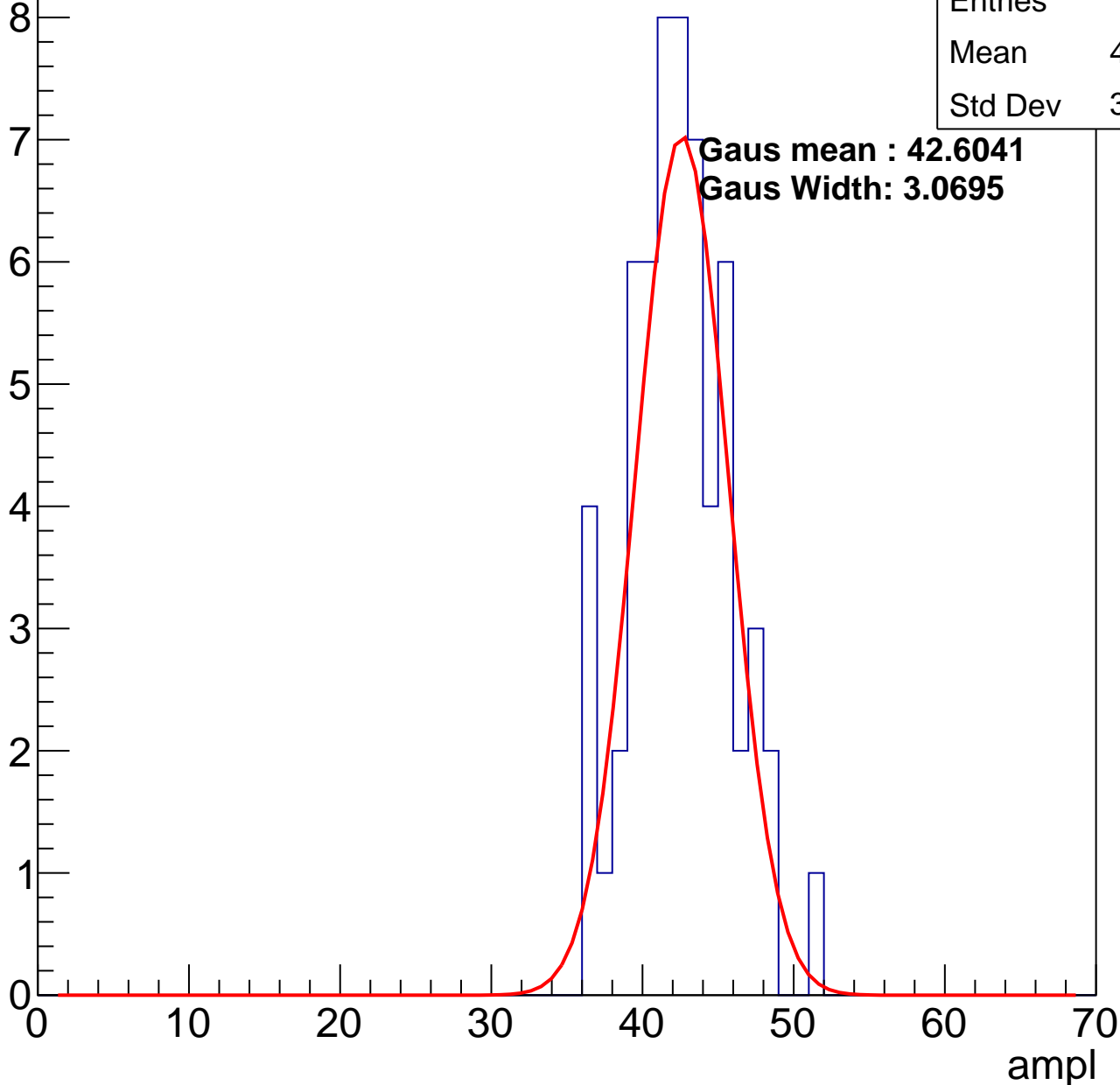
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.03
Std Dev	3.225

Gaus mean : 42.6041

Gaus Width: 3.0695

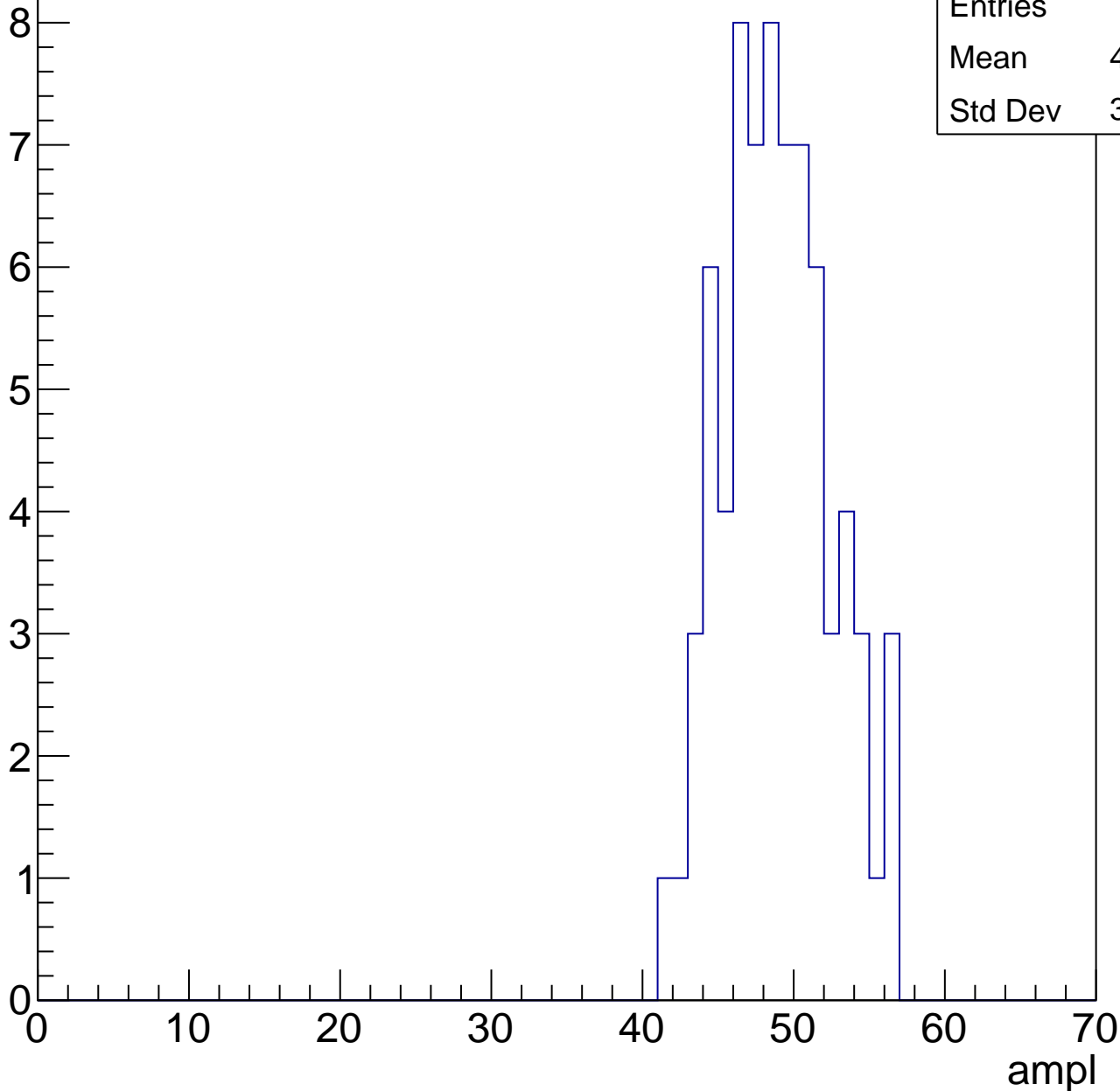


B1L102S, U19-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

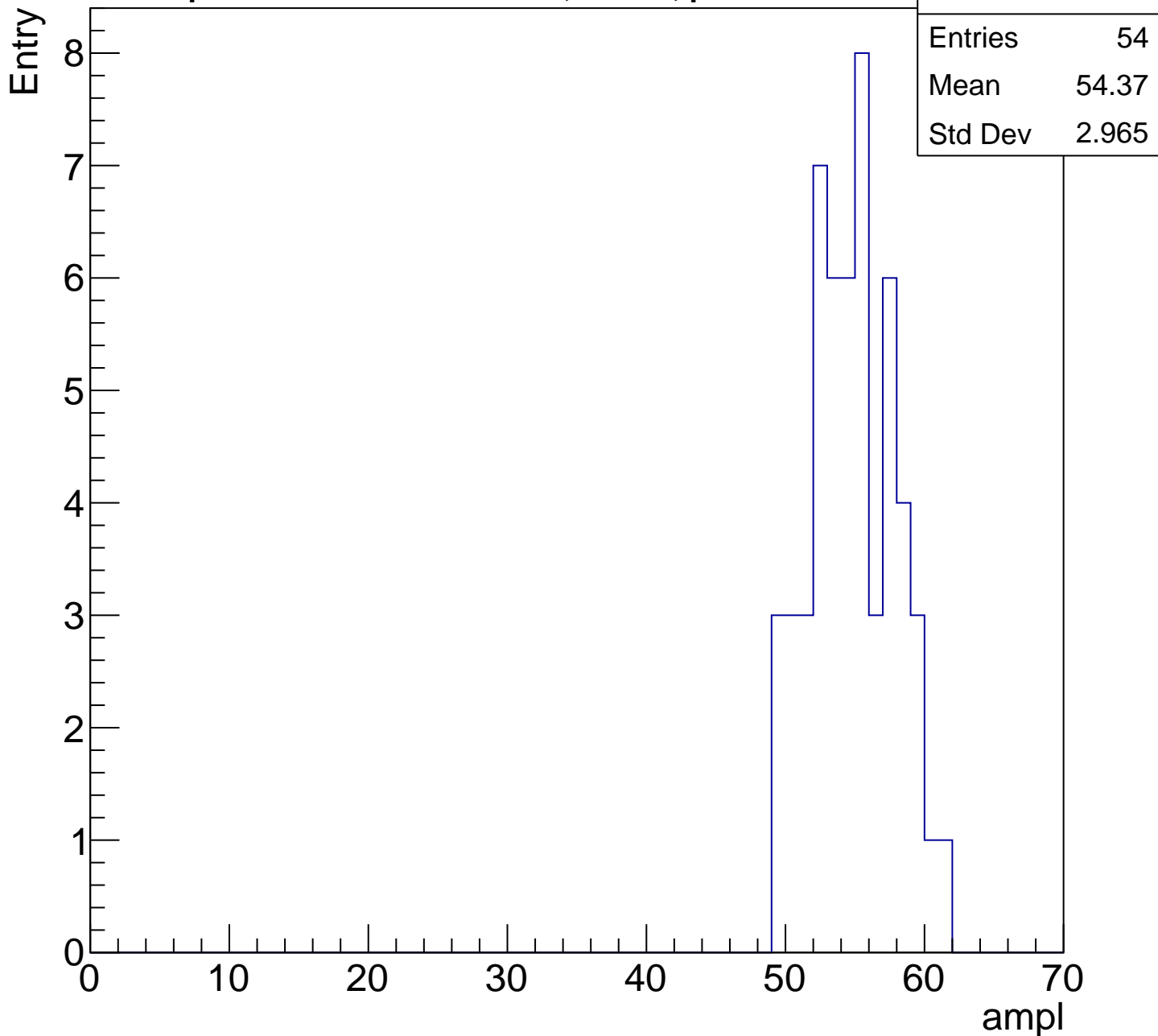
Entry

Entries	72
Mean	48.46
Std Dev	3.539



B1L102S, U19-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

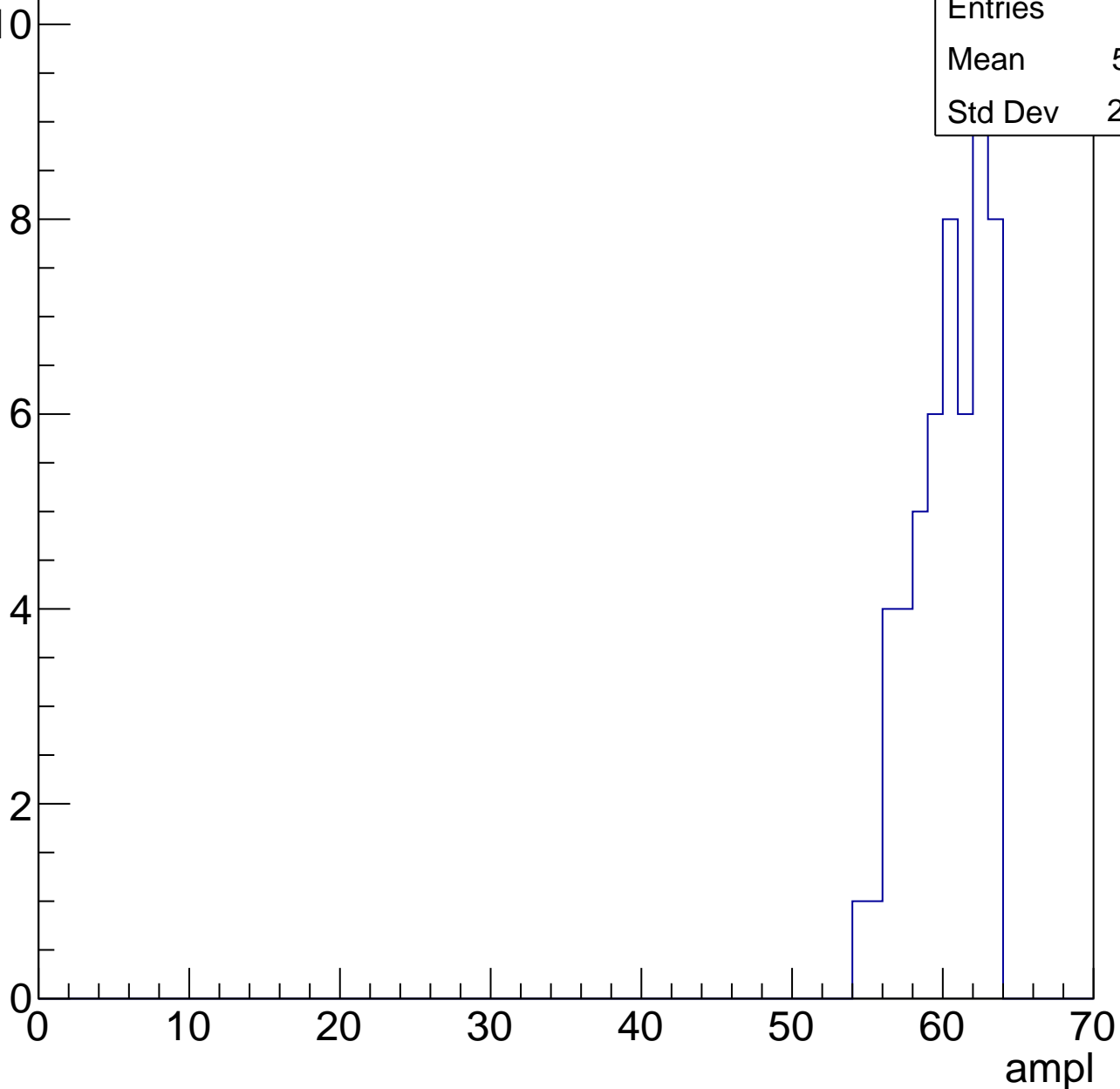


B1L102S, U19-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

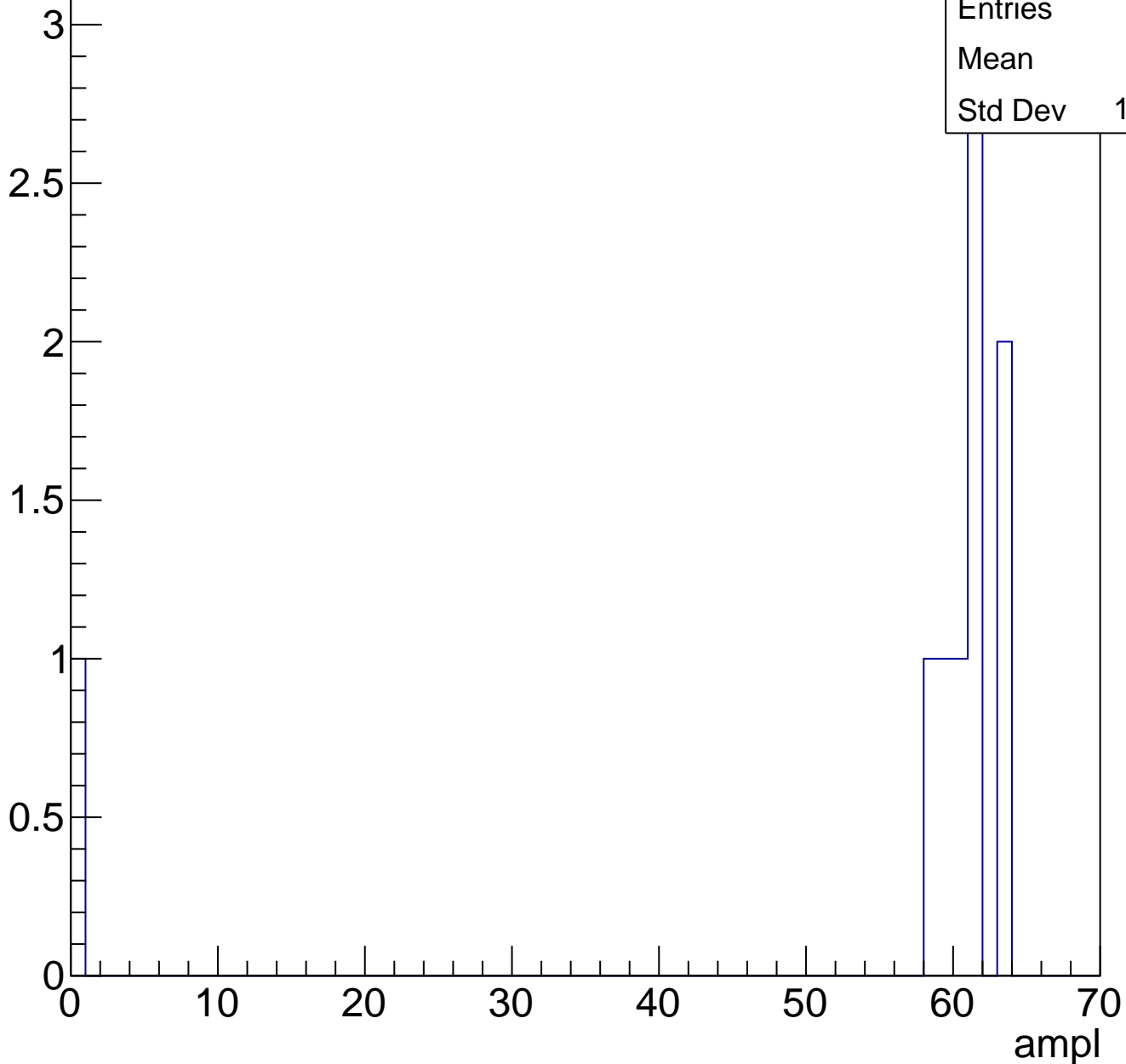
Entries	53
Mean	59.91
Std Dev	2.397



B1L102S, U19-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch25, adc0

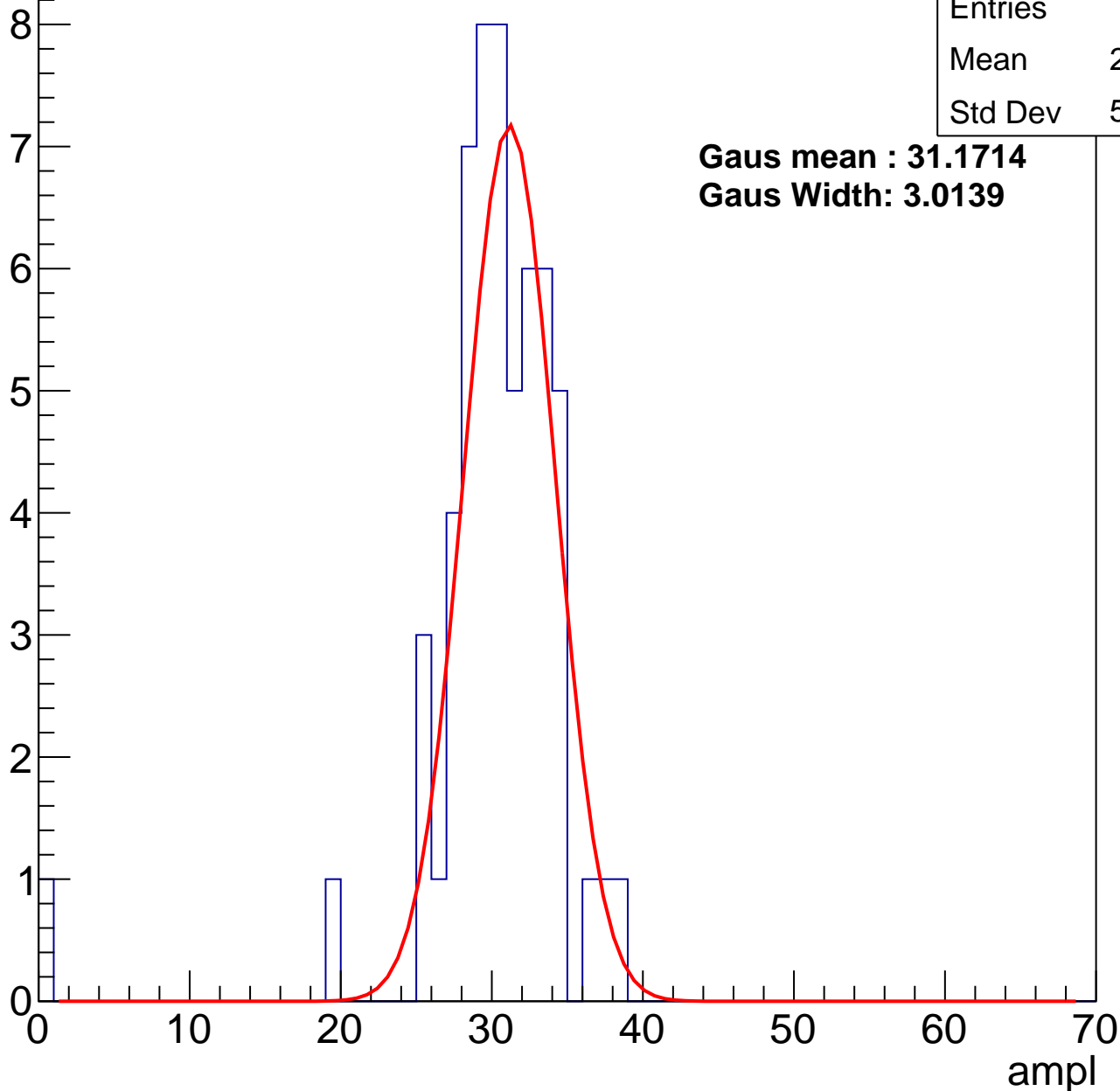
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.69
Std Dev	5.069

Gaus mean : 31.1714

Gaus Width: 3.0139



B1L102S, U19-ch25, adc1

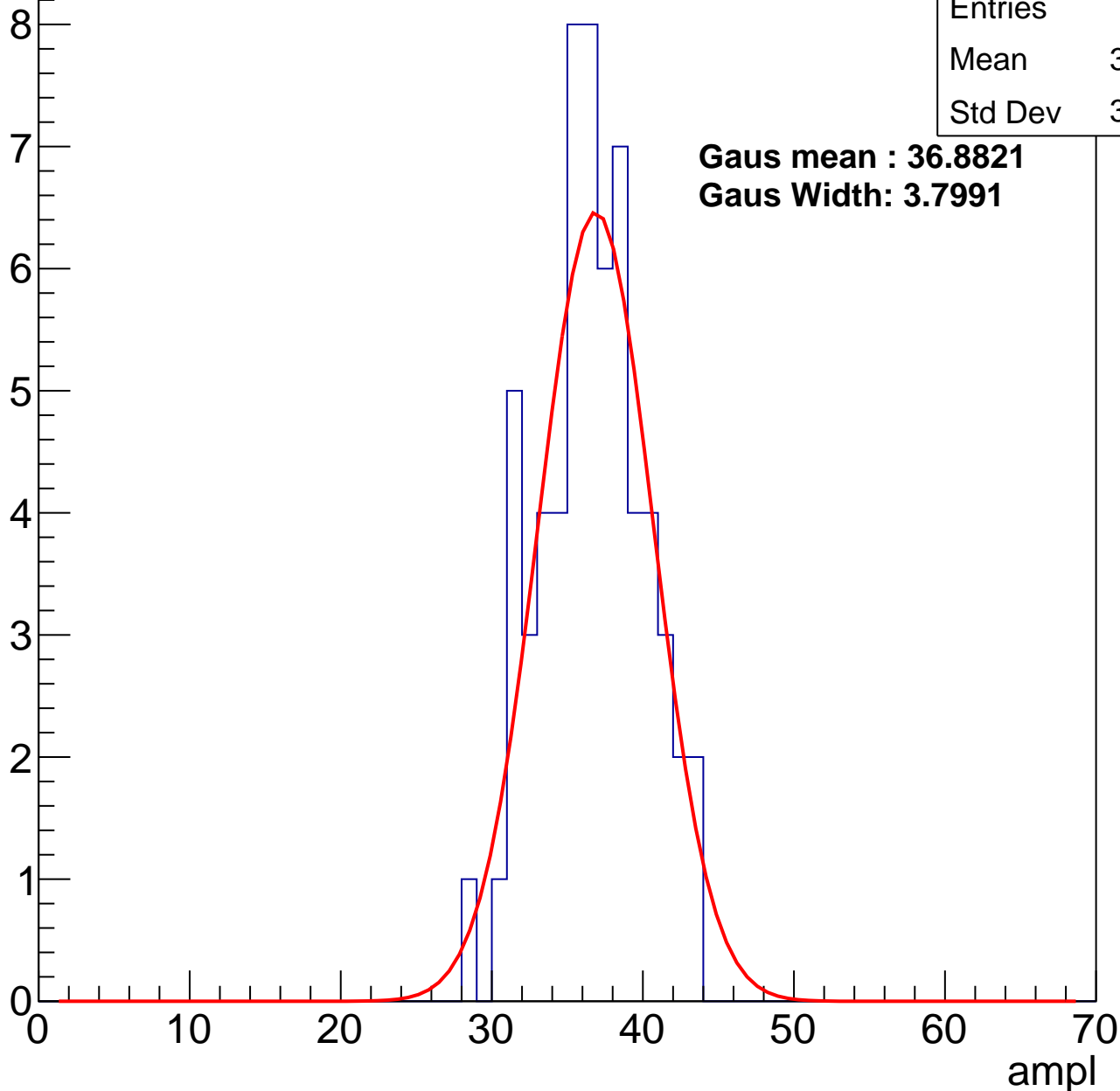
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.16
Std Dev	3.385

Gaus mean : 36.8821

Gaus Width: 3.7991



B1L102S, U19-ch25, adc2

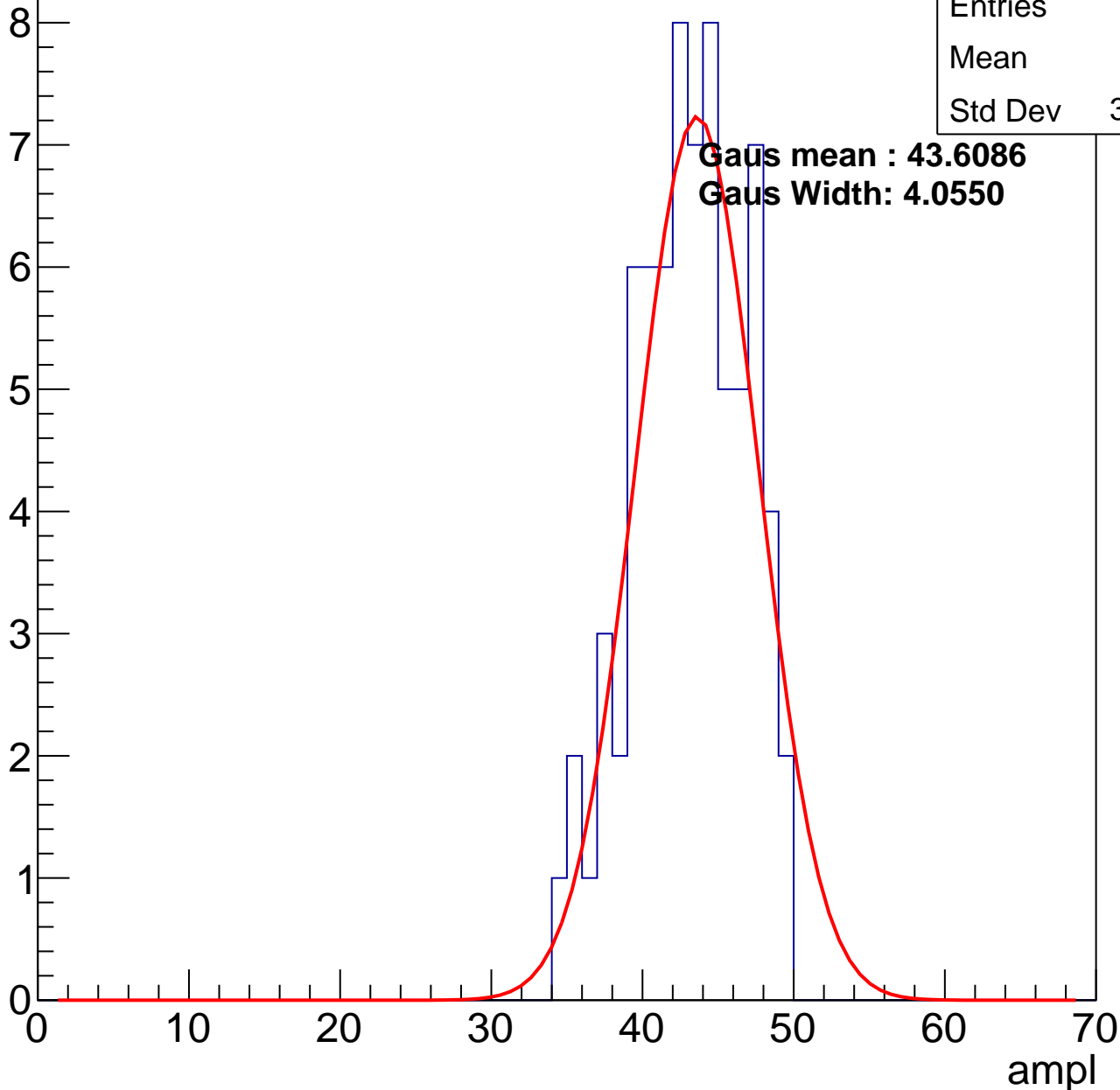
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.6
Std Dev	3.595

Gaus mean : 43.6086

Gaus Width: 4.0550

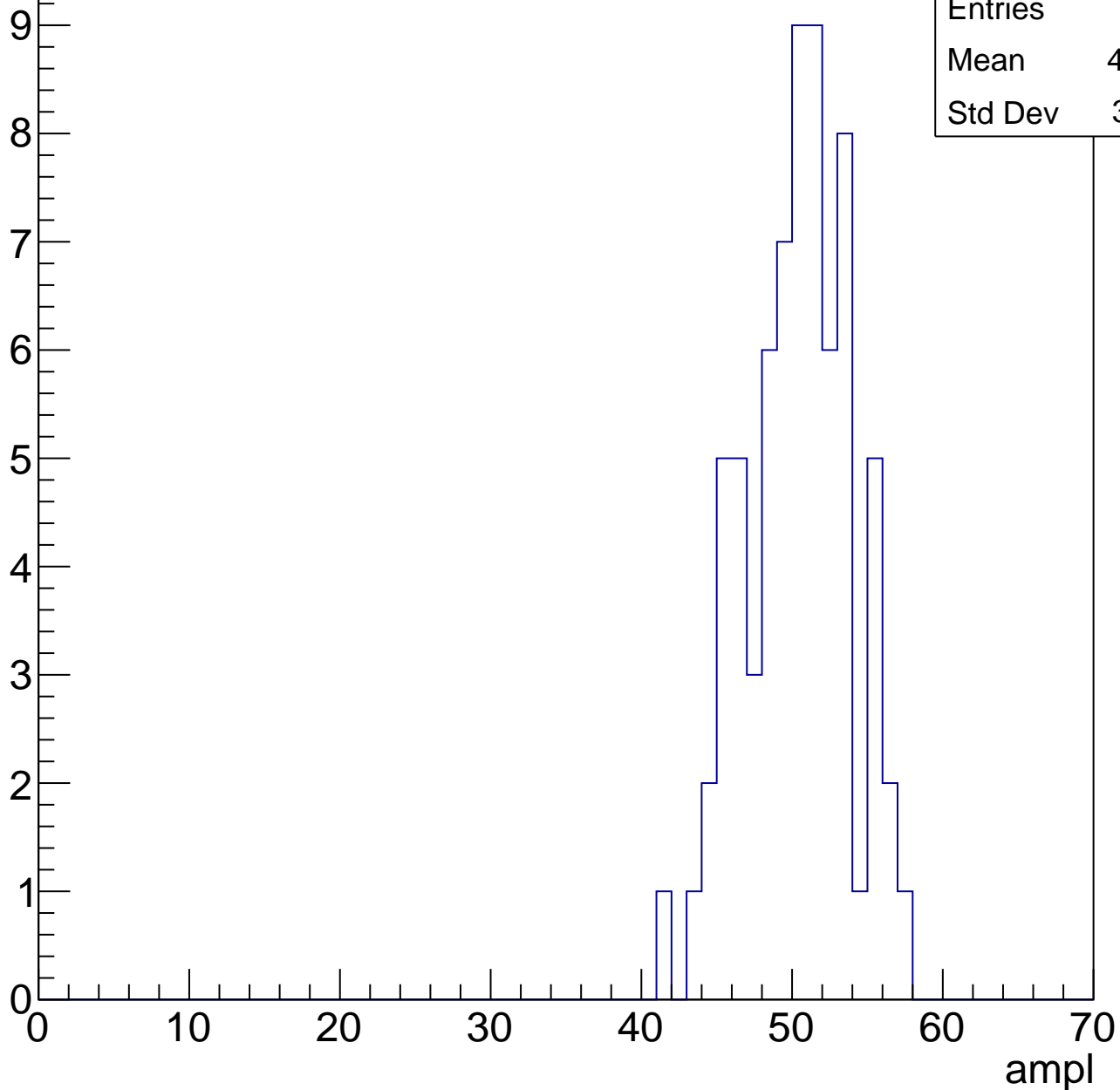


B1L102S, U19-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.89
Std Dev	3.421



B1L102S, U19-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 50

Mean 56.26

Std Dev 3.058

ampl

0

10

20

30

40

50

60

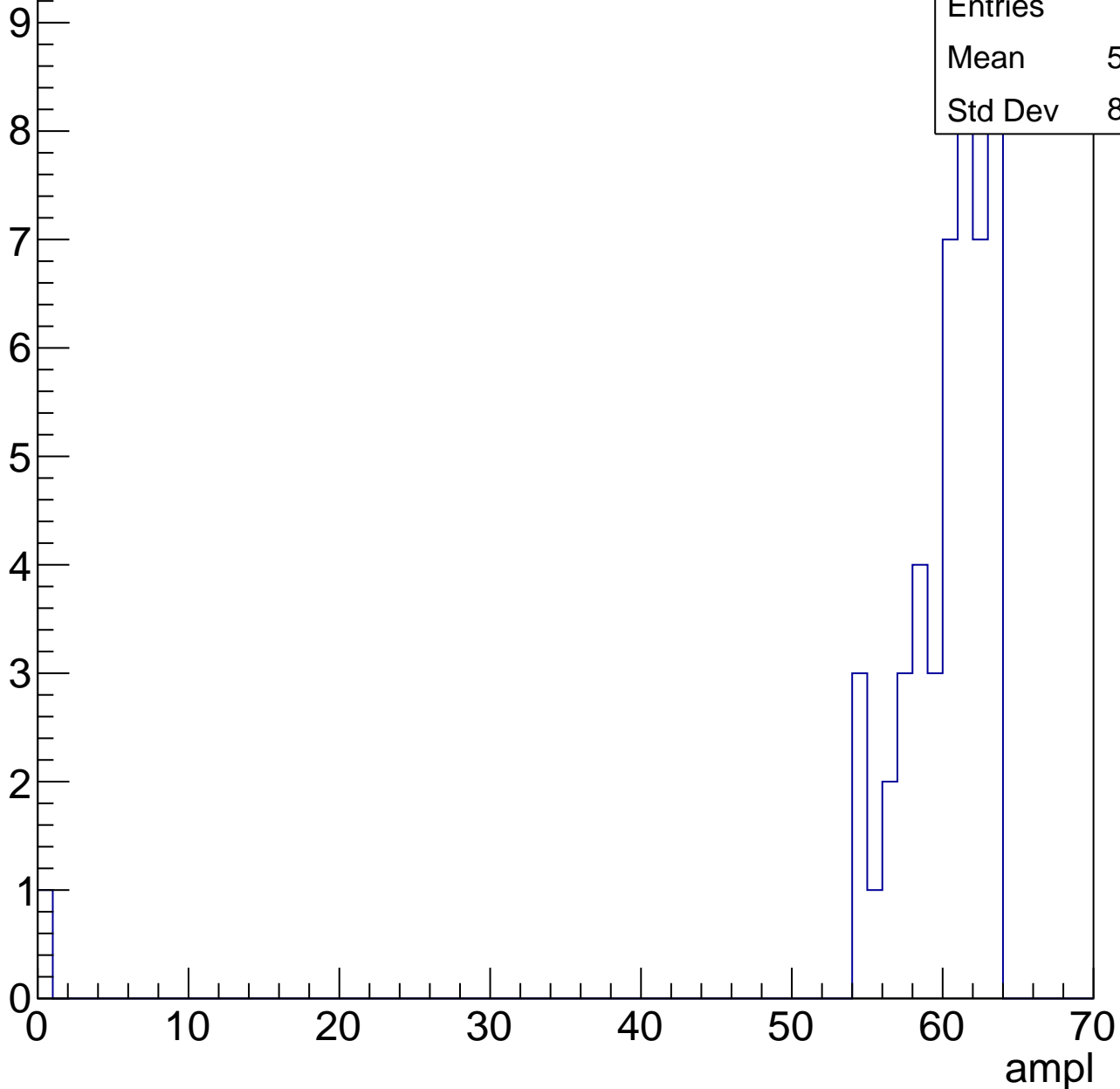
70

B1L102S, U19-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.76
Std Dev	8.868



B1L102S, U19-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch26, adc0

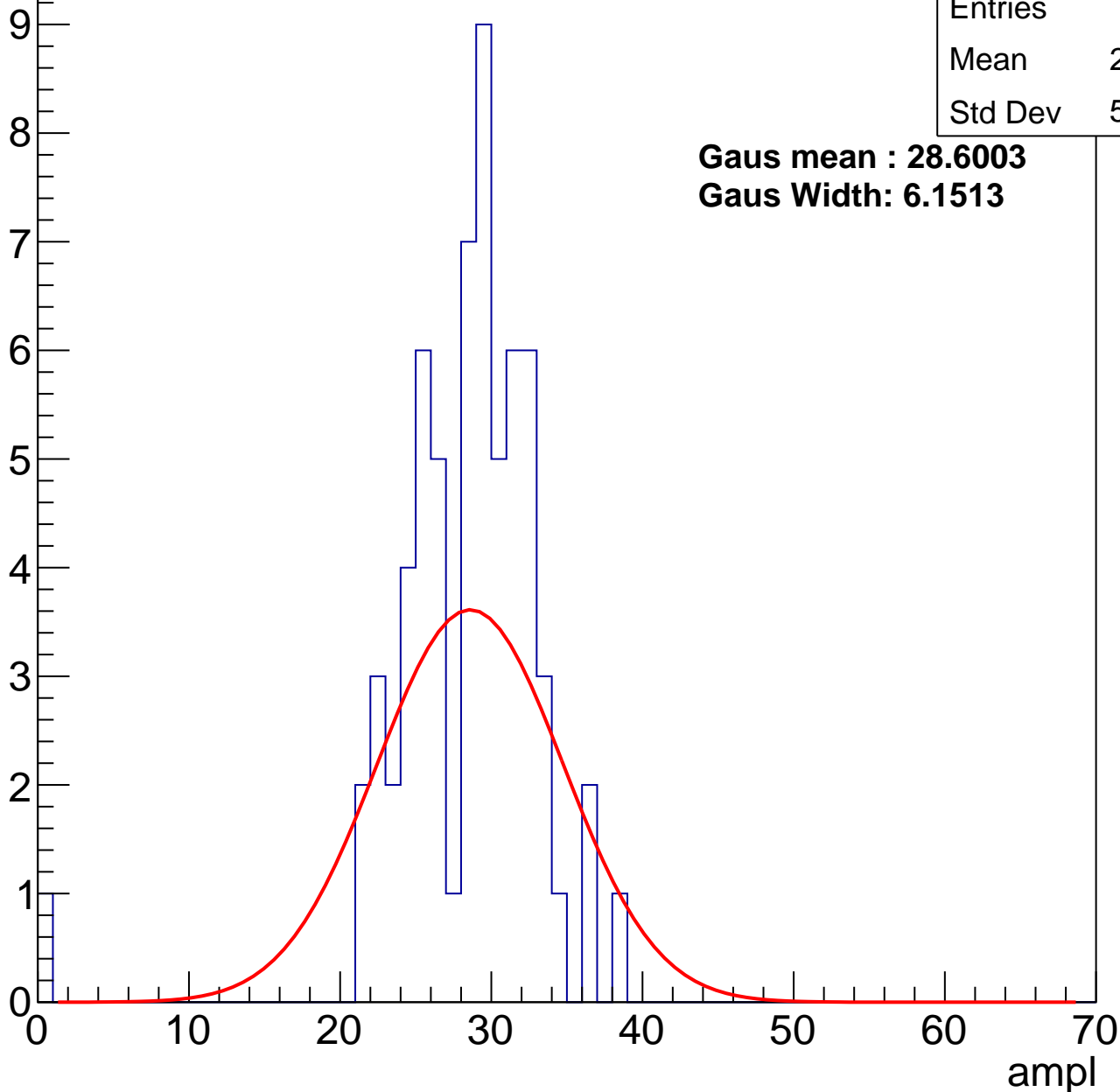
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.89
Std Dev	5.148

Gaus mean : 28.6003

Gaus Width: 6.1513



B1L102S, U19-ch26, adc1

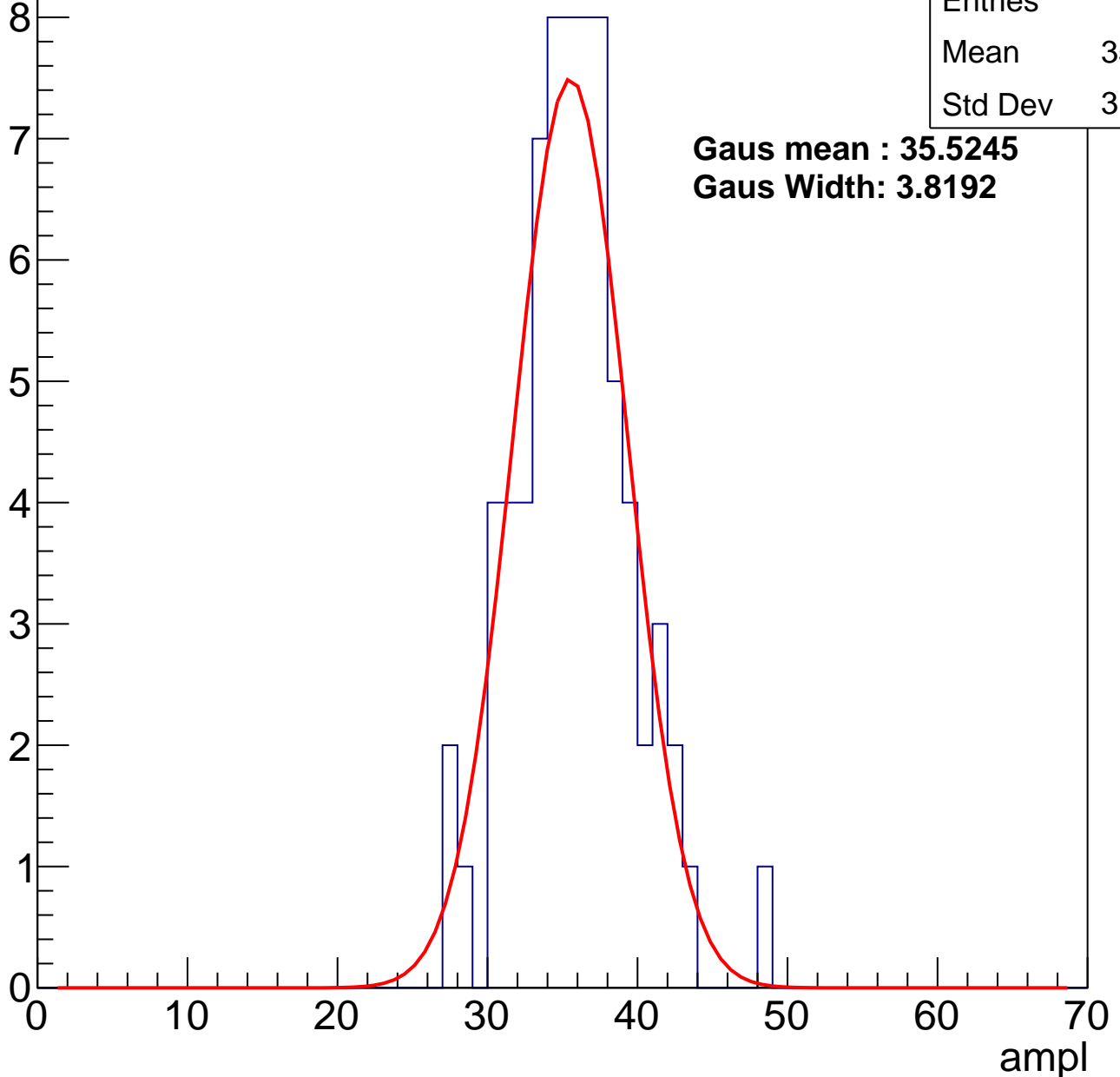
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.35
Std Dev	3.805

Gaus mean : 35.5245

Gaus Width: 3.8192



B1L102S, U19-ch26, adc2

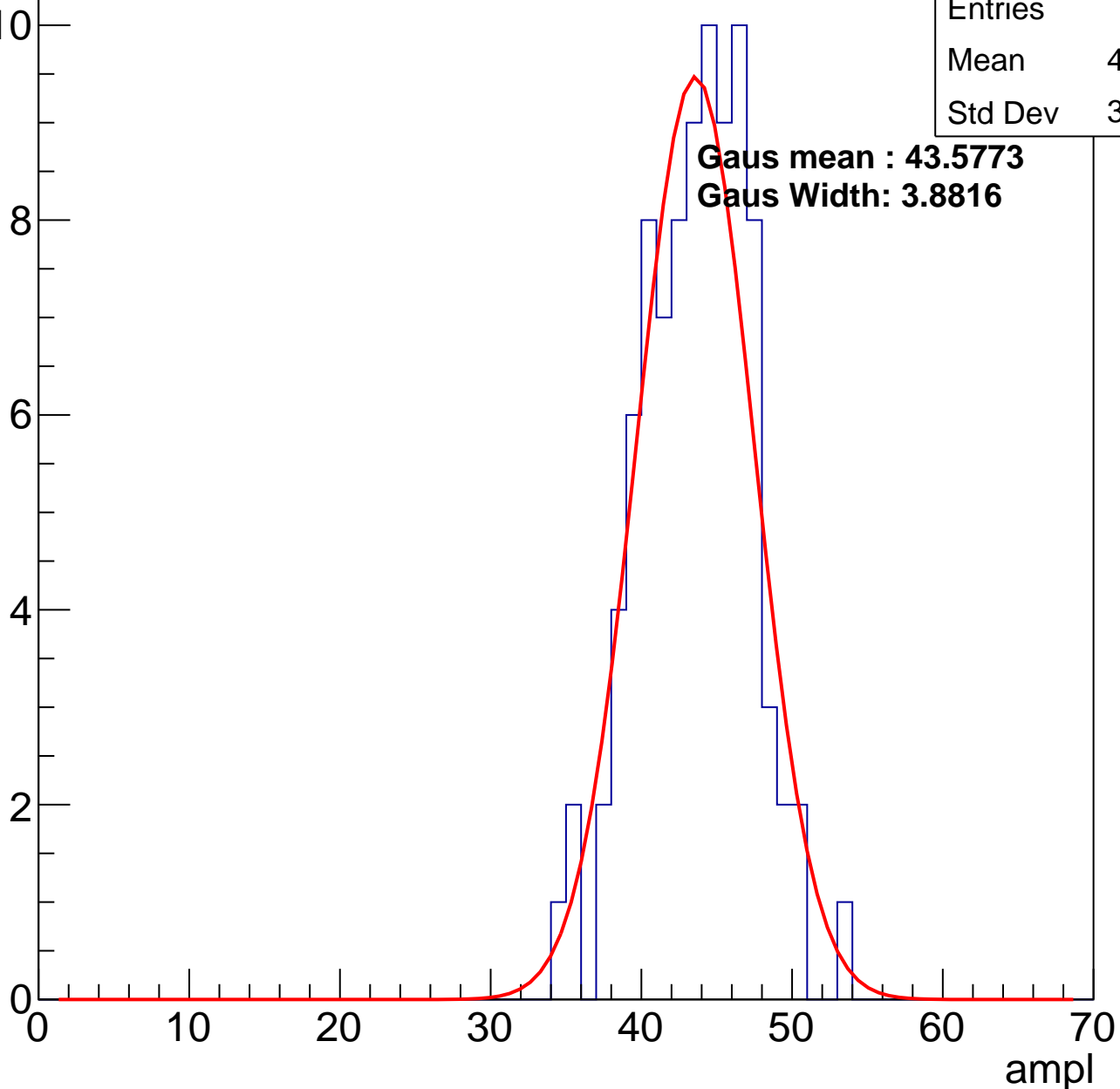
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	92
Mean	43.15
Std Dev	3.599

Gaus mean : 43.5773

Gaus Width: 3.8816

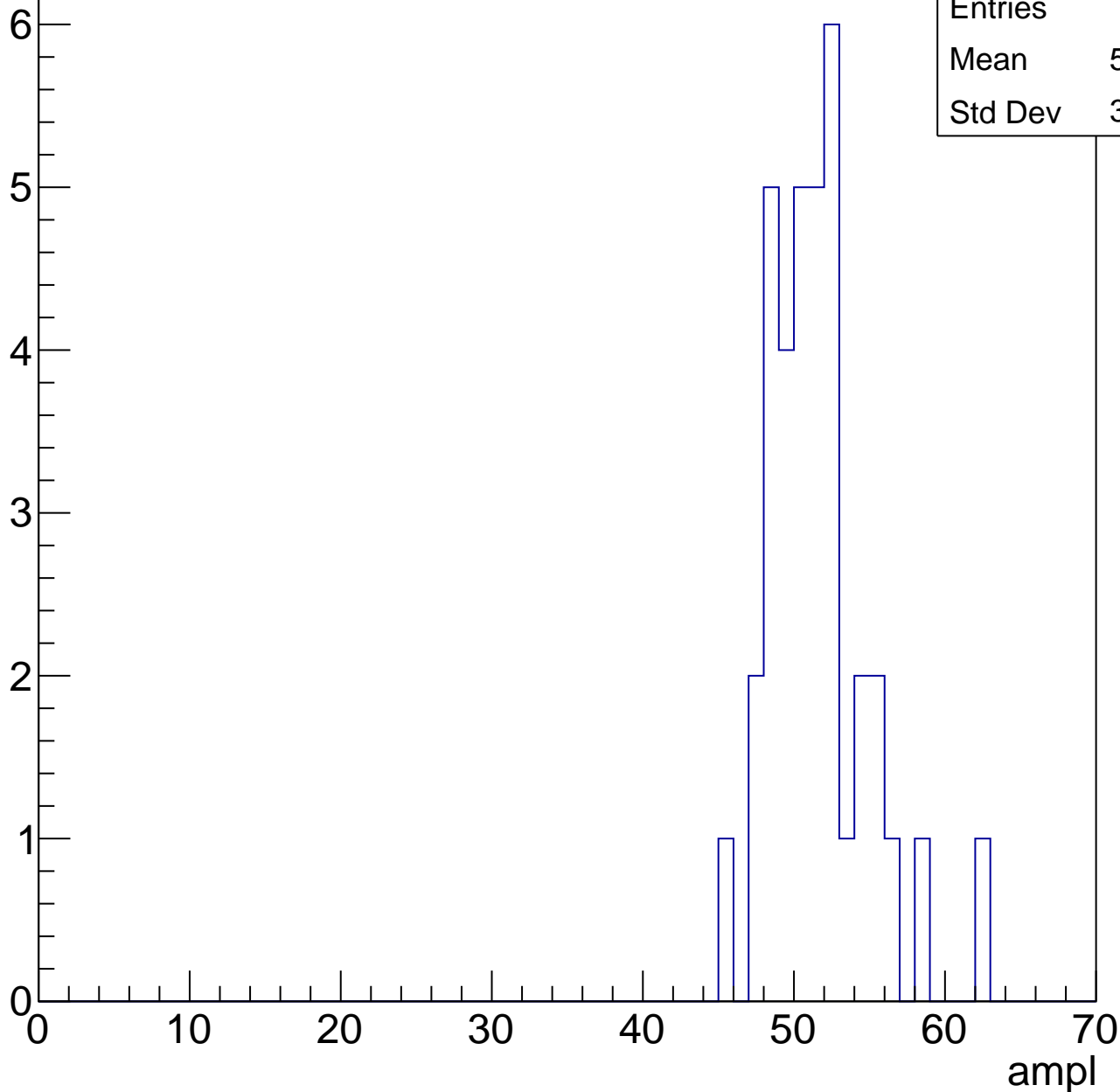


B1L102S, U19-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	51.08
Std Dev	3.286

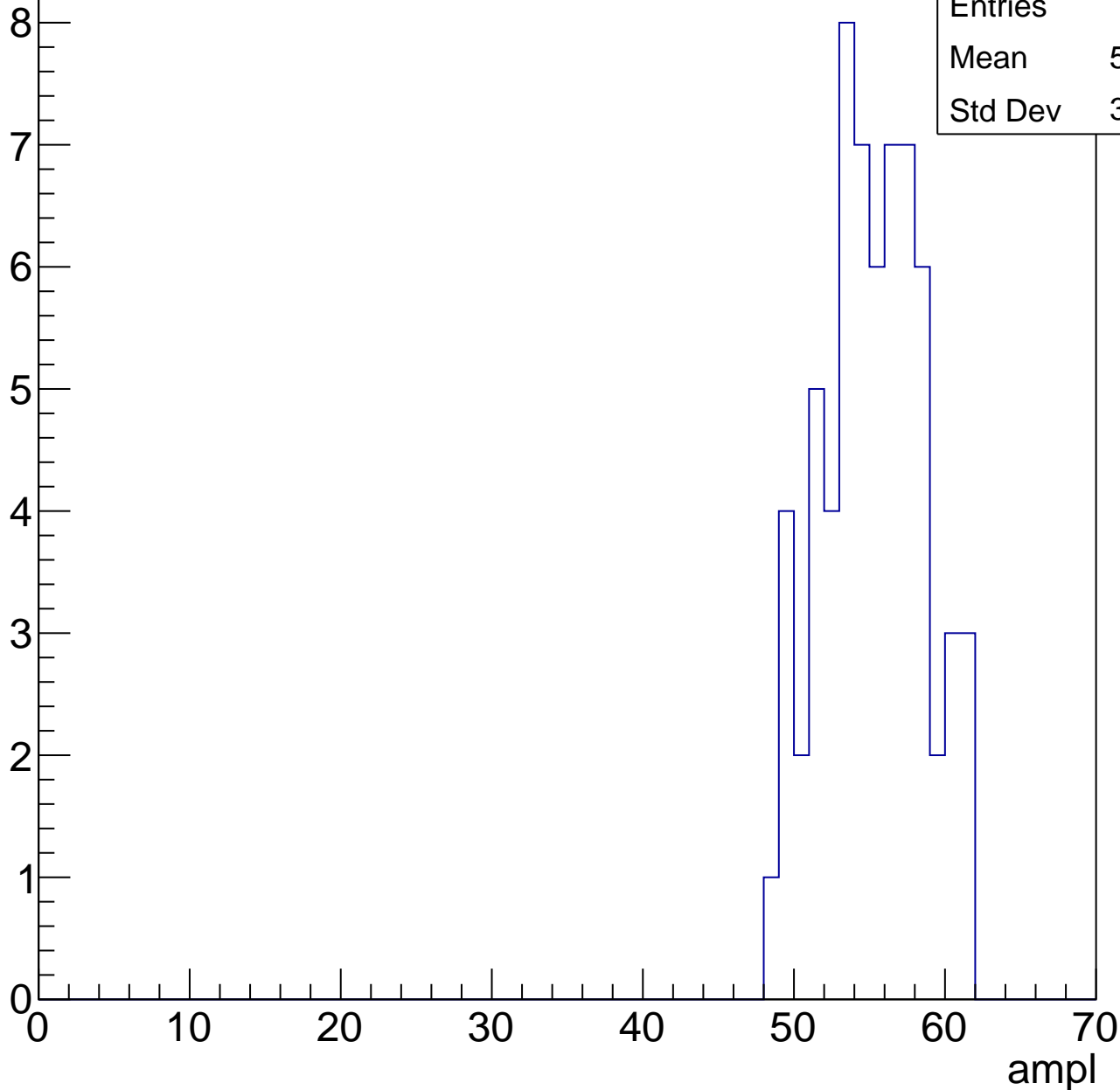


B1L102S, U19-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.75
Std Dev	3.277

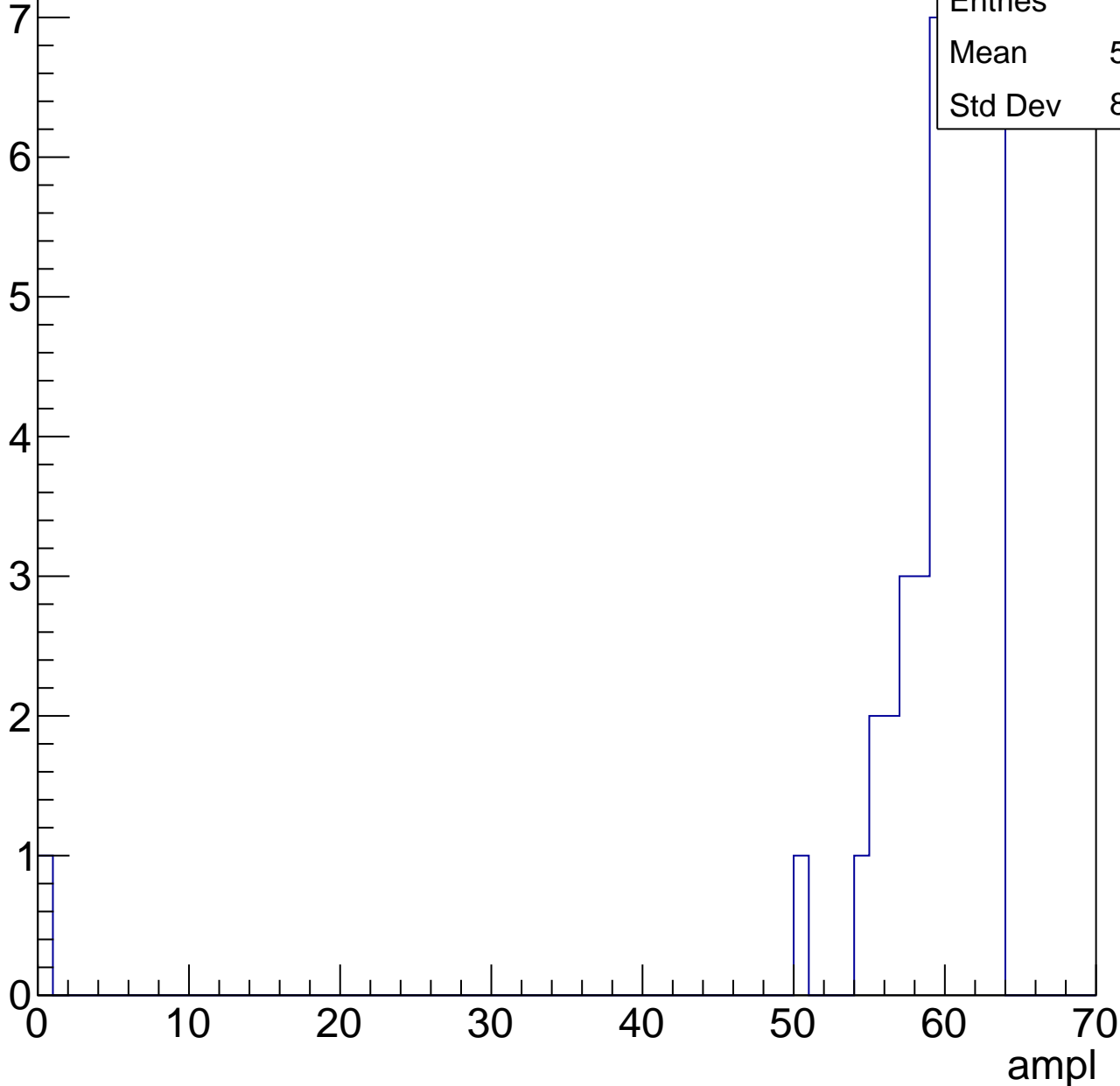


B1L102S, U19-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.46
Std Dev	8.953



B1L102S, U19-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch27, adc0

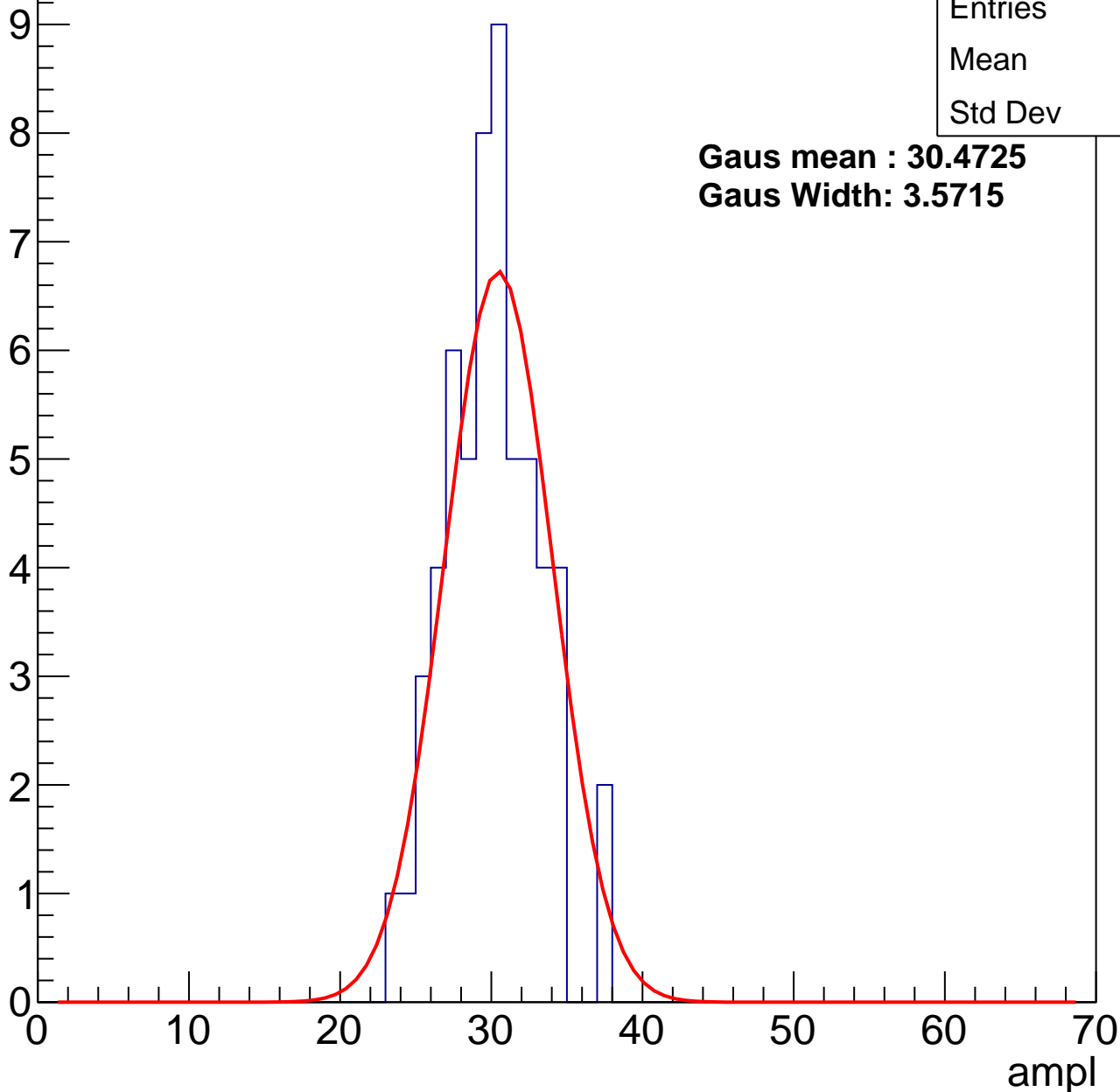
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	29.6
Std Dev	3.02

Gaus mean : 30.4725

Gaus Width: 3.5715



B1L102S, U19-ch27, adc1

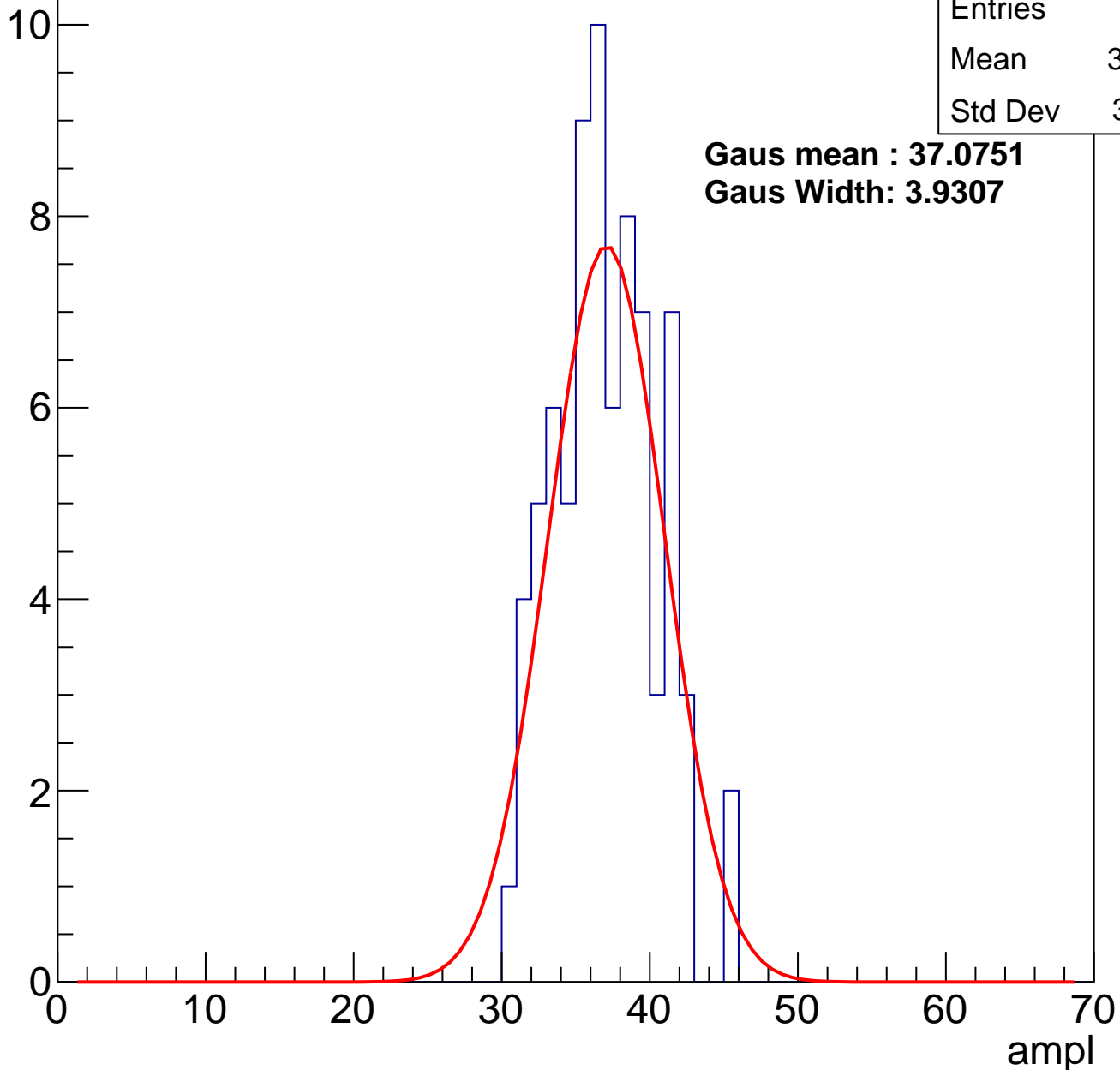
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	36.57
Std Dev	3.381

Gaus mean : 37.0751

Gaus Width: 3.9307

Entry



B1L102S, U19-ch27, adc2

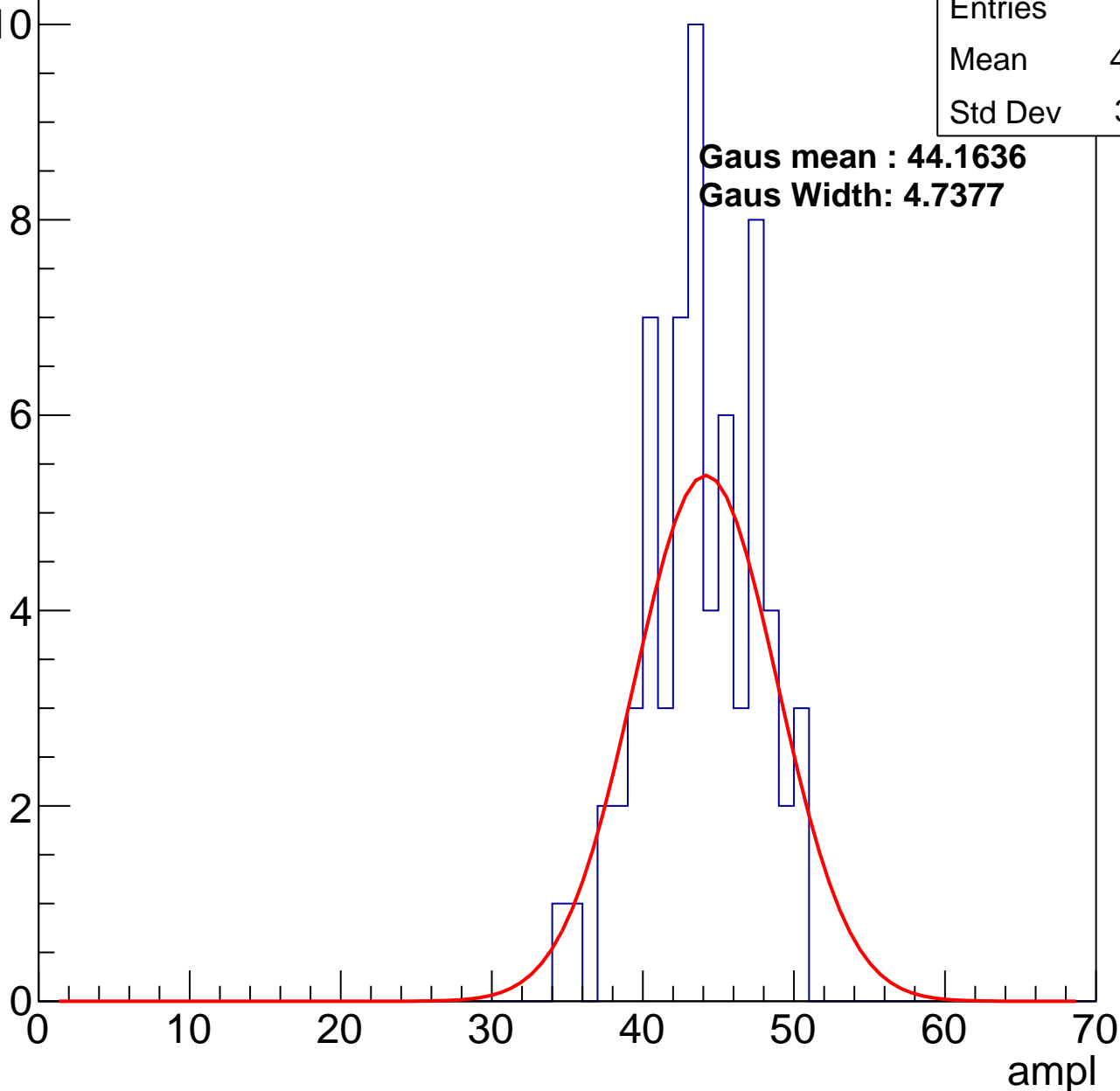
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.38
Std Dev	3.671

Gaus mean : 44.1636

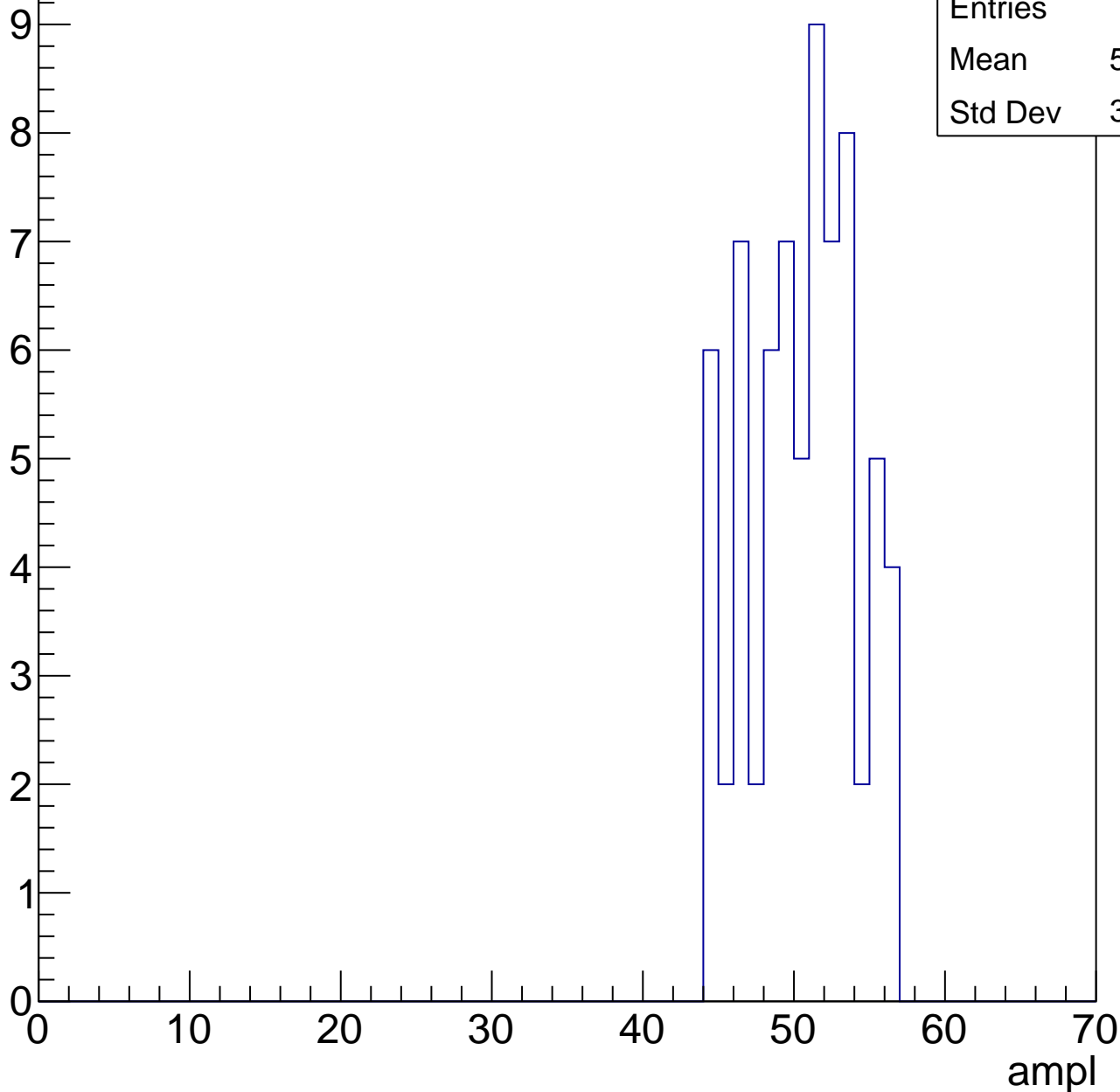
Gaus Width: 4.7377



B1L102S, U19-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

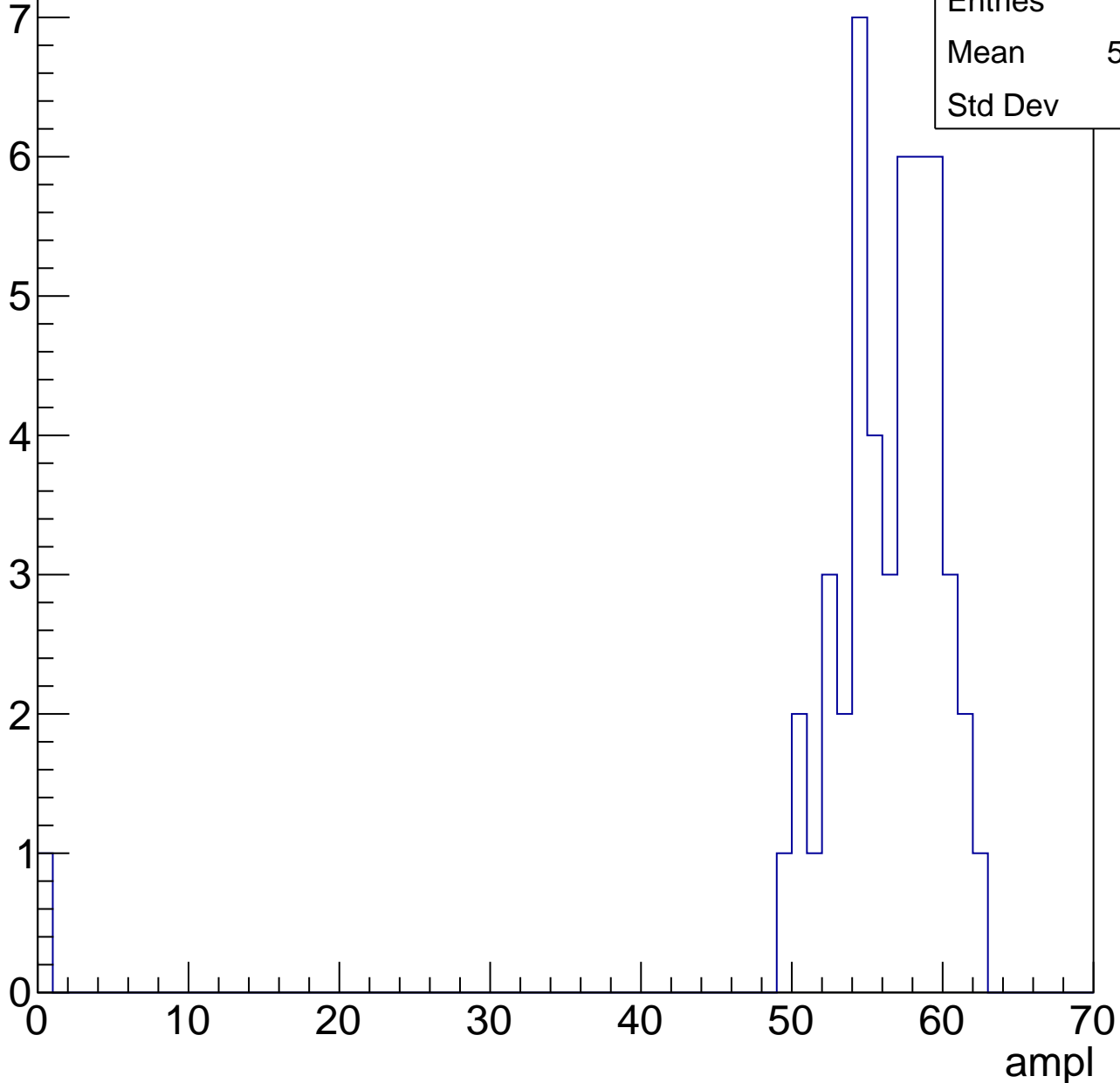


B1L102S, U19-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.92
Std Dev	8.59

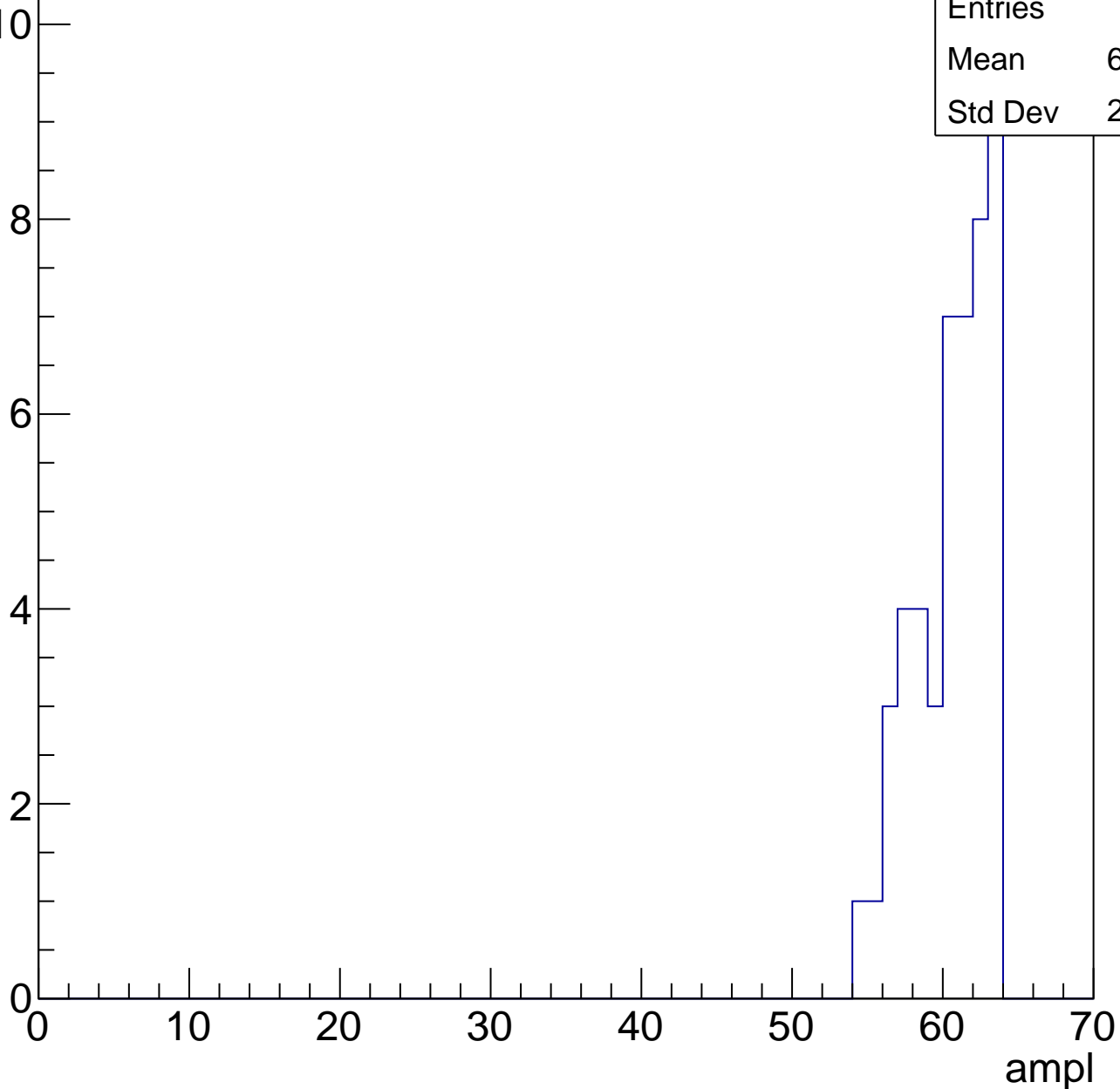


B1L102S, U19-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

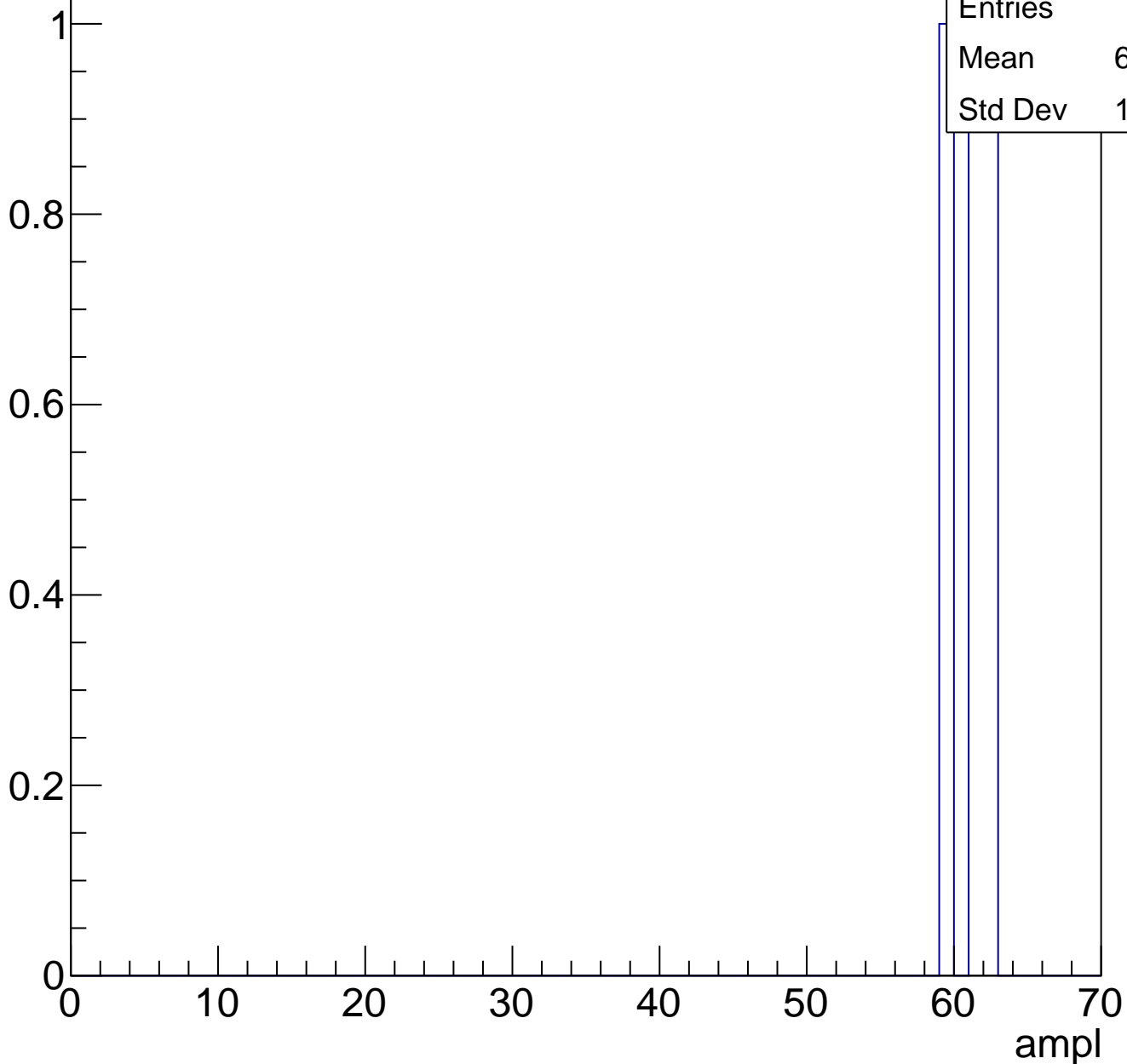
Entries	48
Mean	60.15
Std Dev	2.466



B1L102S, U19-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L102S, U19-ch28, adc0

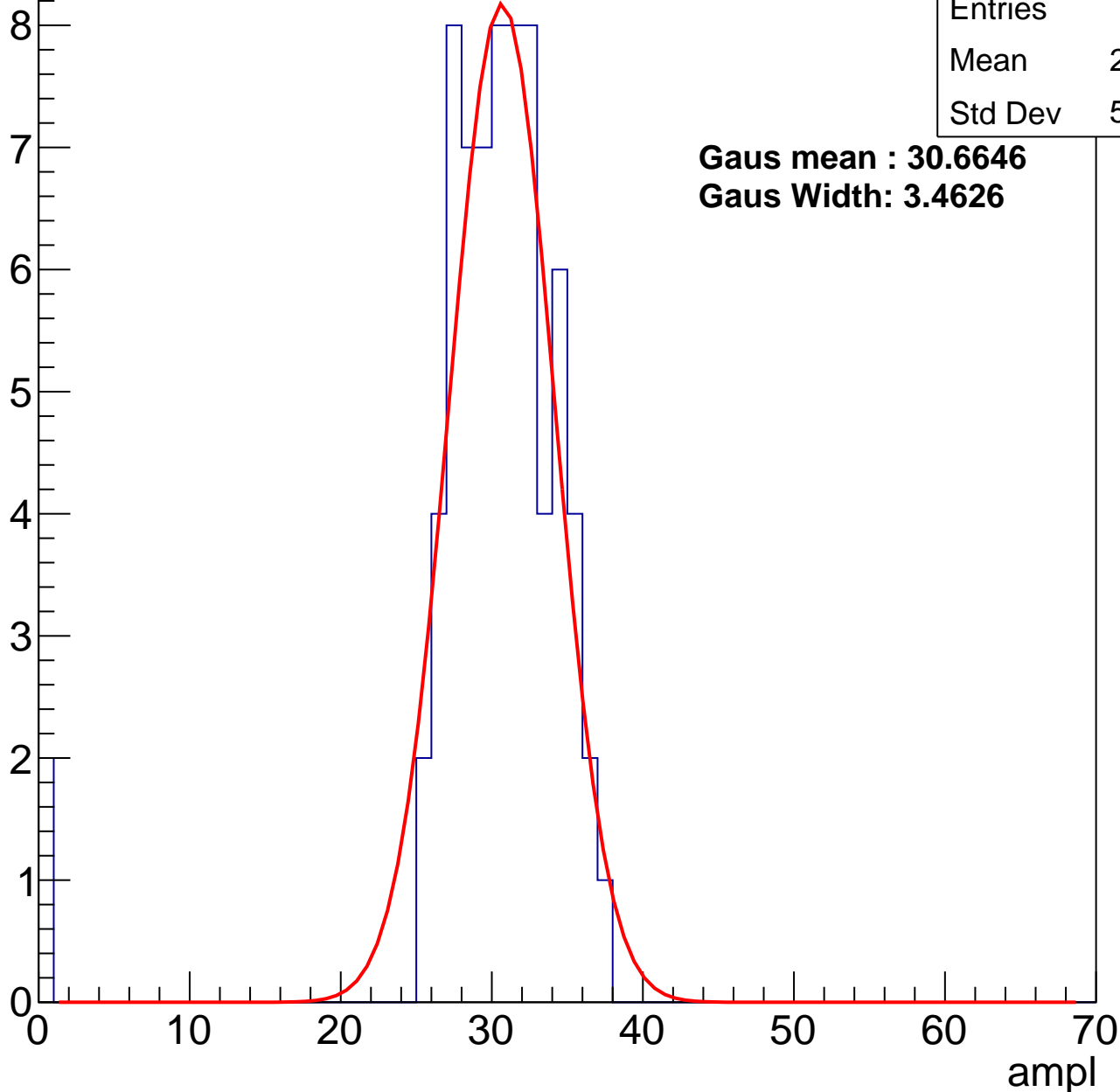
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.55
Std Dev	5.814

Gaus mean : 30.6646

Gaus Width: 3.4626



B1L102S, U19-ch28, adc1

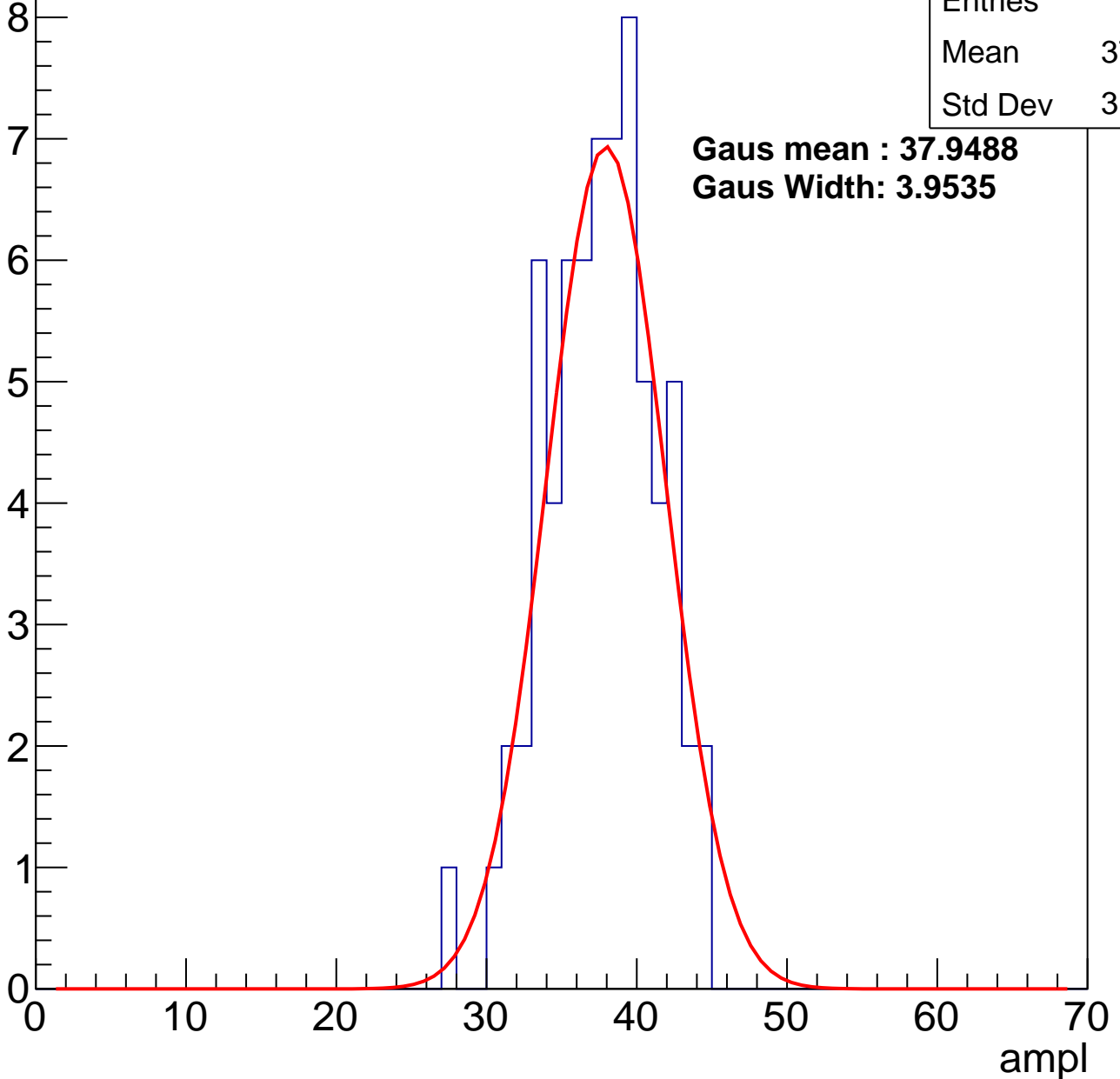
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.18
Std Dev	3.585

Gaus mean : 37.9488

Gaus Width: 3.9535



B1L102S, U19-ch28, adc2

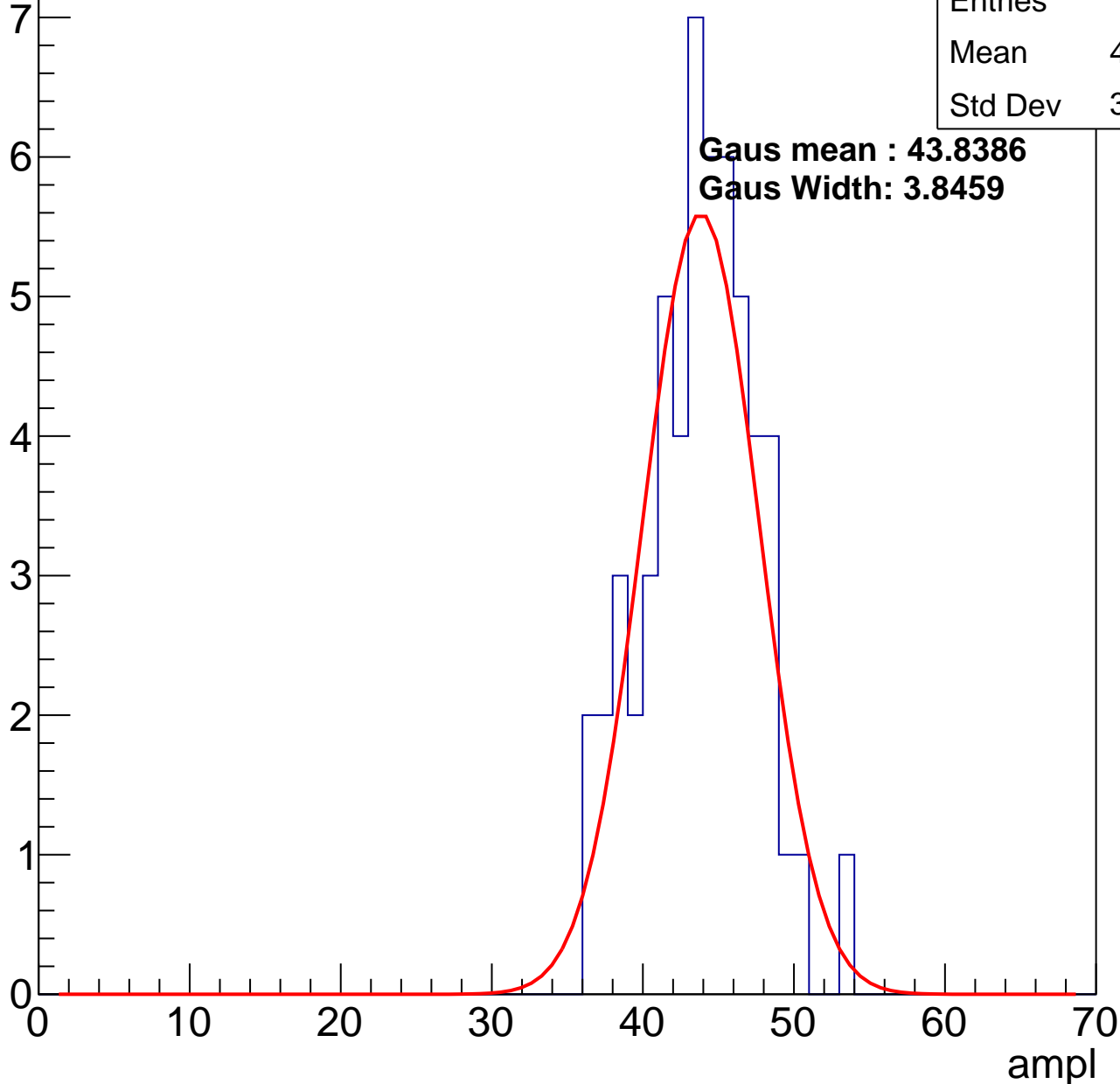
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.36
Std Dev	3.647

Gaus mean : 43.8386

Gaus Width: 3.8459

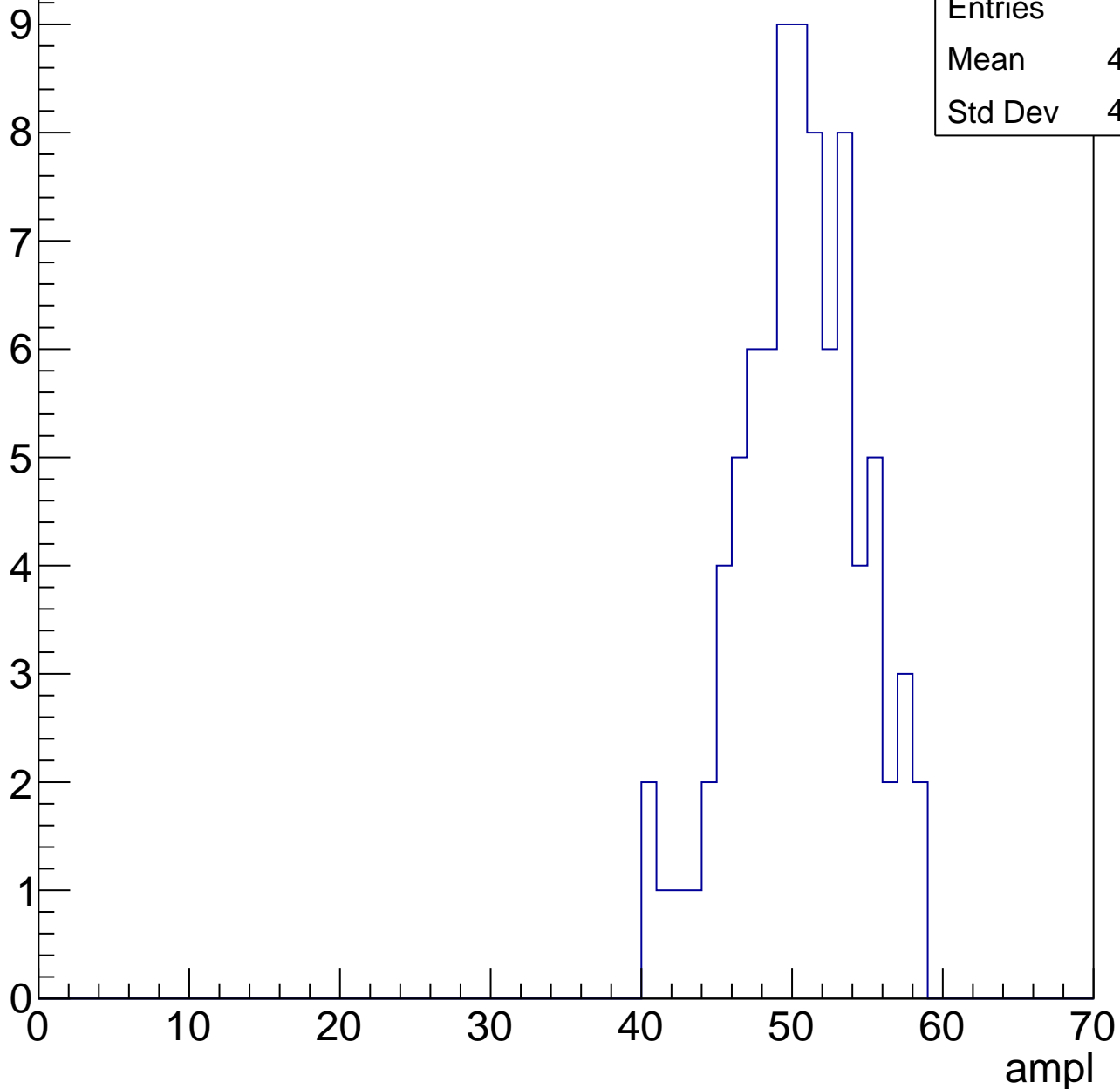


B1L102S, U19-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	49.99
Std Dev	4.043

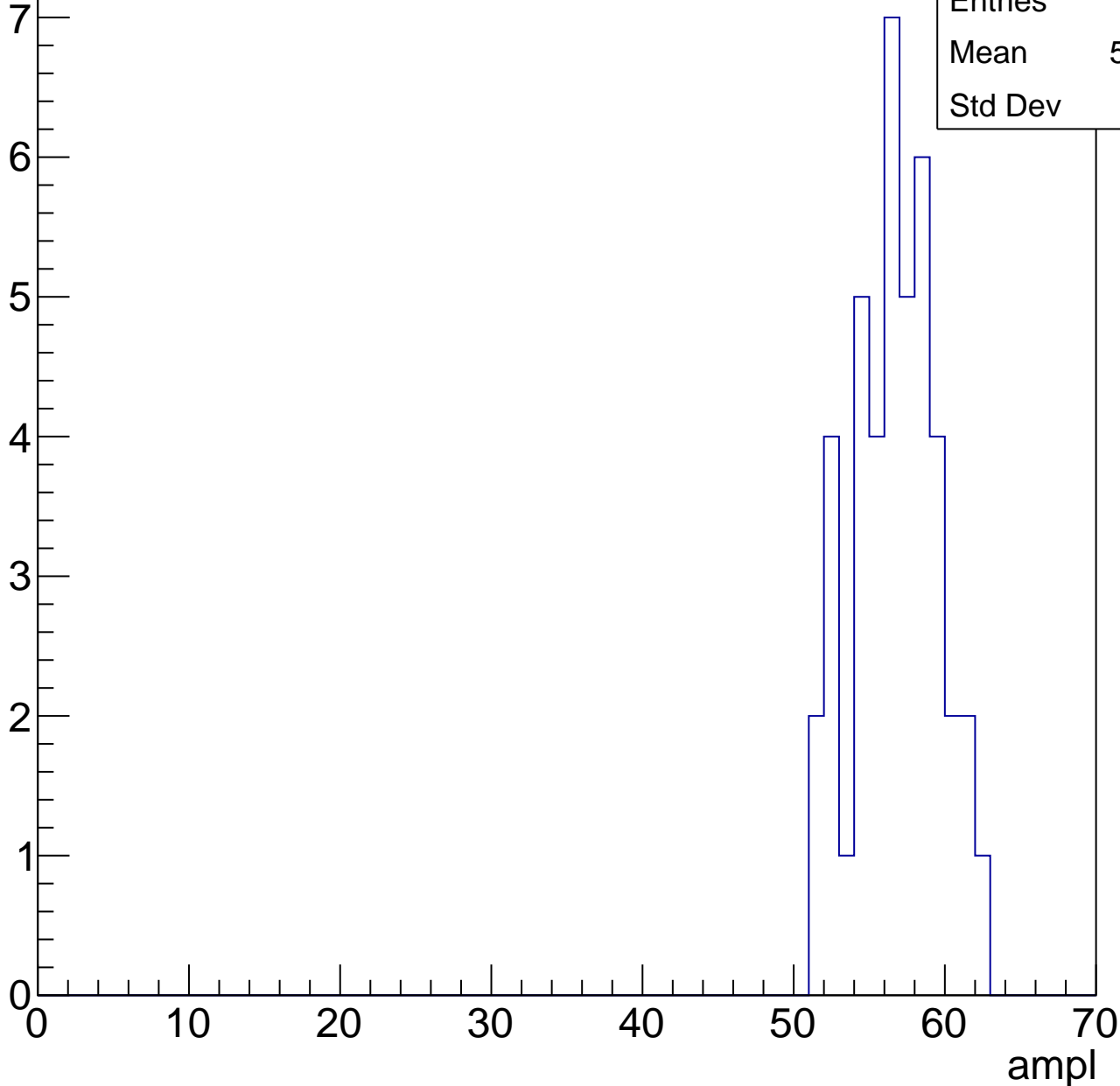


B1L102S, U19-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

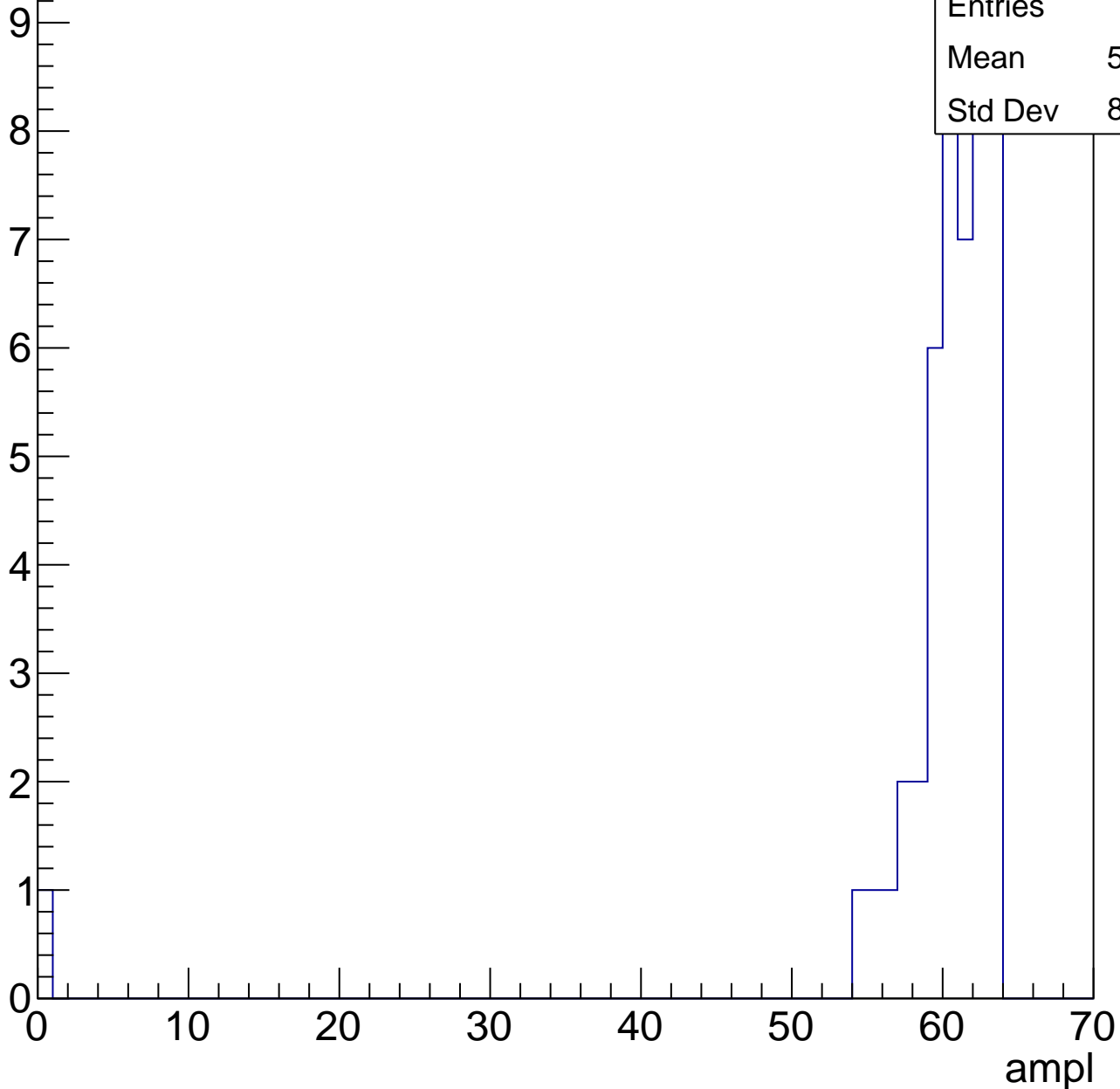
Entries	43
Mean	56.23
Std Dev	2.76



B1L102S, U19-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch29, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	28.88
Std Dev	4.838

Gaus mean : 29.6912

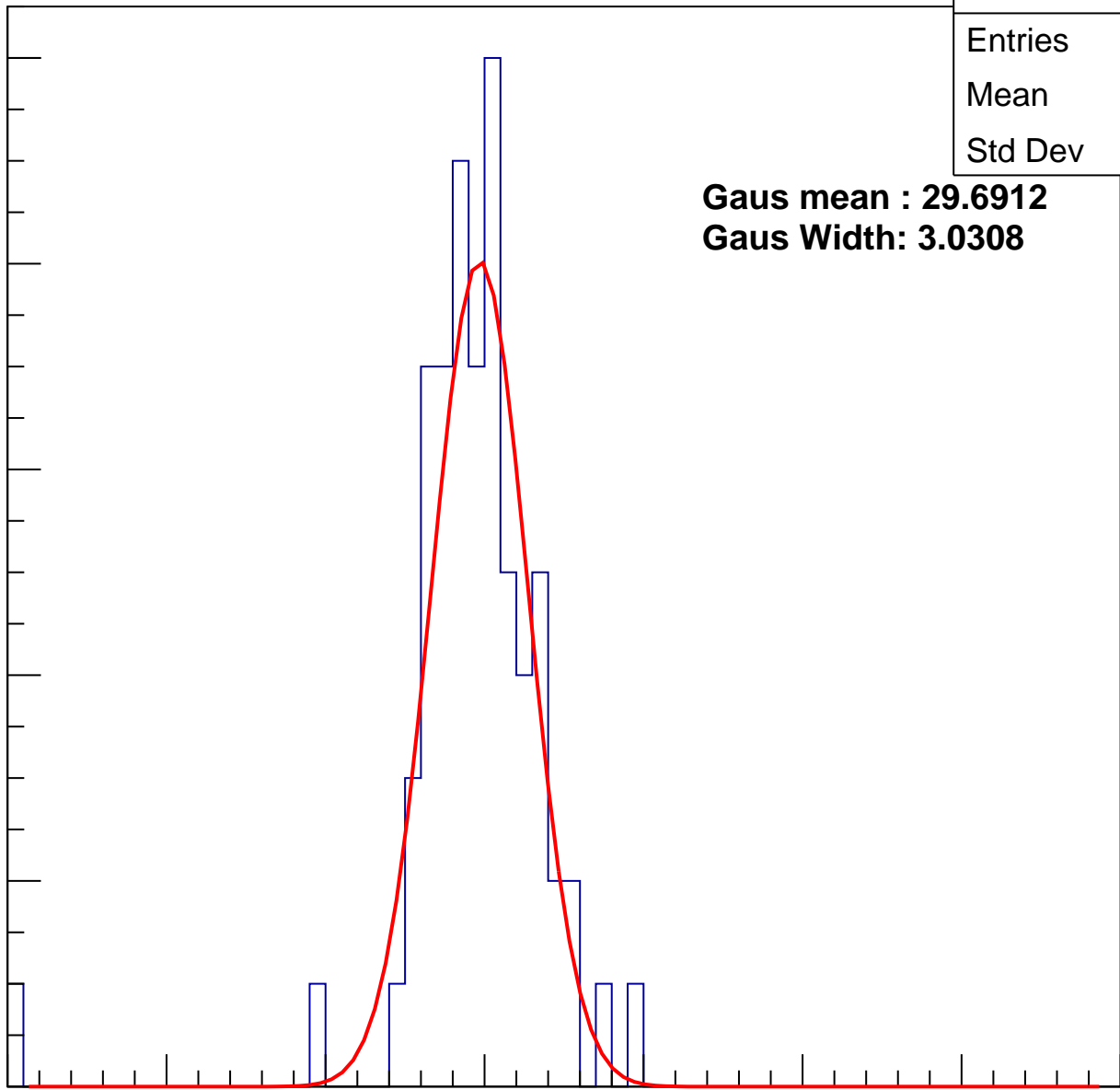
Gaus Width: 3.0308

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch29, adc1

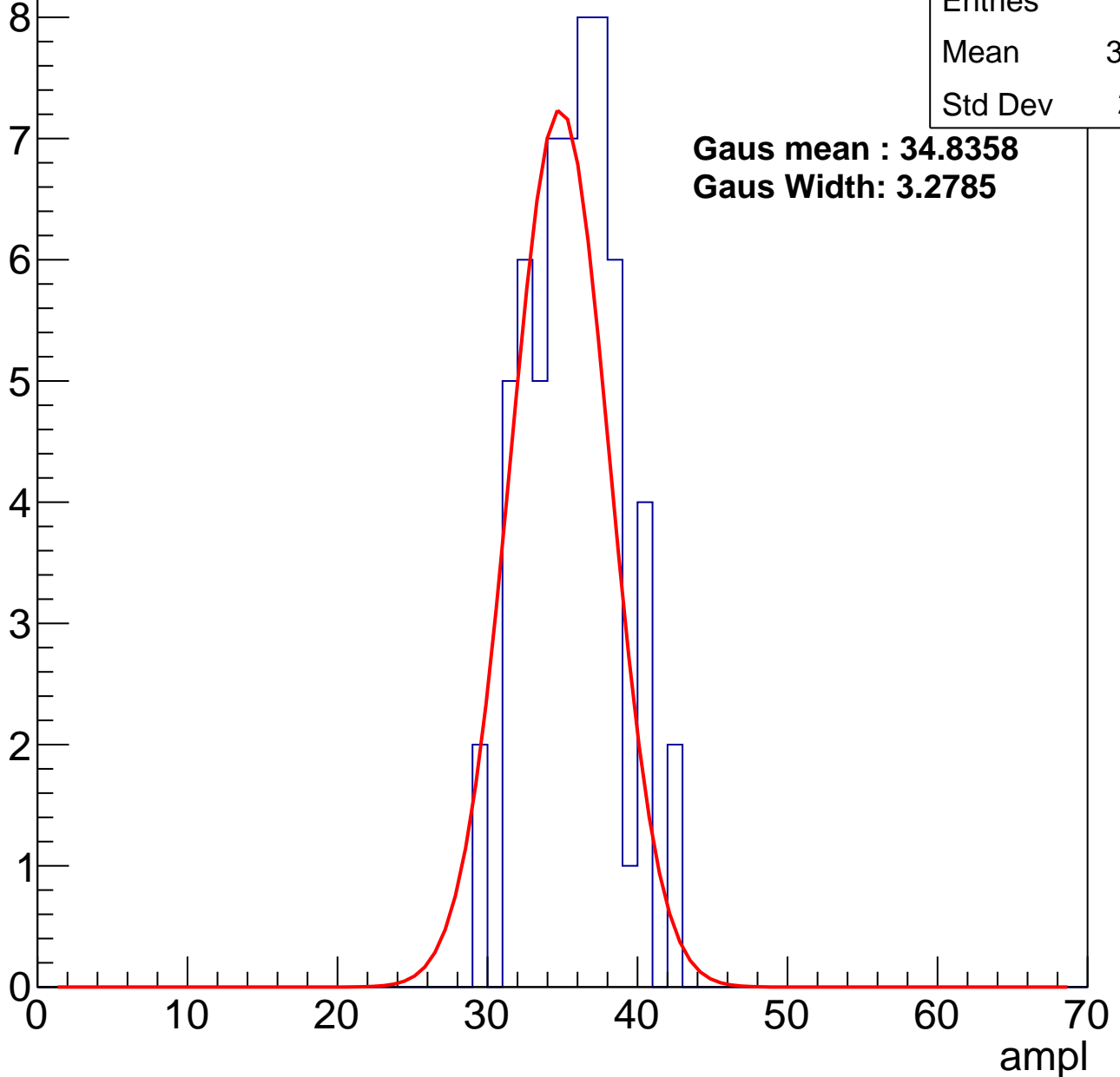
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.21
Std Dev	2.97

Gaus mean : 34.8358

Gaus Width: 3.2785



B1L102S, U19-ch29, adc2

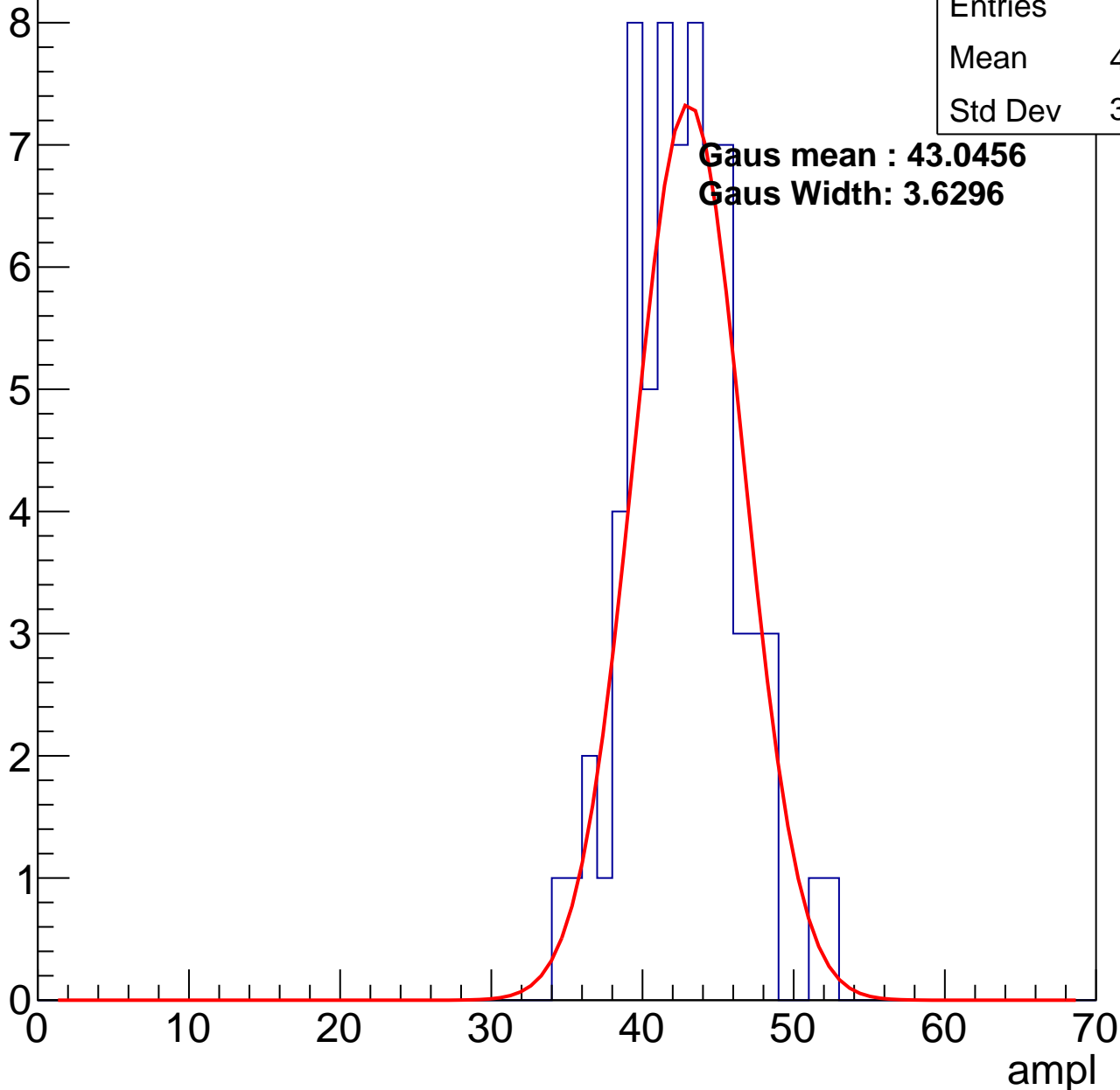
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.24
Std Dev	3.547

Gaus mean : 43.0456

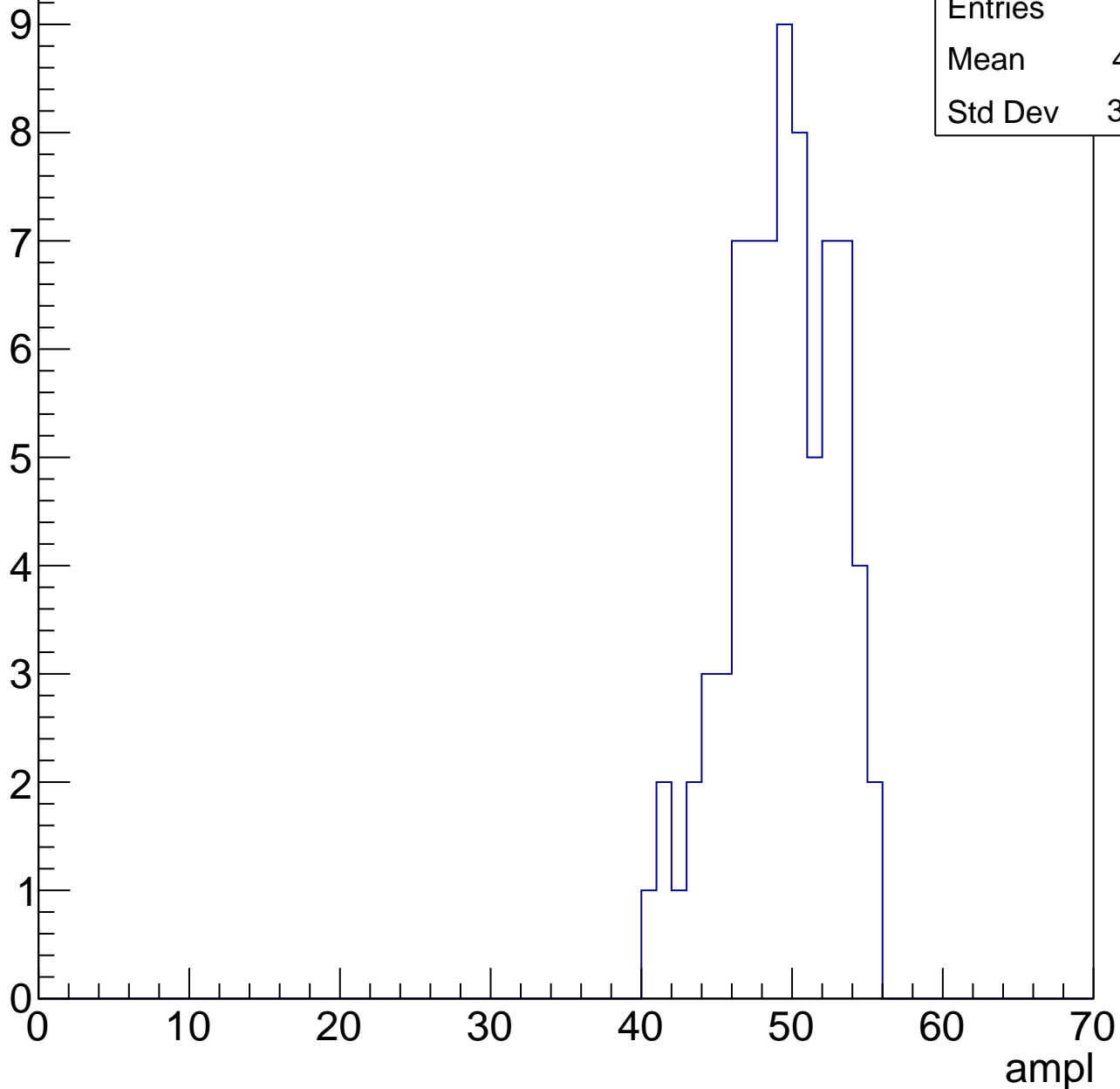
Gaus Width: 3.6296



B1L102S, U19-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

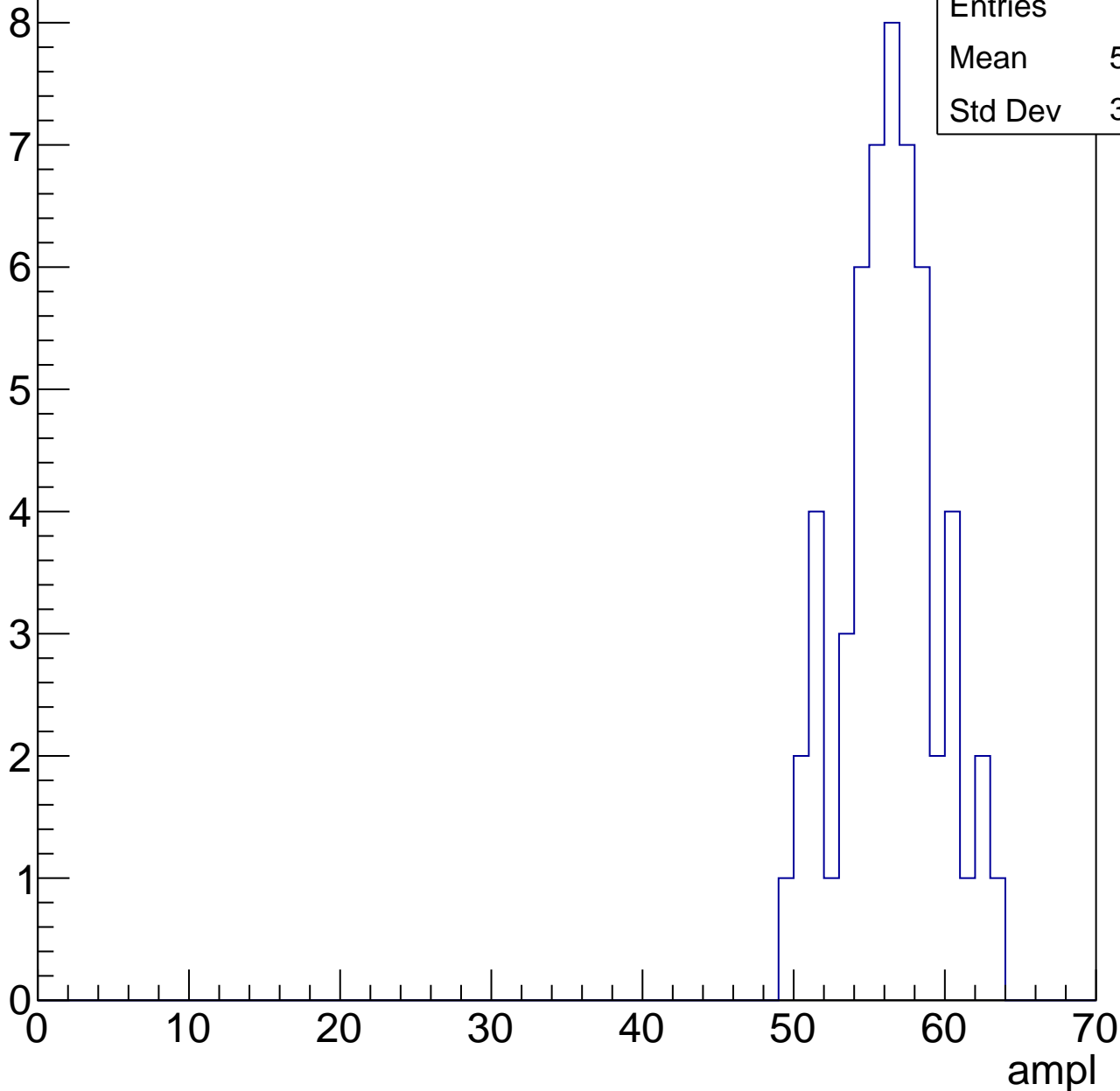


B1L102S, U19-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.89
Std Dev	3.172

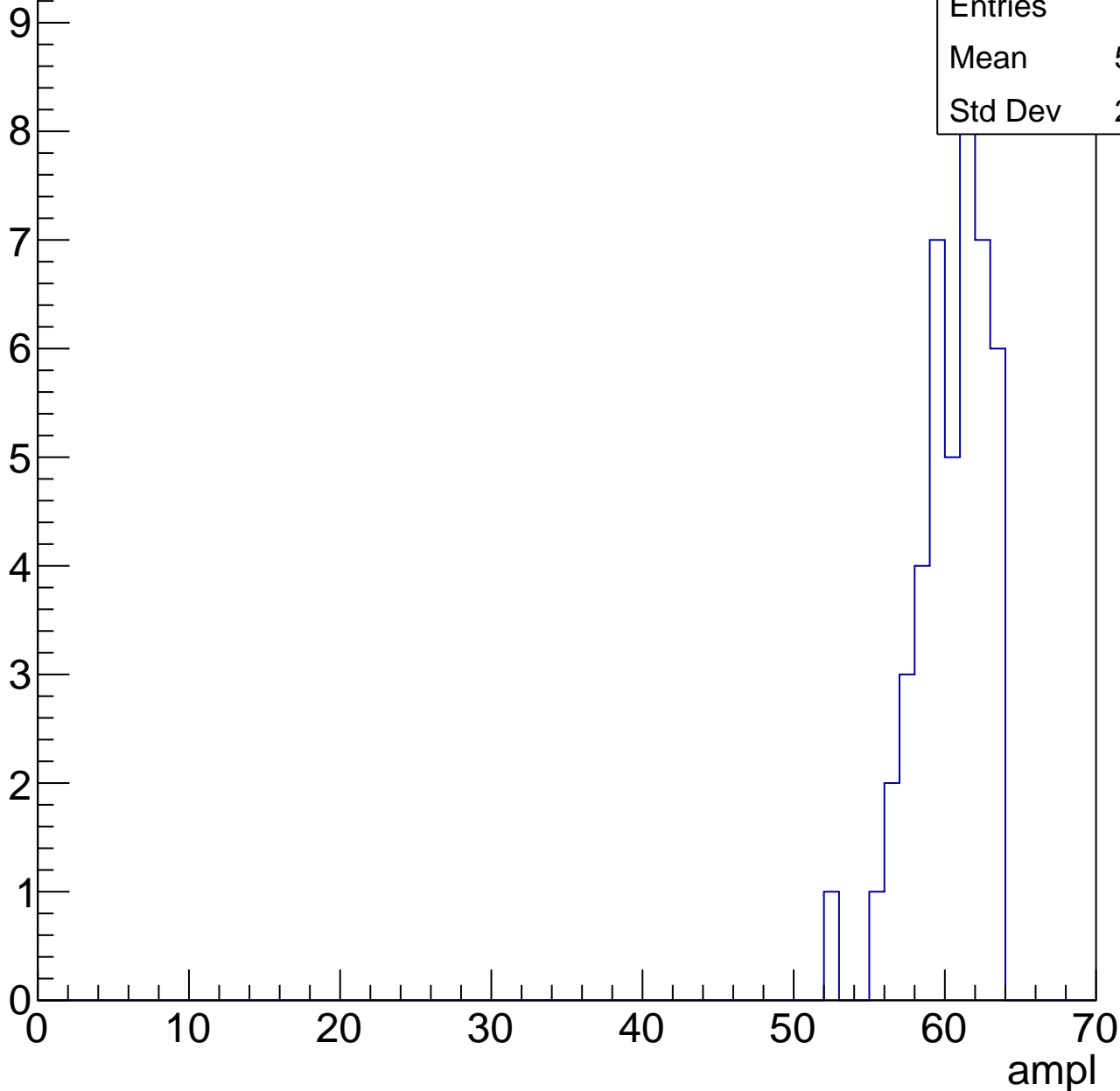


B1L102S, U19-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

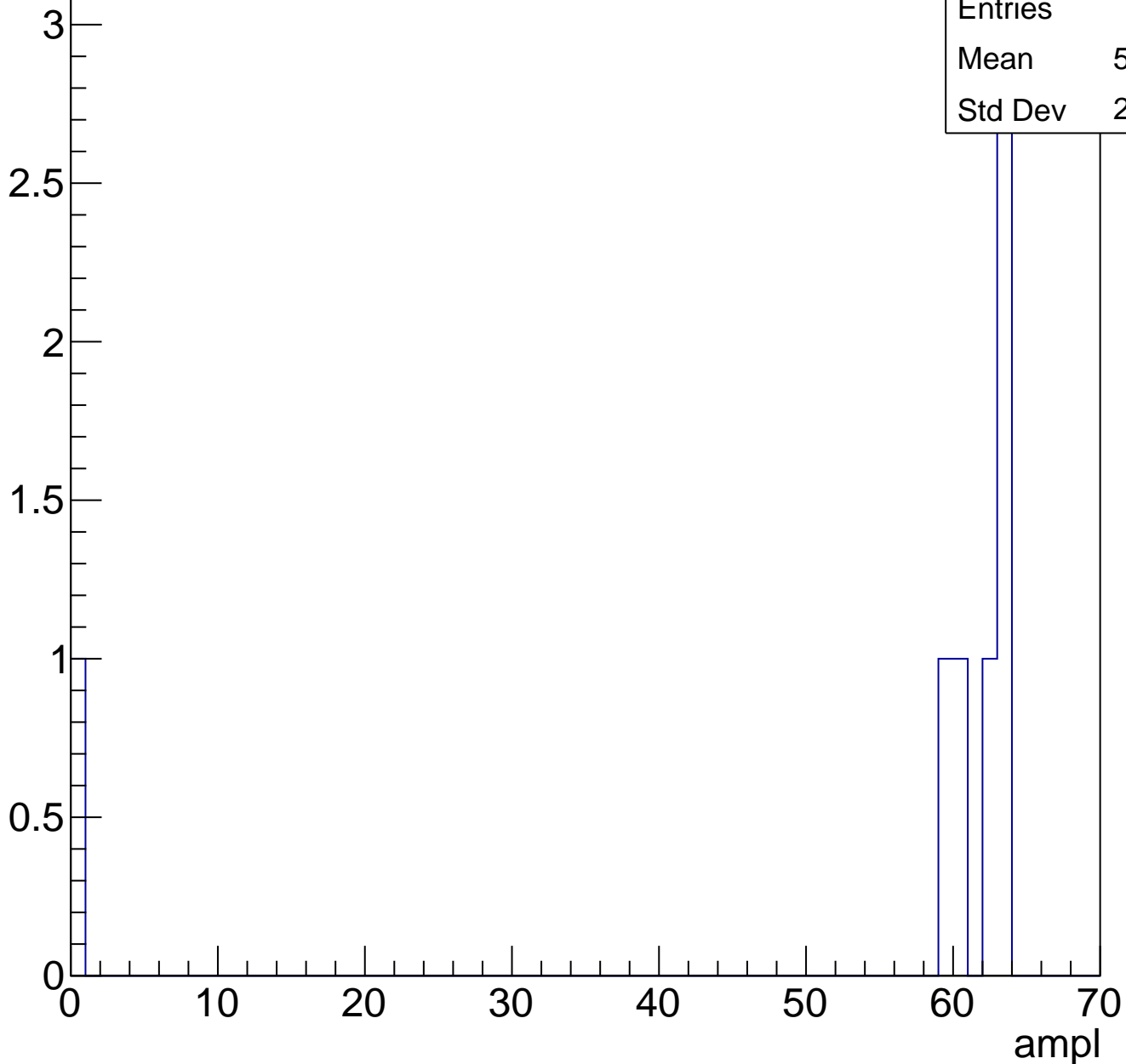
Entries	45
Mean	59.91
Std Dev	2.411



B1L102S, U19-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch30, adc0

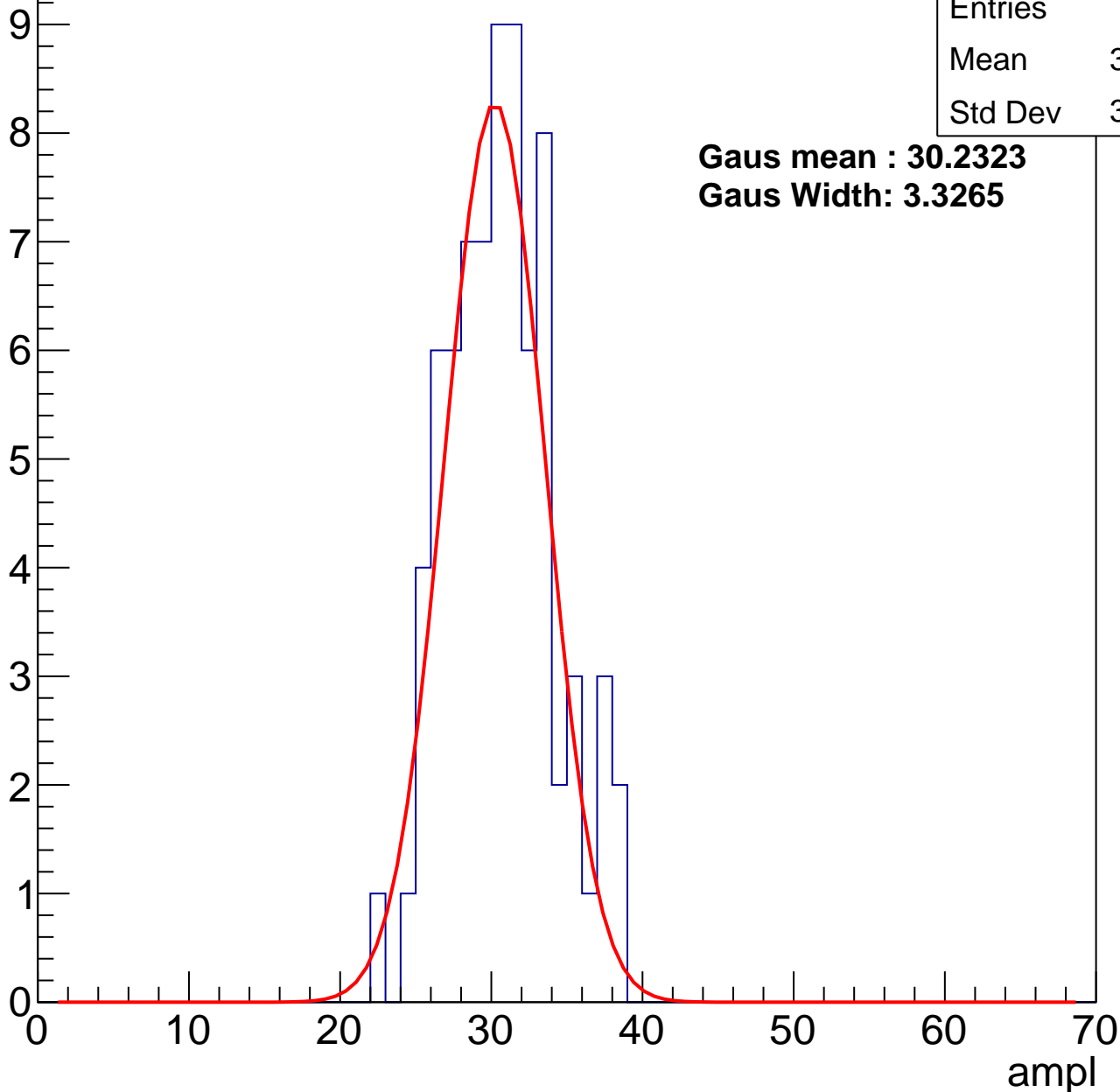
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	30.19
Std Dev	3.474

Gaus mean : 30.2323

Gaus Width: 3.3265



B1L102S, U19-ch30, adc1

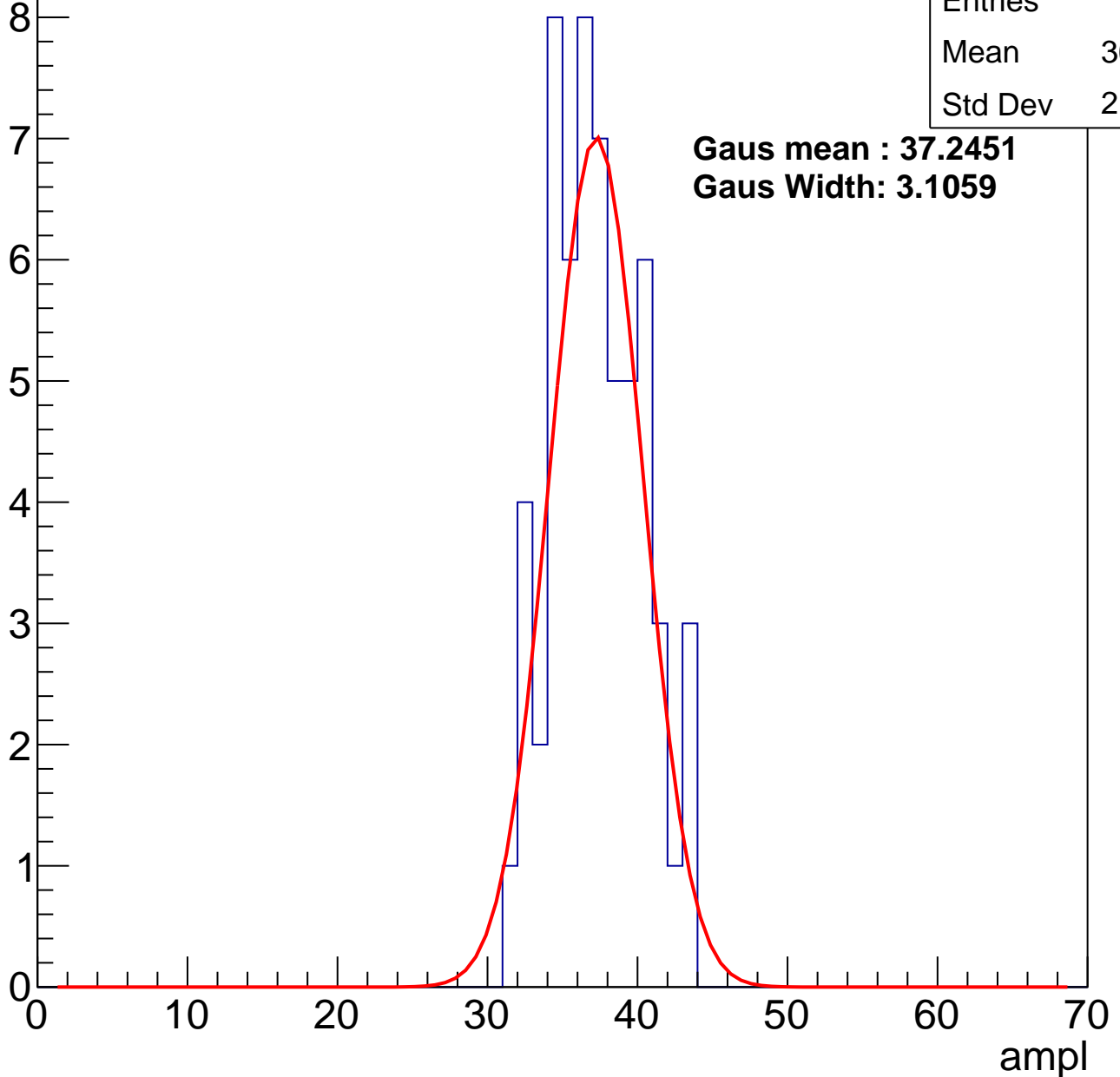
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.83
Std Dev	2.998

Gaus mean : 37.2451

Gaus Width: 3.1059



B1L102S, U19-ch30, adc2

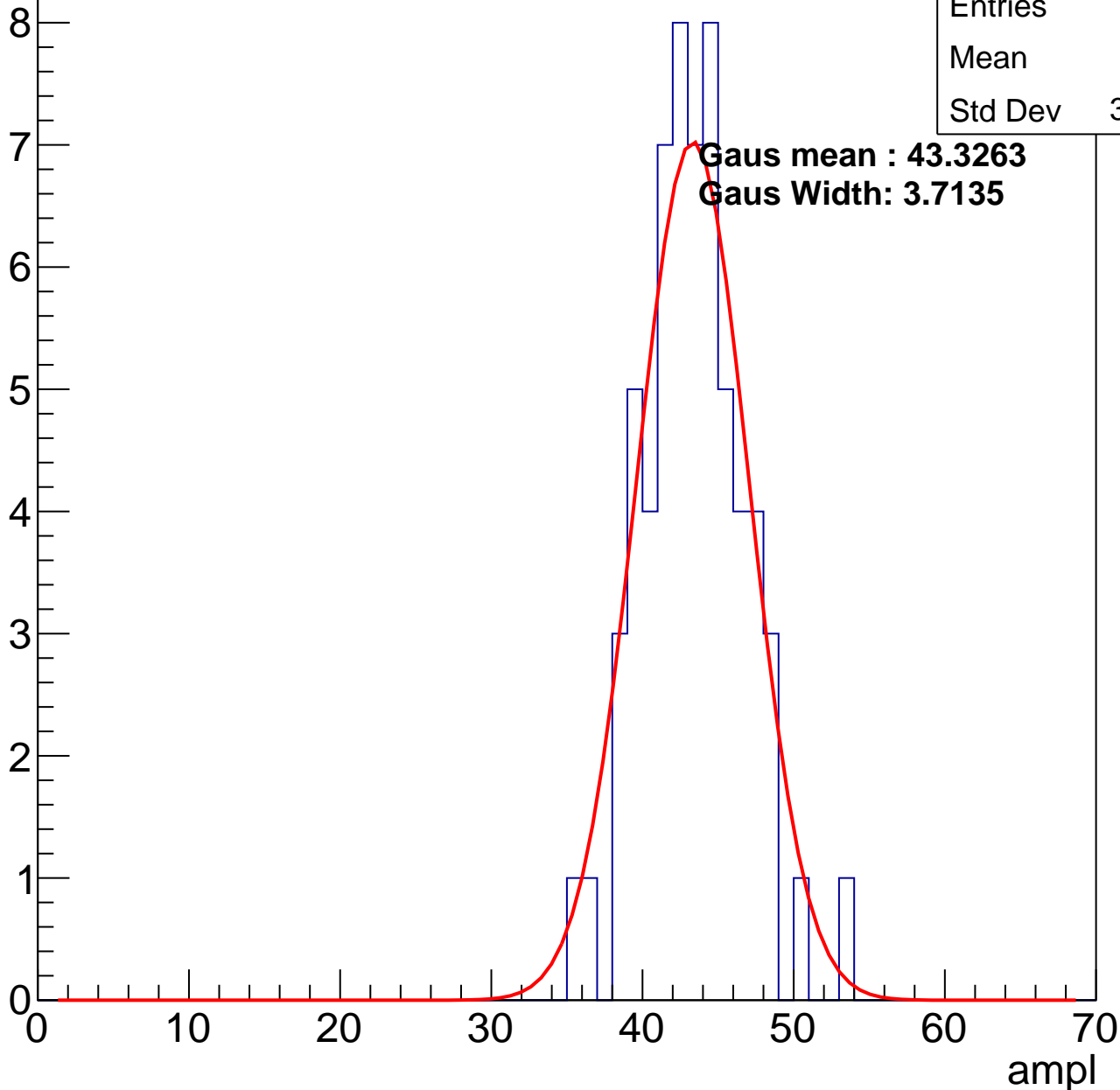
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.9
Std Dev	3.339

Gaus mean : 43.3263

Gaus Width: 3.7135

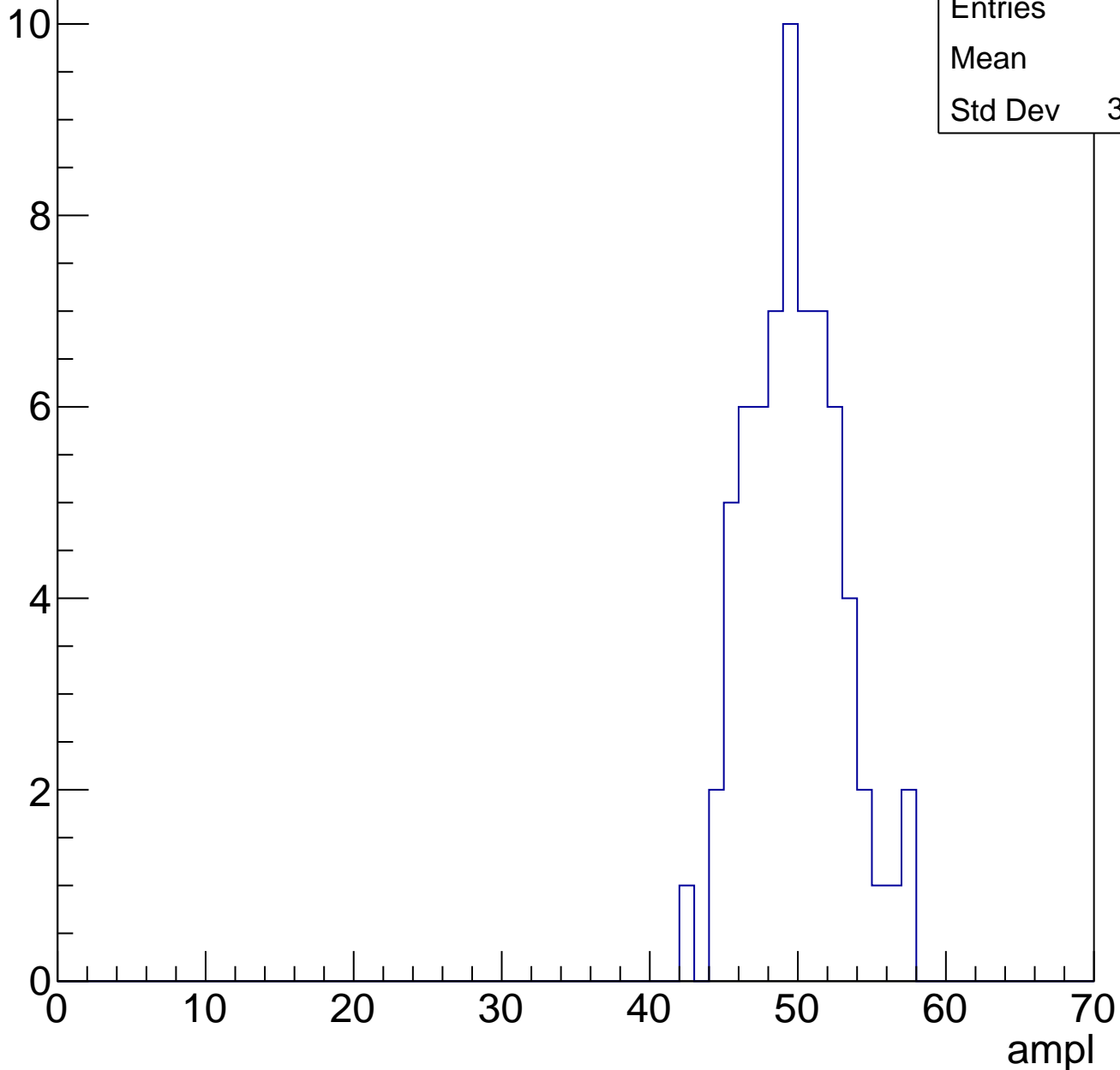


B1L102S, U19-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	49.3
Std Dev	3.172

Entry

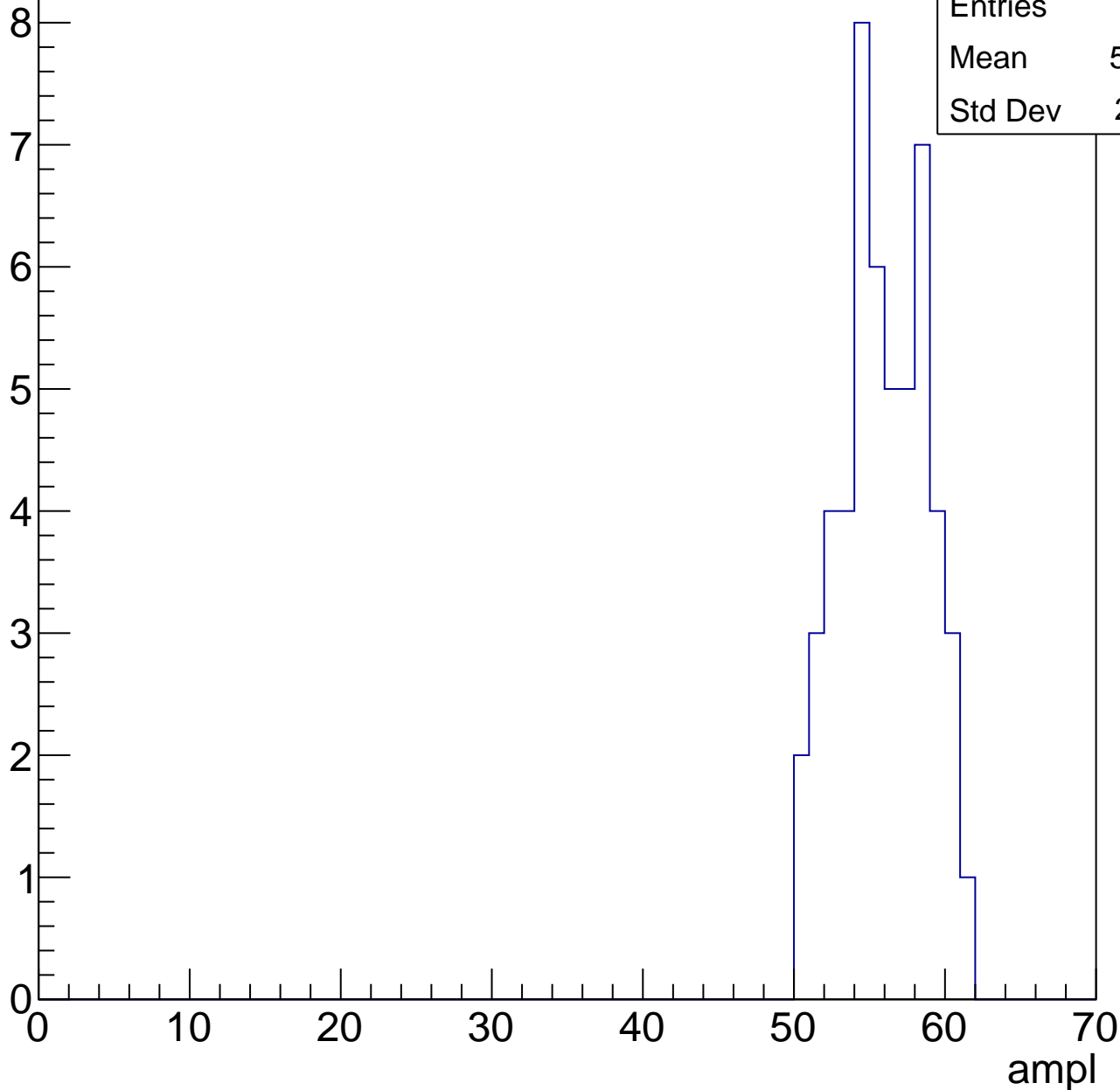


B1L102S, U19-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.44
Std Dev	2.811

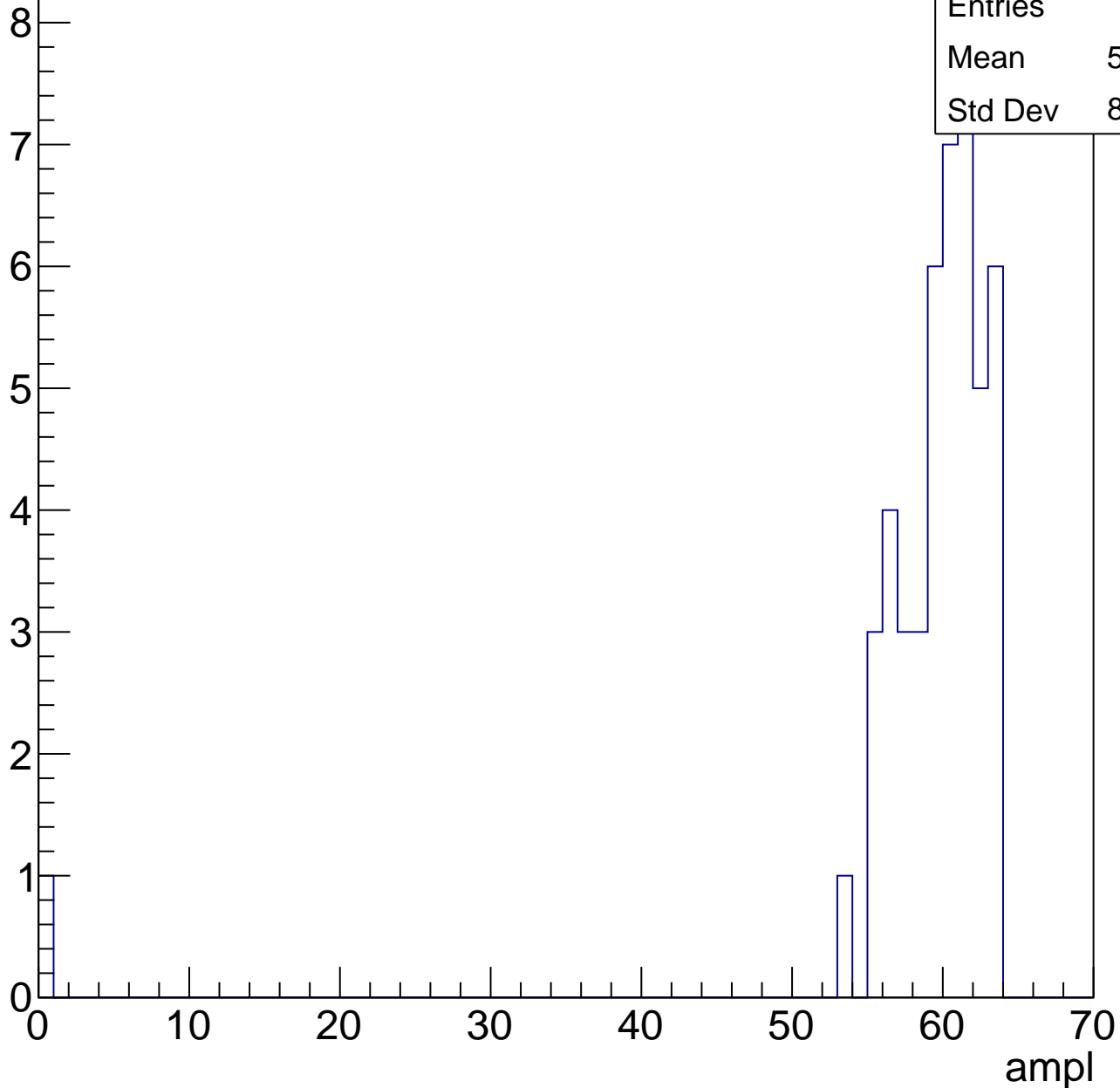


B1L102S, U19-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.23
Std Dev	8.954

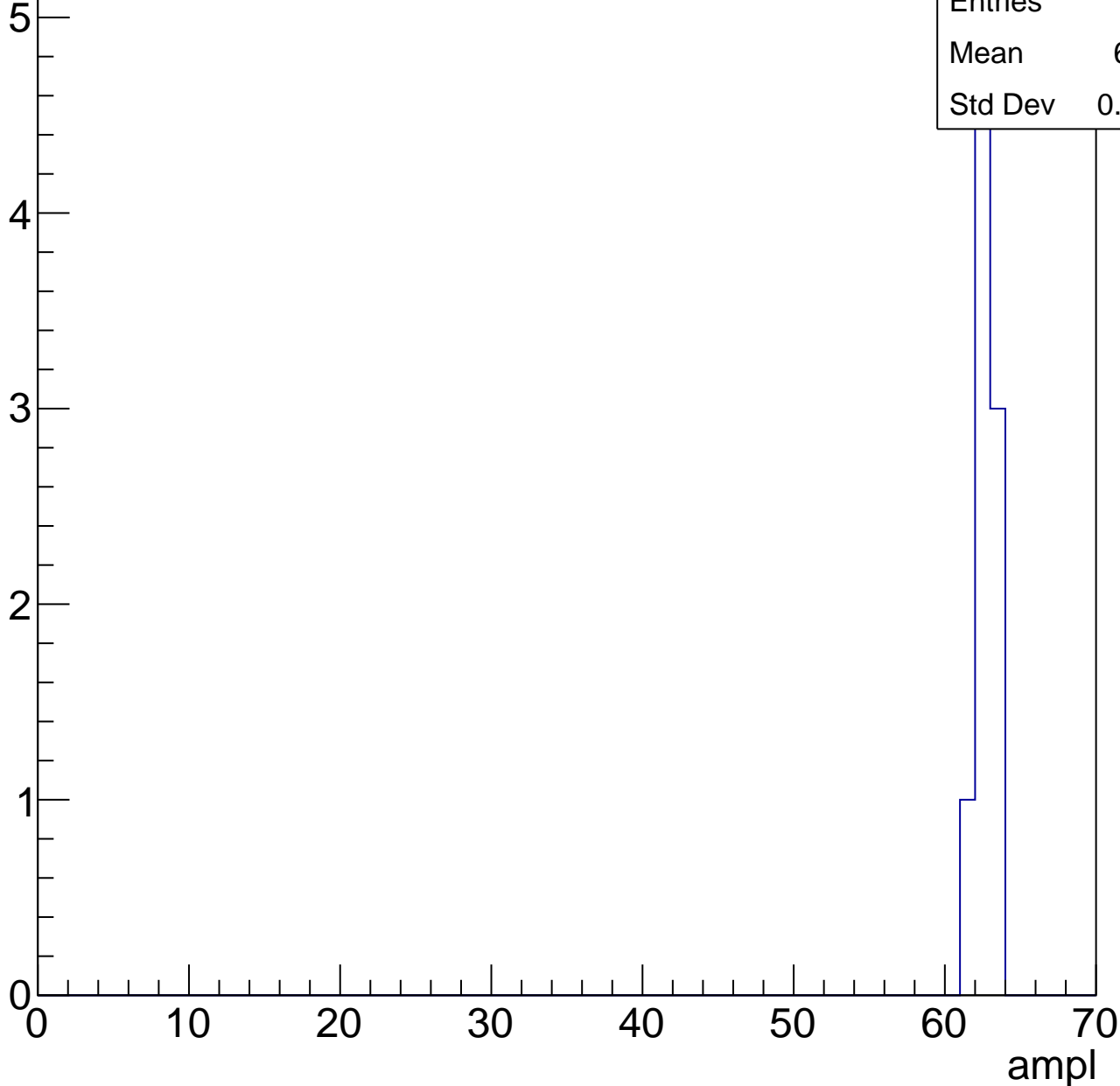


B1L102S, U19-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.22
Std Dev	0.6285



B1L102S, U19-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

B1L102S, U19-ch31, adc0

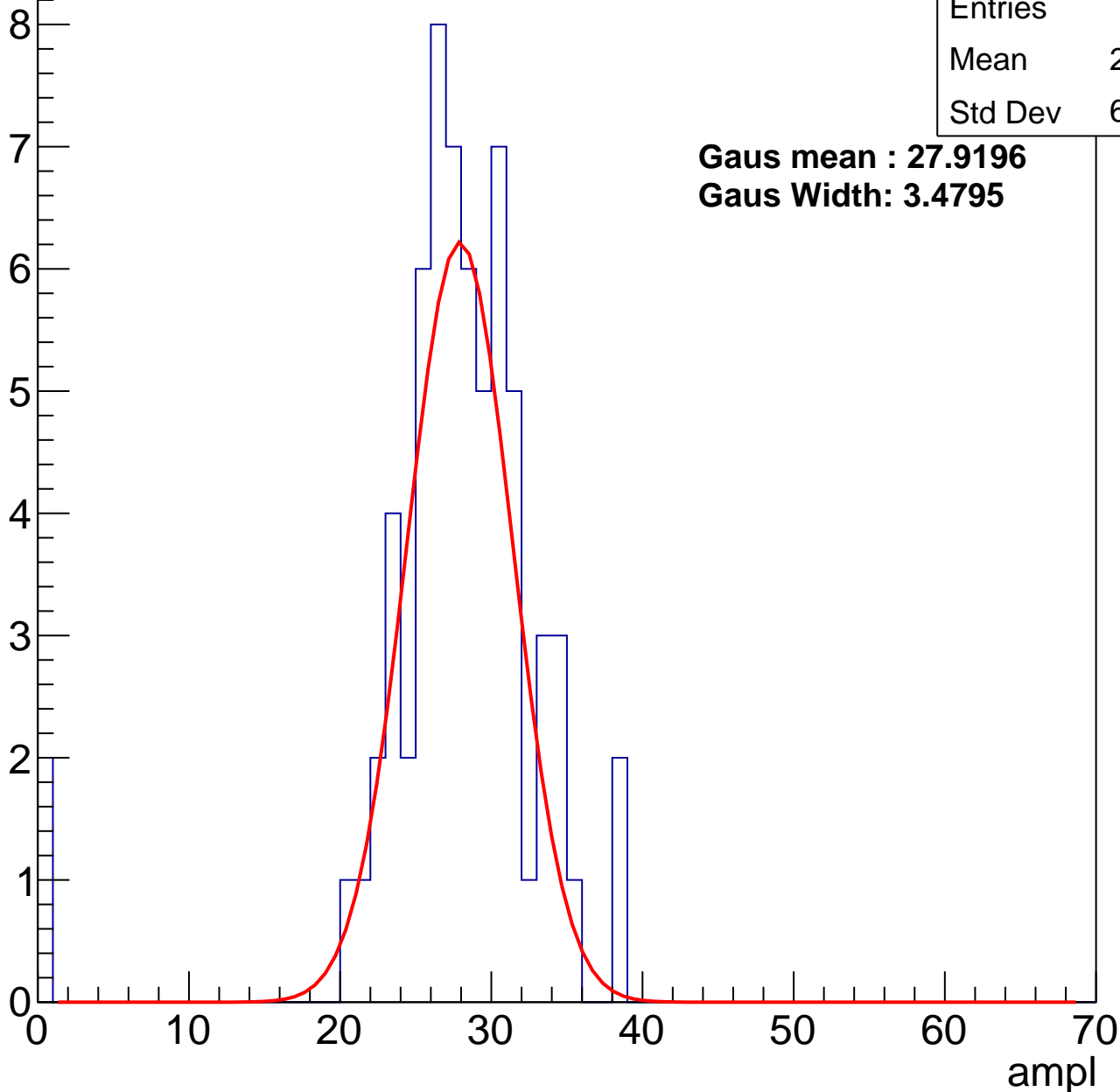
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.18
Std Dev	6.105

Gaus mean : 27.9196

Gaus Width: 3.4795



B1L102S, U19-ch31, adc1

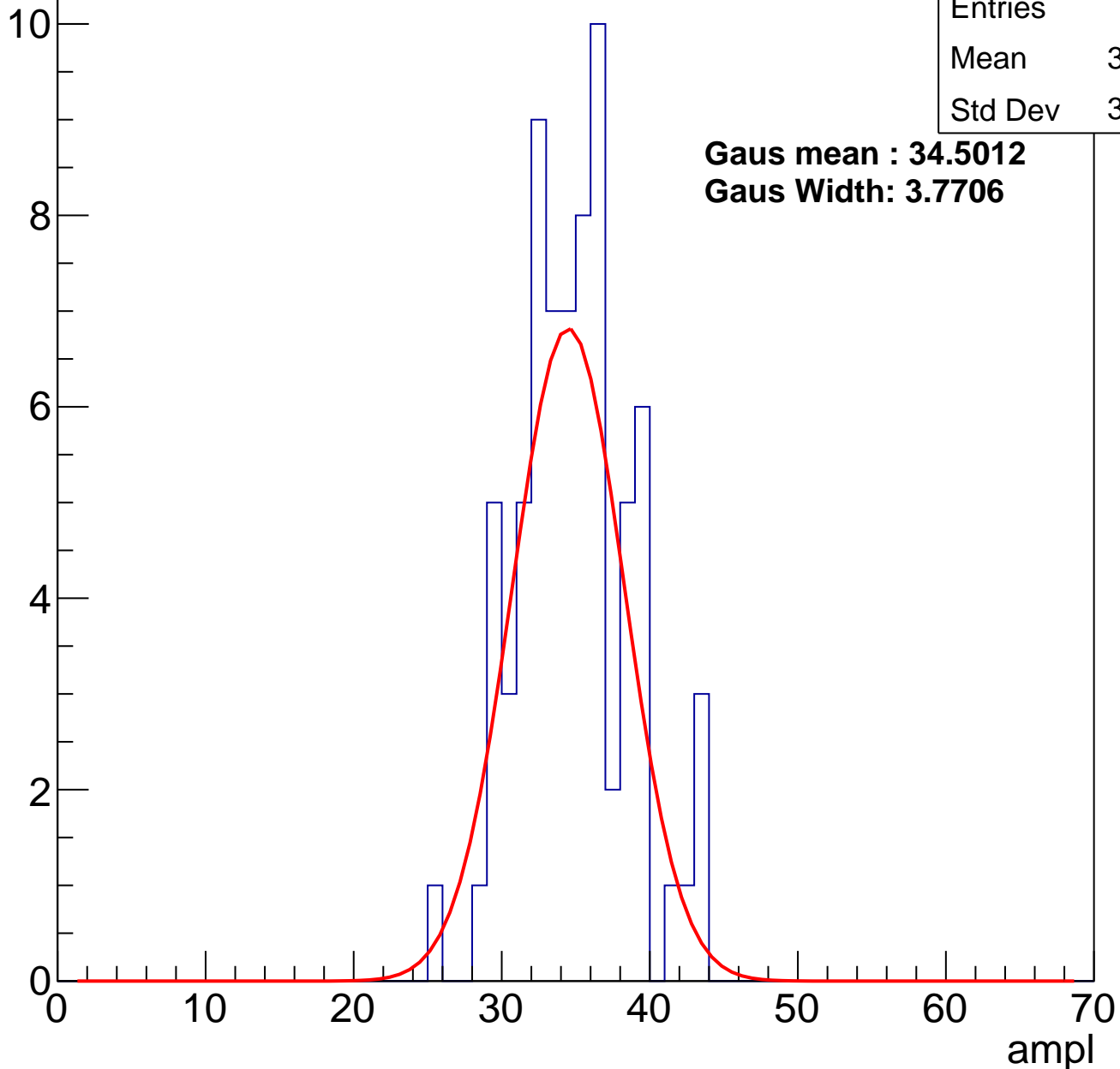
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	34.46
Std Dev	3.702

Gaus mean : 34.5012

Gaus Width: 3.7706

Entry



B1L102S, U19-ch31, adc2

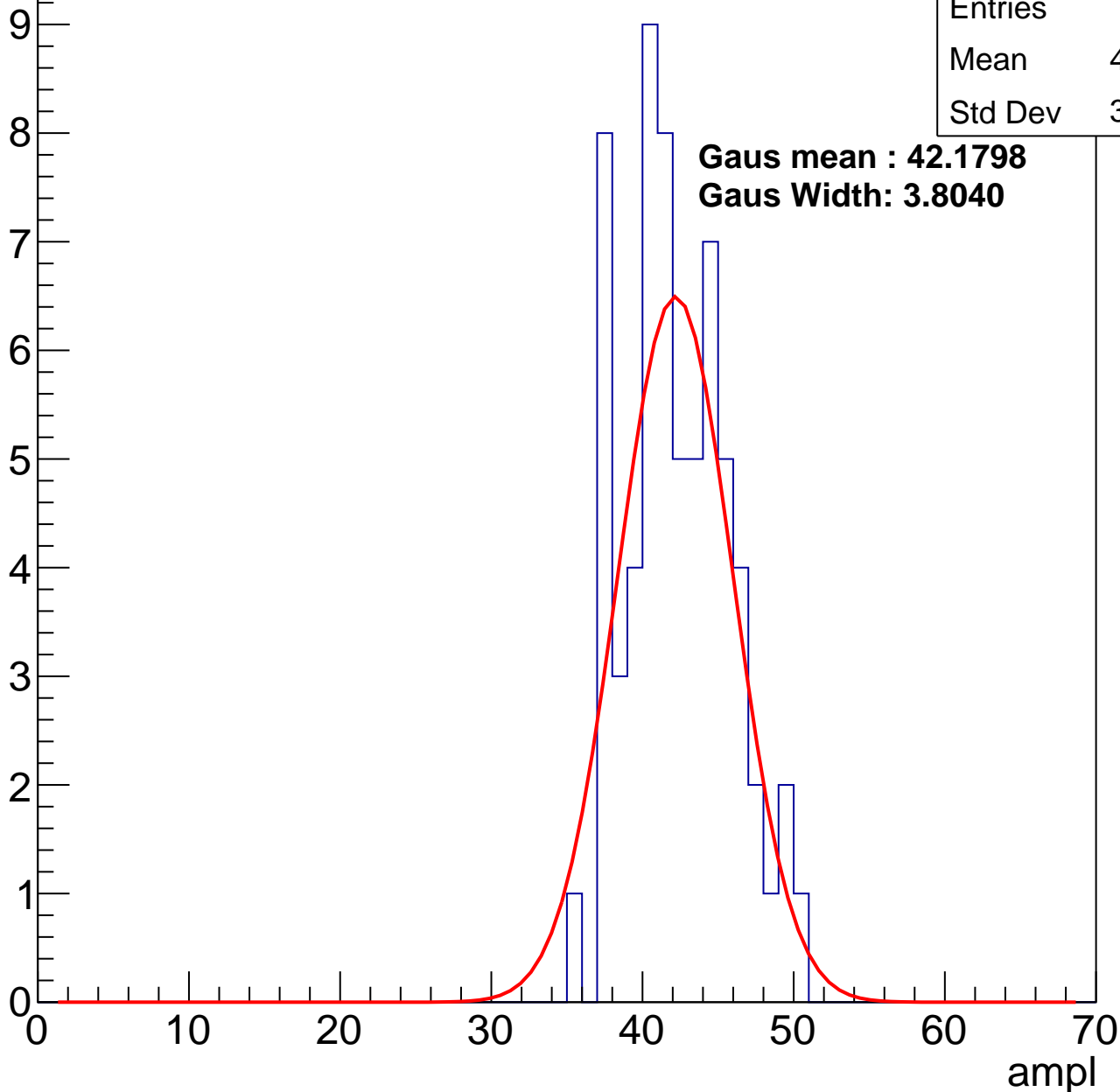
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.86
Std Dev	3.432

Gaus mean : 42.1798

Gaus Width: 3.8040

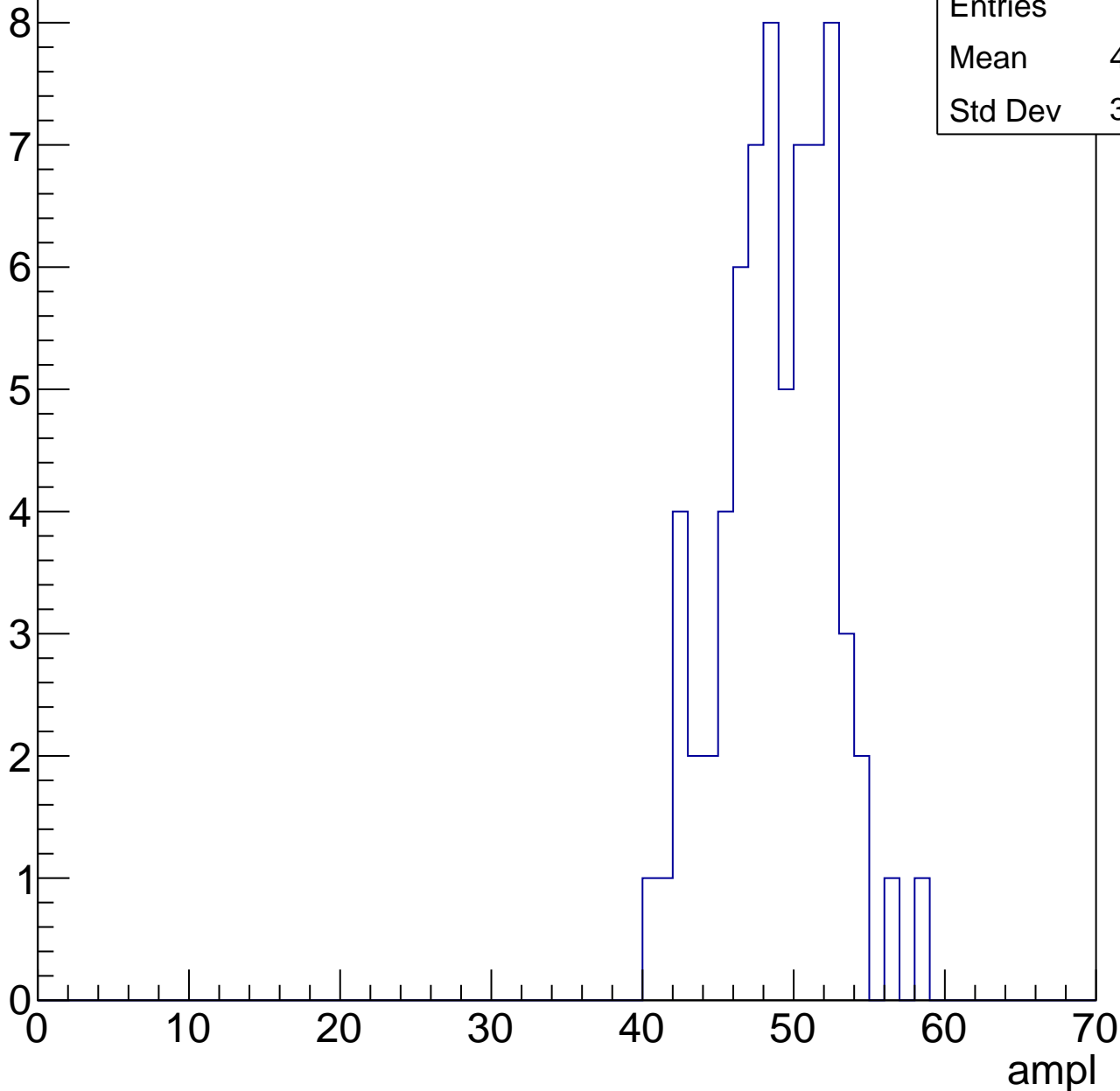


B1L102S, U19-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.42
Std Dev	3.669

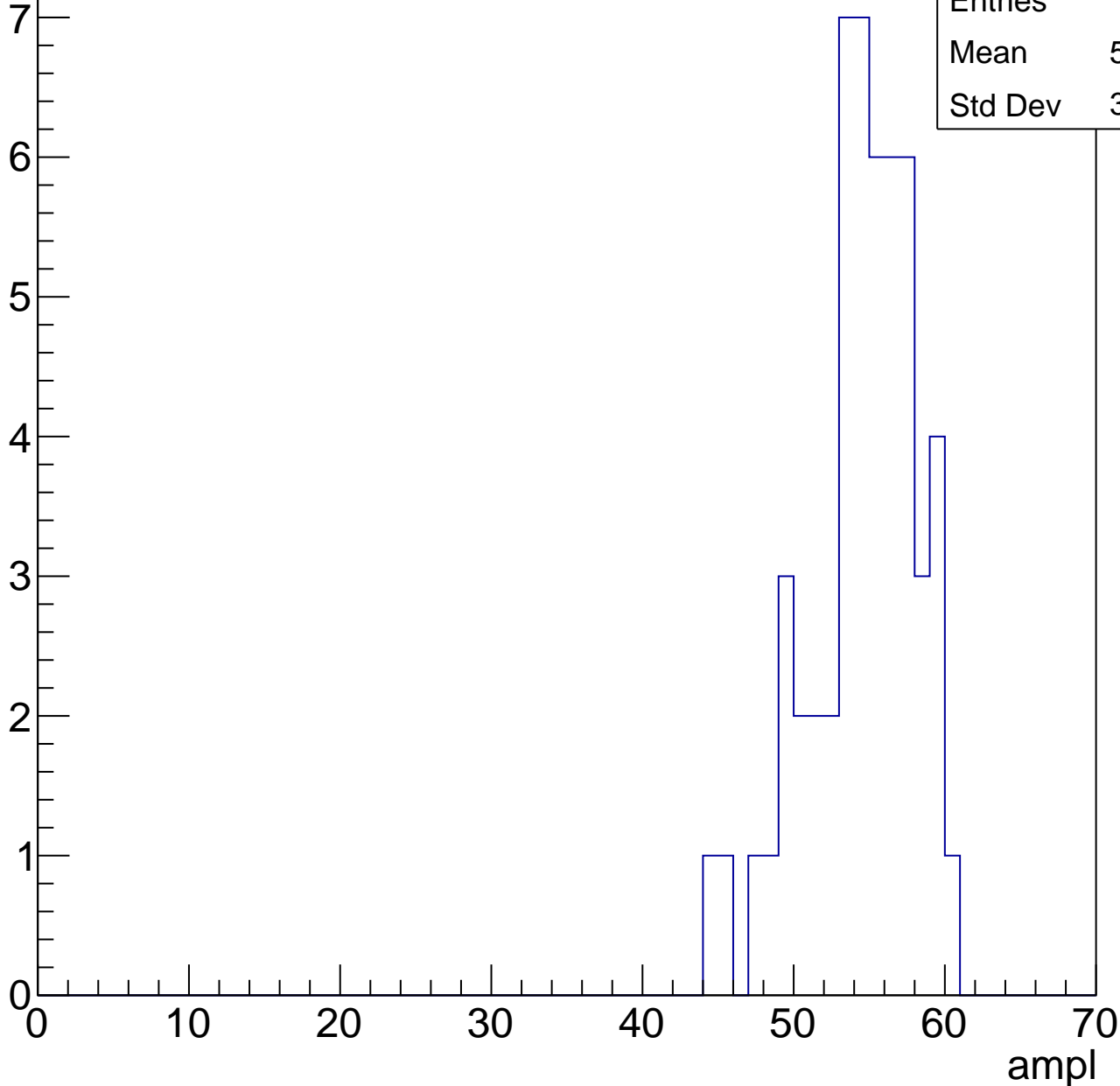


B1L102S, U19-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.04
Std Dev	3.566

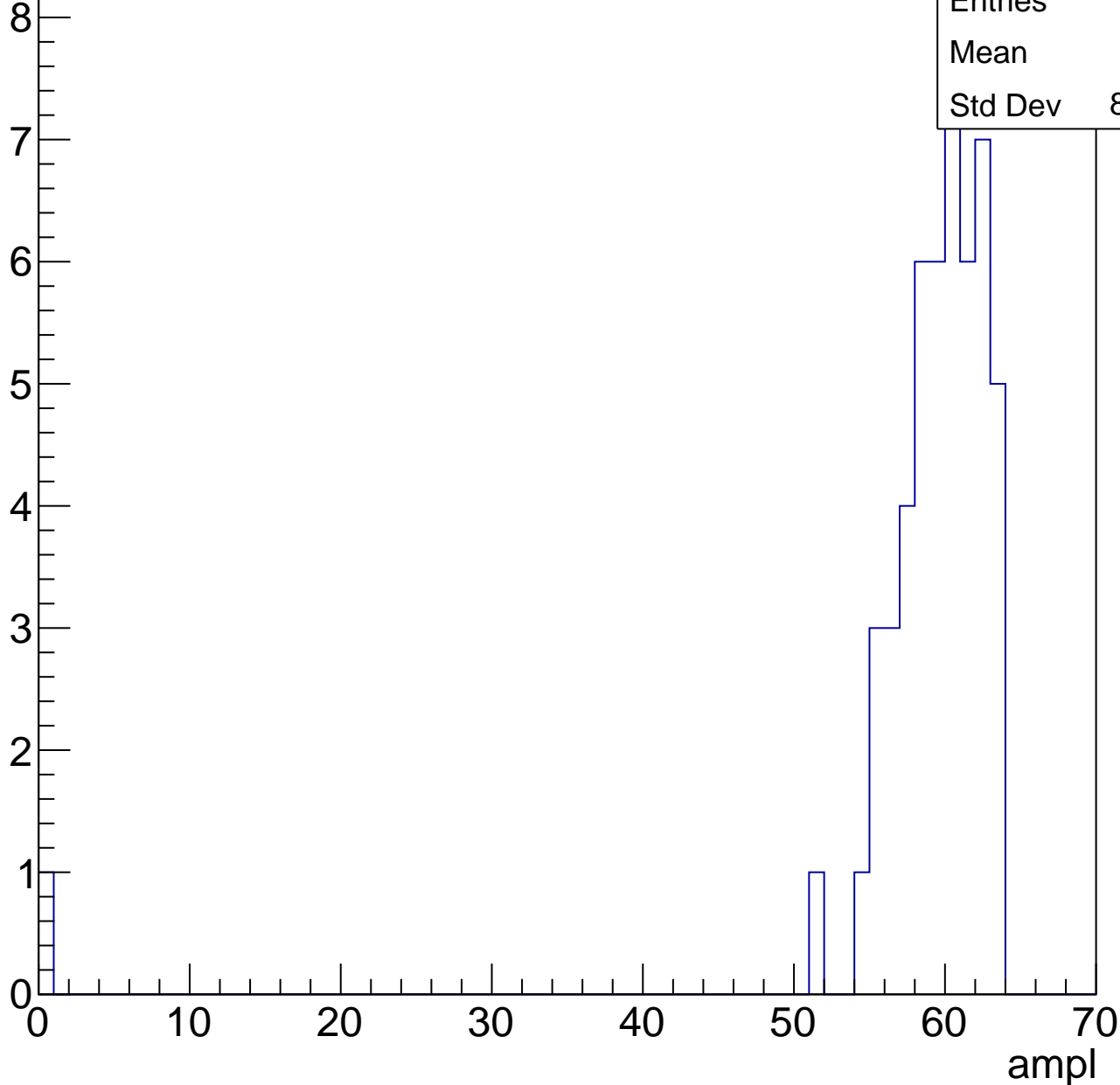


B1L102S, U19-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.1
Std Dev	8.632

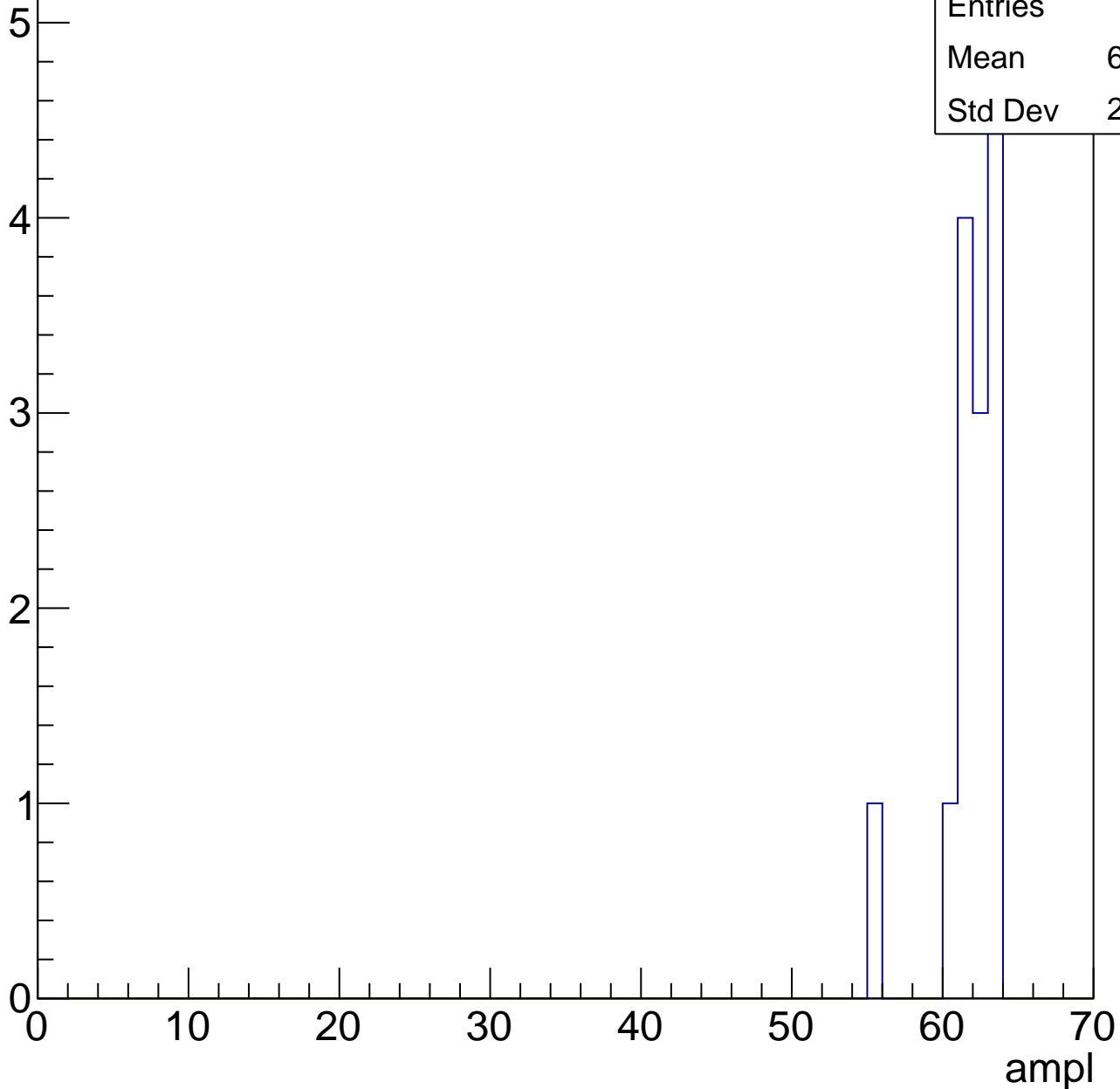


B1L102S, U19-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.43
Std Dev	2.025



B1L102S, U19-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch32, adc0

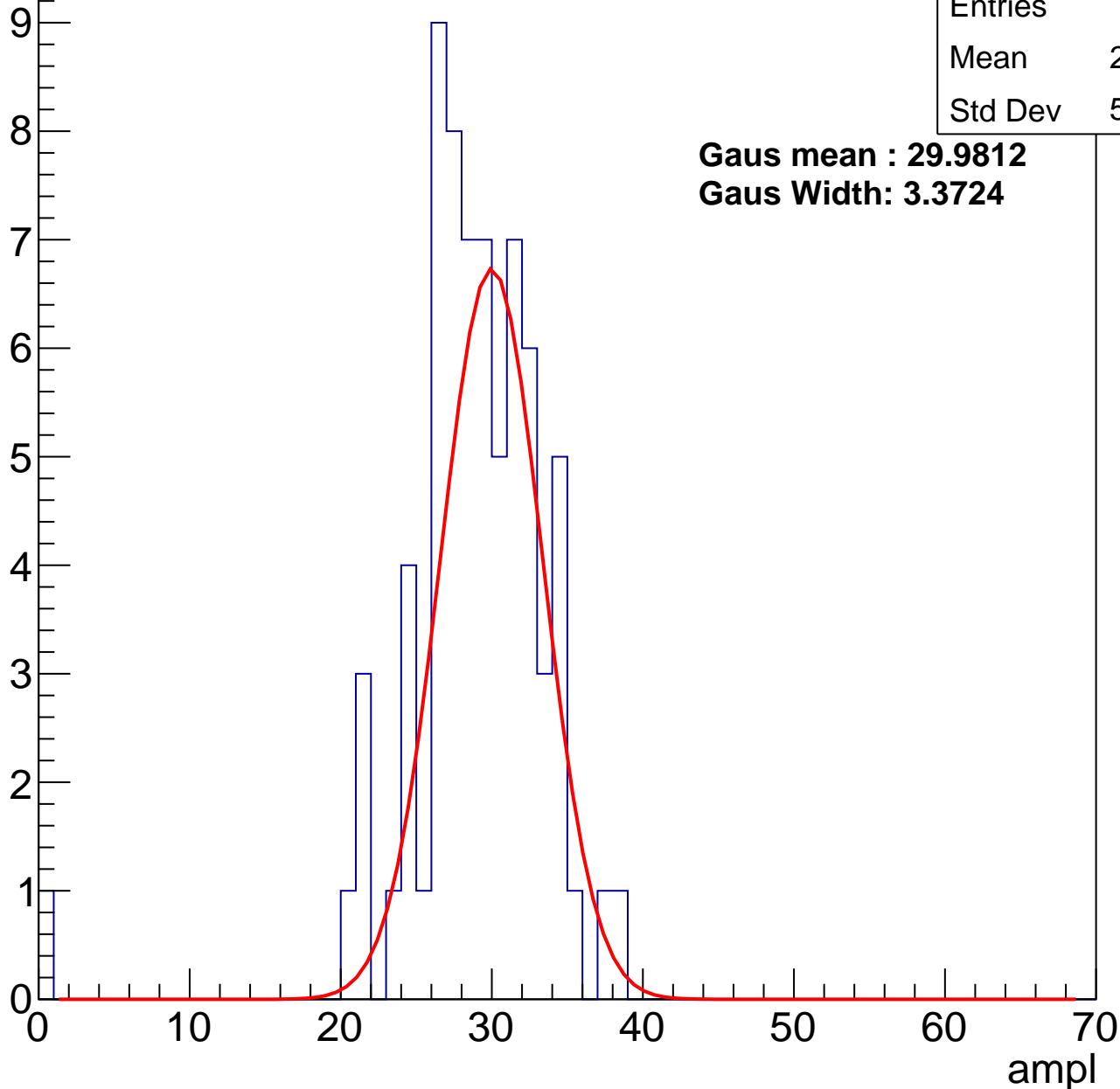
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.37
Std Dev	5.033

Gaus mean : 29.9812

Gaus Width: 3.3724



B1L102S, U19-ch32, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	35.23
Std Dev	3.055

Gaus mean : 36.2053

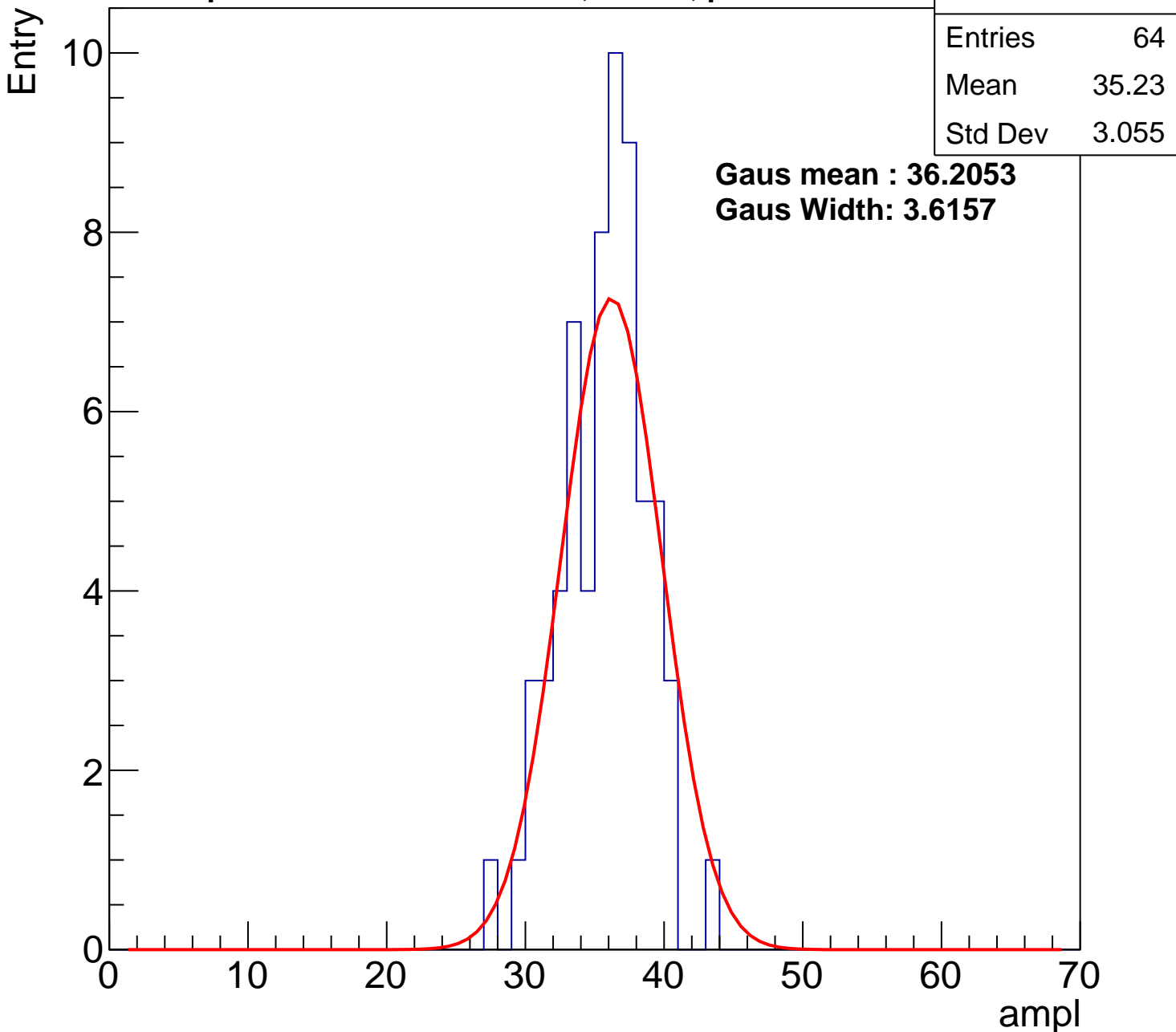
Gaus Width: 3.6157

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U19-ch32, adc2

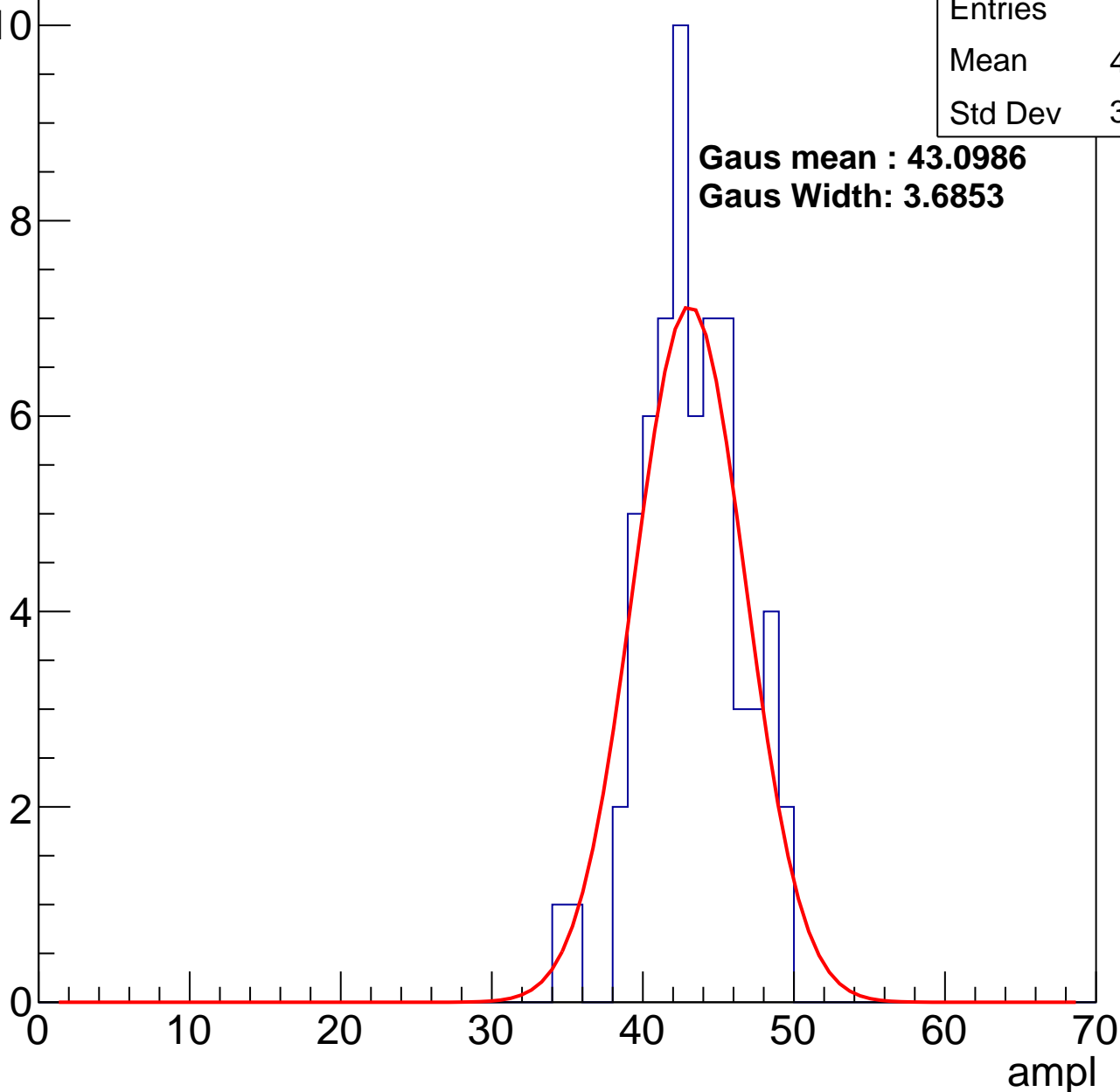
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.77
Std Dev	3.176

Gaus mean : 43.0986

Gaus Width: 3.6853

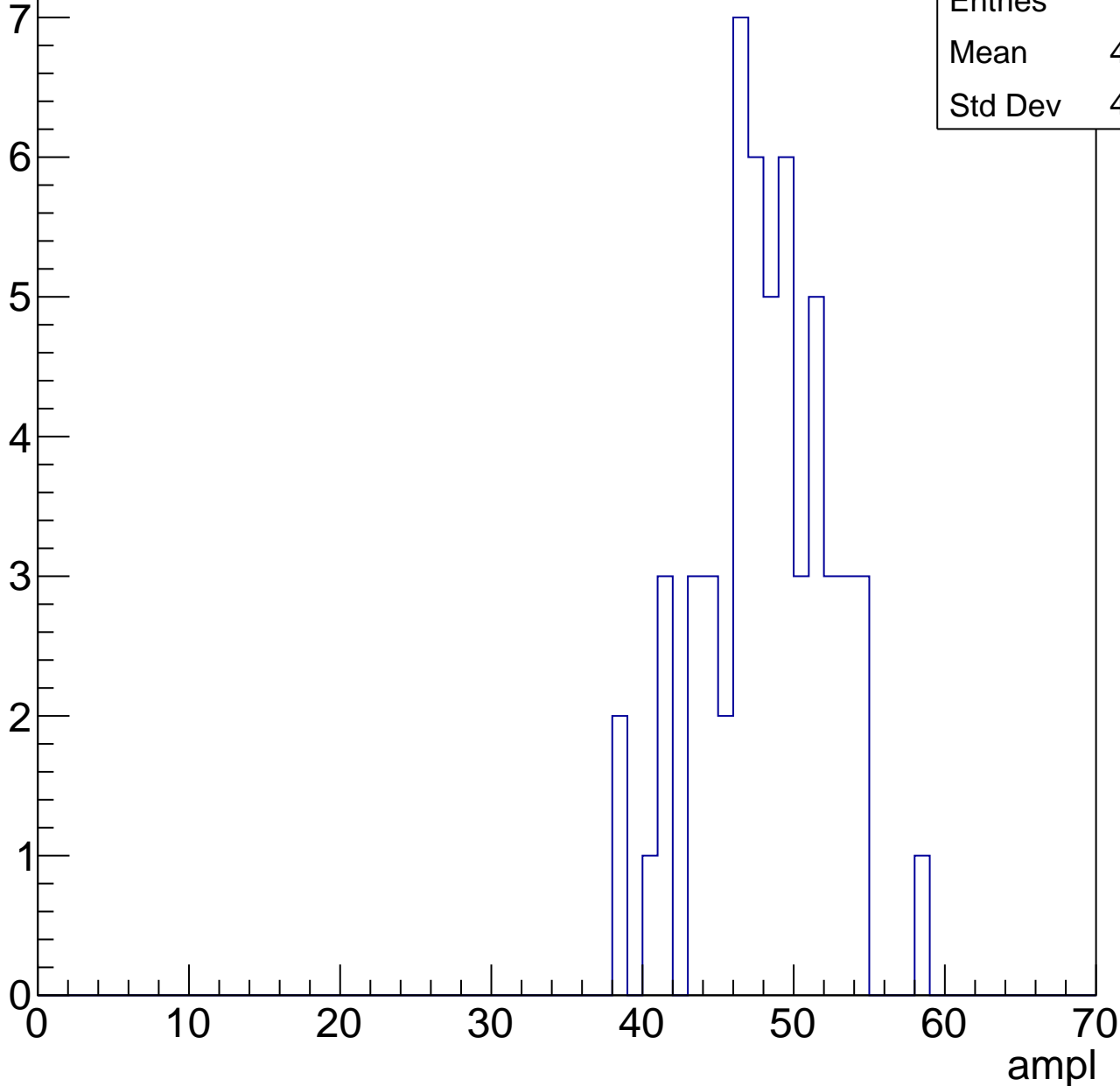


B1L102S, U19-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.64
Std Dev	4.168

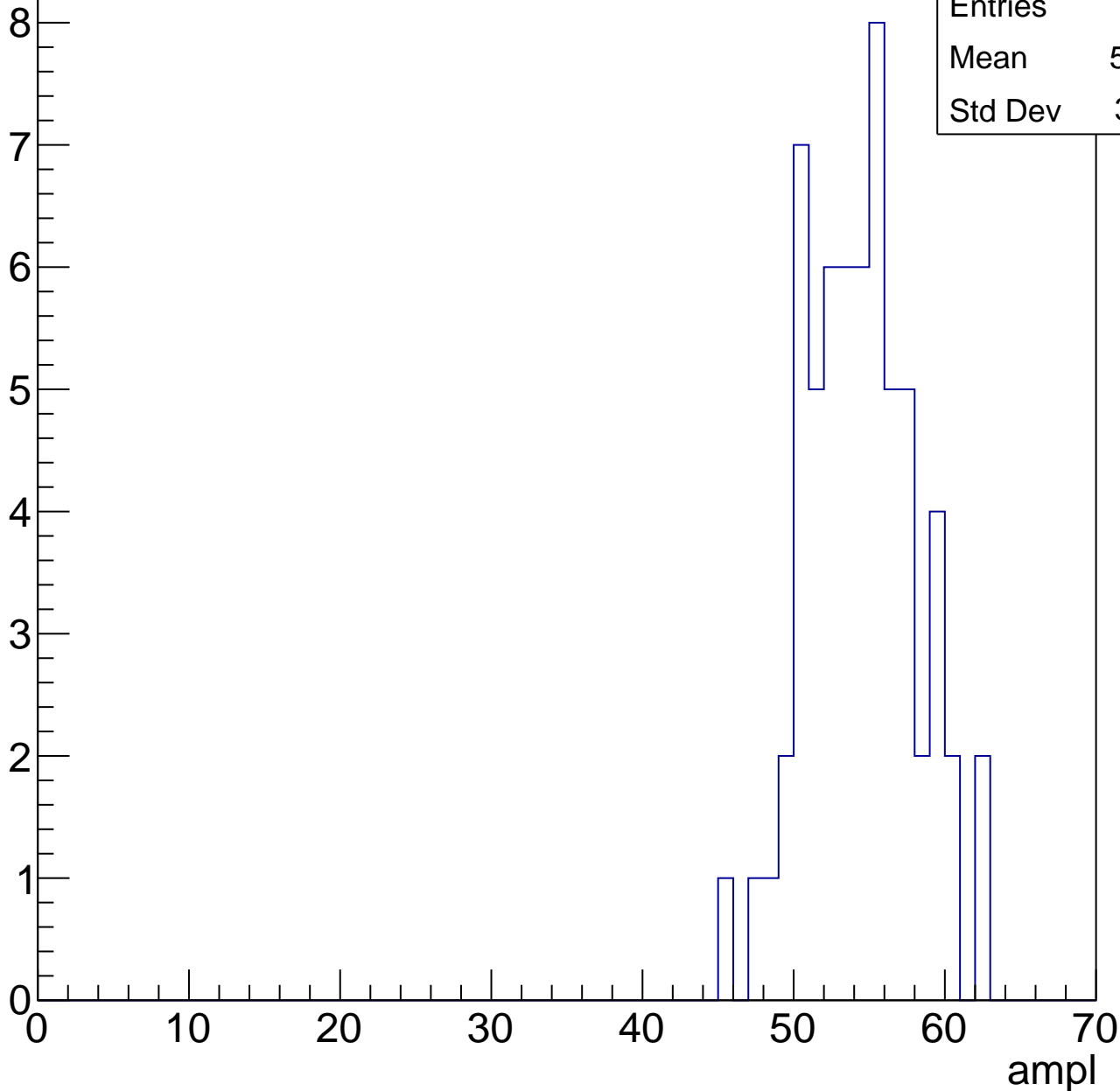


B1L102S, U19-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.94
Std Dev	3.581

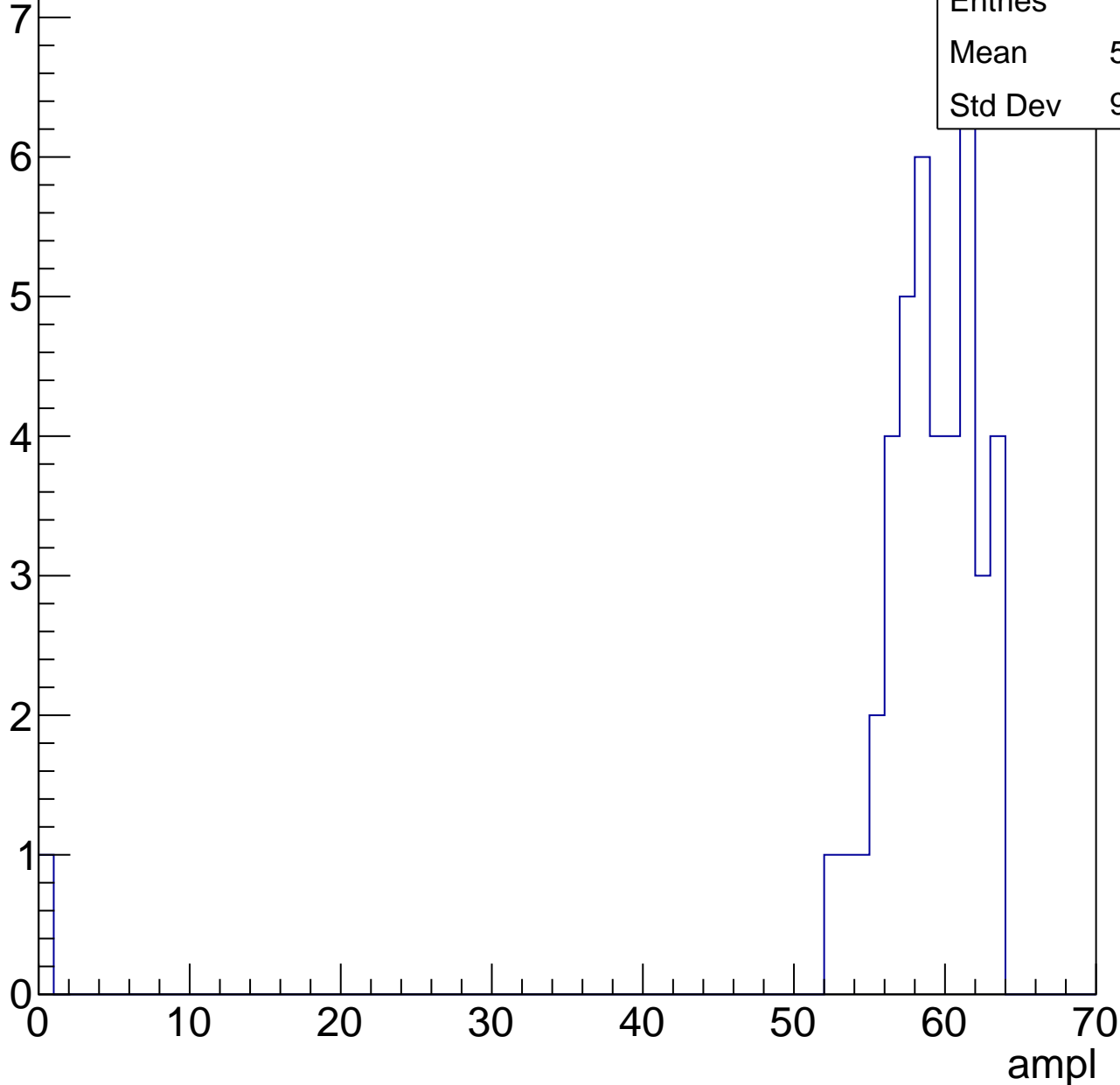


B1L102S, U19-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.37
Std Dev	9.269

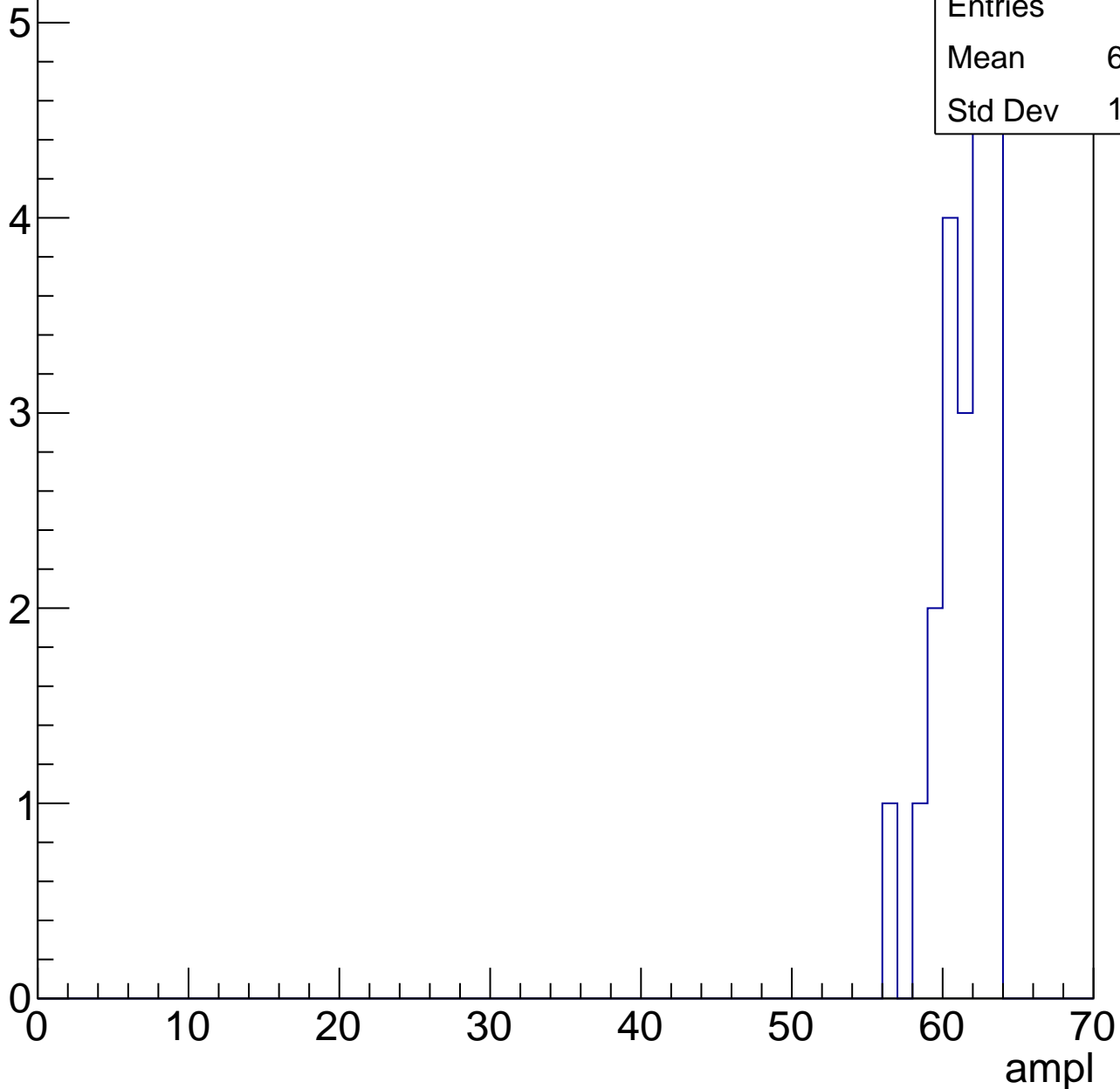


B1L102S, U19-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	60.95
Std Dev	1.838



B1L102S, U19-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch33, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	27.03
Std Dev	2.774

Gaus mean : 27.6006

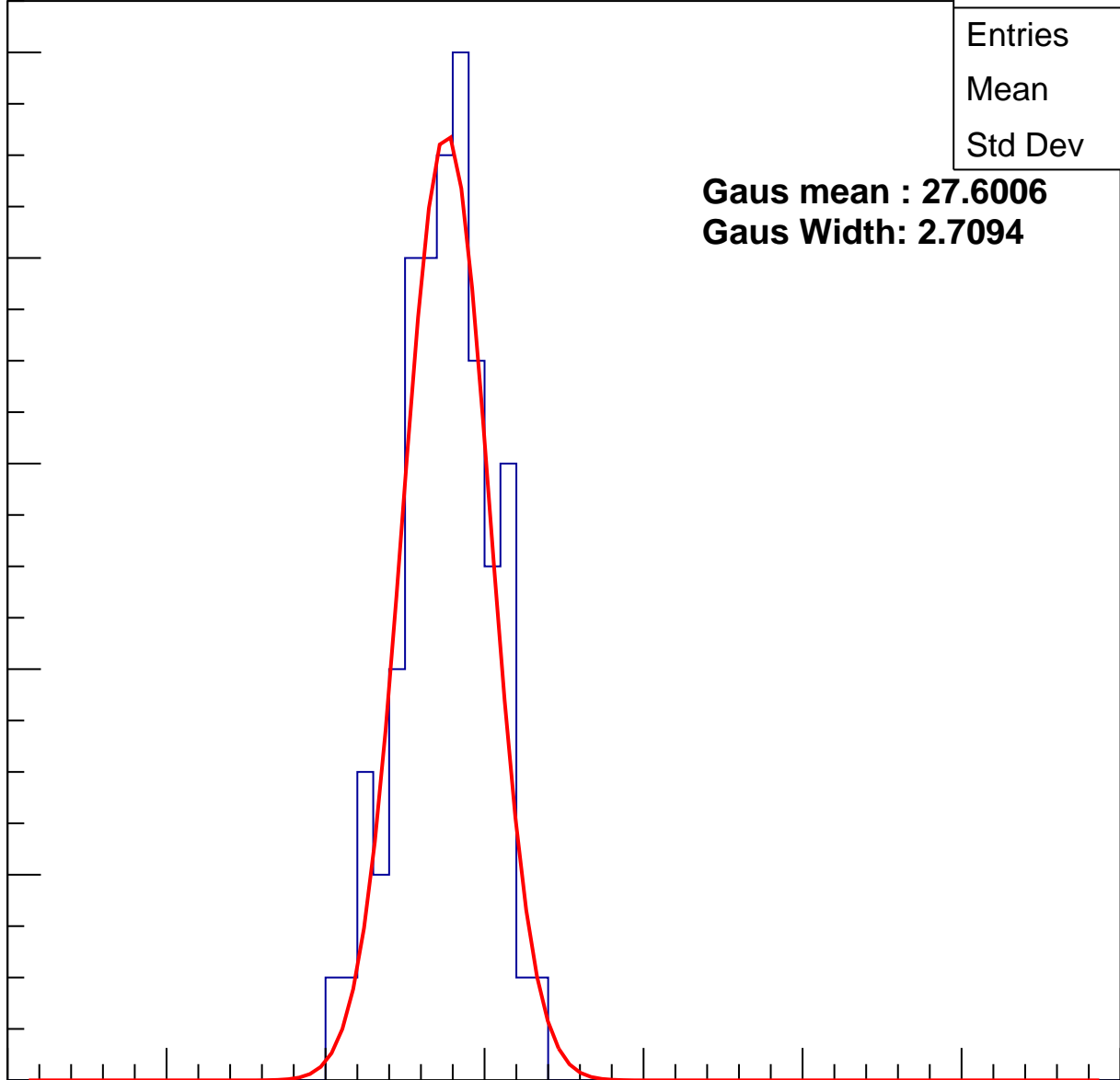
Gaus Width: 2.7094

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



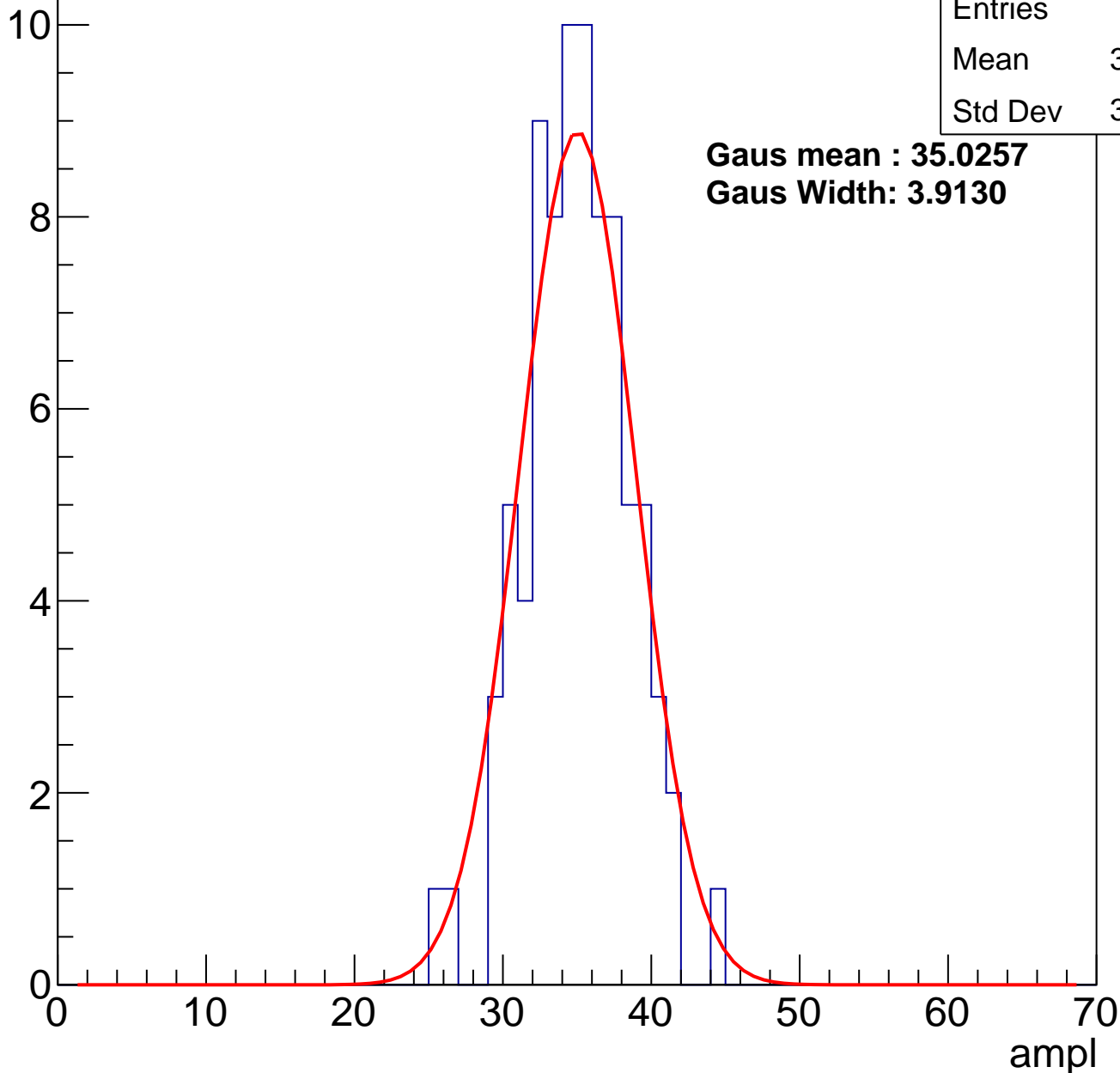
B1L102S, U19-ch33, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	34.57
Std Dev	3.433

Gaus mean : 35.0257
Gaus Width: 3.9130

Entry



B1L102S, U19-ch33, adc2

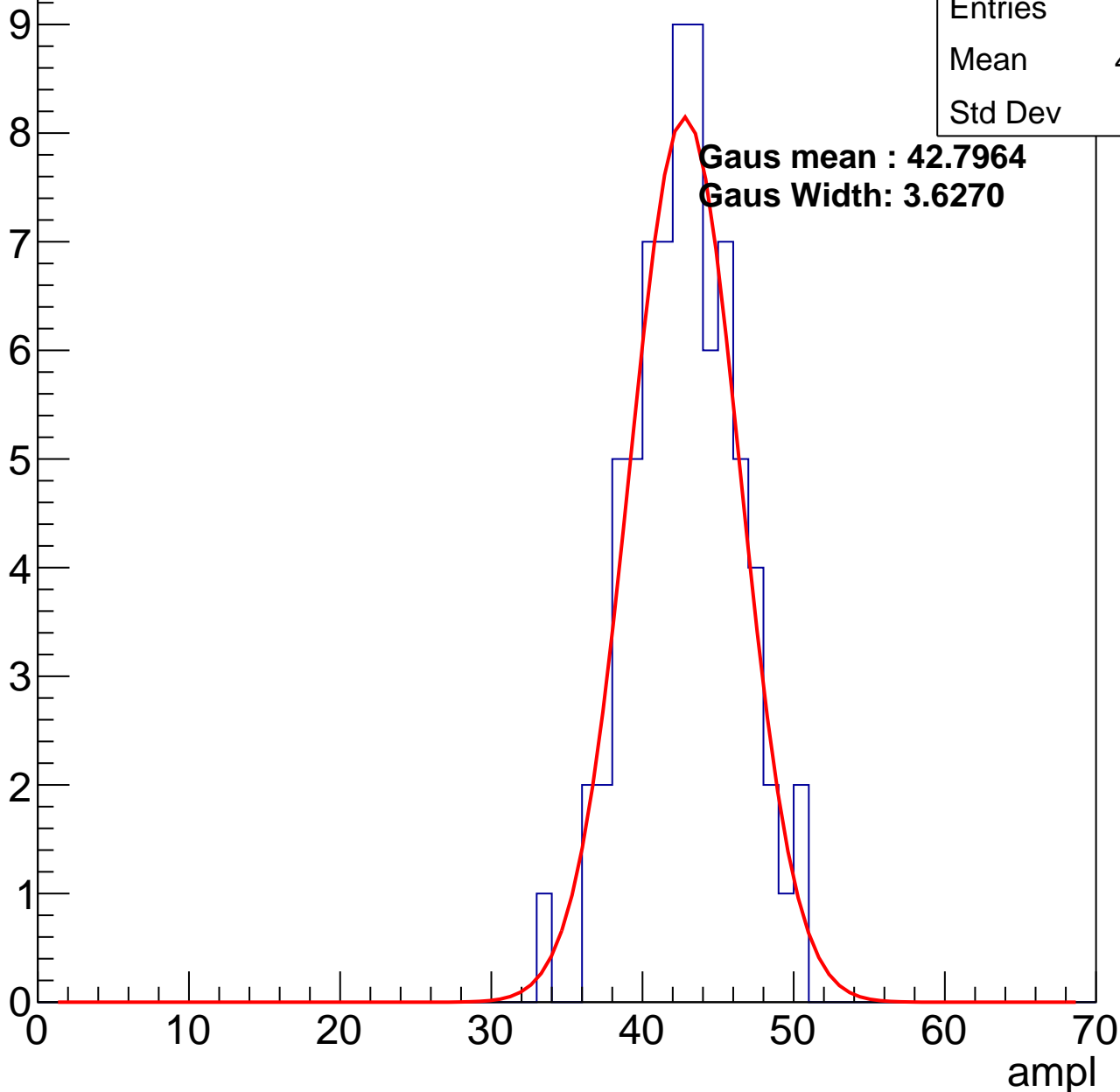
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.41
Std Dev	3.44

Gaus mean : 42.7964

Gaus Width: 3.6270

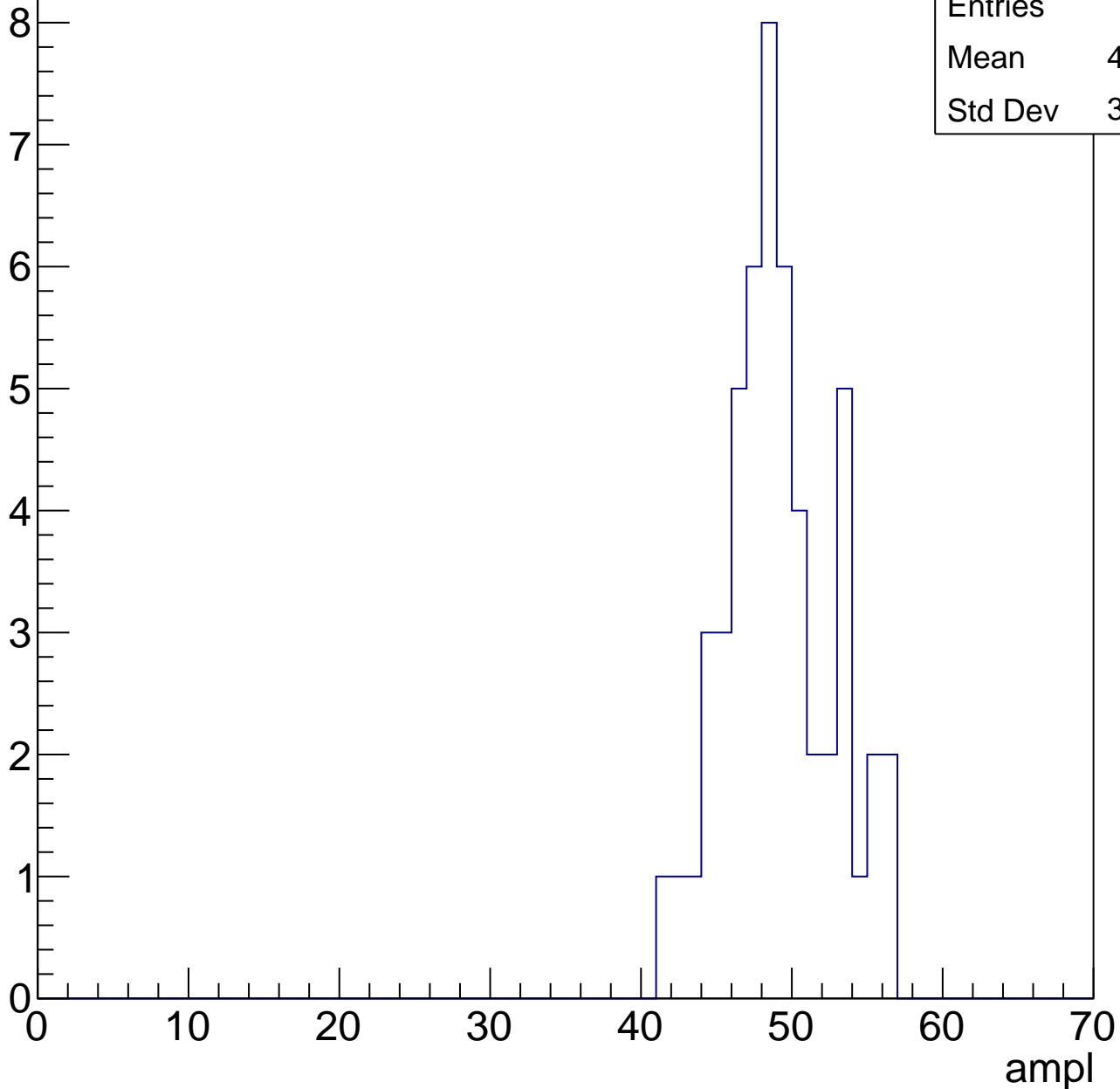


B1L102S, U19-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

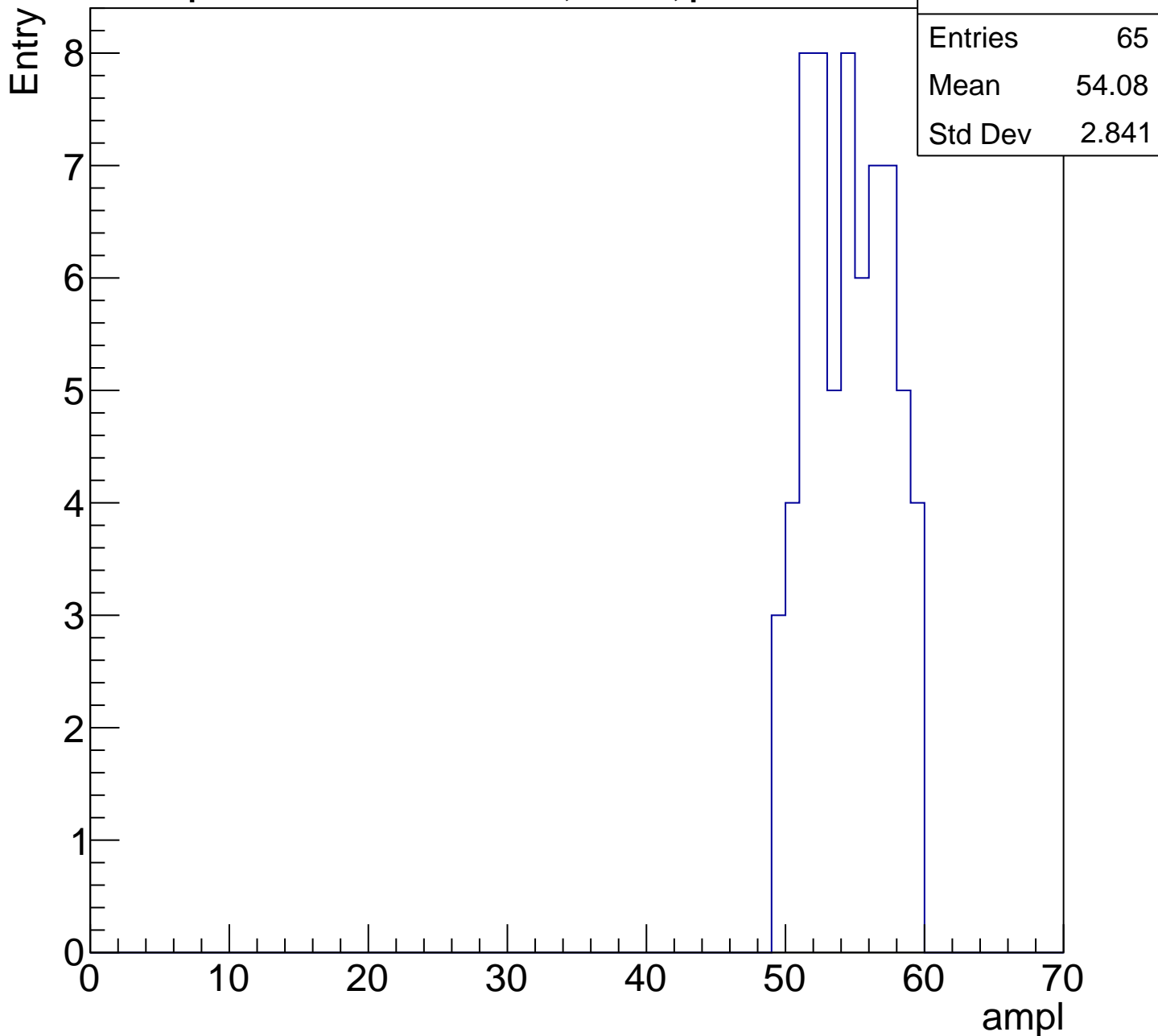
Entry

Entries	52
Mean	48.65
Std Dev	3.529



B1L102S, U19-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

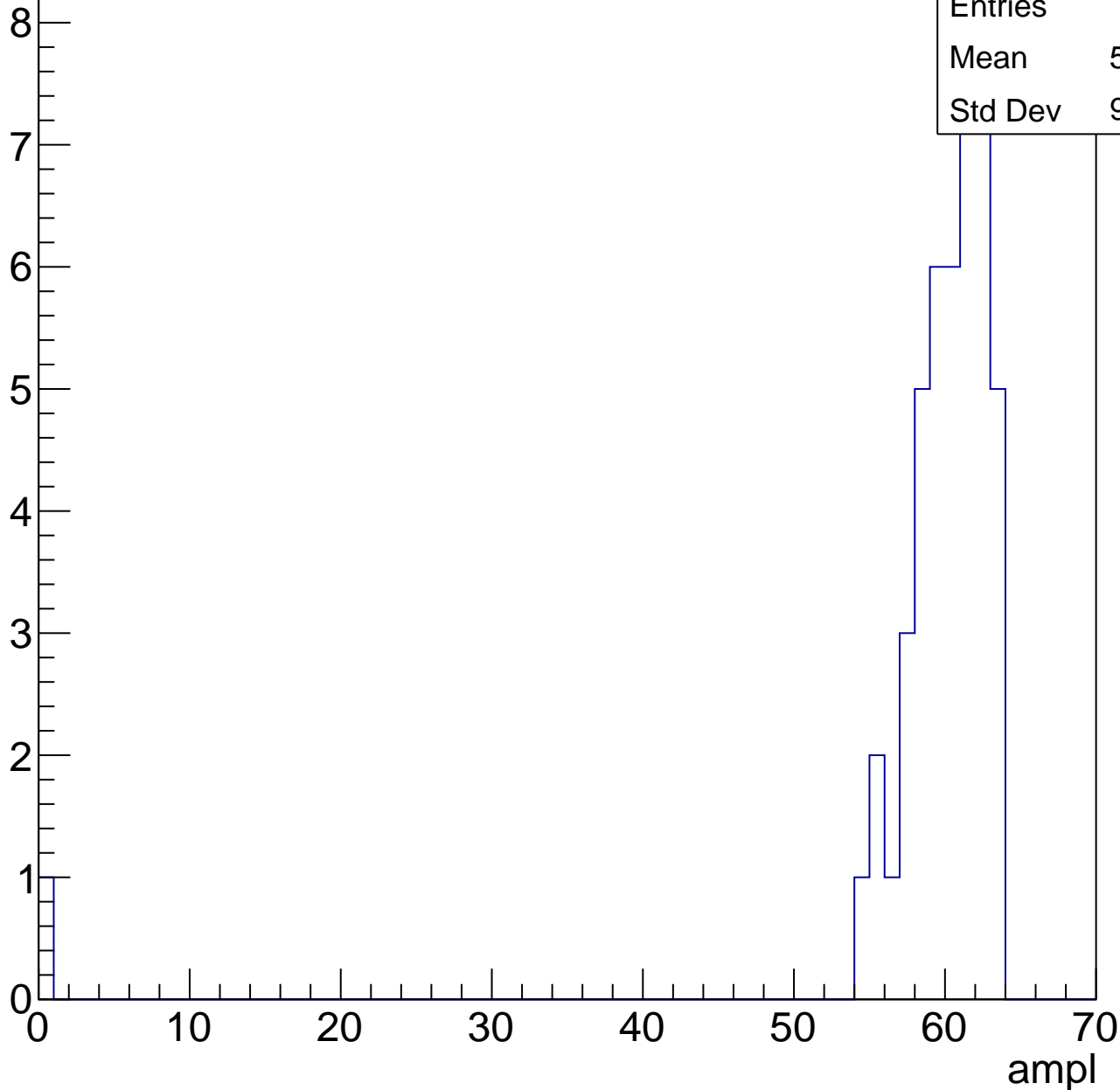


B1L102S, U19-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.57
Std Dev	9.023

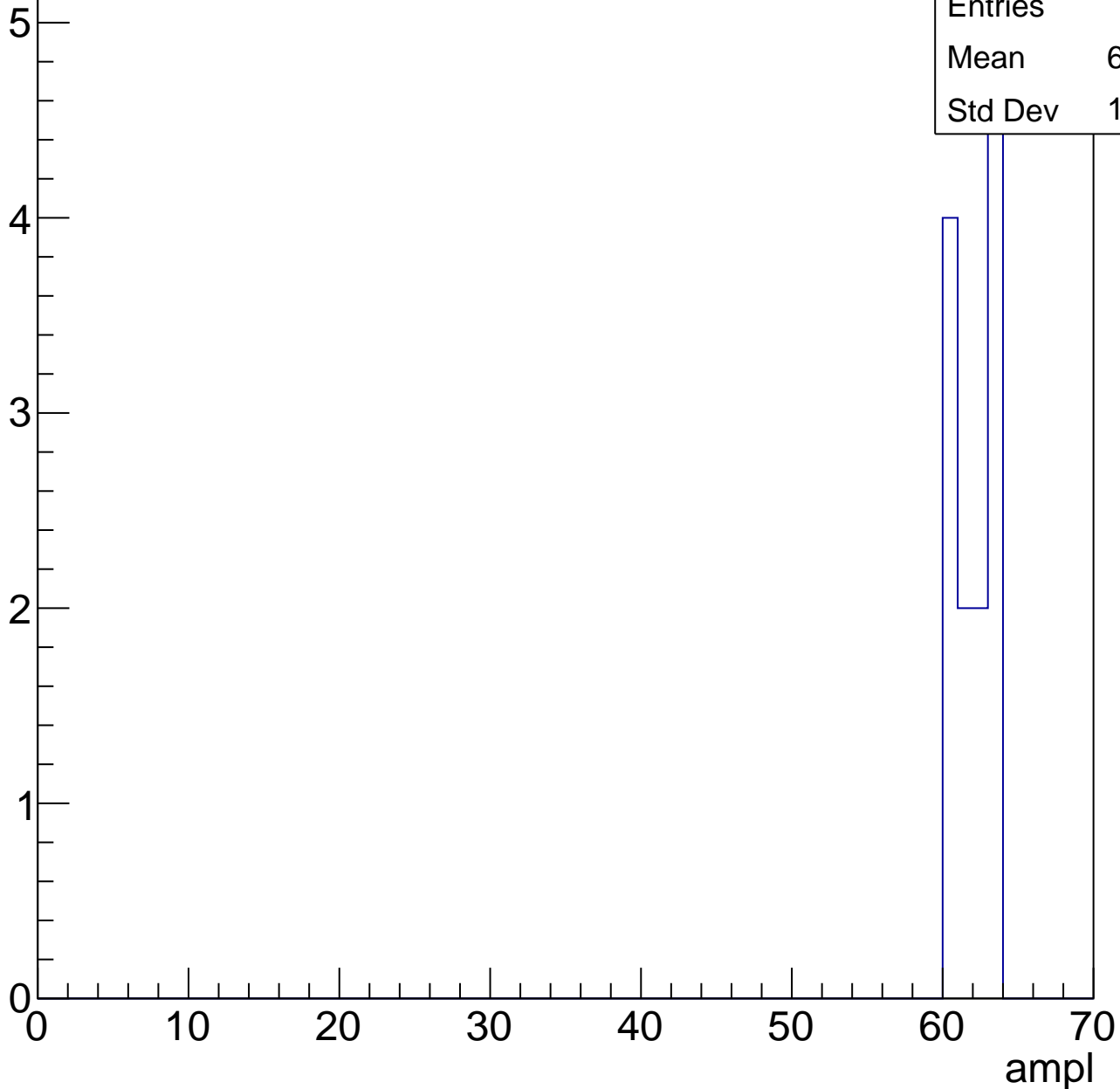


B1L102S, U19-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.62
Std Dev	1.273



B1L102S, U19-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch34, adc0

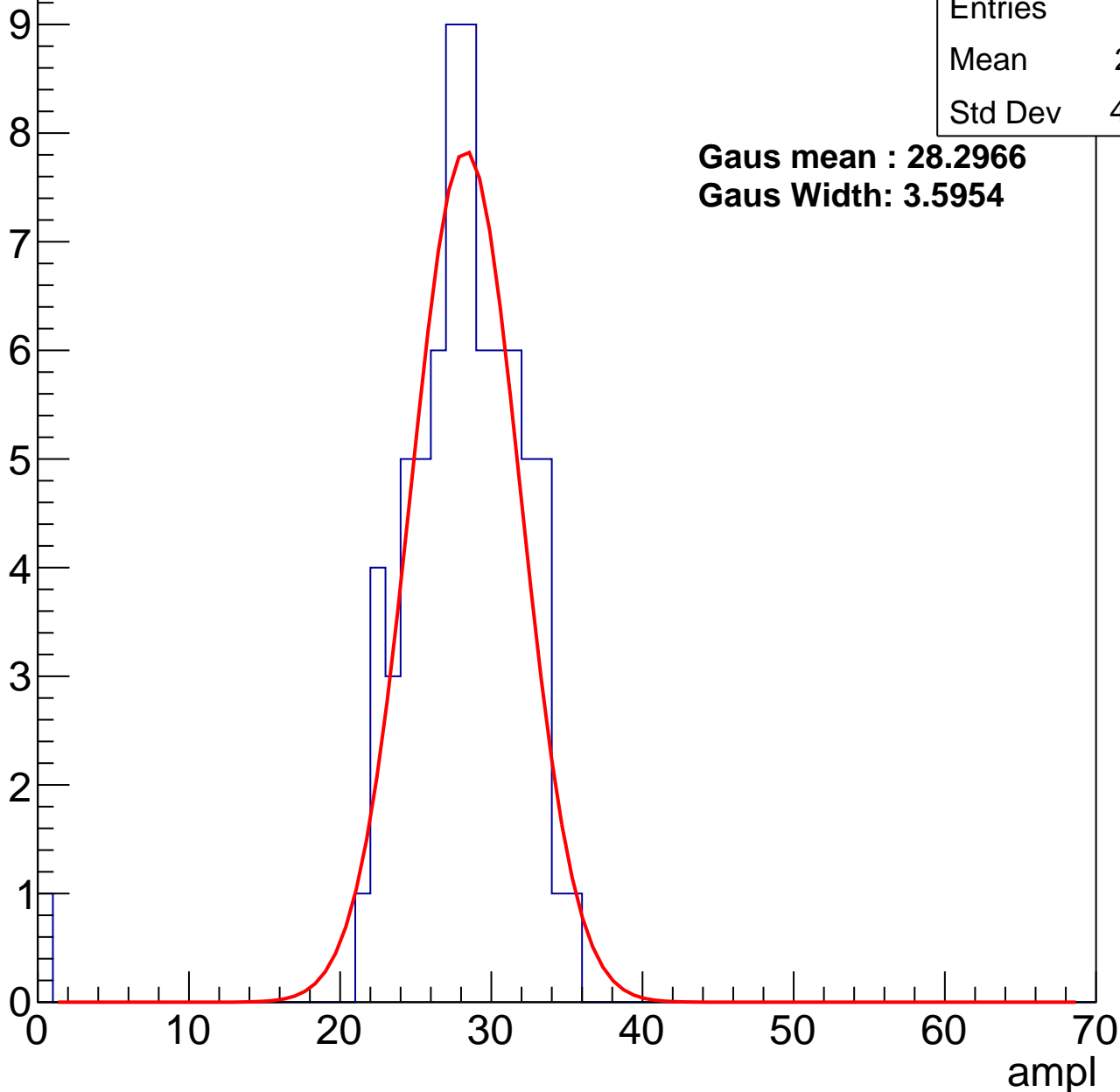
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.51
Std Dev	4.632

Gaus mean : 28.2966

Gaus Width: 3.5954



B1L102S, U19-ch34, adc1

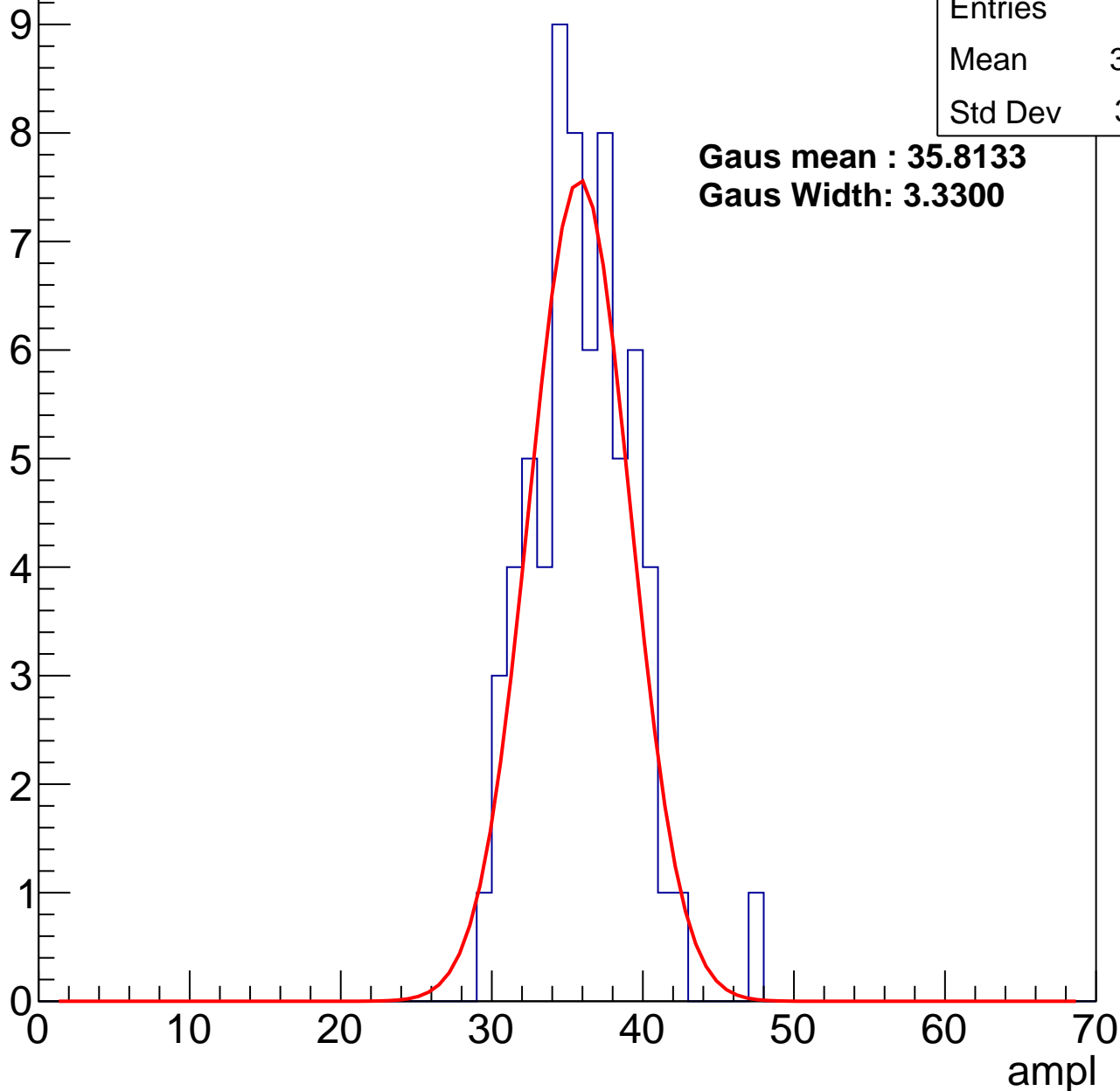
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.56
Std Dev	3.331

Gaus mean : 35.8133

Gaus Width: 3.3300



B1L102S, U19-ch34, adc2

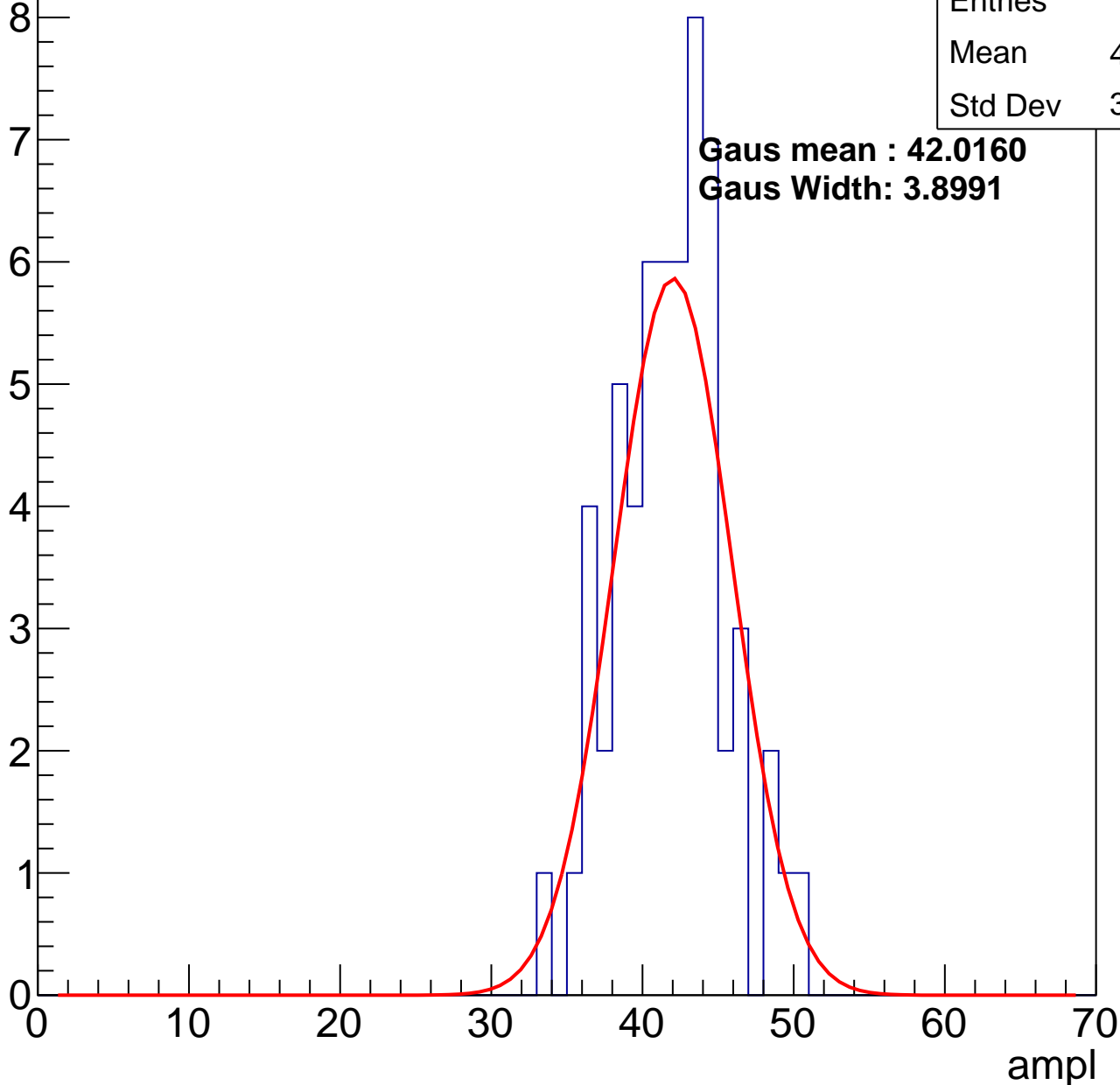
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.44
Std Dev	3.538

Gaus mean : 42.0160

Gaus Width: 3.8991

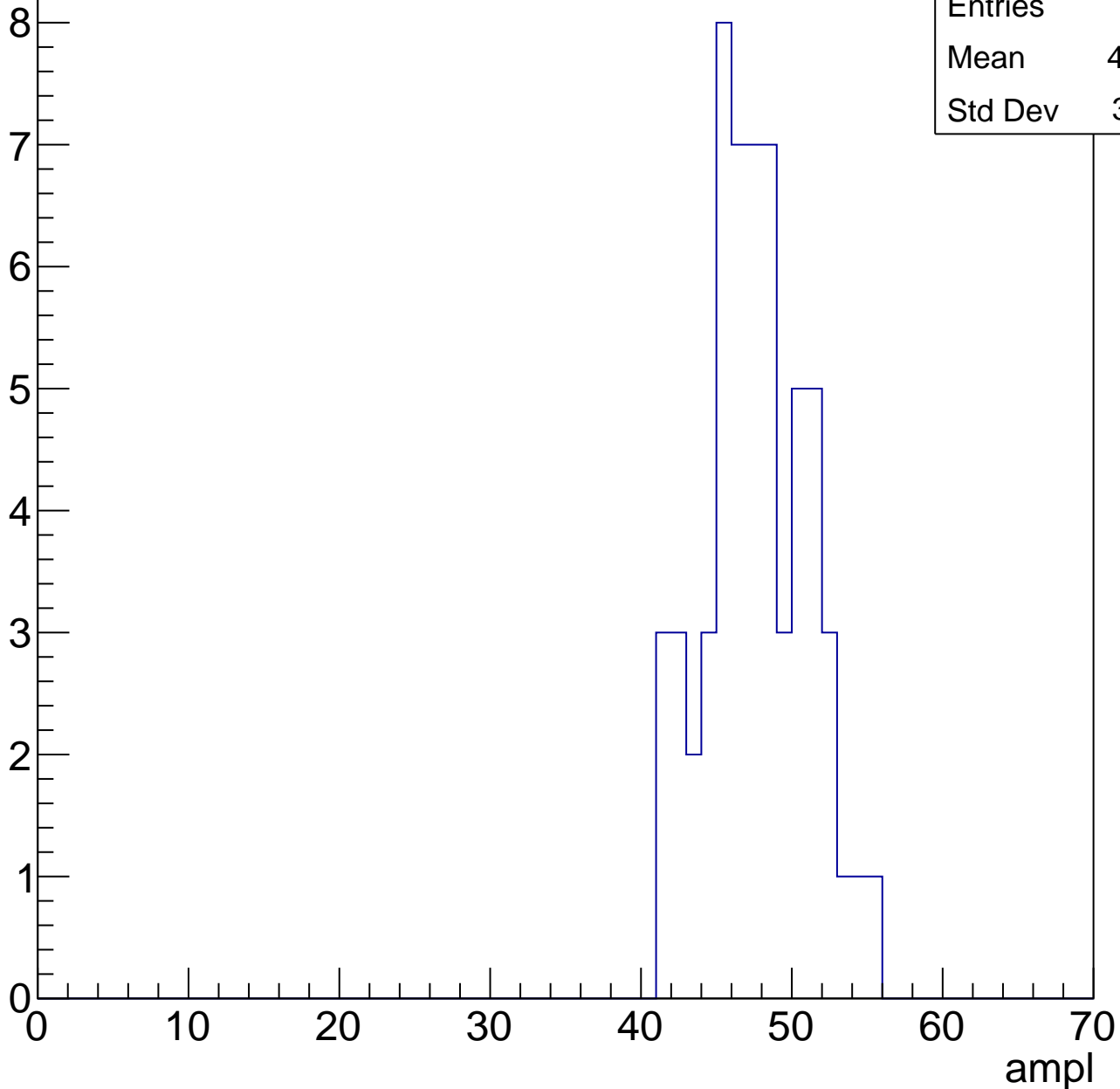


B1L102S, U19-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.19
Std Dev	3.311

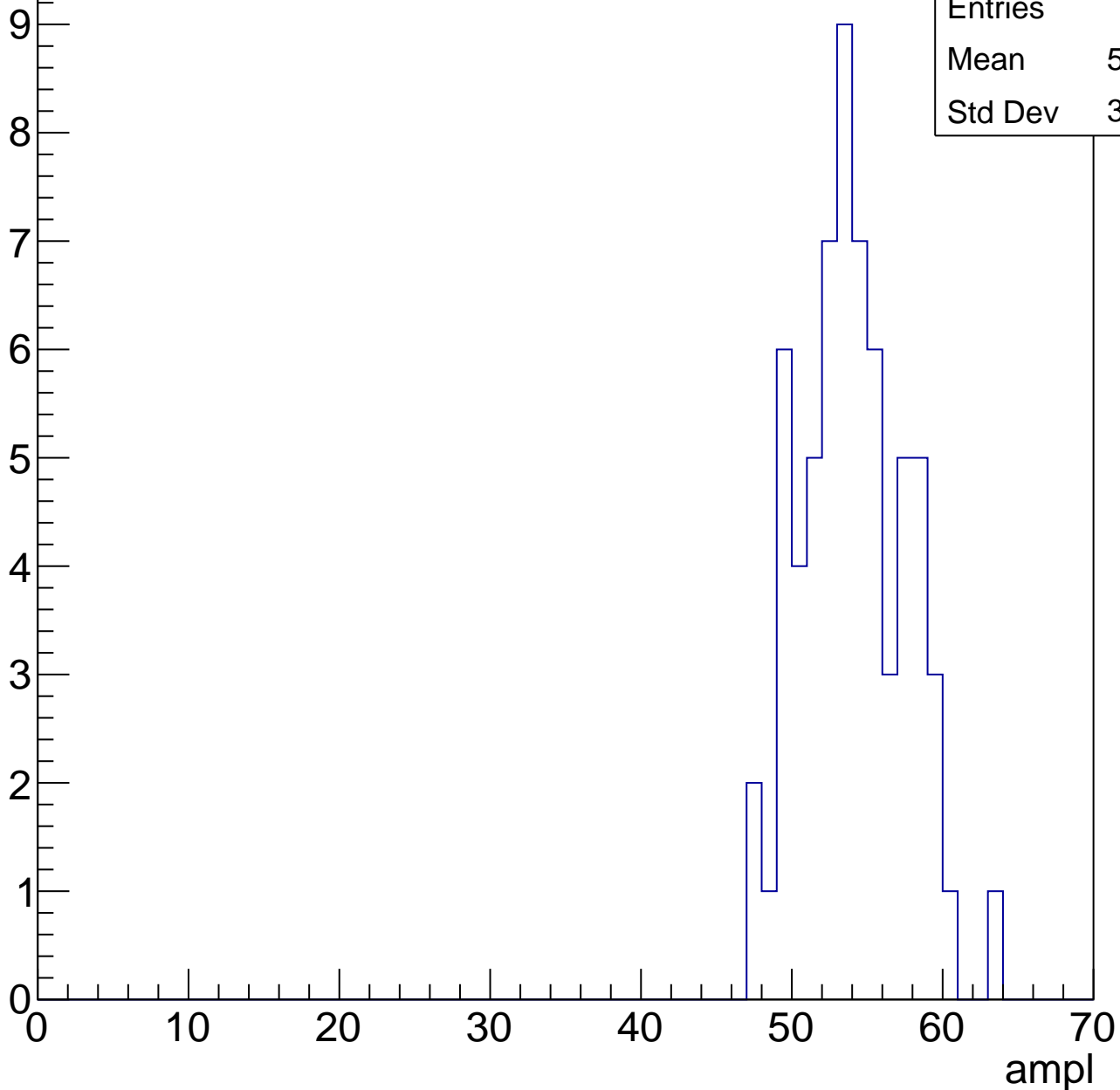


B1L102S, U19-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.58
Std Dev	3.405

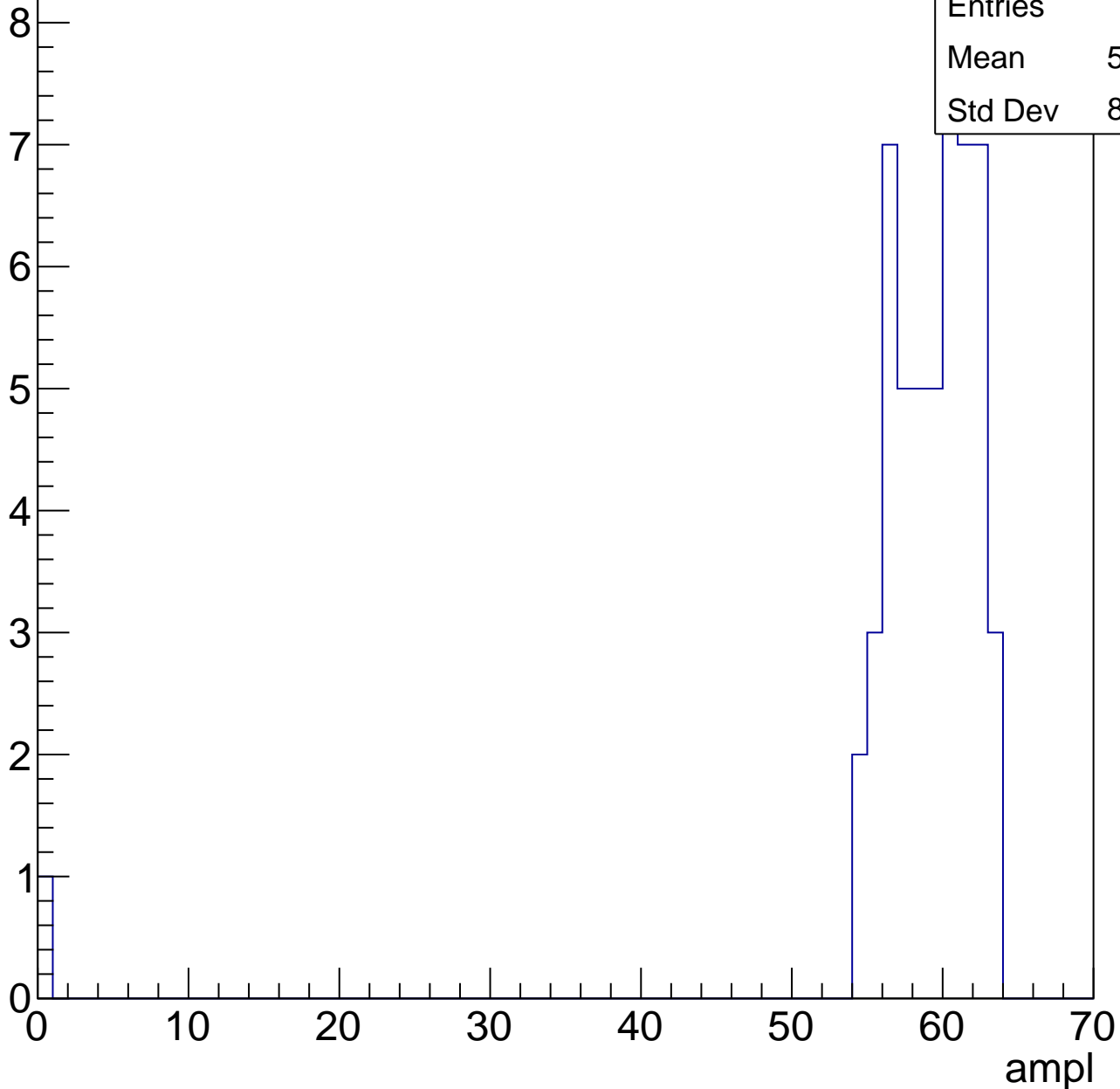


B1L102S, U19-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.83
Std Dev	8.402

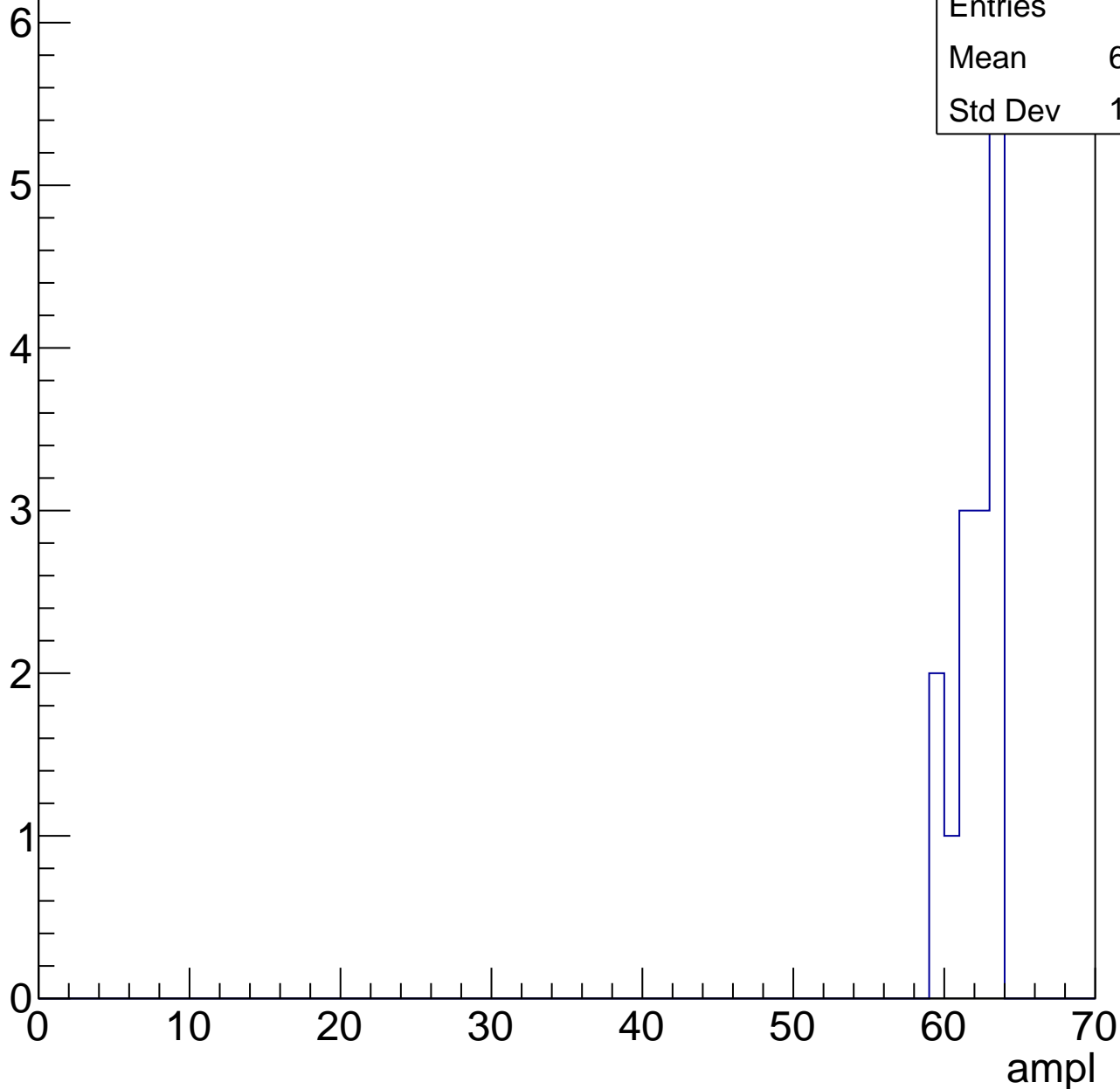


B1L102S, U19-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.67
Std Dev	1.398



B1L102S, U19-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch35, adc0

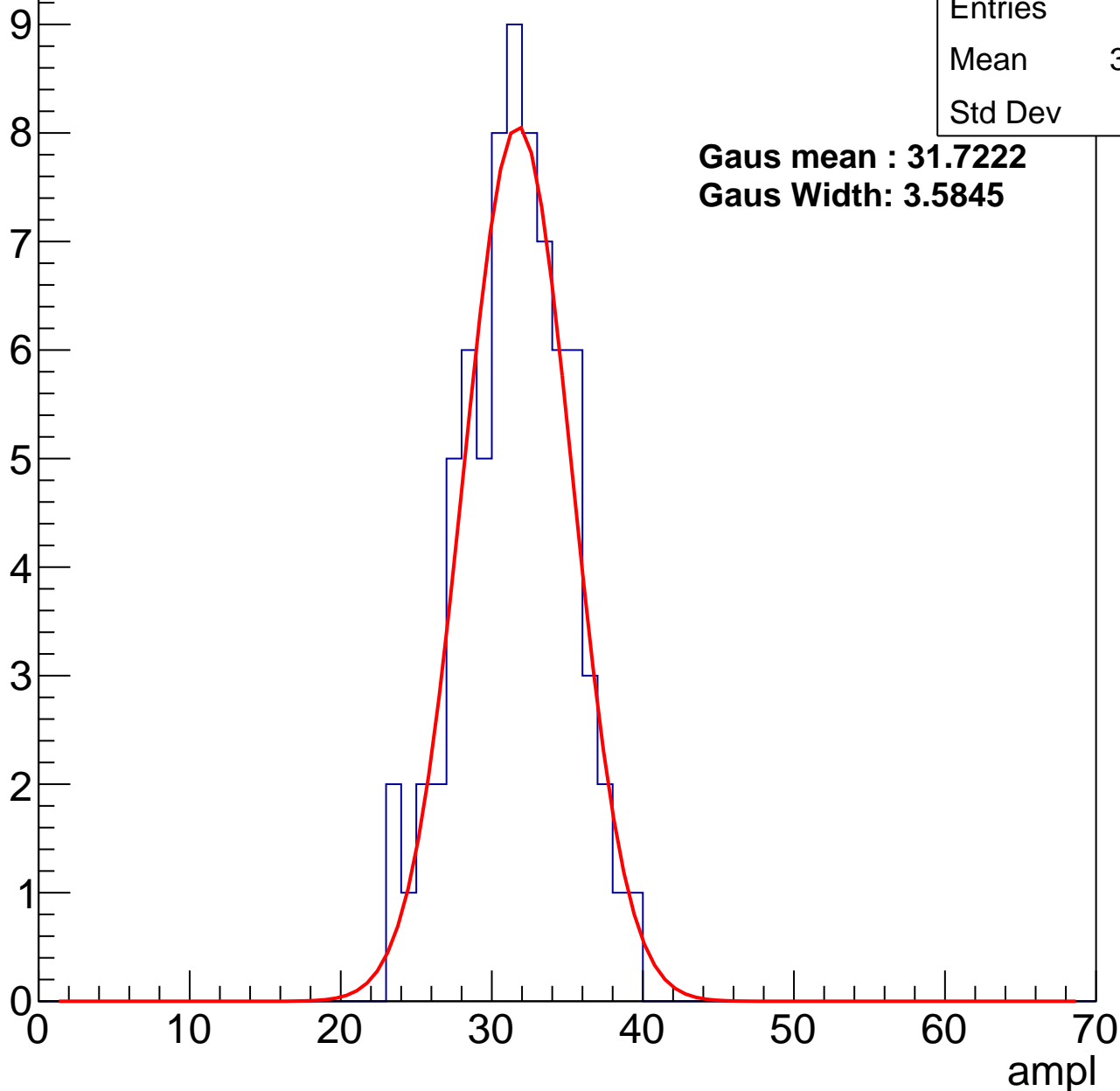
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	31.07
Std Dev	3.5

Gaus mean : 31.7222

Gaus Width: 3.5845



B1L102S, U19-ch35, adc1

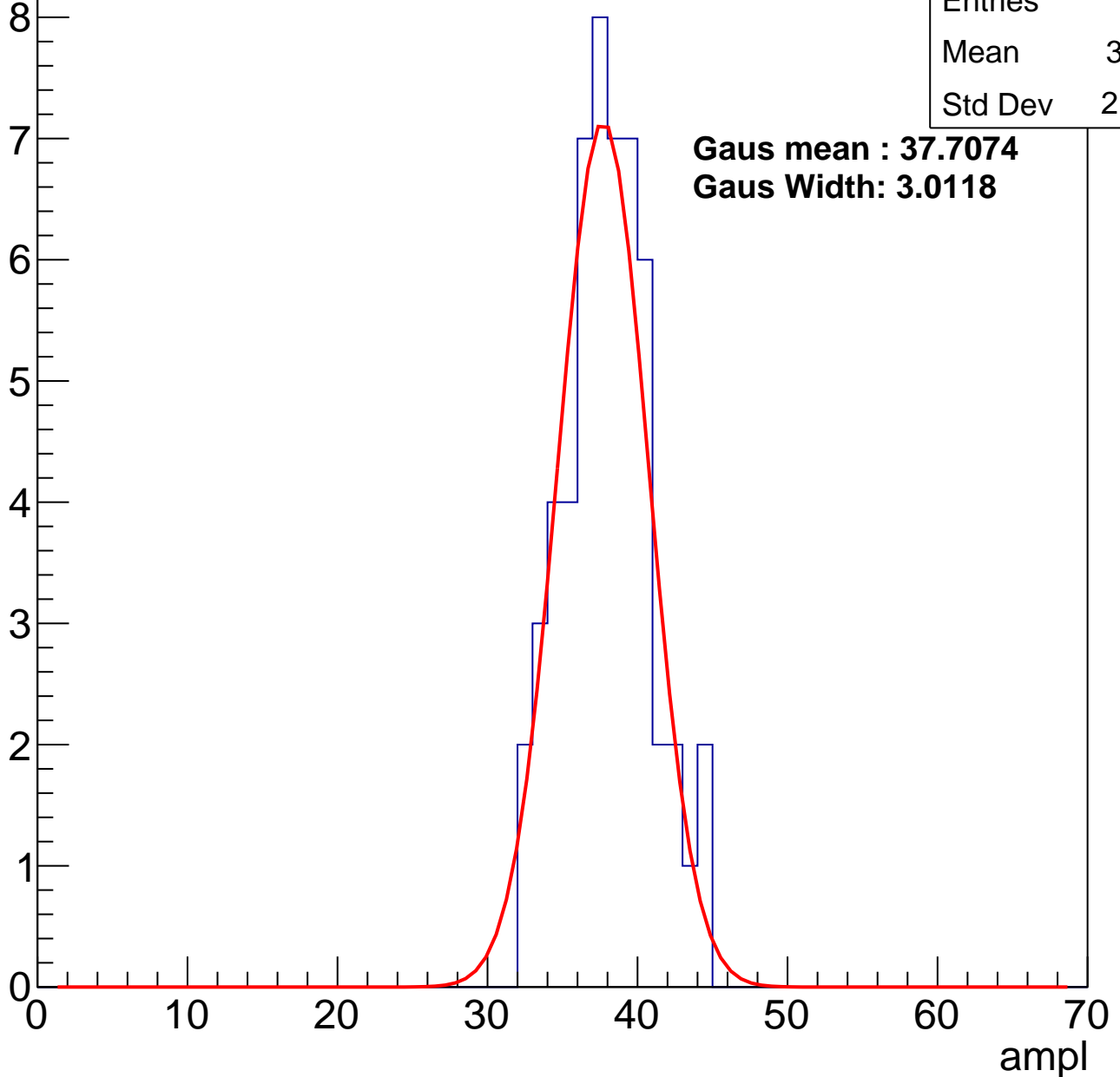
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.51
Std Dev	2.853

Gaus mean : 37.7074

Gaus Width: 3.0118



B1L102S, U19-ch35, adc2

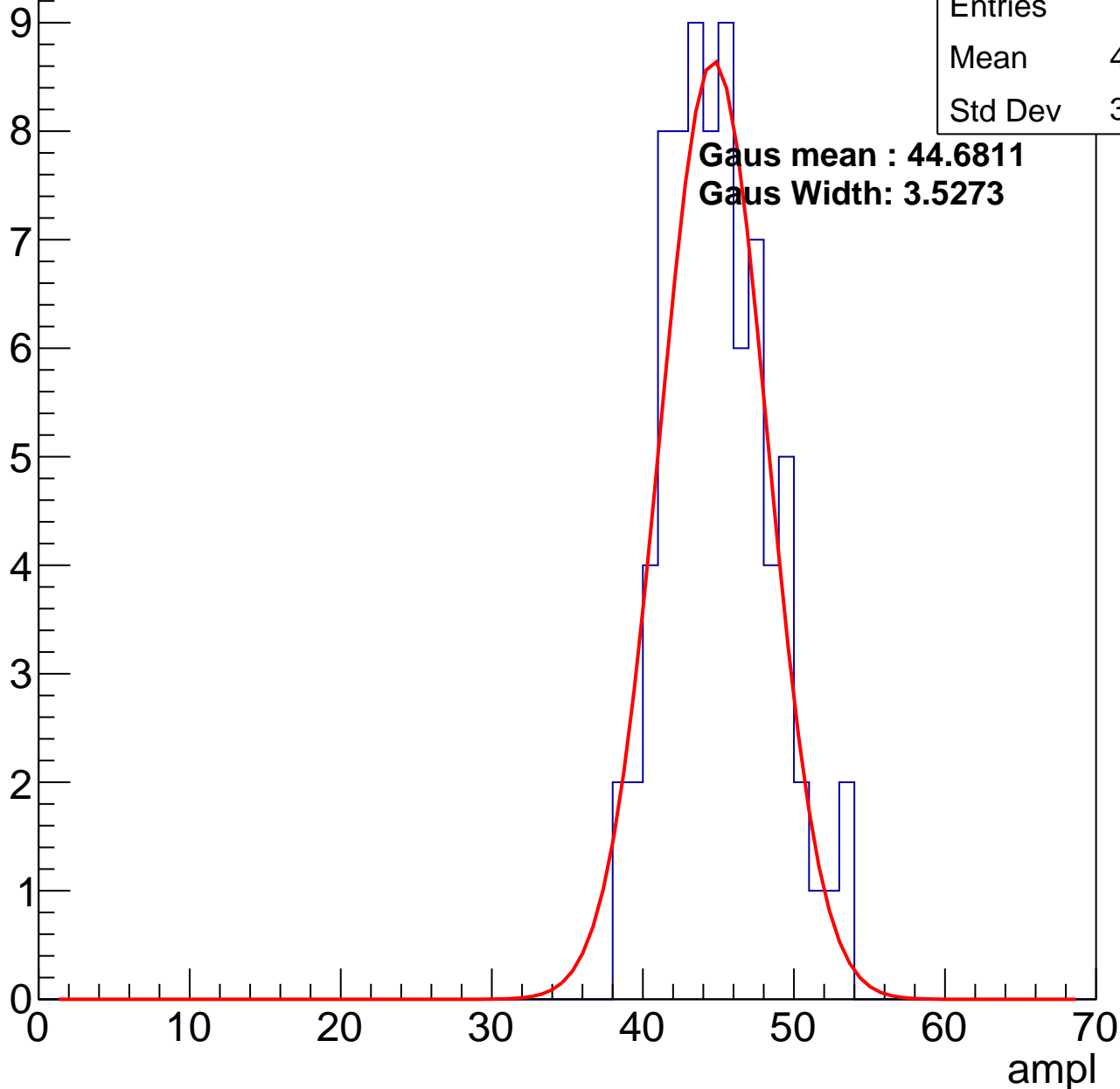
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	44.53
Std Dev	3.415

Gaus mean : 44.6811

Gaus Width: 3.5273

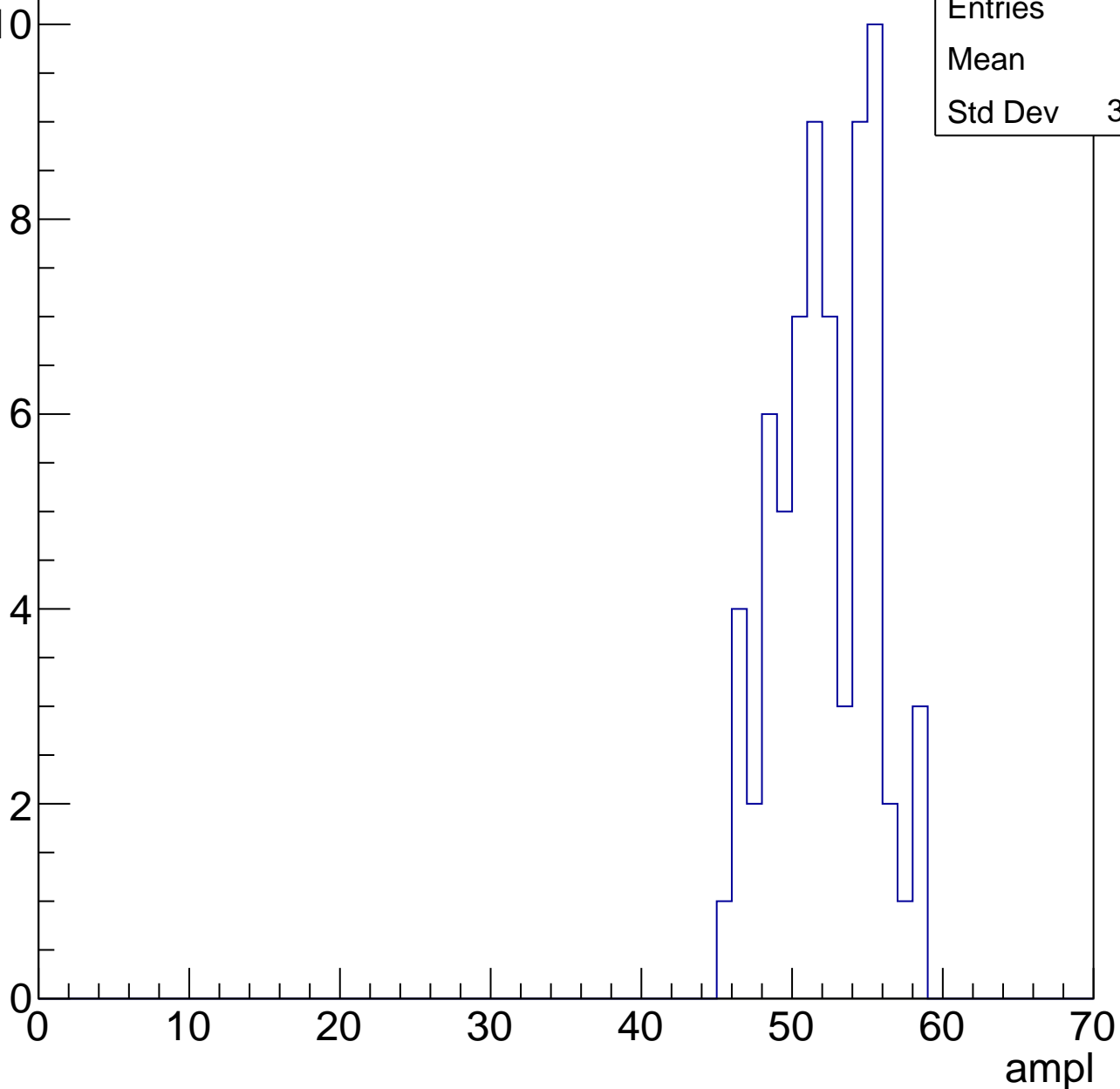


B1L102S, U19-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	51.7
Std Dev	3.205

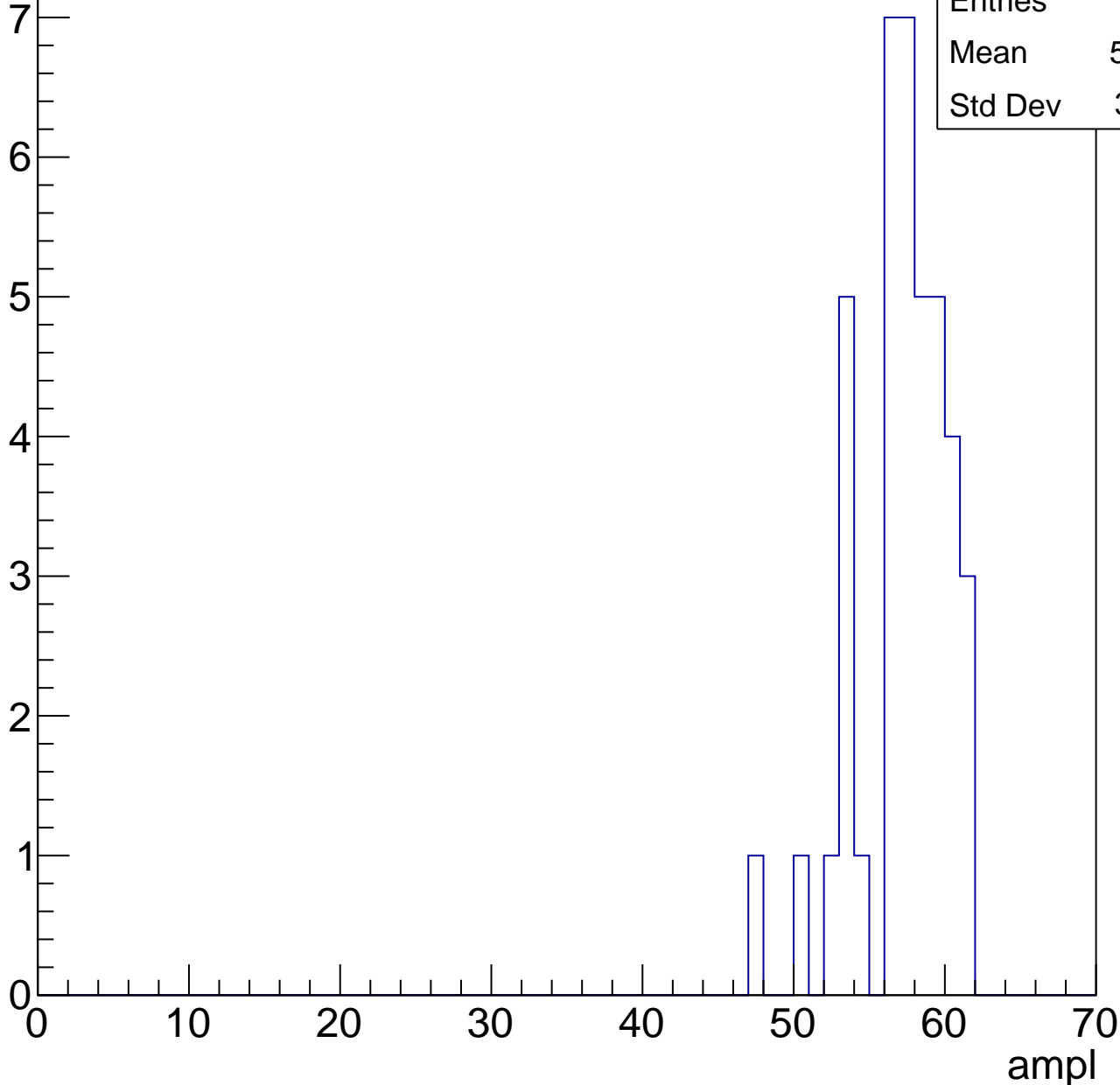


B1L102S, U19-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	56.67
Std Dev	3.061

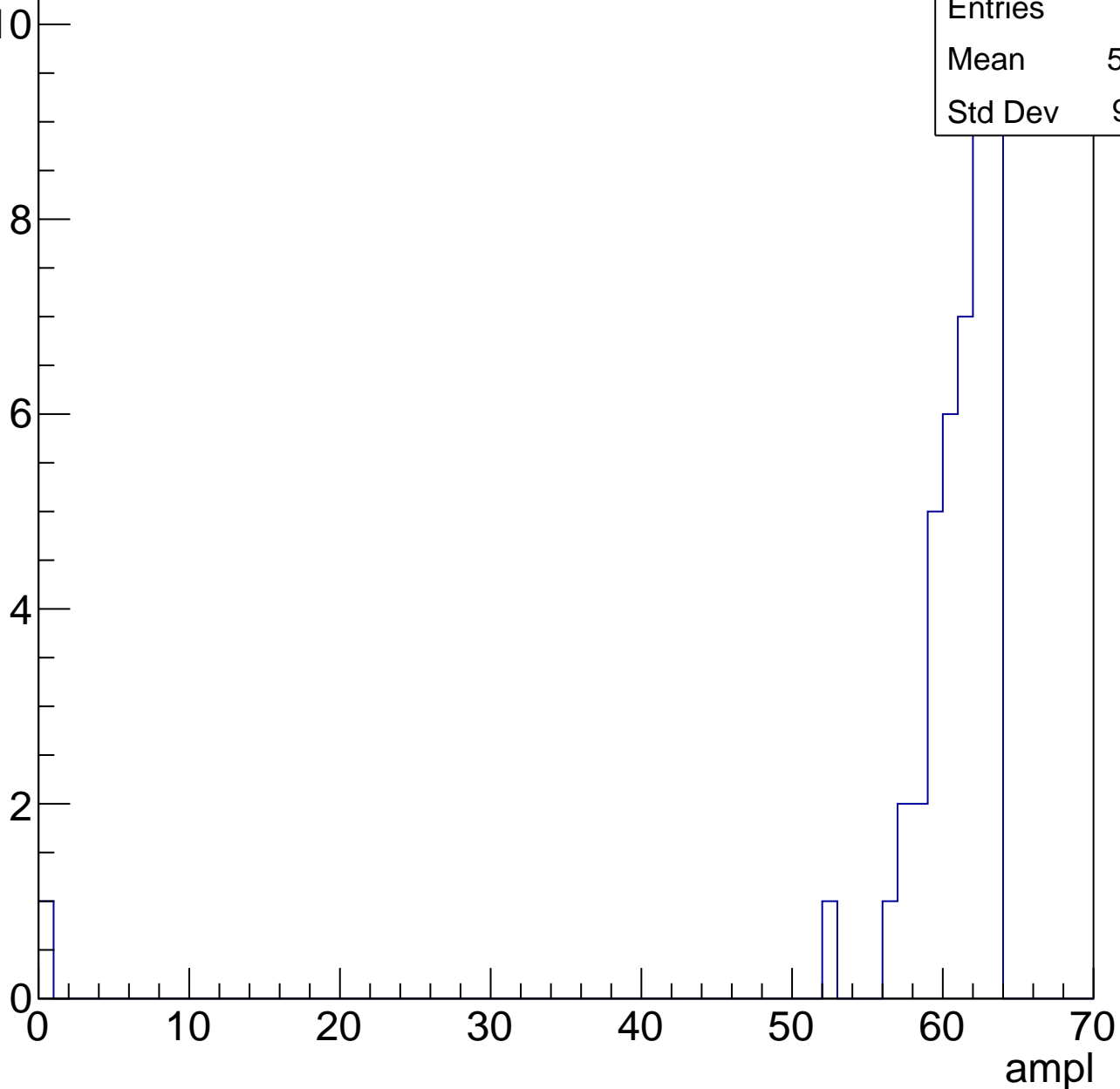


B1L102S, U19-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.25
Std Dev	9.311



B1L102S, U19-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L102S, U19-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch36, adc0

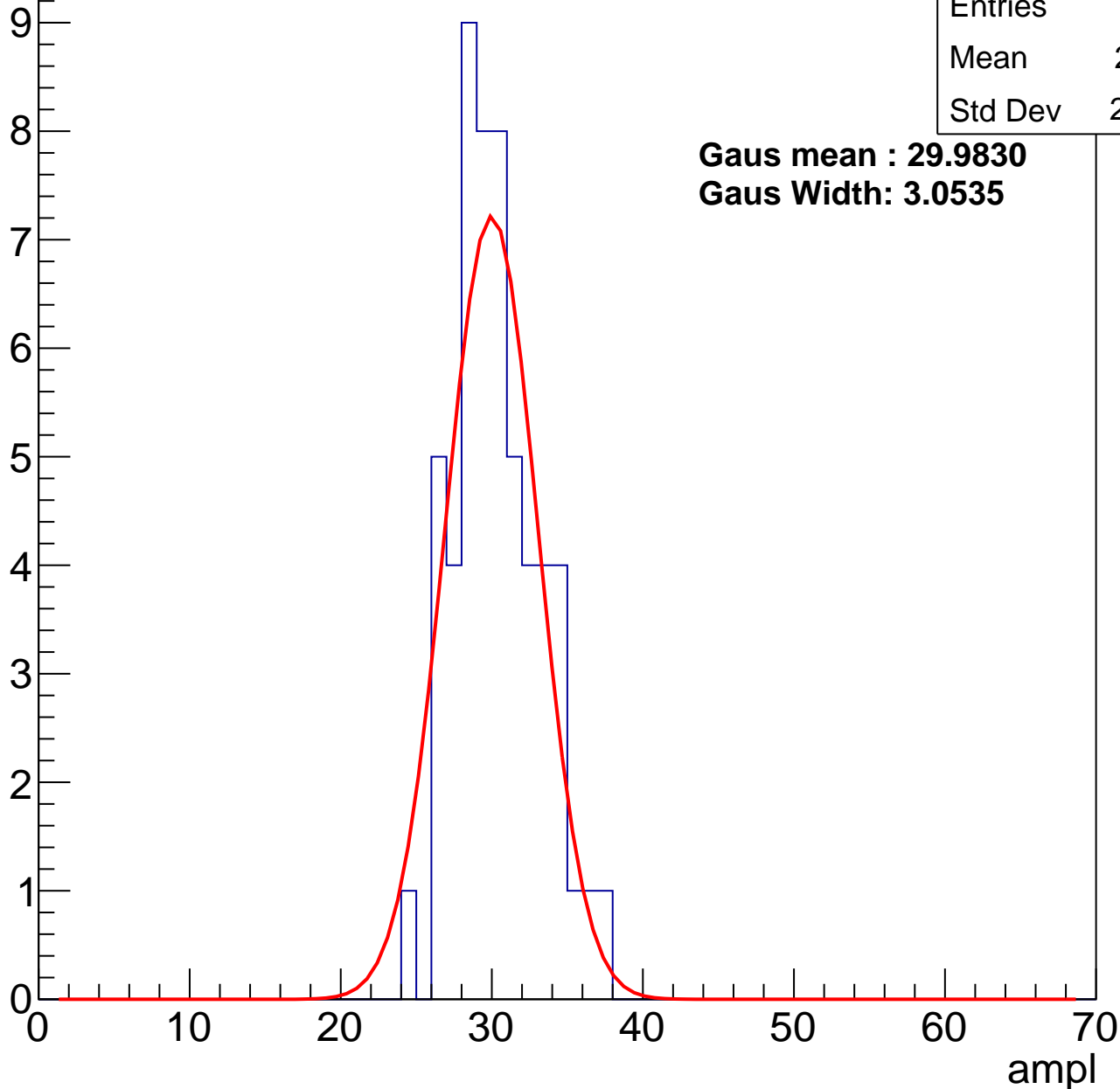
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	29.91
Std Dev	2.785

Gaus mean : 29.9830

Gaus Width: 3.0535



B1L102S, U19-ch36, adc1

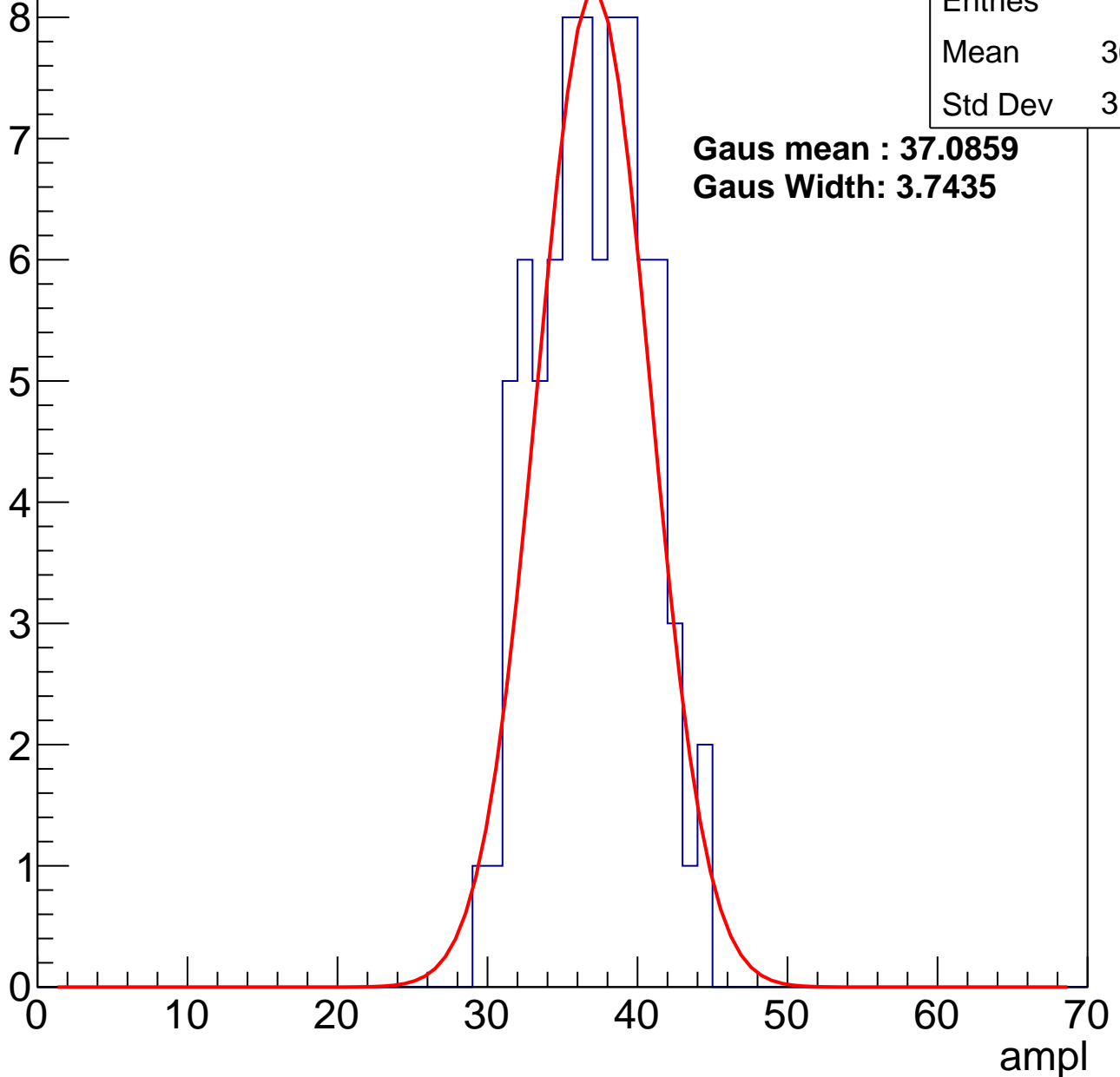
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.55
Std Dev	3.535

Gaus mean : 37.0859

Gaus Width: 3.7435



B1L102S, U19-ch36, adc2

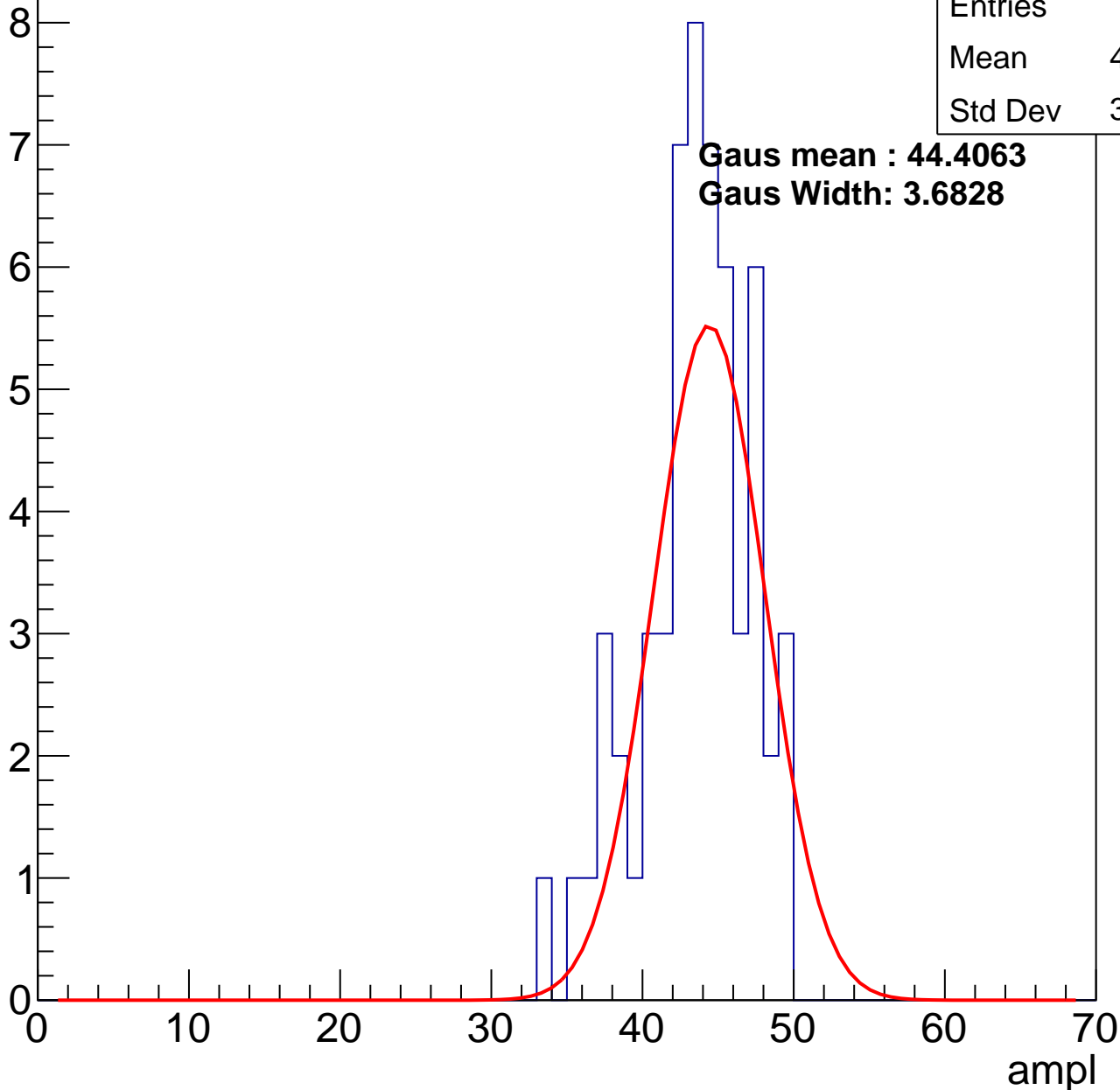
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.02
Std Dev	3.615

Gaus mean : 44.4063

Gaus Width: 3.6828

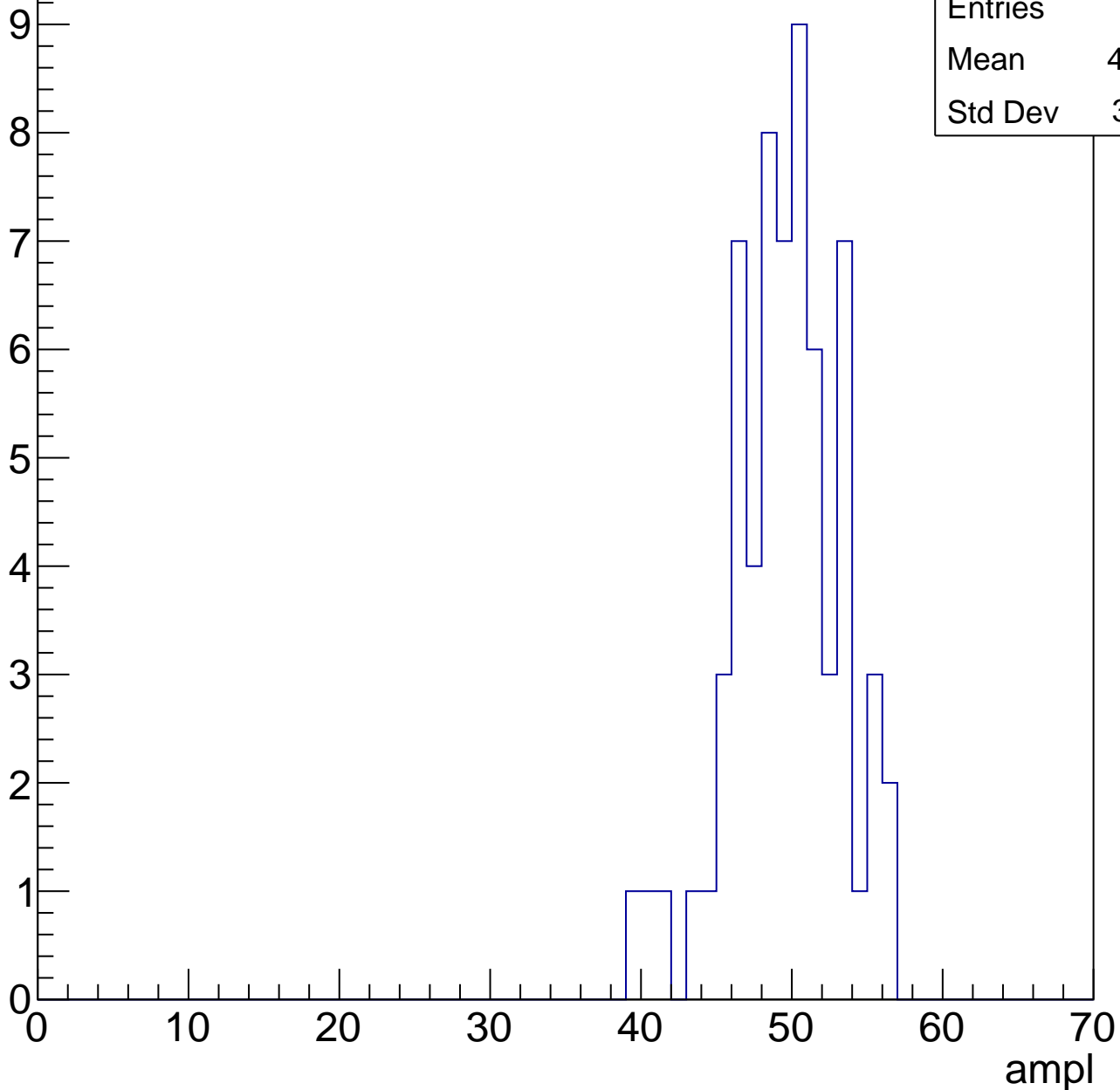


B1L102S, U19-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.12
Std Dev	3.601

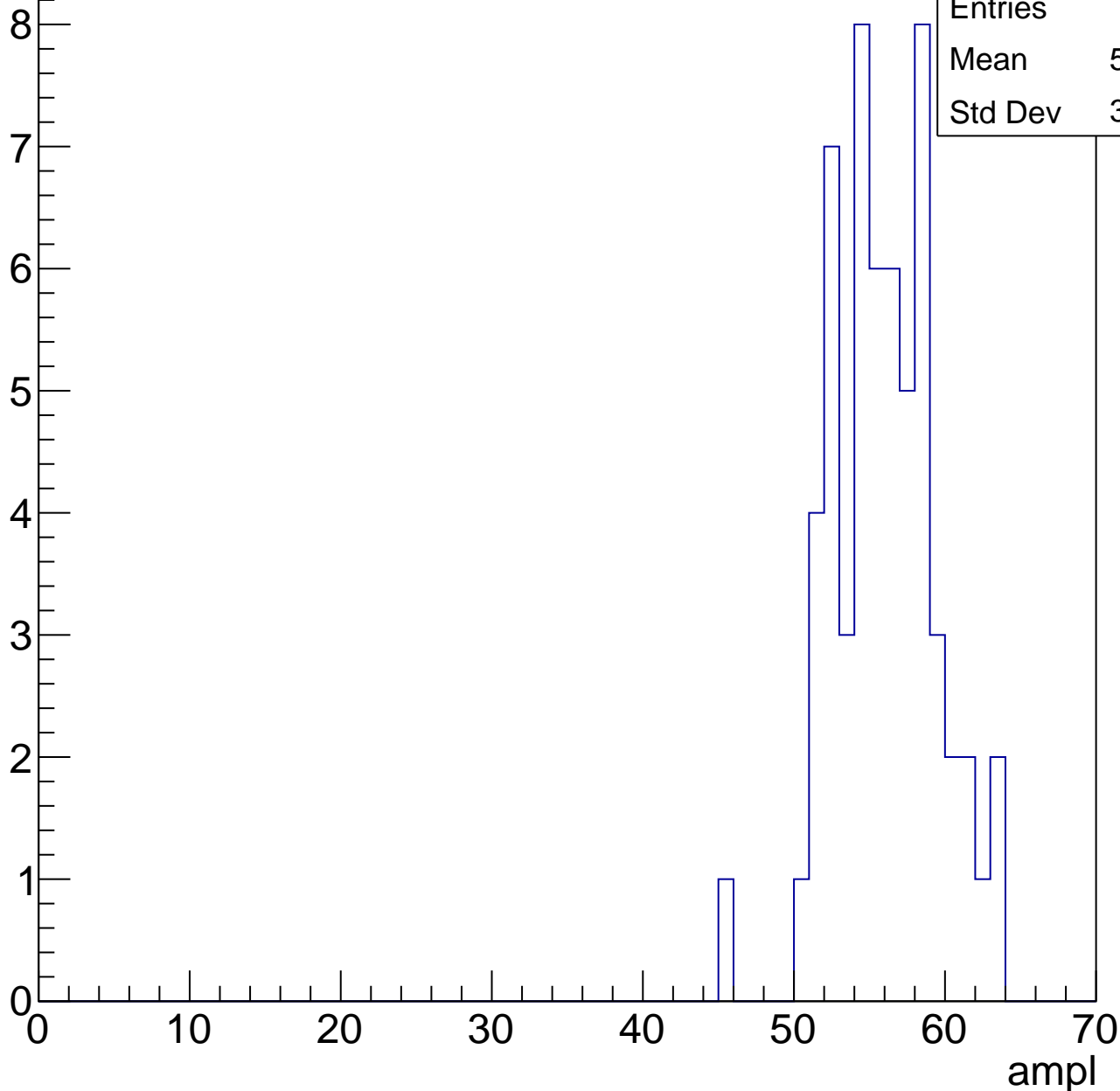


B1L102S, U19-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.53
Std Dev	3.446

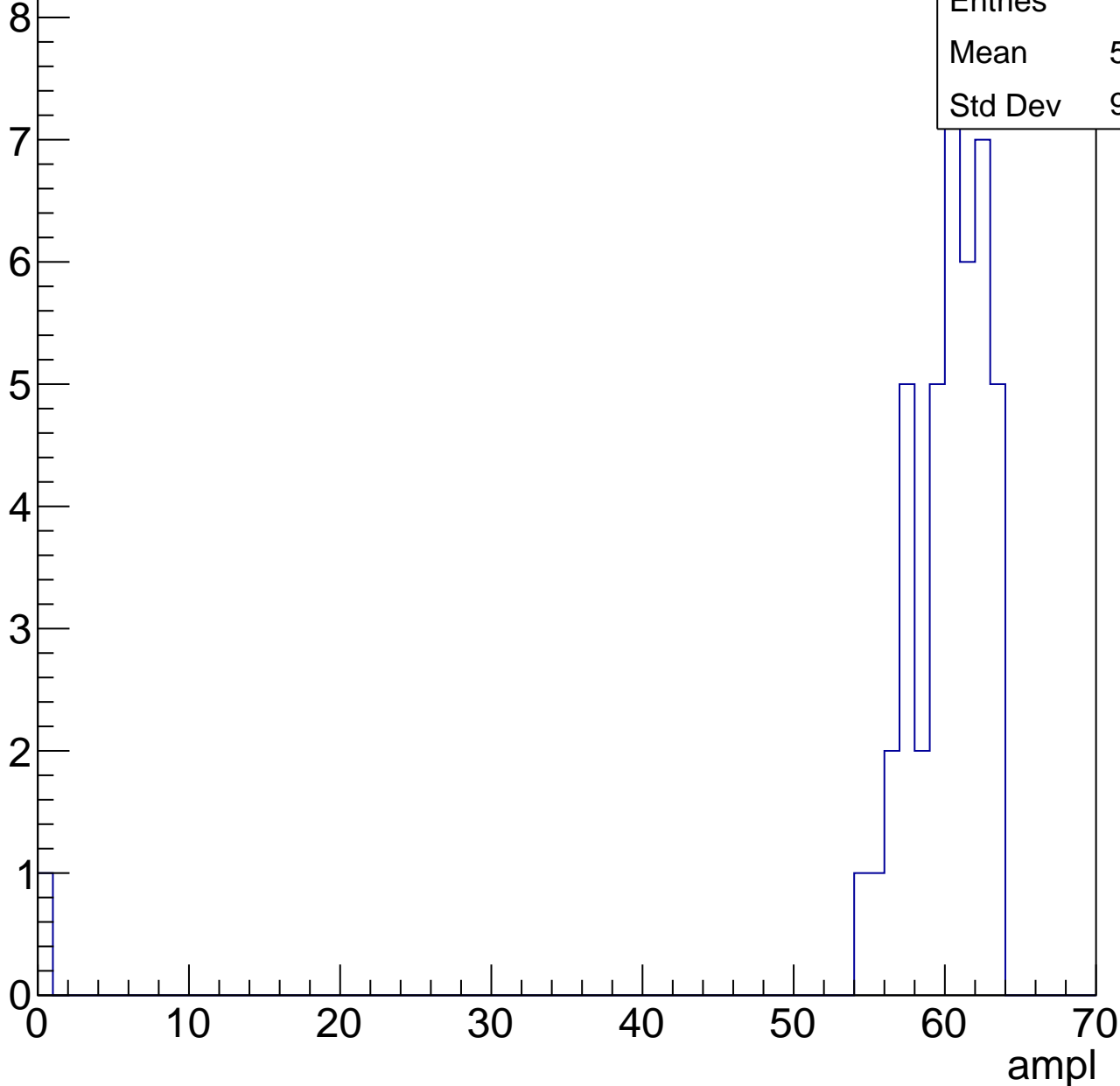


B1L102S, U19-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

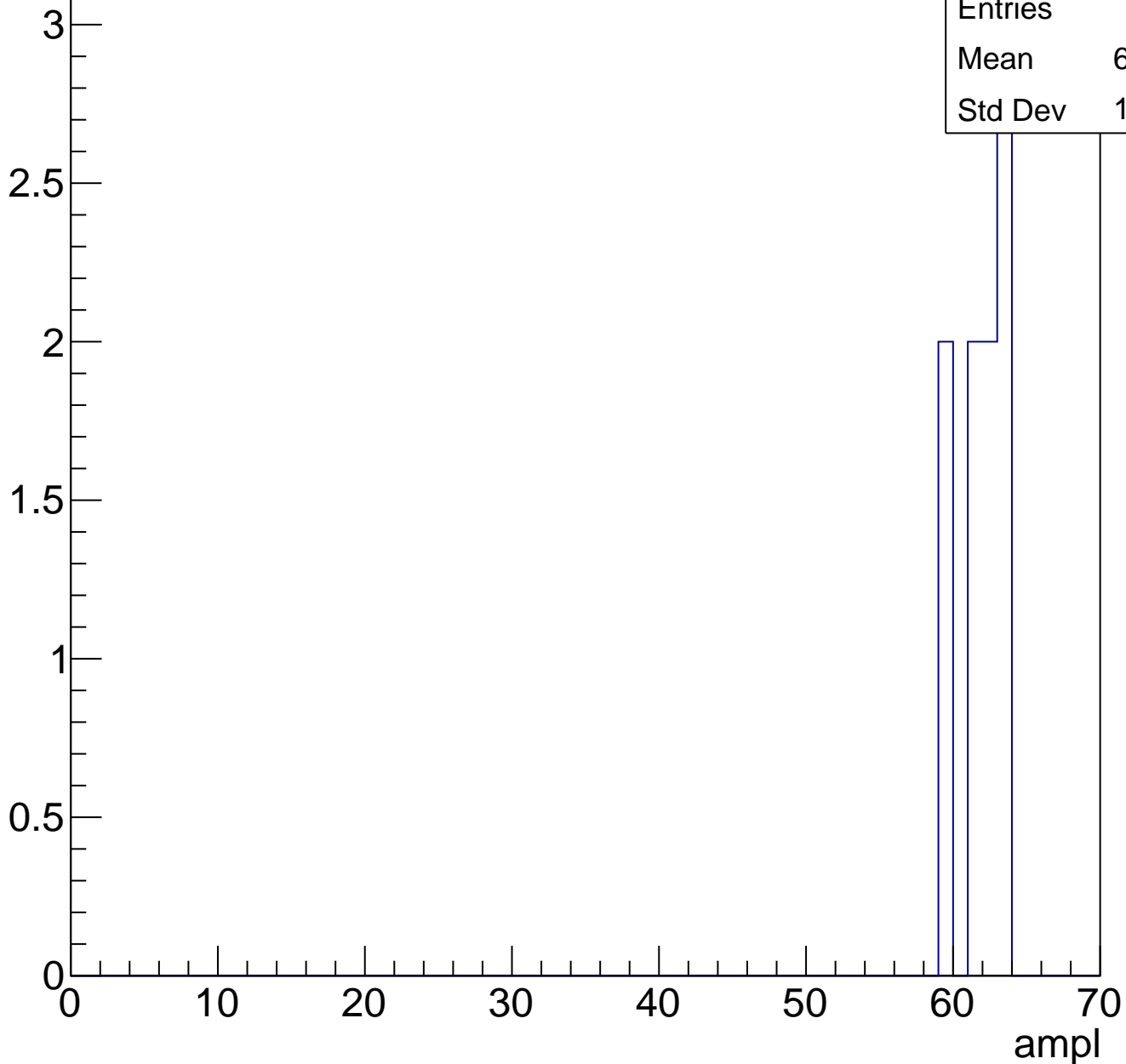
Entries	43
Mean	58.42
Std Dev	9.304



B1L102S, U19-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

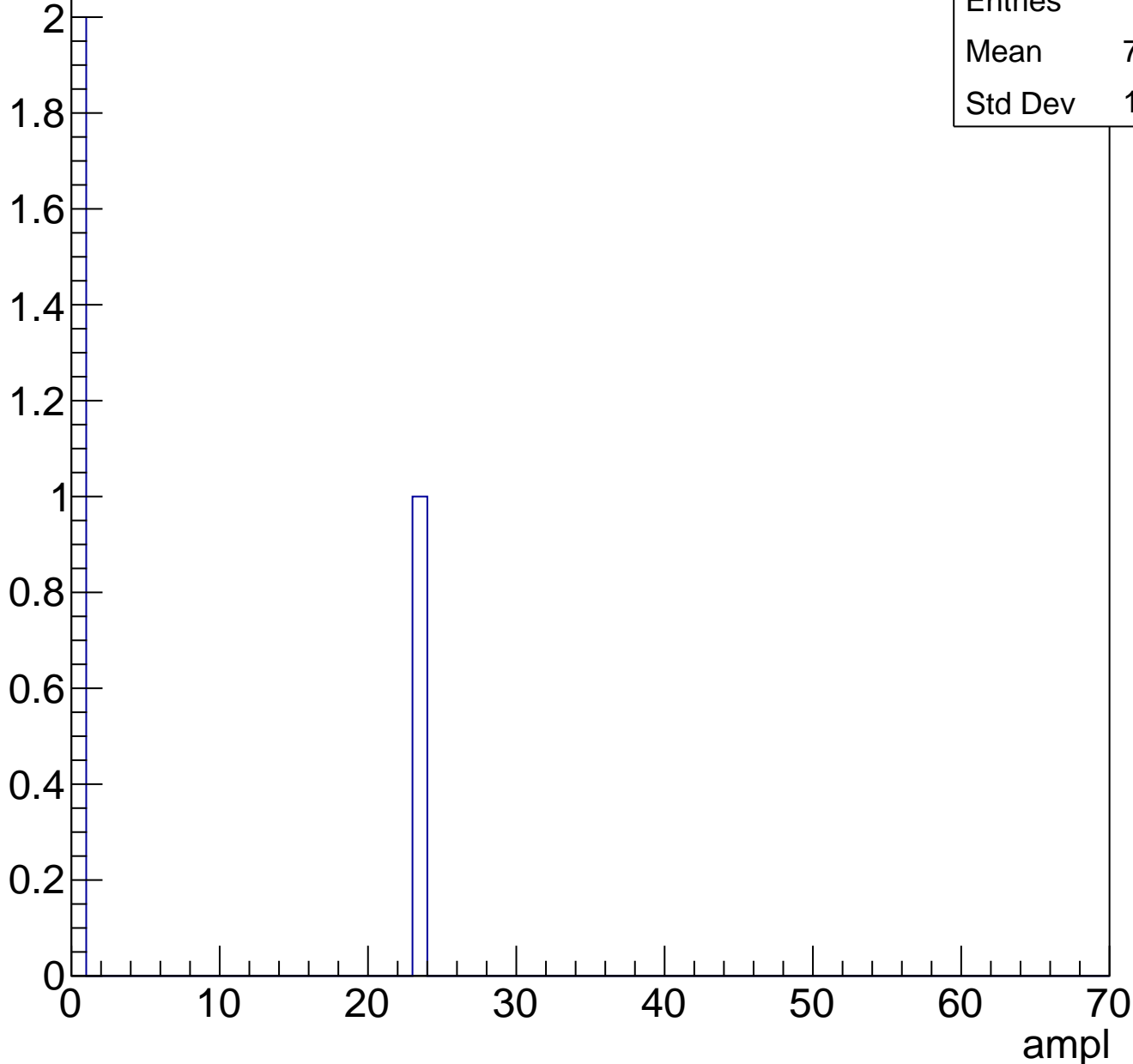
Entry



B1L102S, U19-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L102S, U19-ch37, adc0

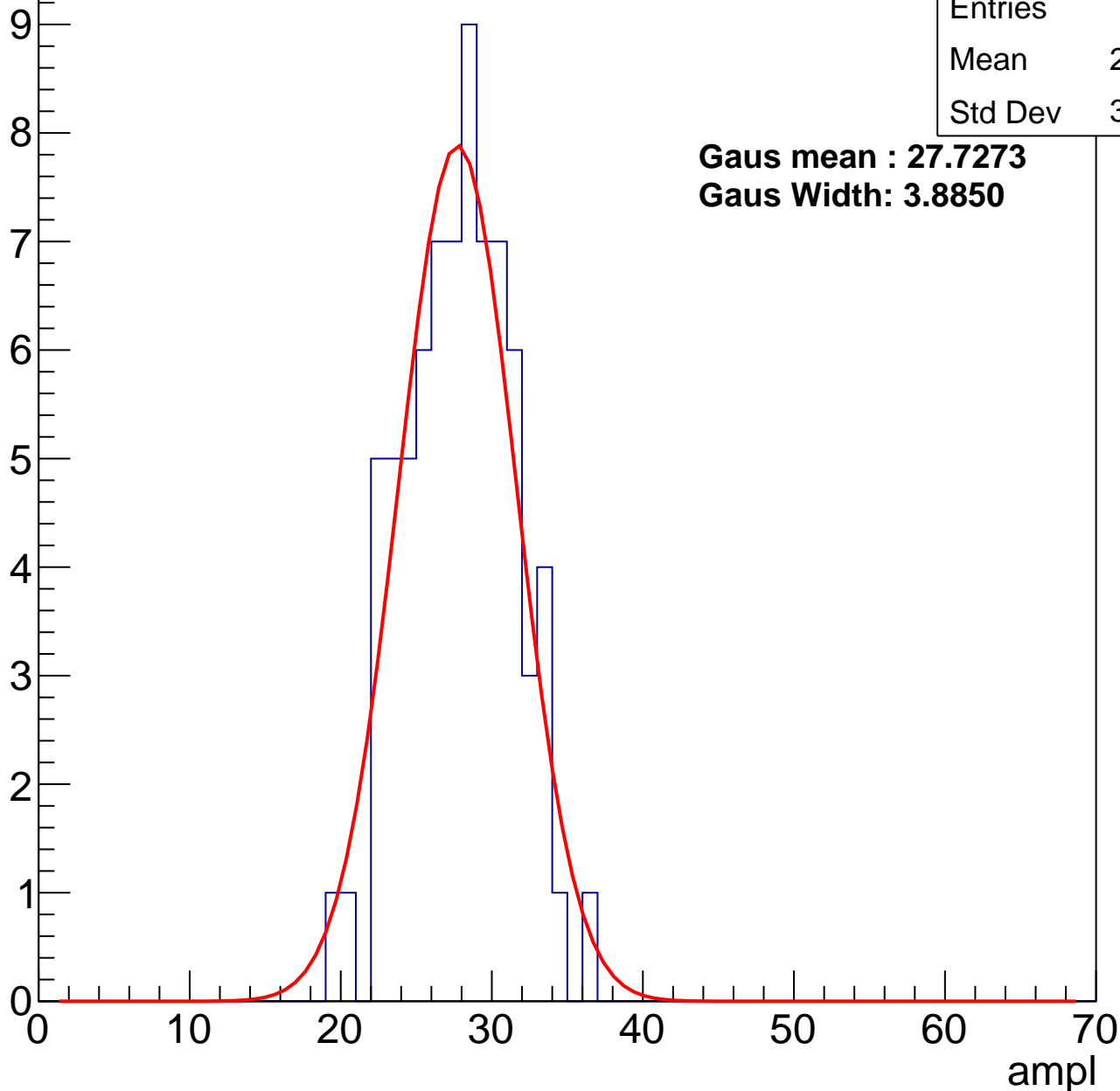
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.39
Std Dev	3.517

Gaus mean : 27.7273

Gaus Width: 3.8850



B1L102S, U19-ch37, adc1

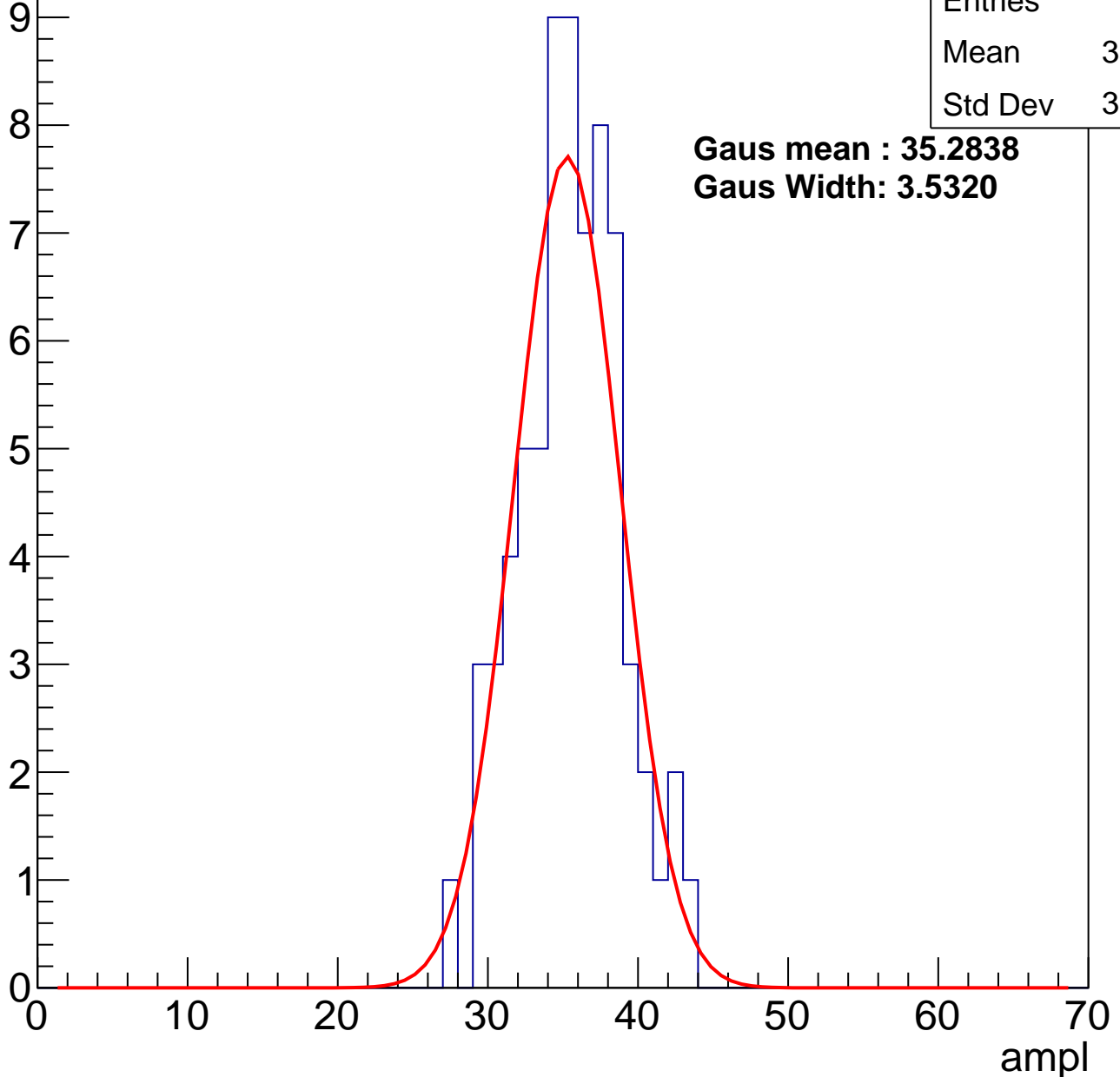
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.04
Std Dev	3.344

Gaus mean : 35.2838

Gaus Width: 3.5320



B1L102S, U19-ch37, adc2

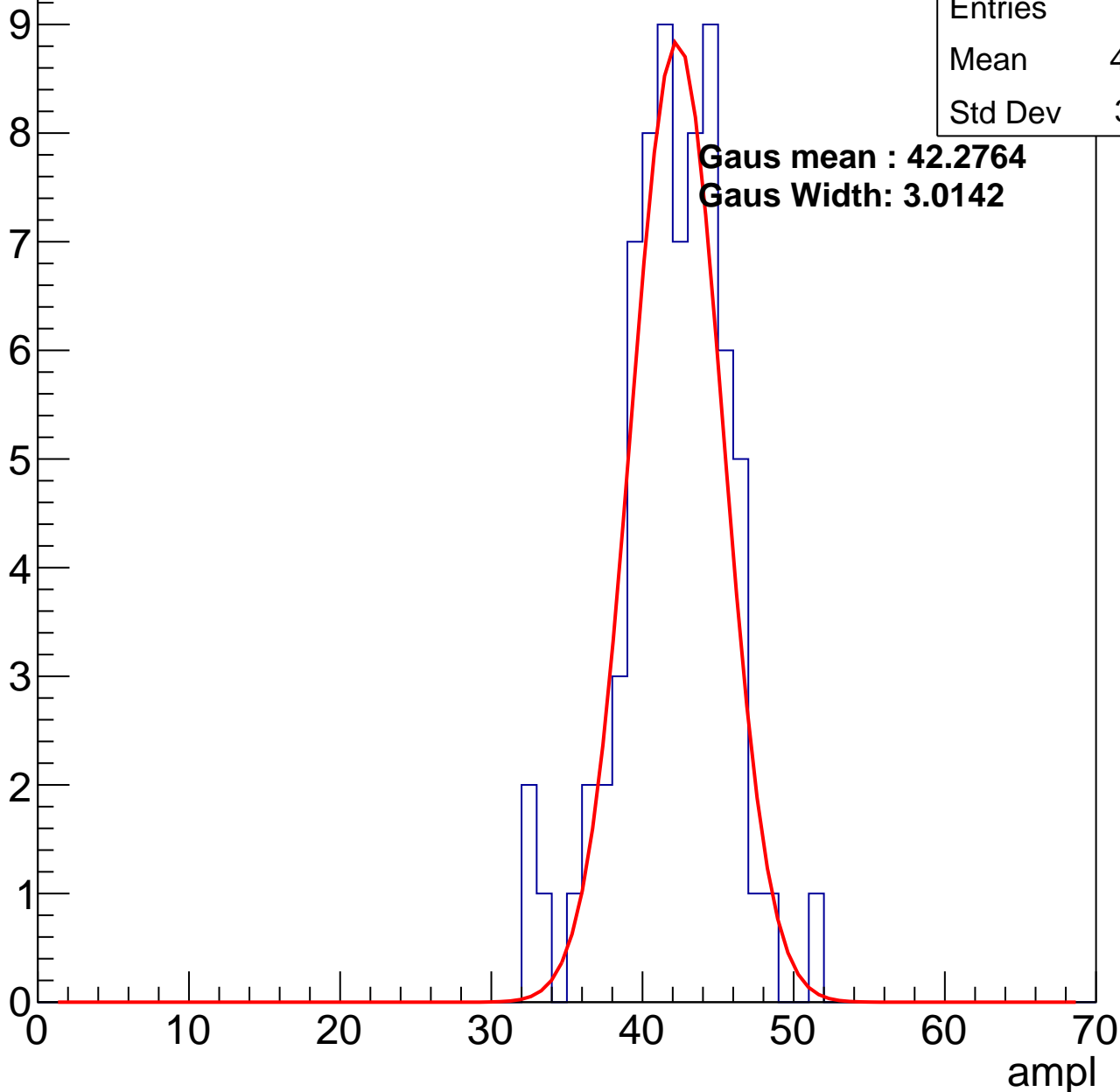
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.56
Std Dev	3.531

Gaus mean : 42.2764

Gaus Width: 3.0142



B1L102S, U19-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	61
Mean	48.85
Std Dev	2.839

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

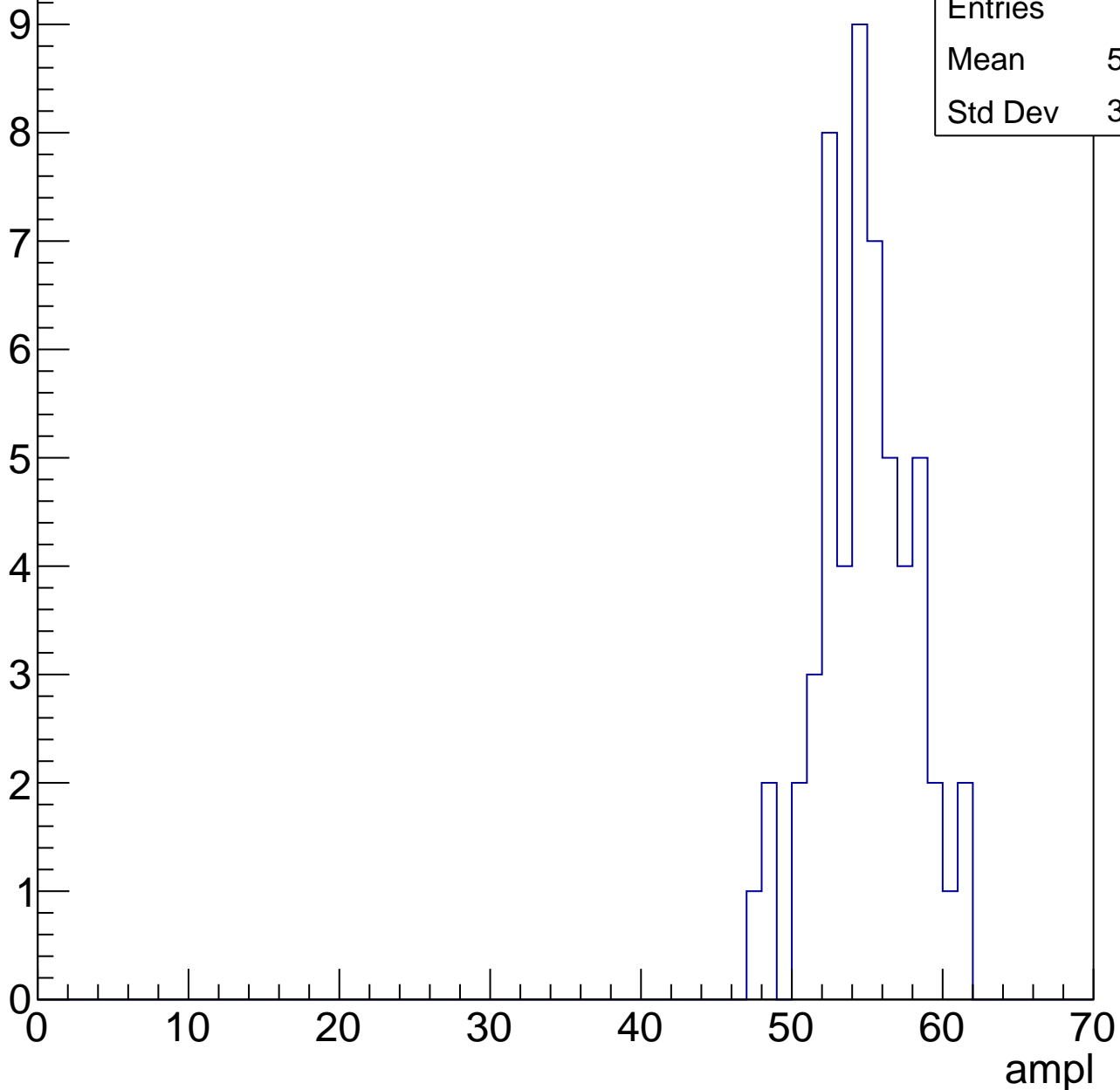
70

B1L102S, U19-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.42
Std Dev	3.114



B1L102S, U19-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

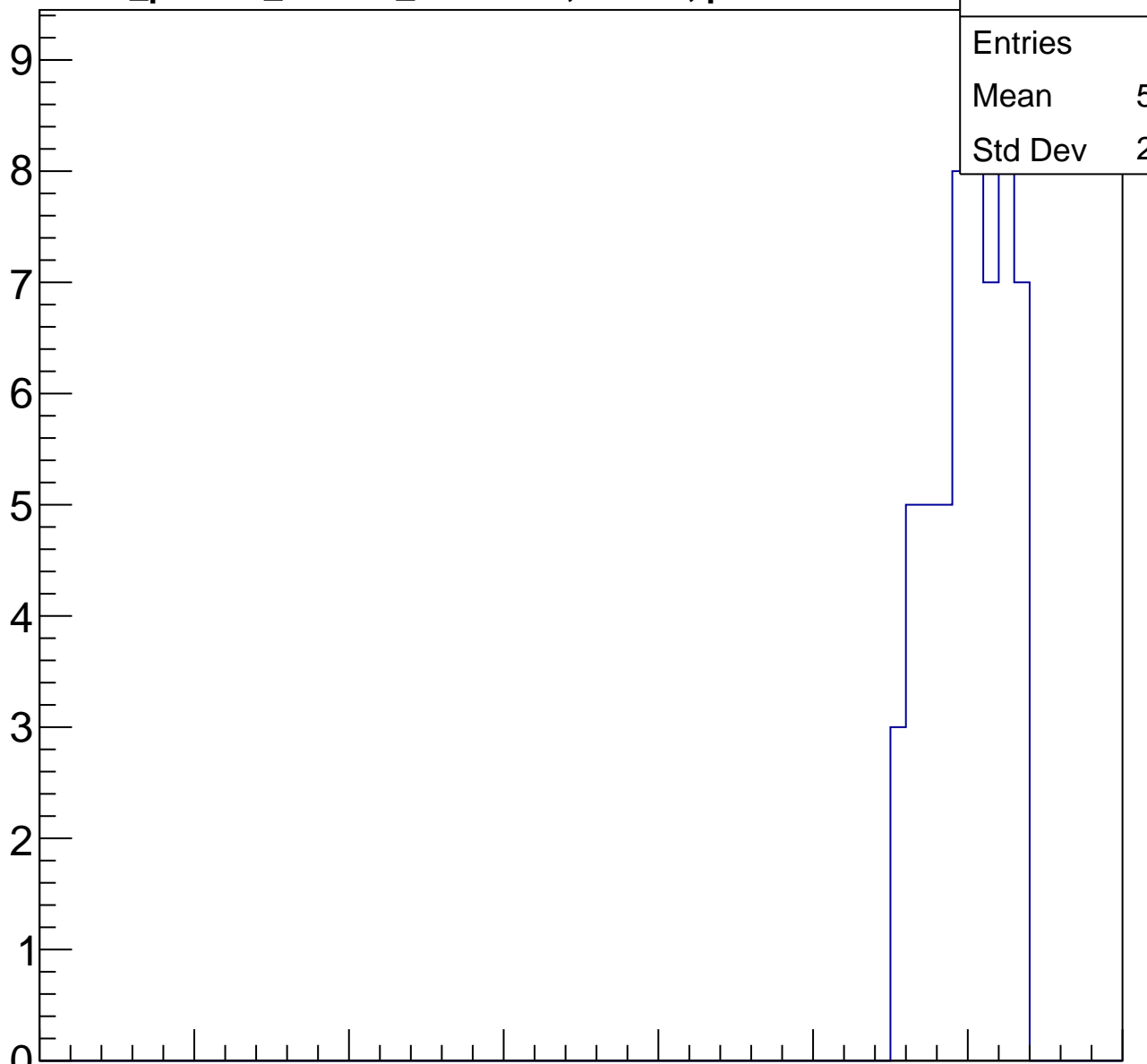
Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	59.58
Std Dev	2.369

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch38, adc0

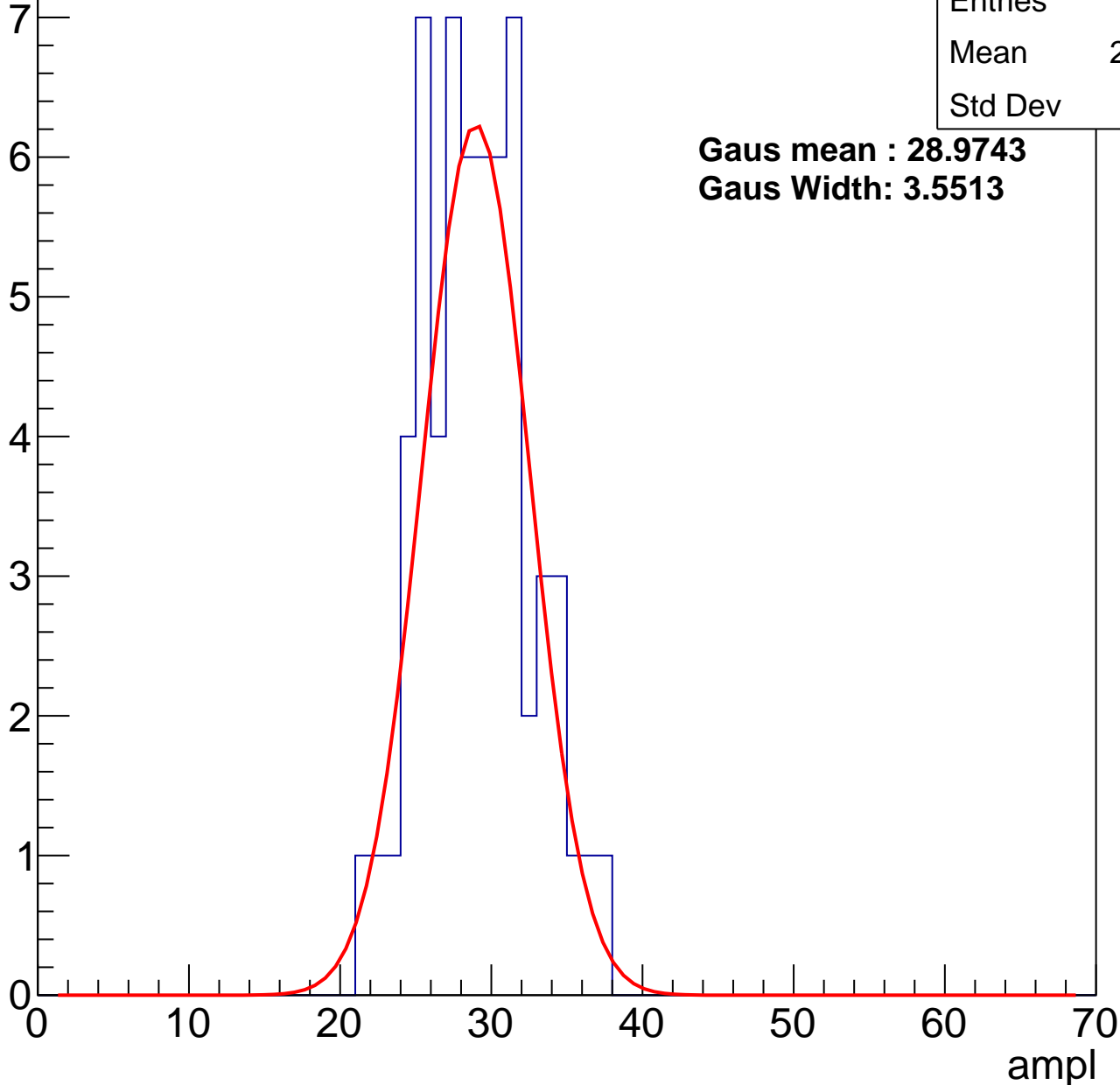
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.56
Std Dev	3.49

Gaus mean : 28.9743

Gaus Width: 3.5513



B1L102S, U19-ch38, adc1

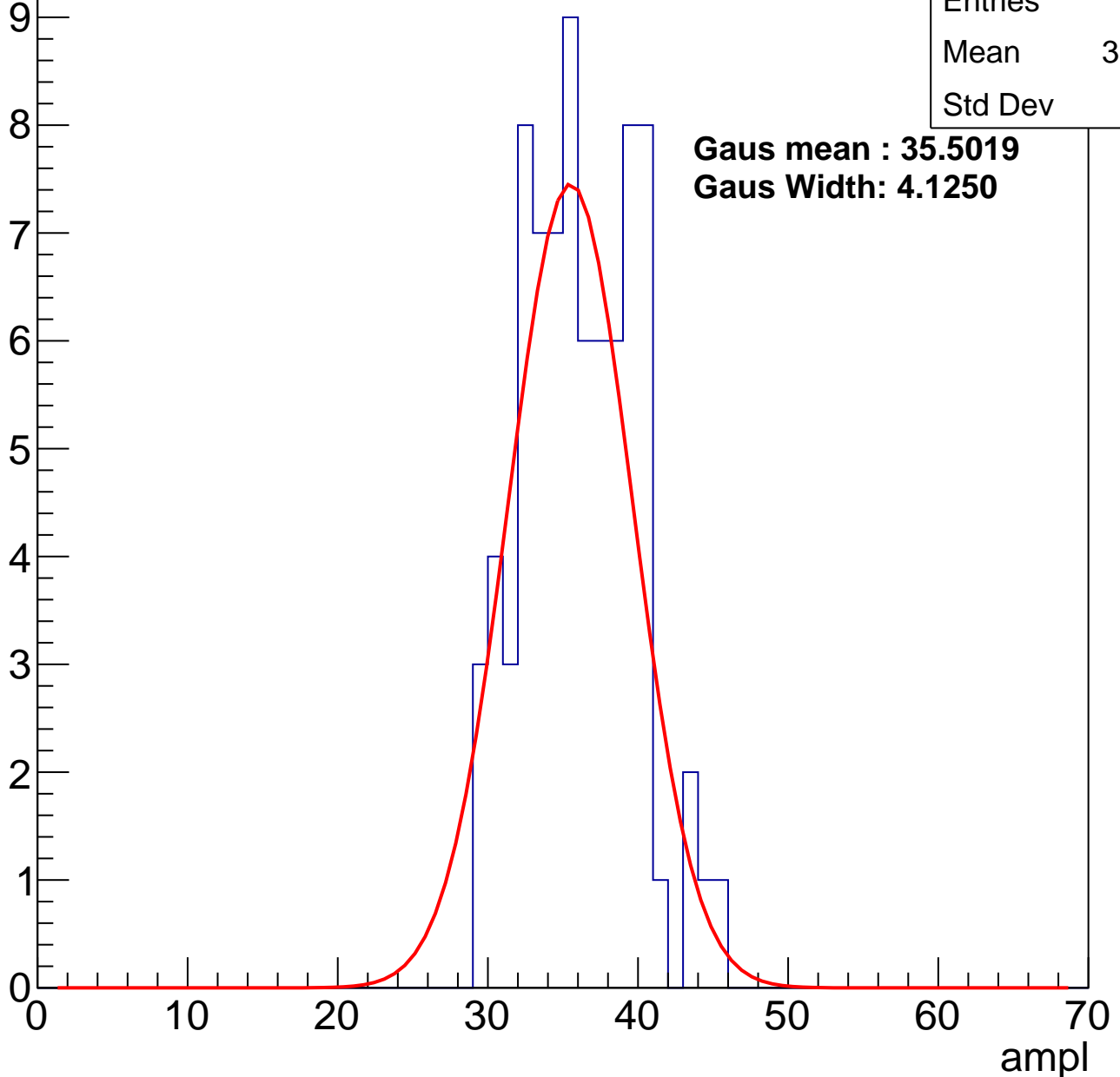
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.67
Std Dev	3.68

Gaus mean : 35.5019

Gaus Width: 4.1250



B1L102S, U19-ch38, adc2

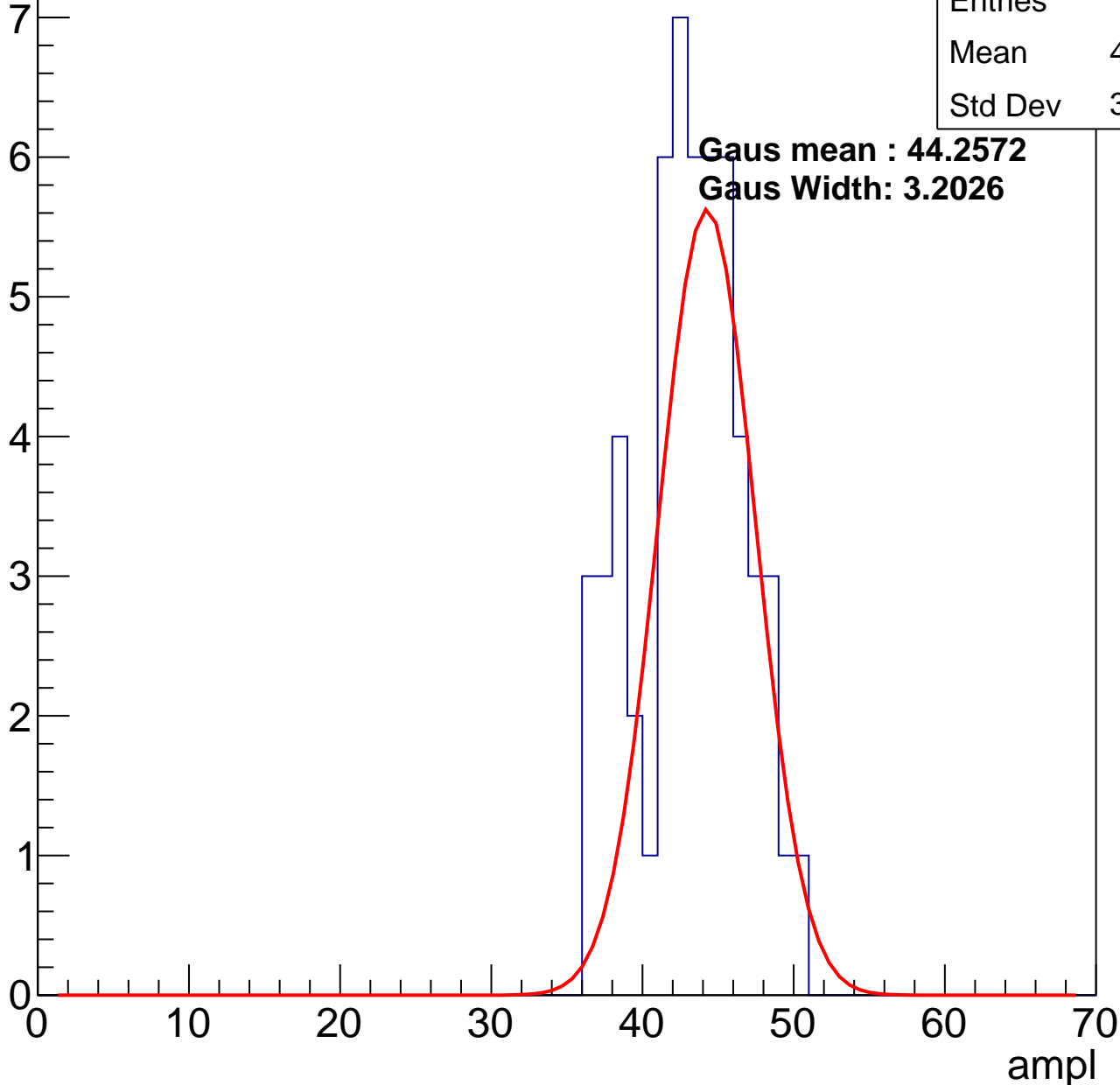
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.66
Std Dev	3.522

Gaus mean : 44.2572

Gaus Width: 3.2026

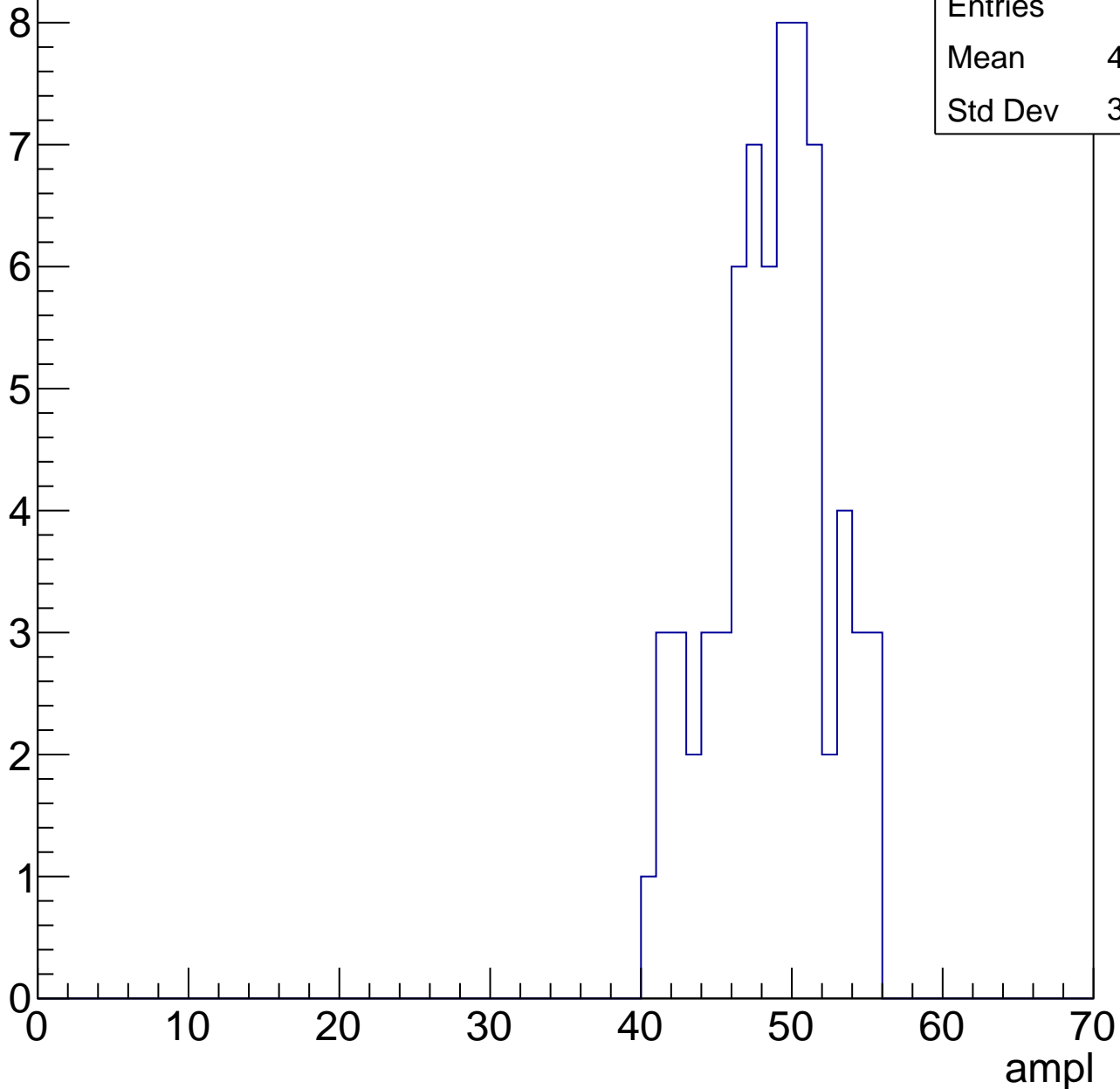


B1L102S, U19-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.22
Std Dev	3.733

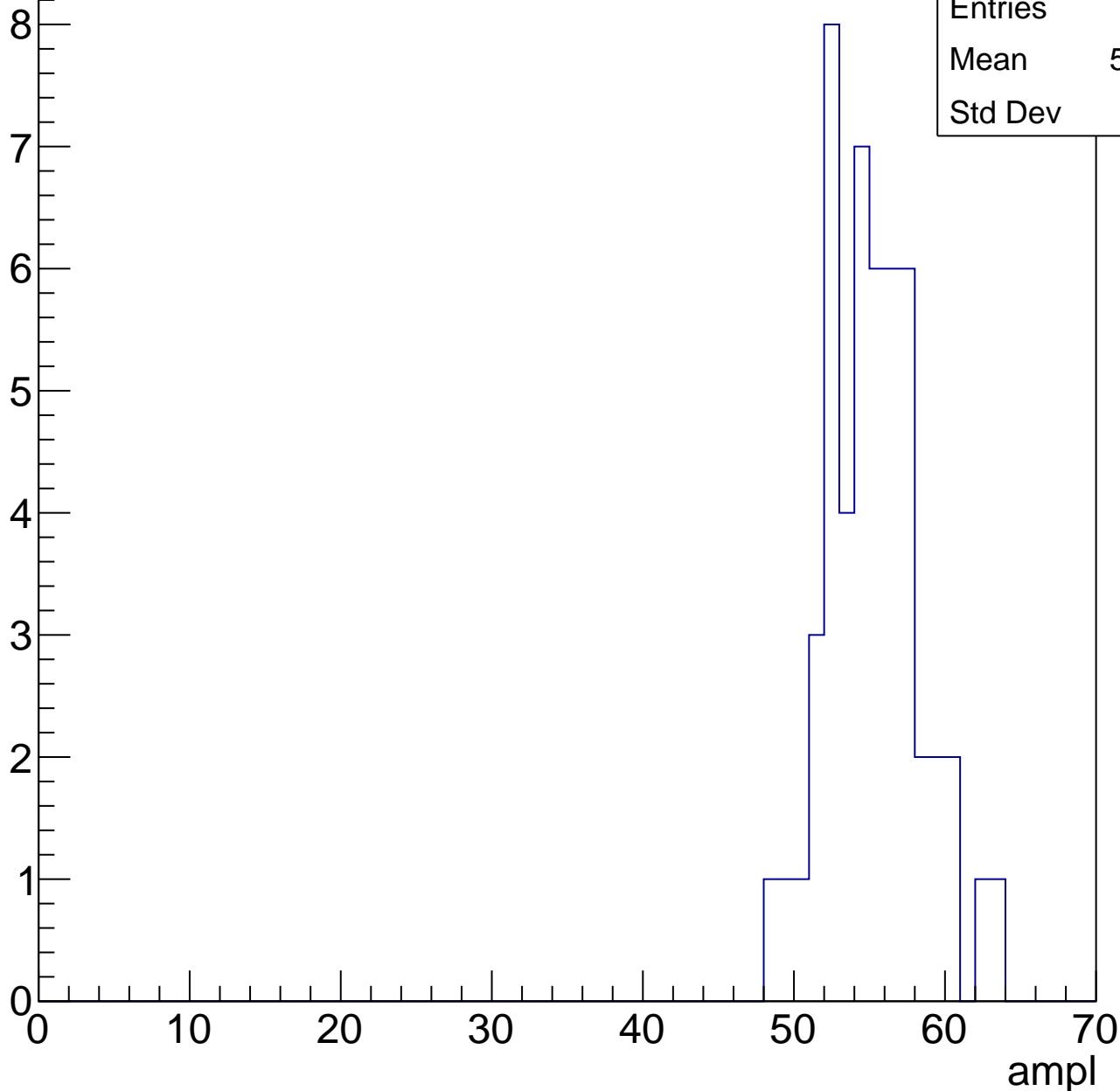


B1L102S, U19-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

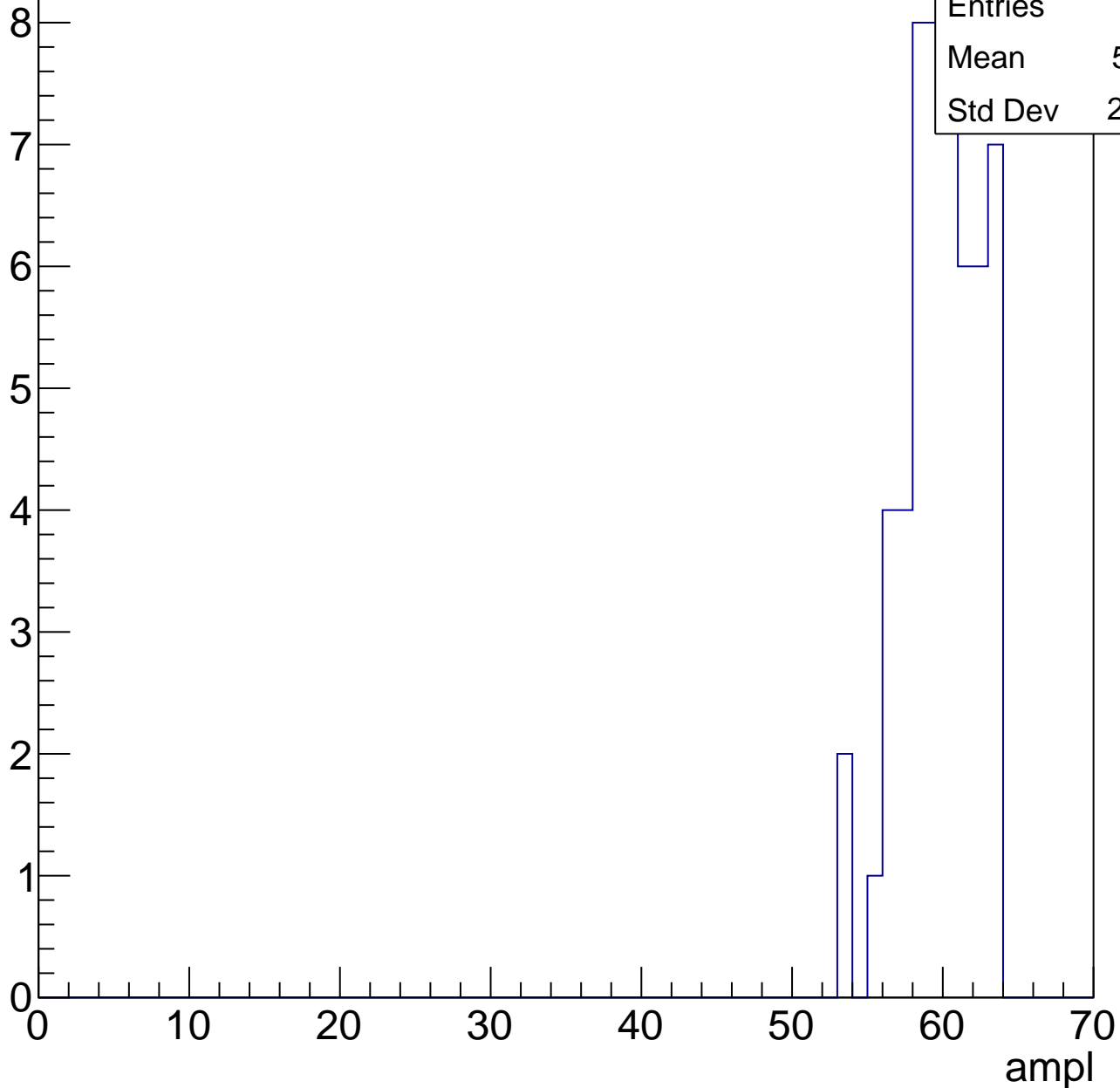
Entries	51
Mean	54.76
Std Dev	3.11



B1L102S, U19-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

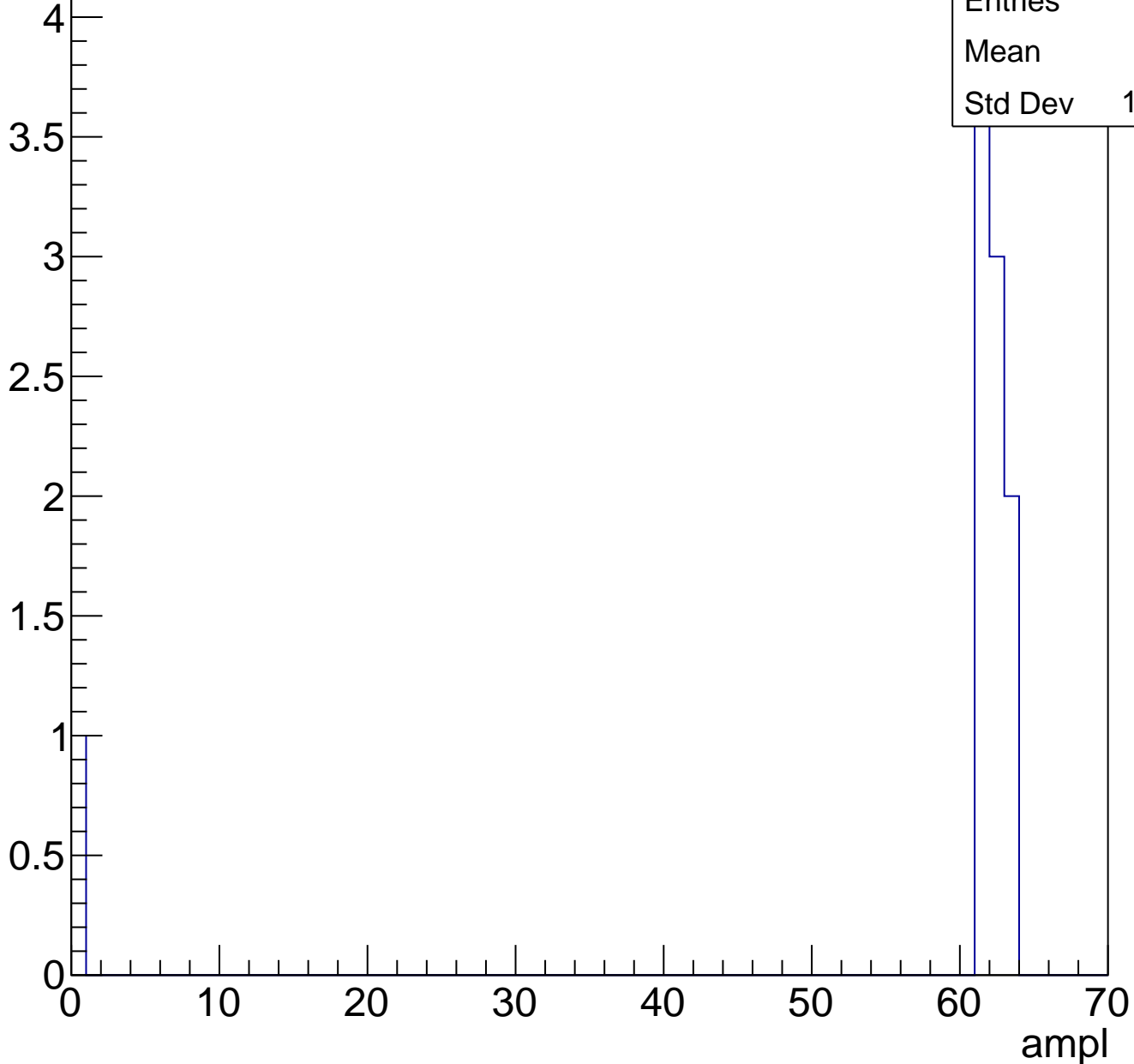
Entry



B1L102S, U19-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	55.6
Std Dev	18.55

B1L102S, U19-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch39, adc0

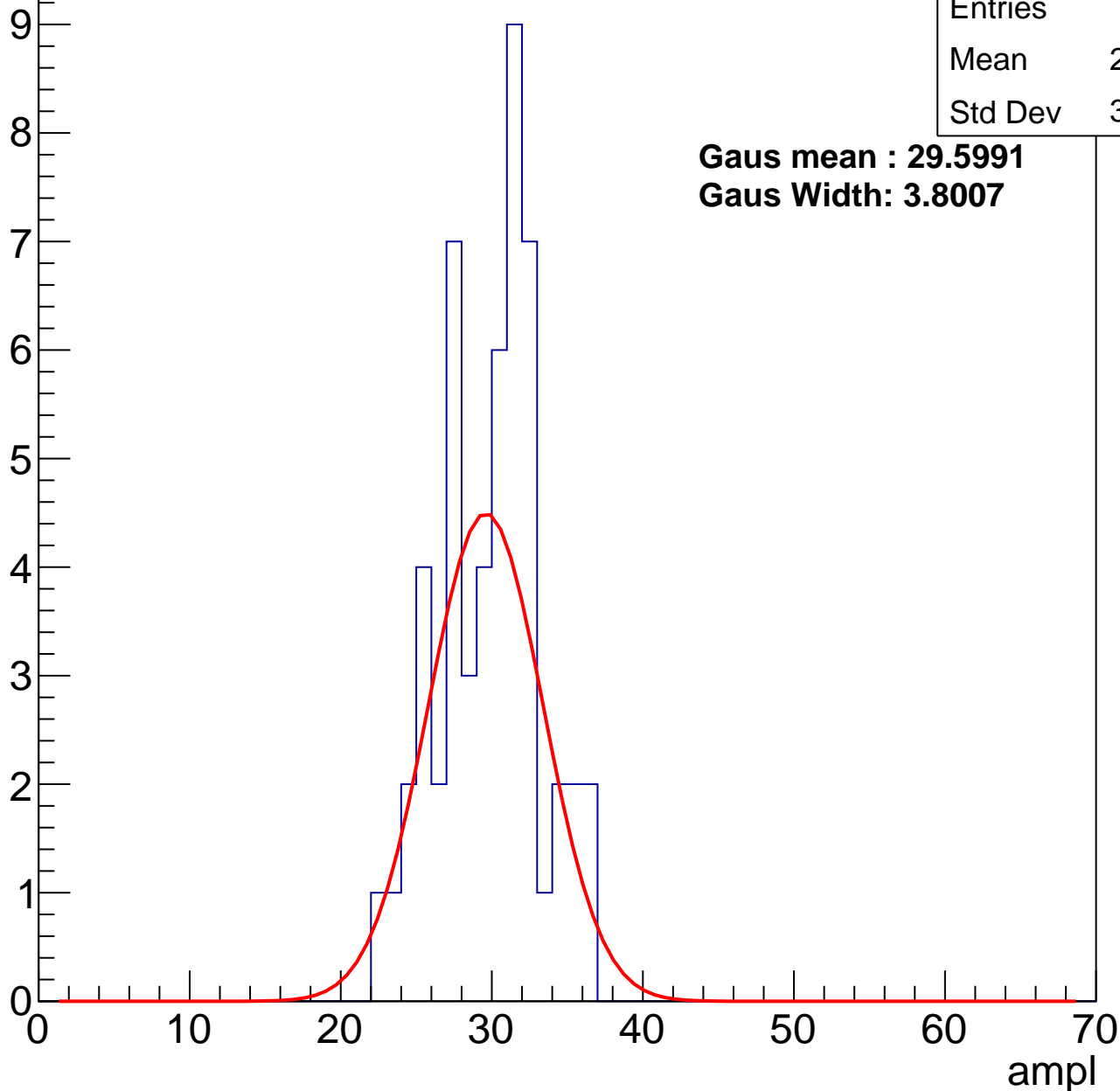
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	29.43
Std Dev	3.305

Gaus mean : 29.5991

Gaus Width: 3.8007



B1L102S, U19-ch39, adc1

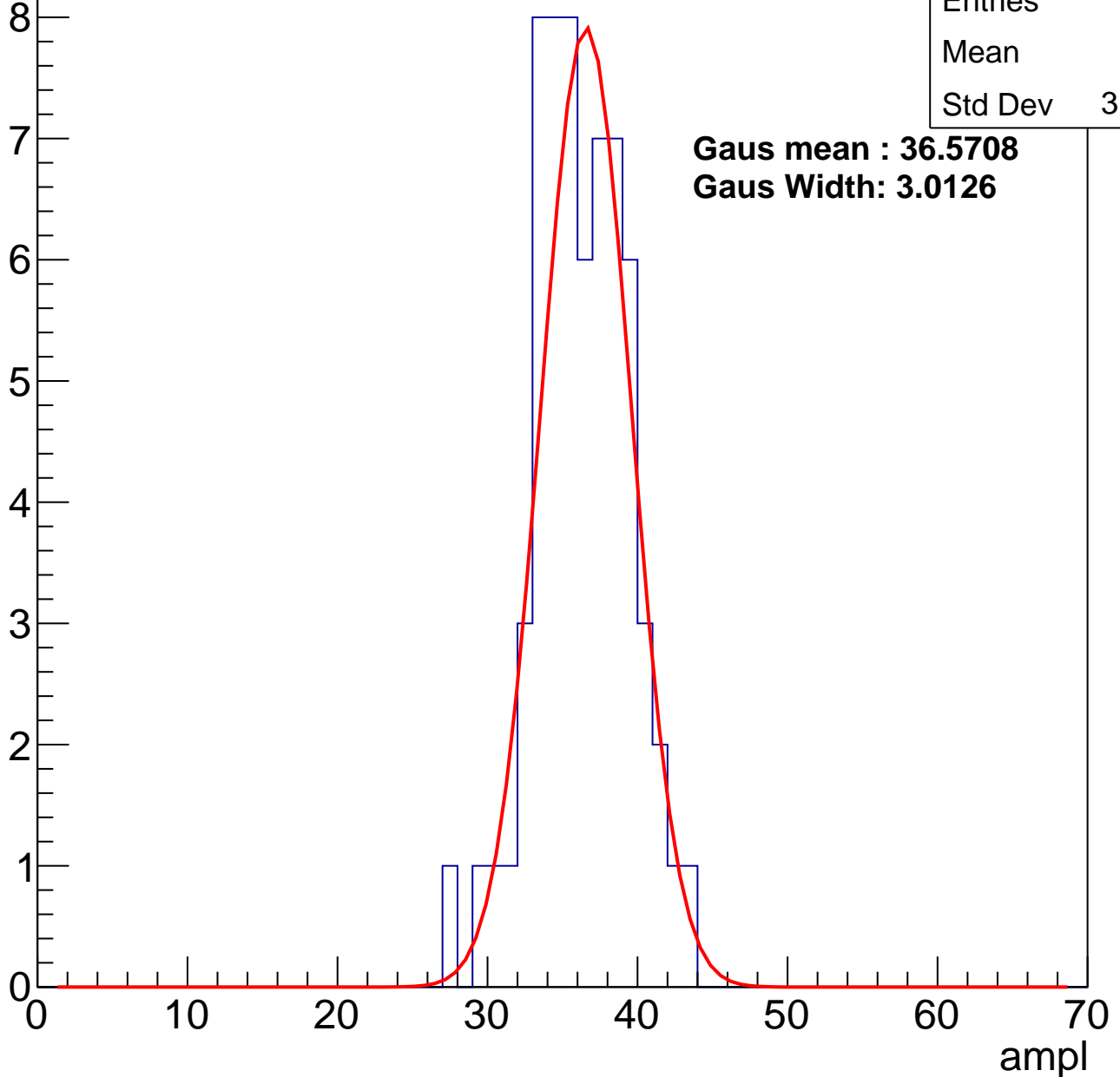
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.8
Std Dev	3.113

Gaus mean : 36.5708

Gaus Width: 3.0126



B1L102S, U19-ch39, adc2

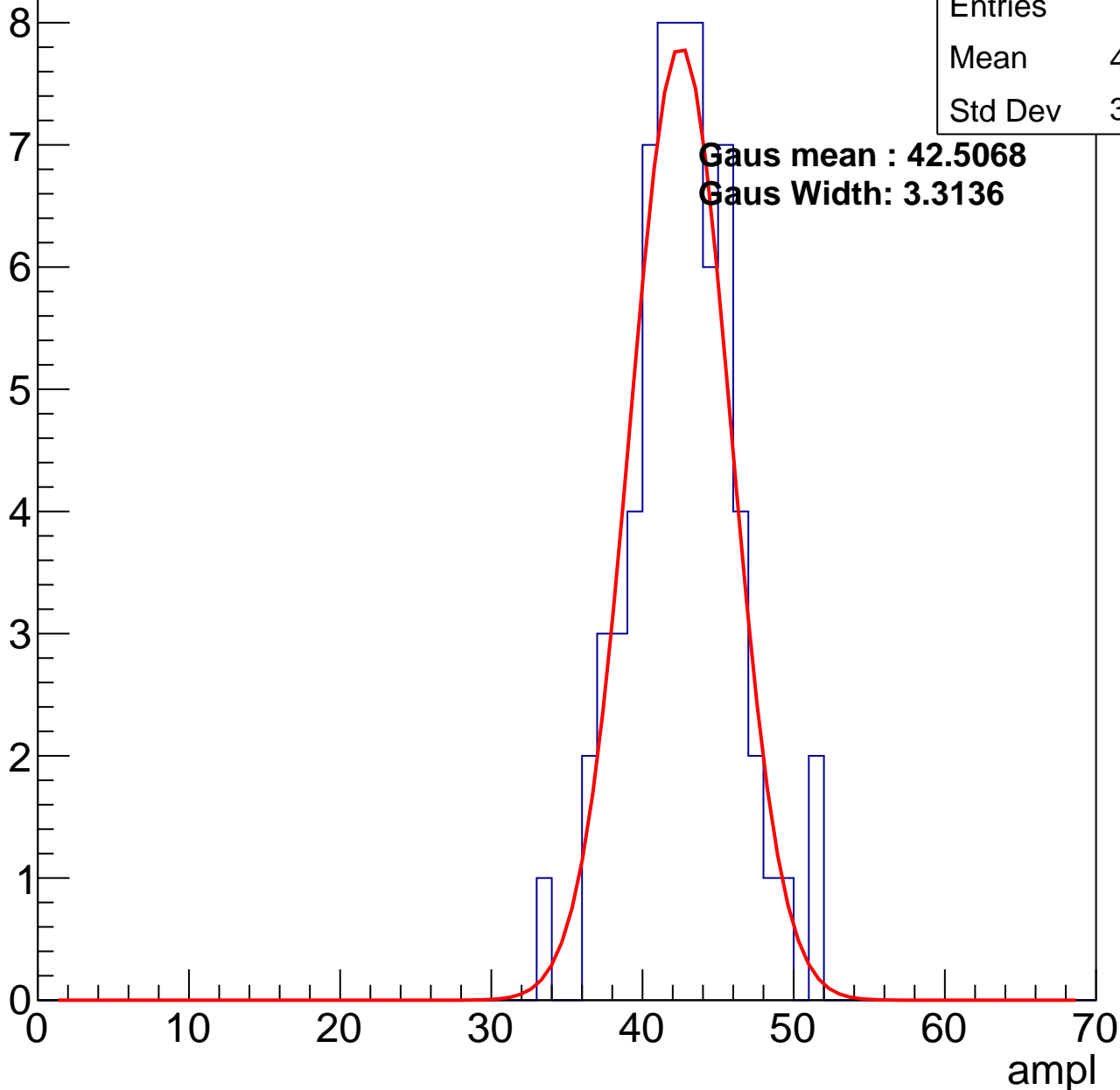
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.24
Std Dev	3.456

Gaus mean : 42.5068

Gaus Width: 3.3136

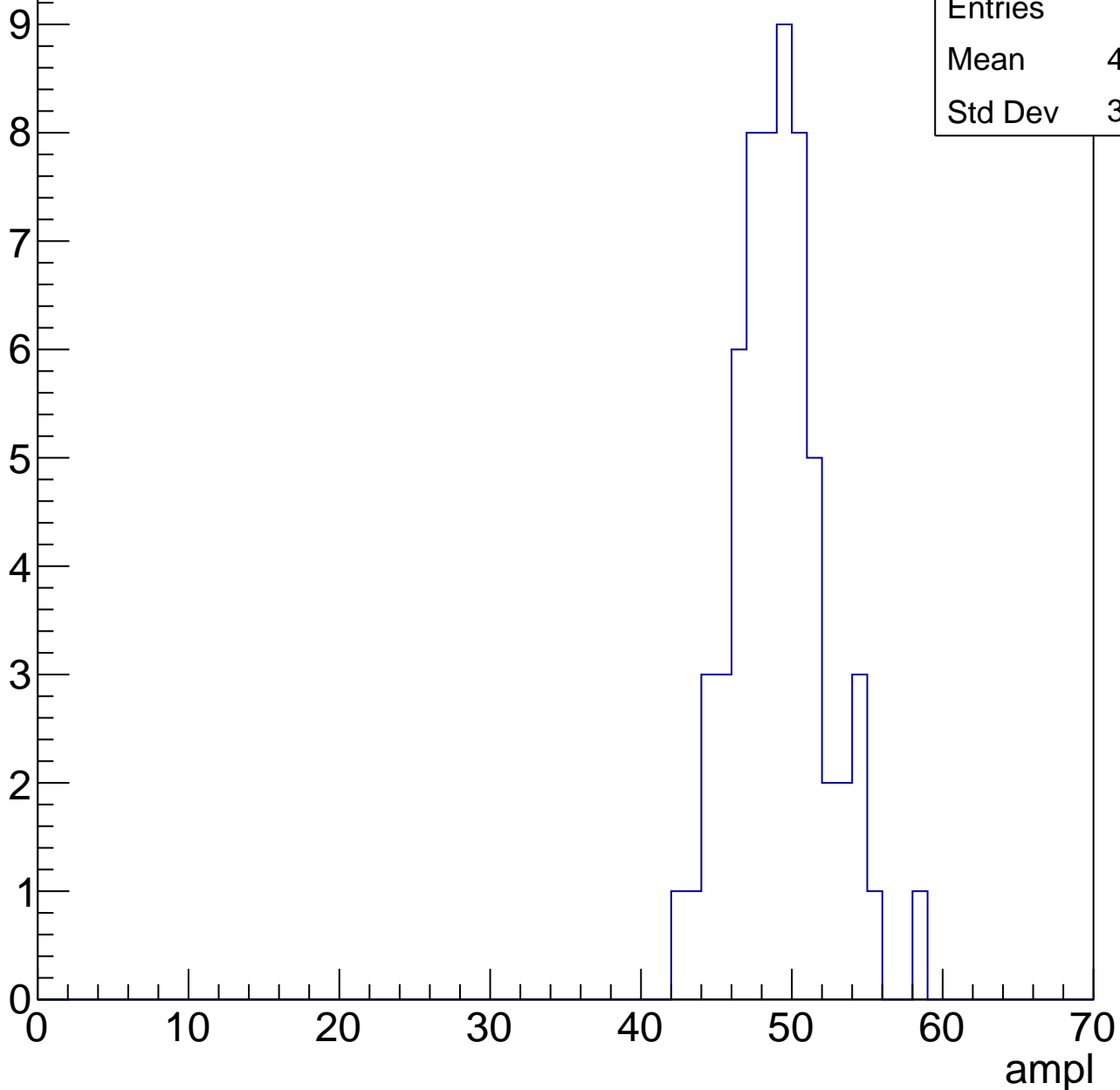


B1L102S, U19-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.67
Std Dev	3.045

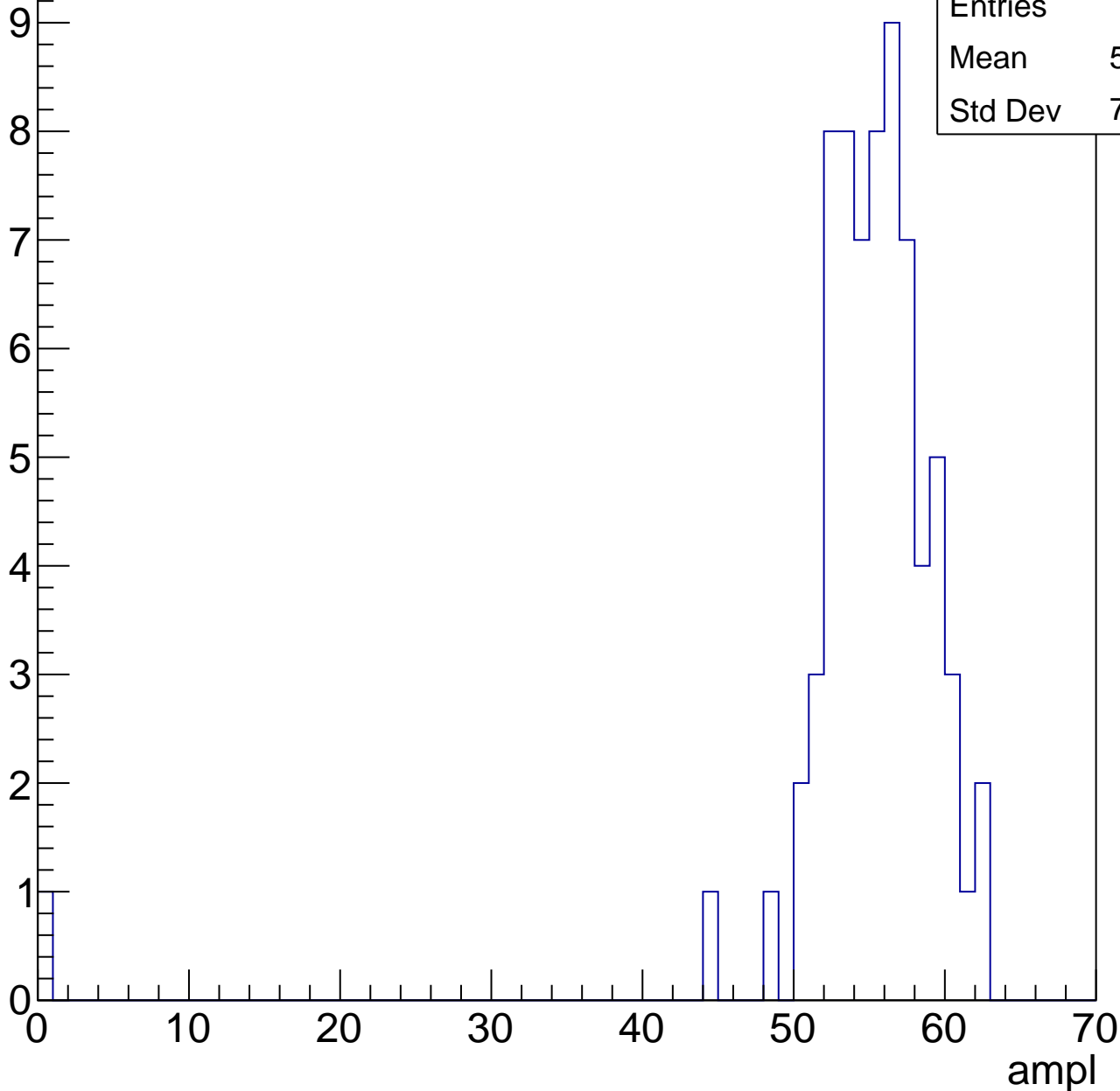


B1L102S, U19-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.26
Std Dev	7.297



B1L102S, U19-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	43
Mean	60.21
Std Dev	2.097

0

10

20

30

40

50

60

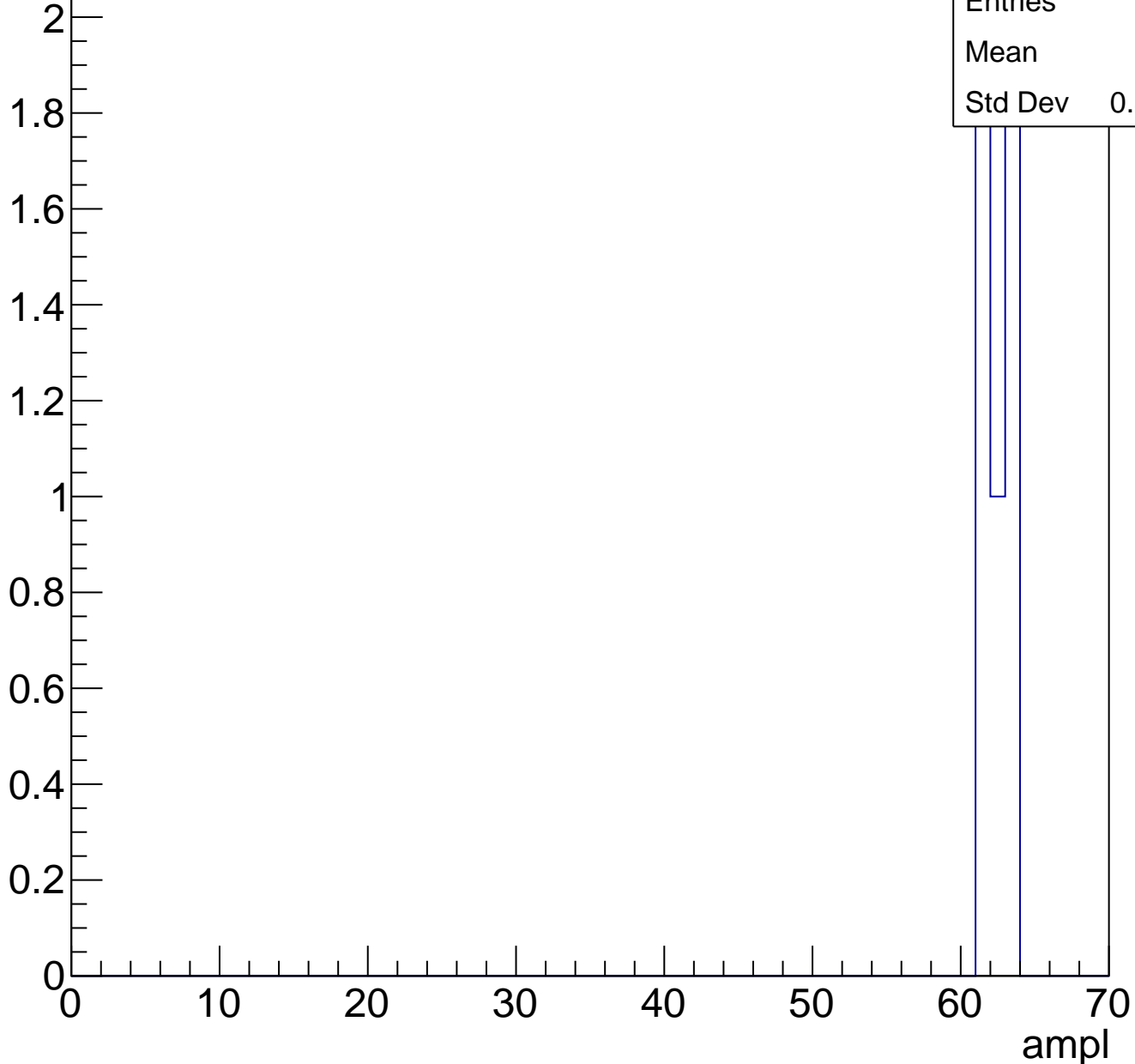
70

ampl

B1L102S, U19-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch40, adc0

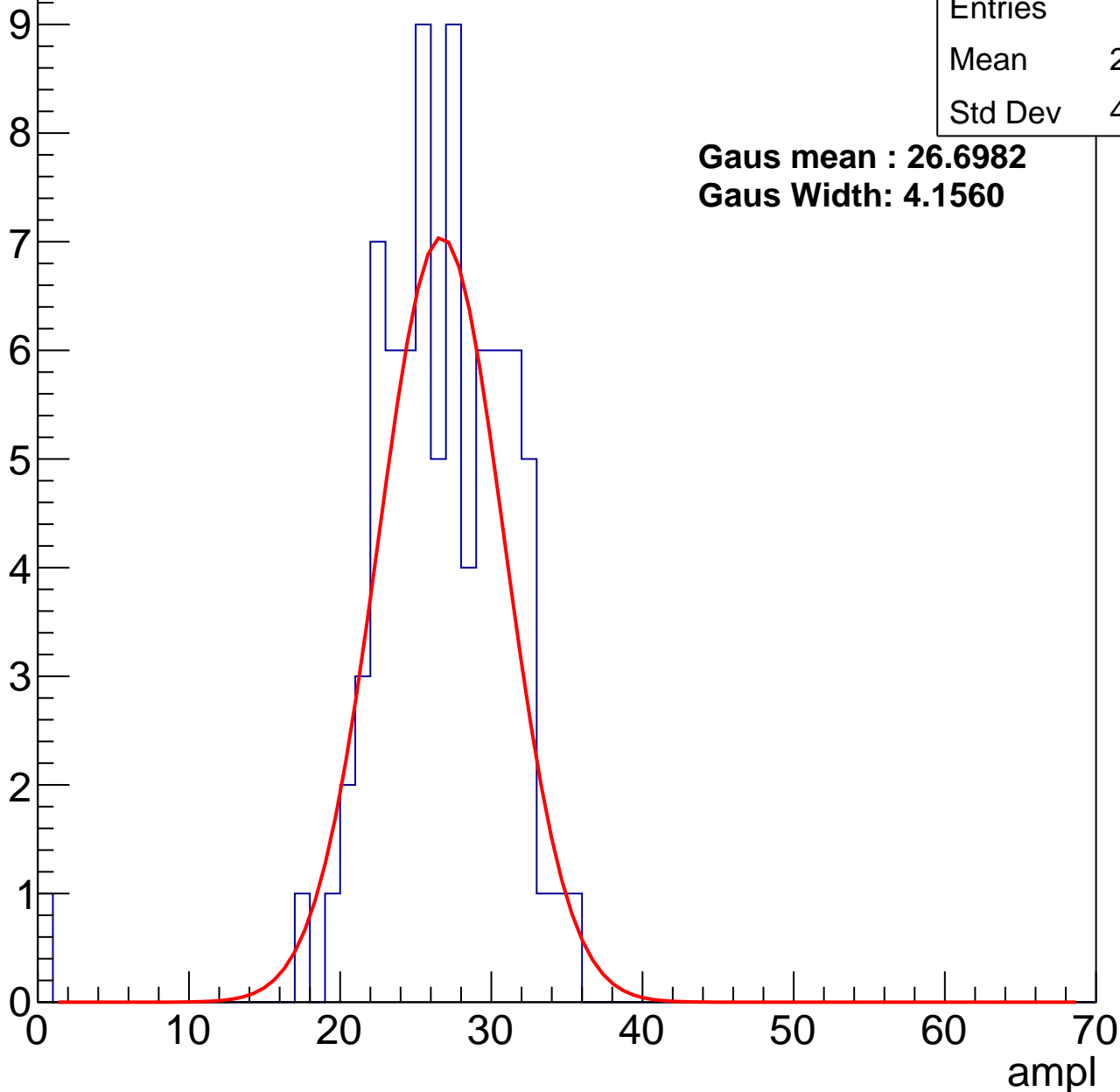
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	26.09
Std Dev	4.809

Gaus mean : 26.6982

Gaus Width: 4.1560



B1L102S, U19-ch40, adc1

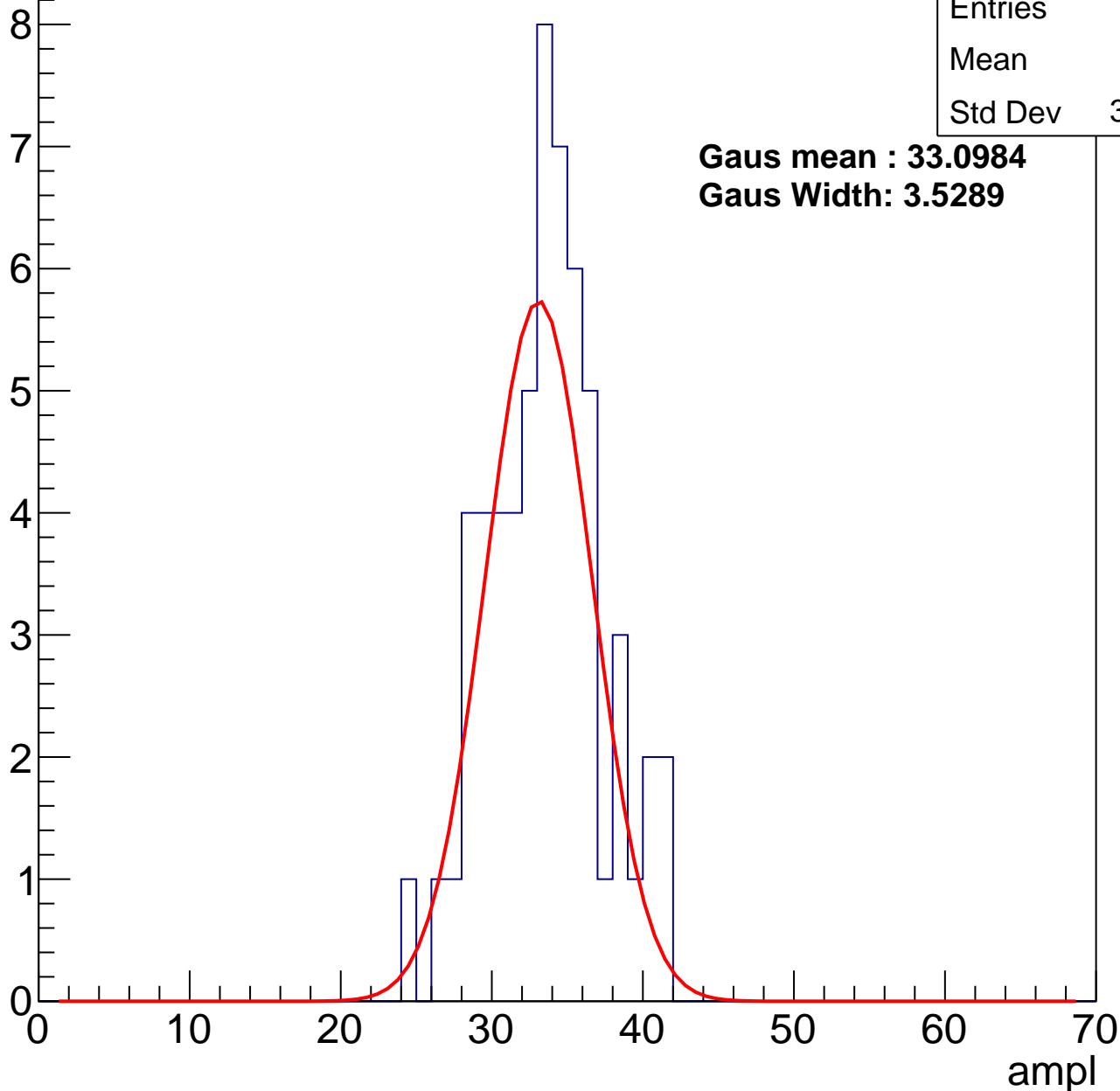
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	33.1
Std Dev	3.718

Gaus mean : 33.0984

Gaus Width: 3.5289



B1L102S, U19-ch40, adc2

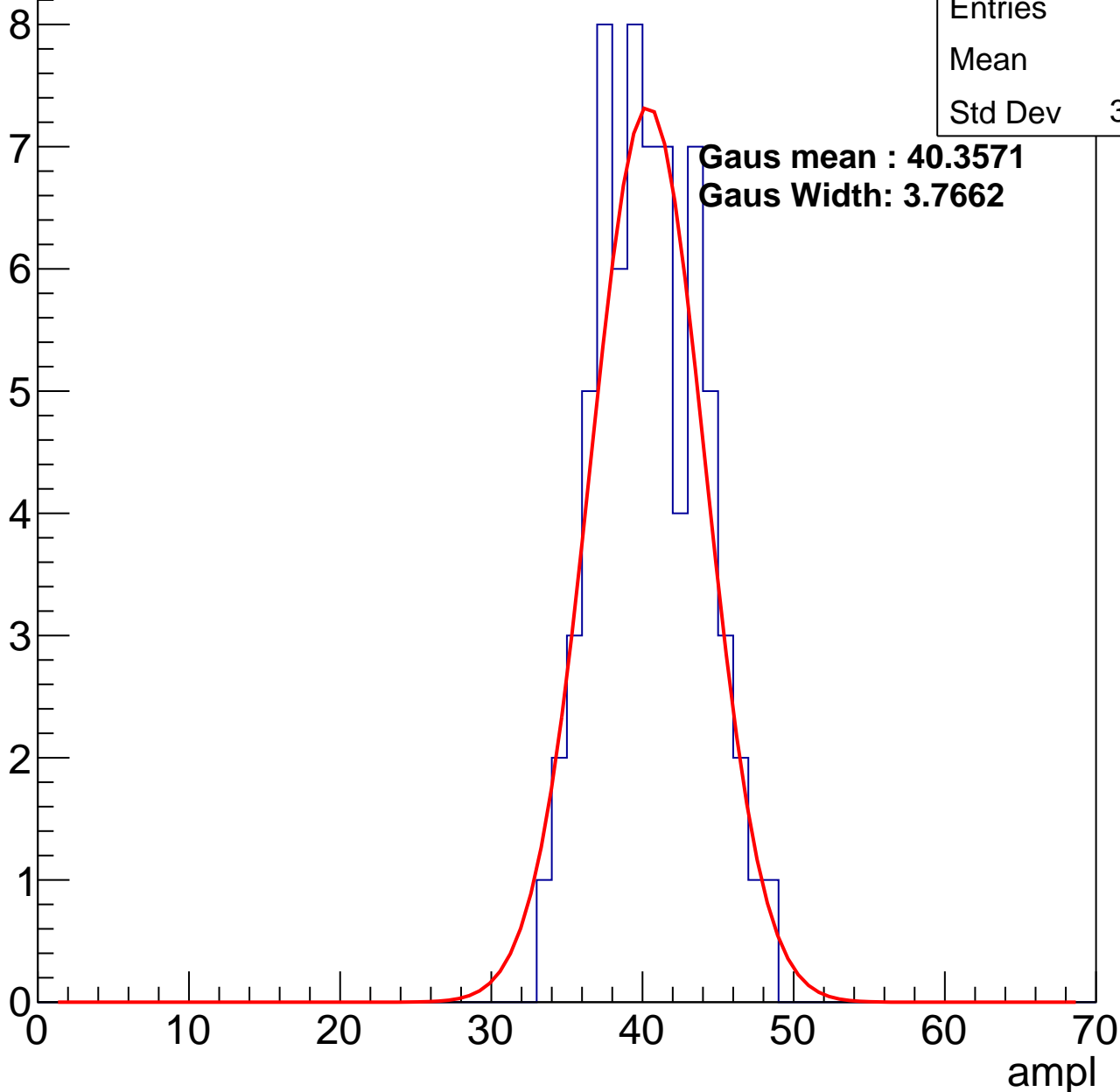
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40
Std Dev	3.393

Gaus mean : 40.3571

Gaus Width: 3.7662

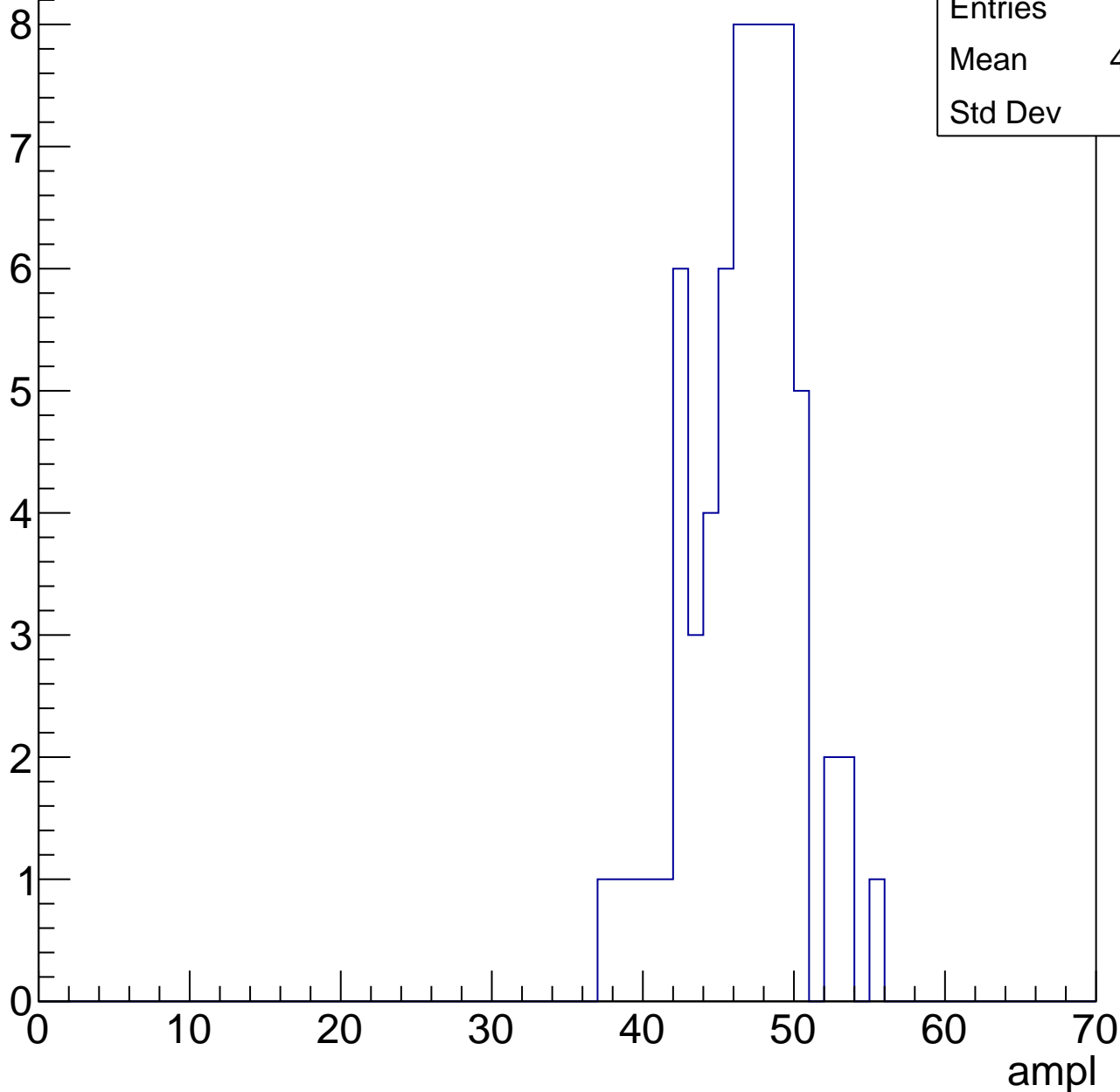


B1L102S, U19-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.32
Std Dev	3.56

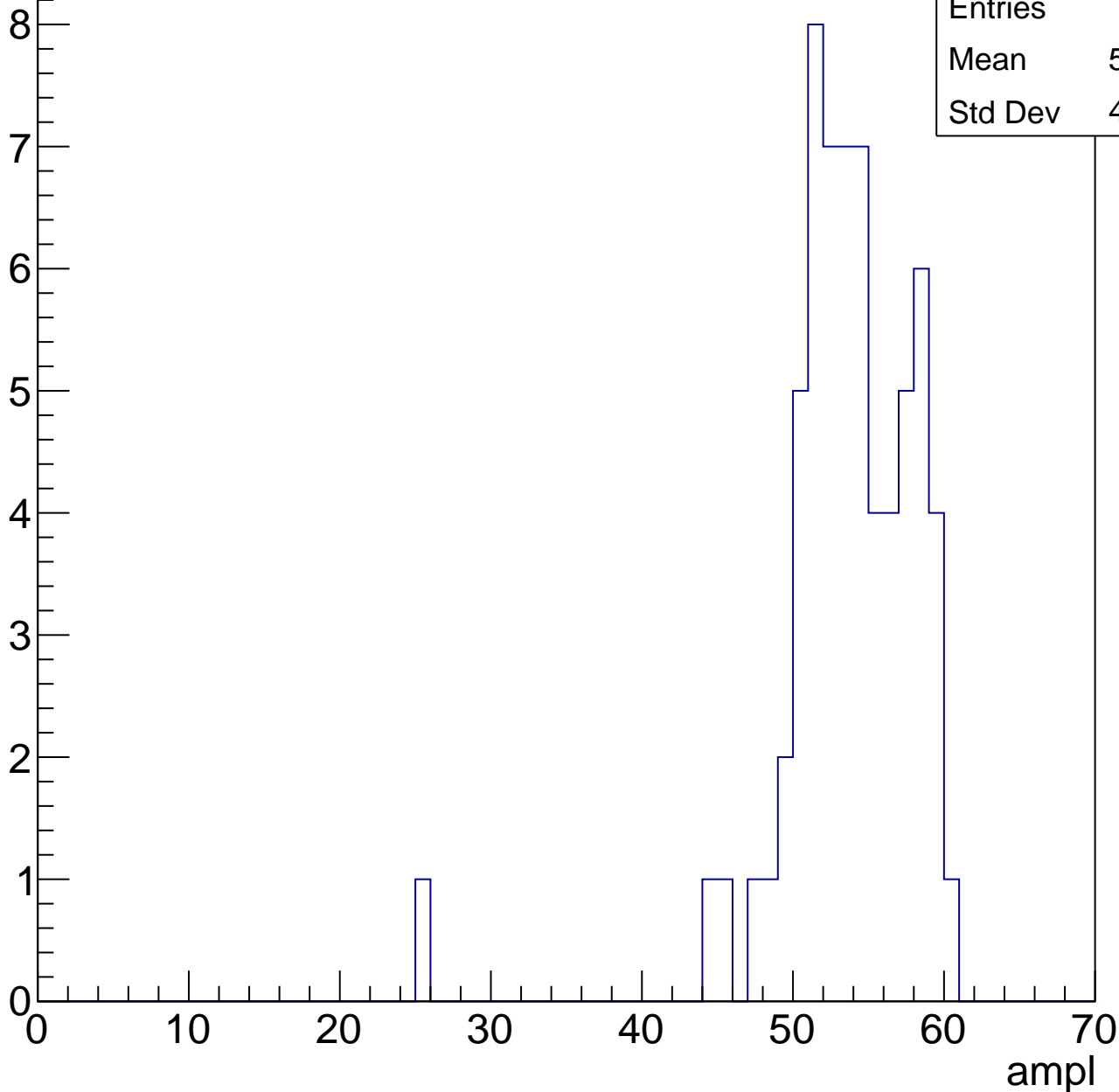


B1L102S, U19-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.09
Std Dev	4.942

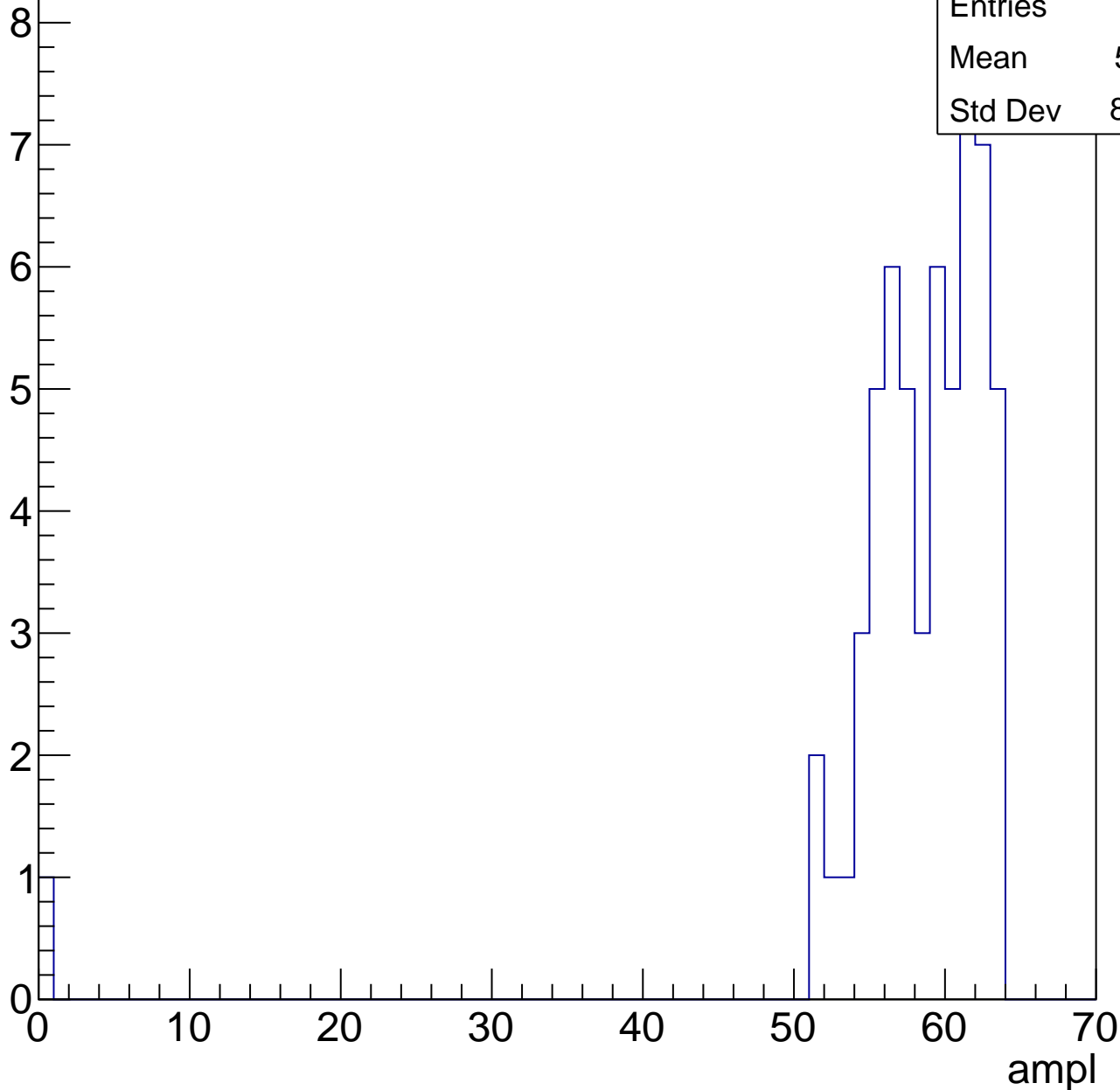


B1L102S, U19-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

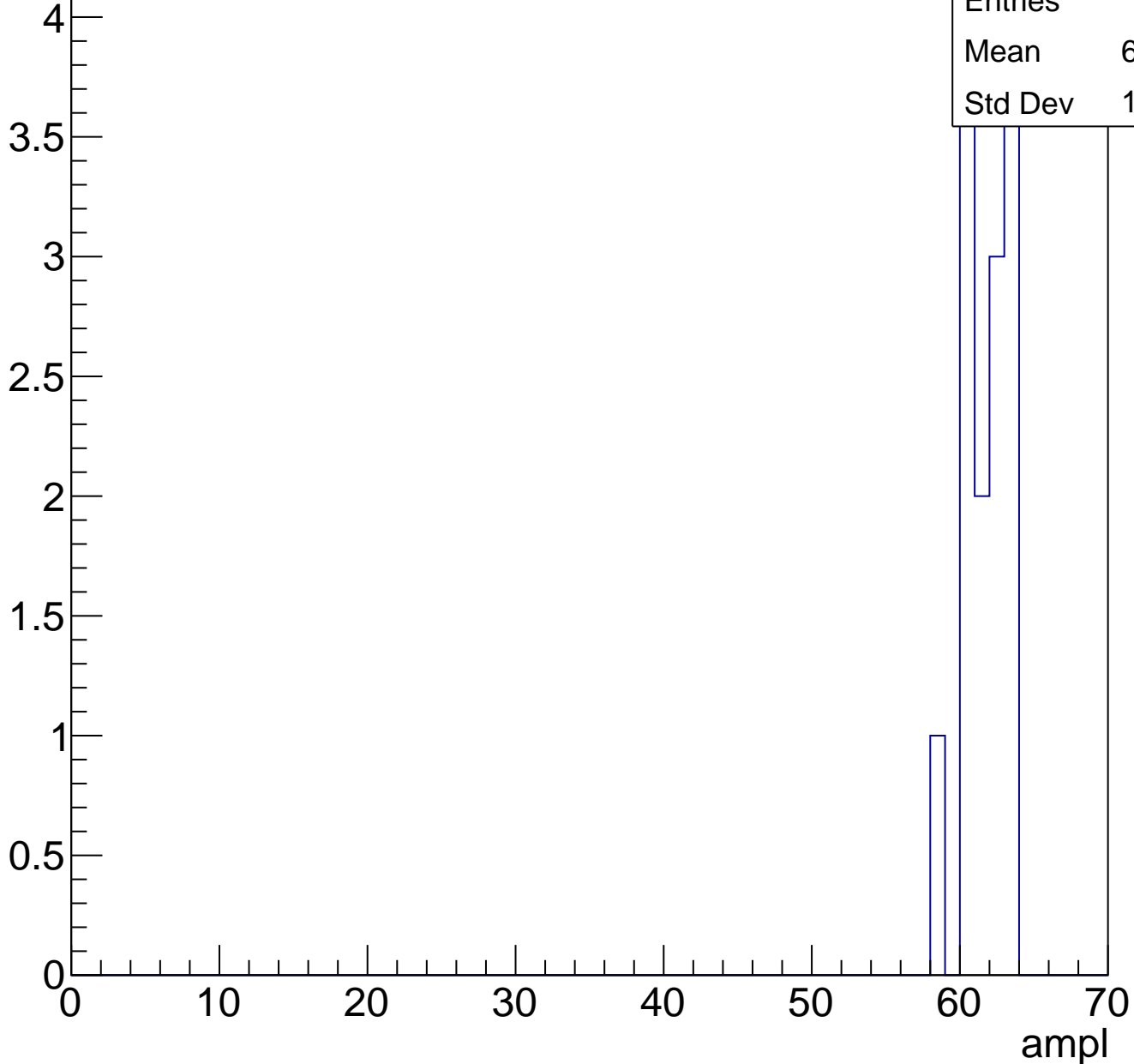
Entries	58
Mean	57.41
Std Dev	8.263



B1L102S, U19-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch41, adc0

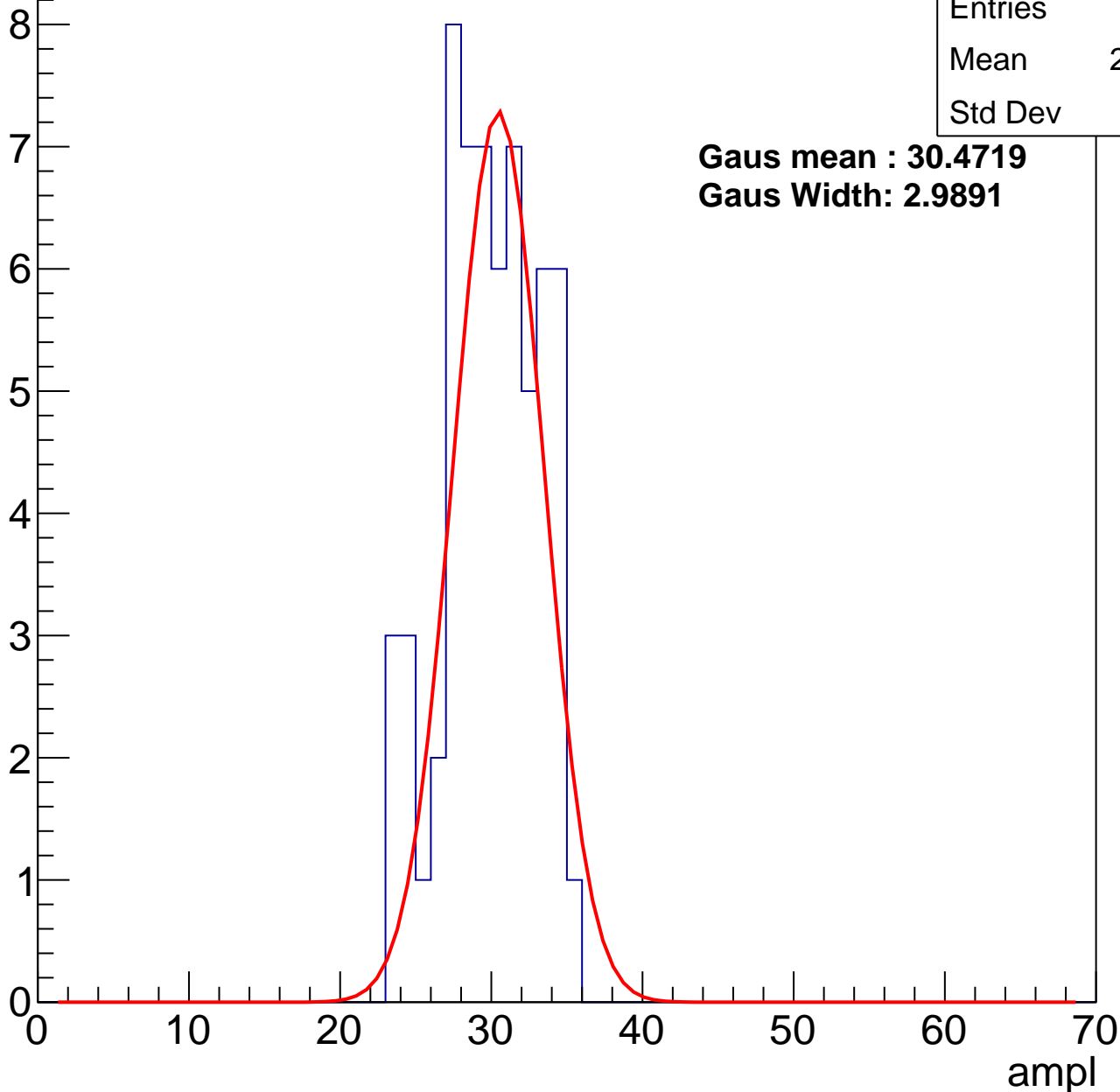
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.47
Std Dev	3.12

Gaus mean : 30.4719

Gaus Width: 2.9891



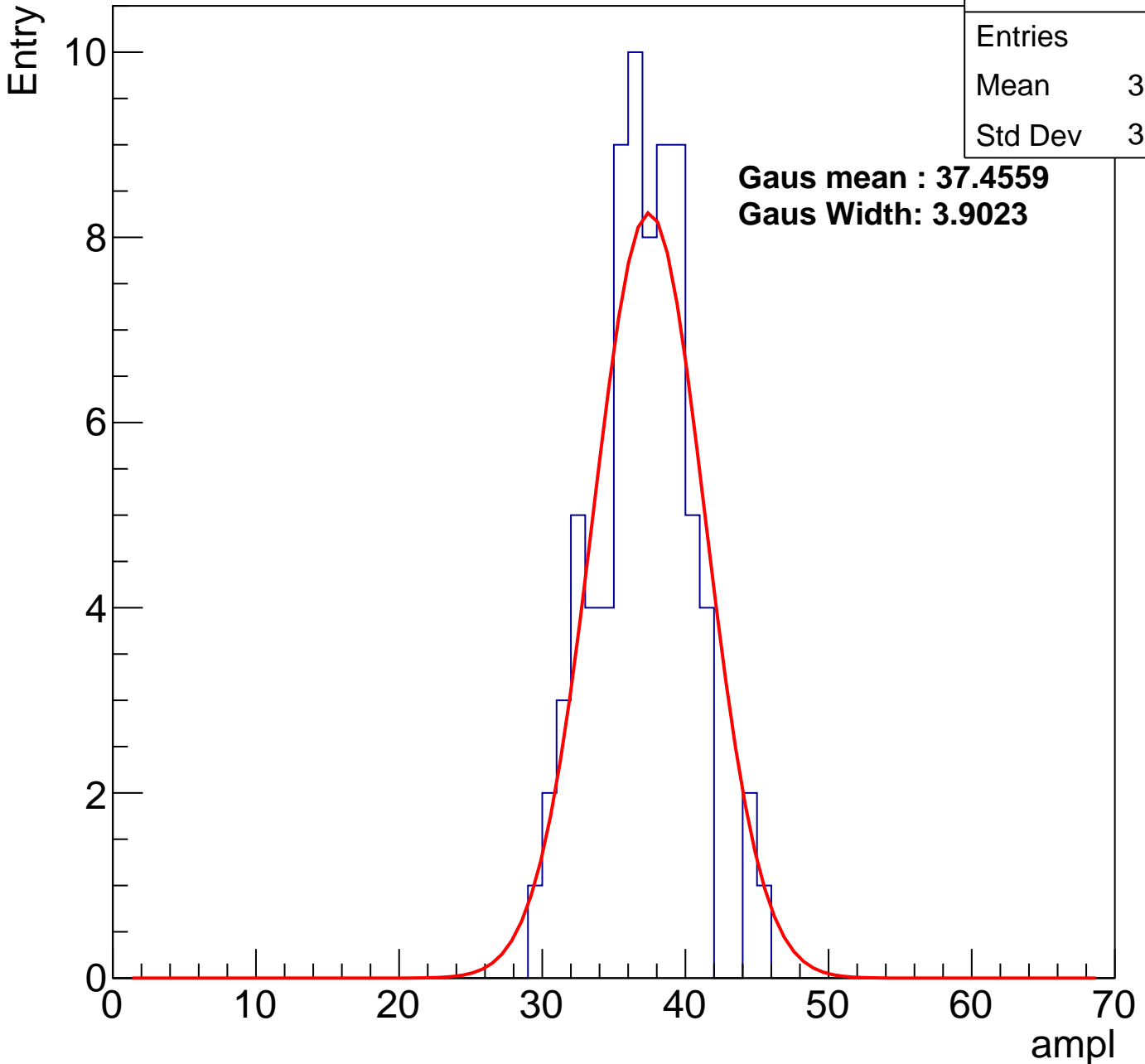
B1L102S, U19-ch41, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	36.46
Std Dev	3.314

Gaus mean : 37.4559

Gaus Width: 3.9023



B1L102S, U19-ch41, adc2

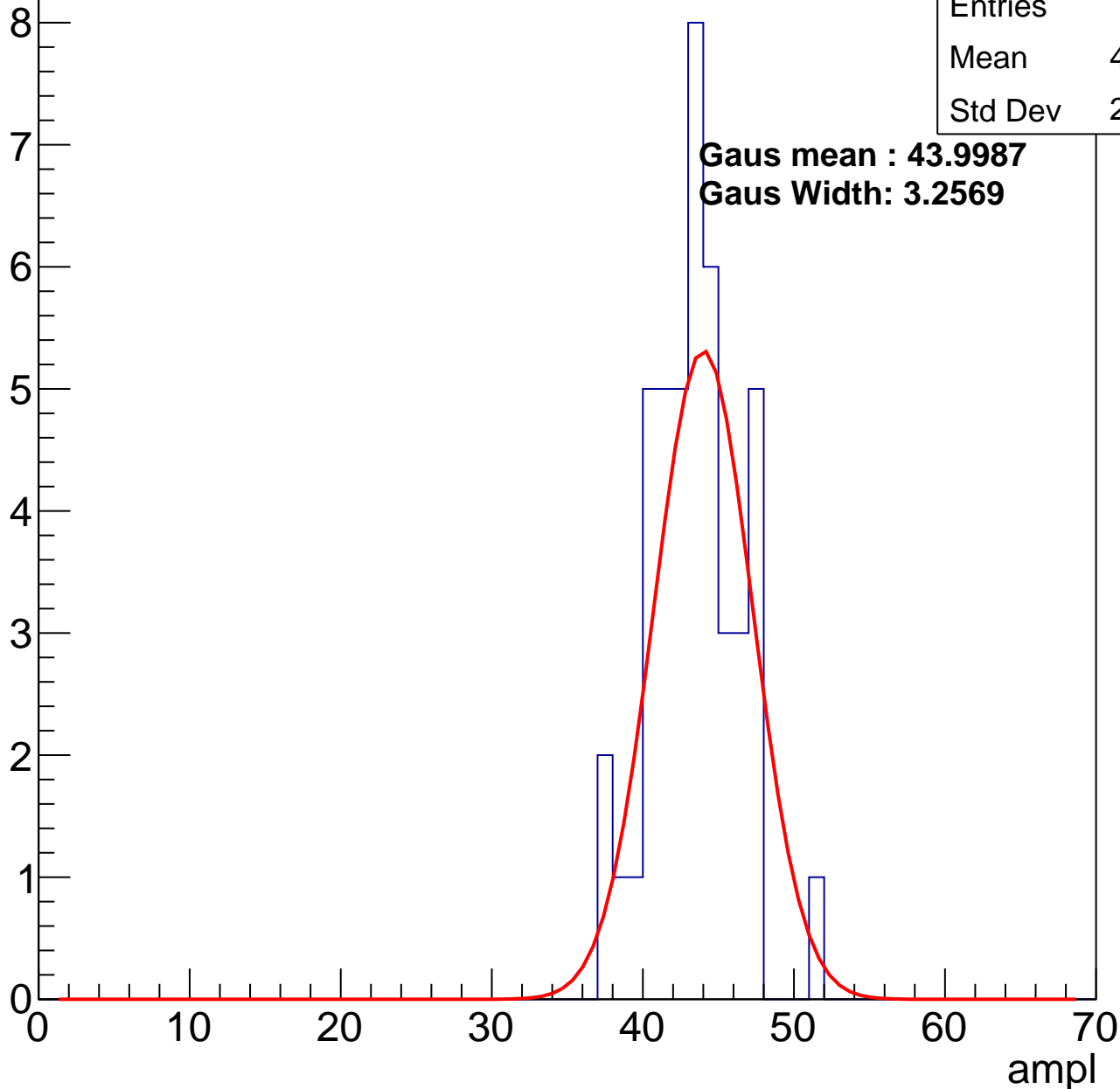
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	42.96
Std Dev	2.875

Gaus mean : 43.9987

Gaus Width: 3.2569



B1L102S, U19-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

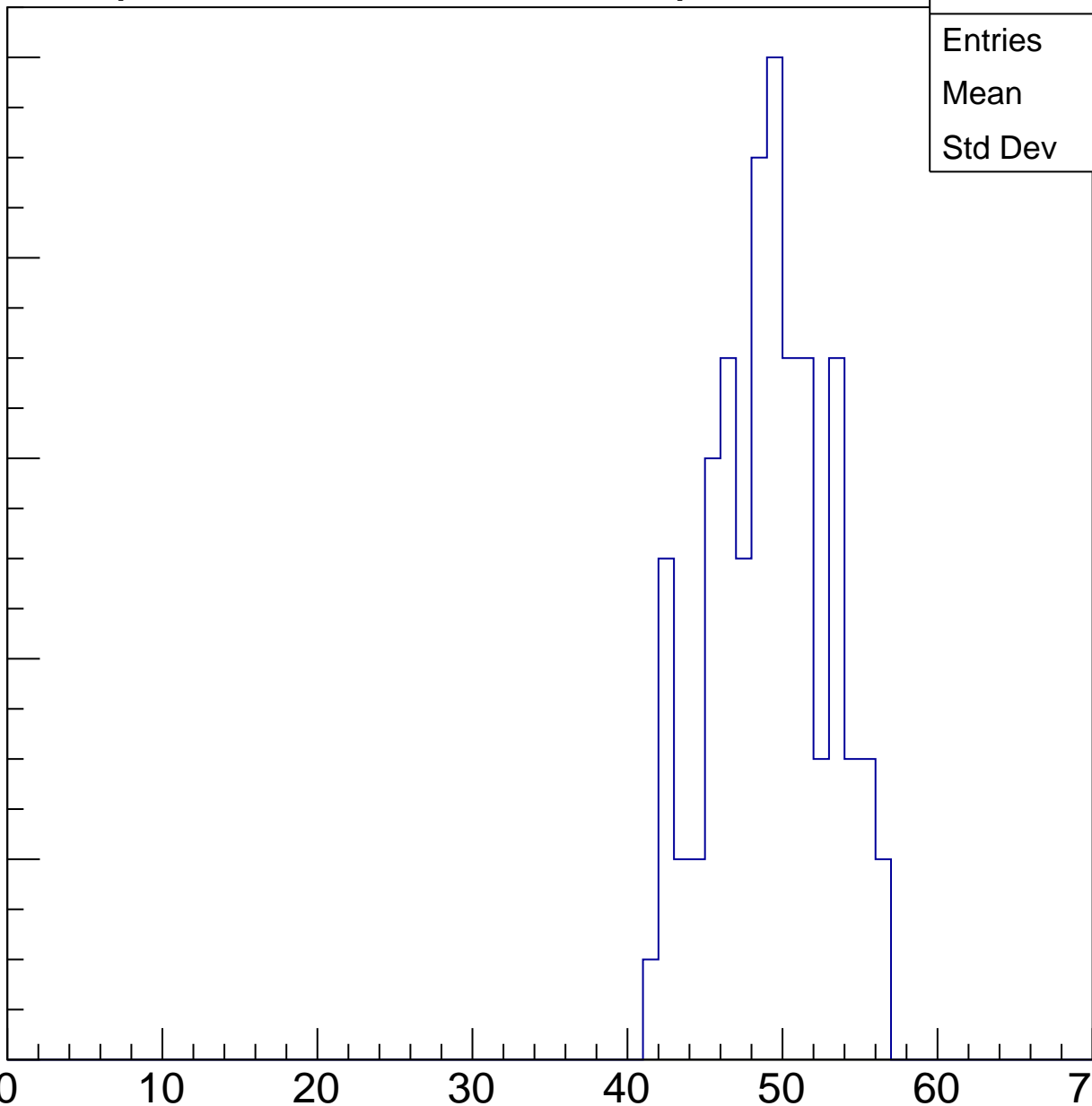
Entries	79
Mean	48.7
Std Dev	3.675

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

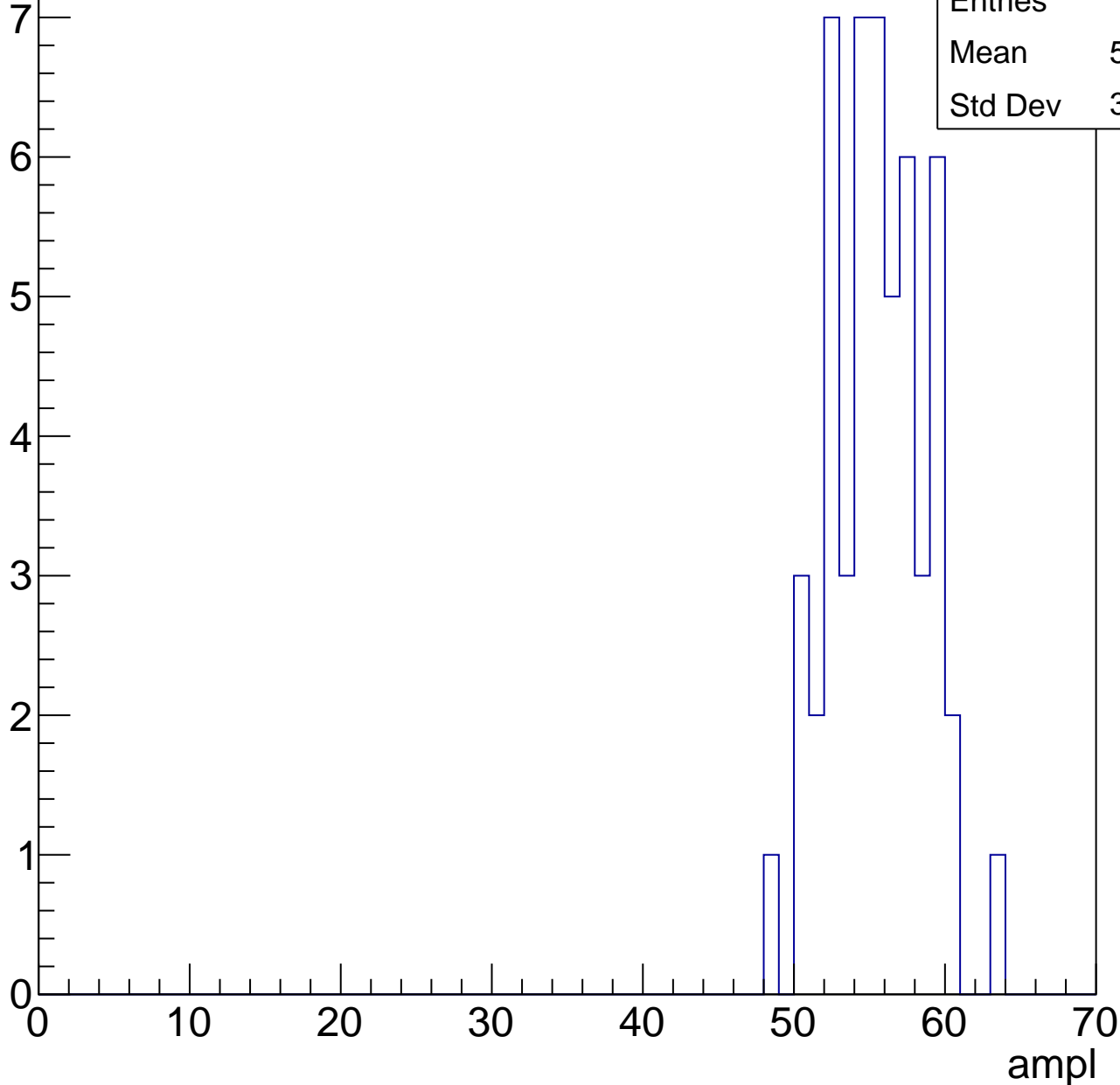


B1L102S, U19-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.08
Std Dev	3.083

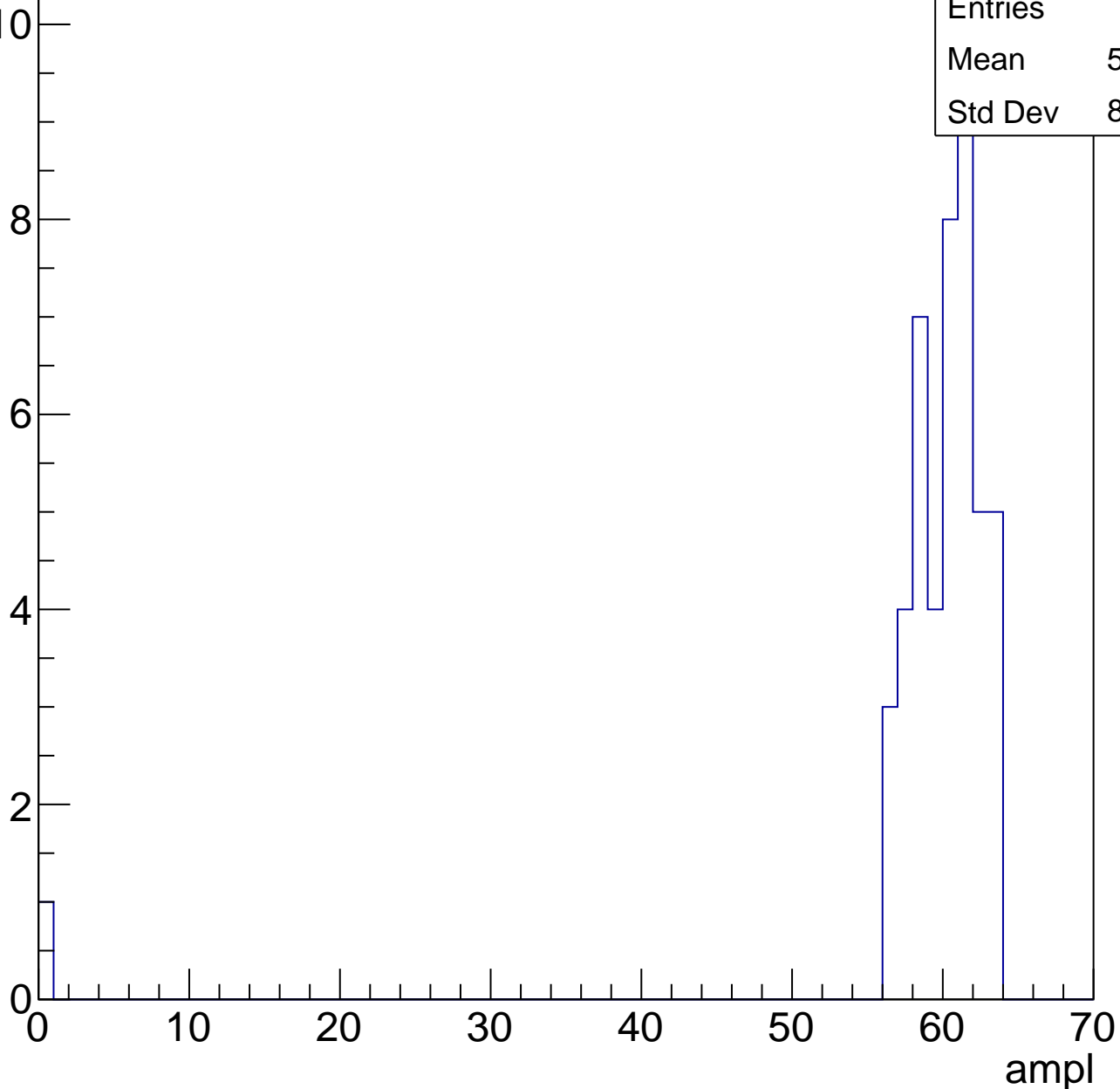


B1L102S, U19-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.57
Std Dev	8.867

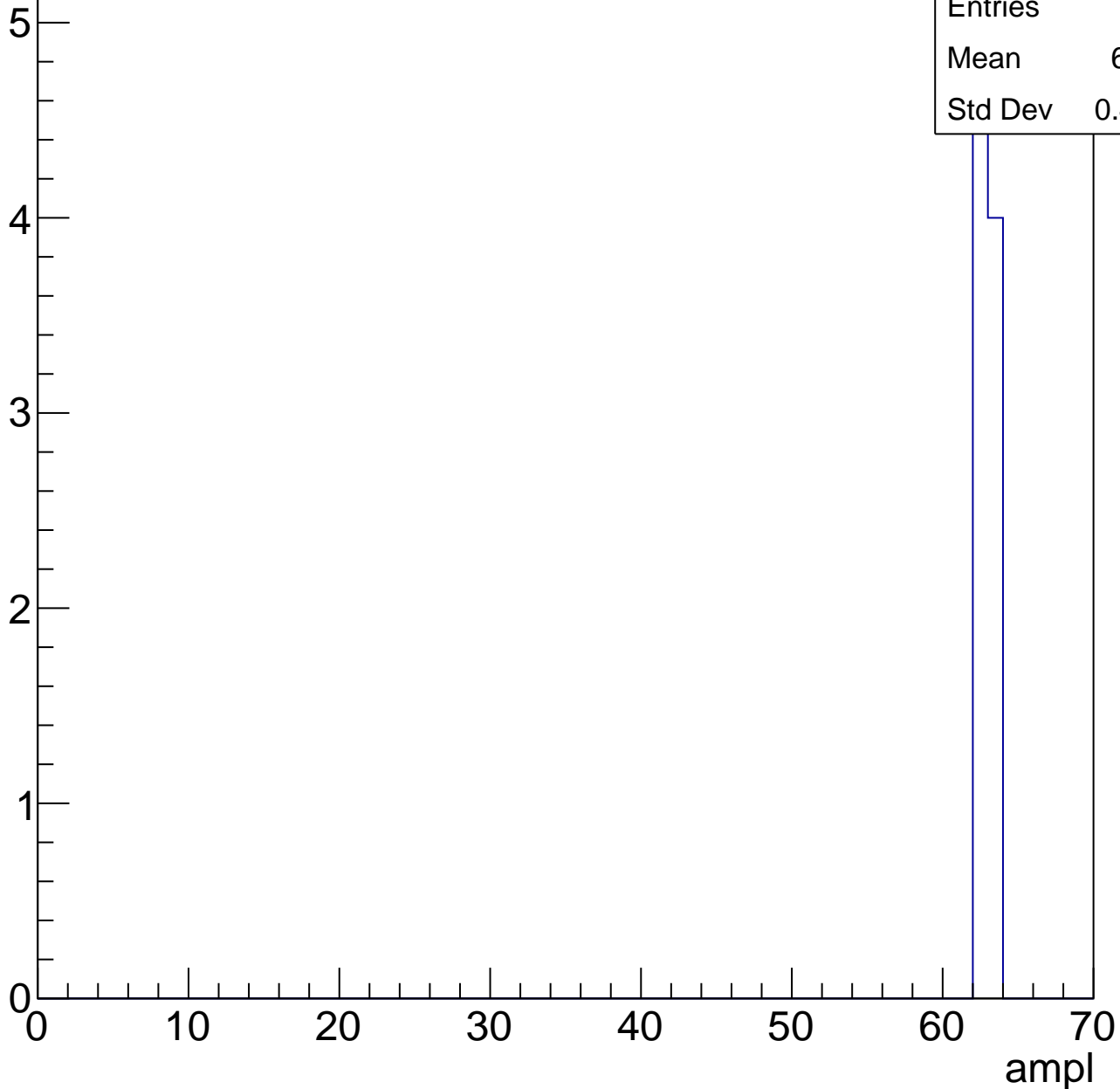


B1L102S, U19-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

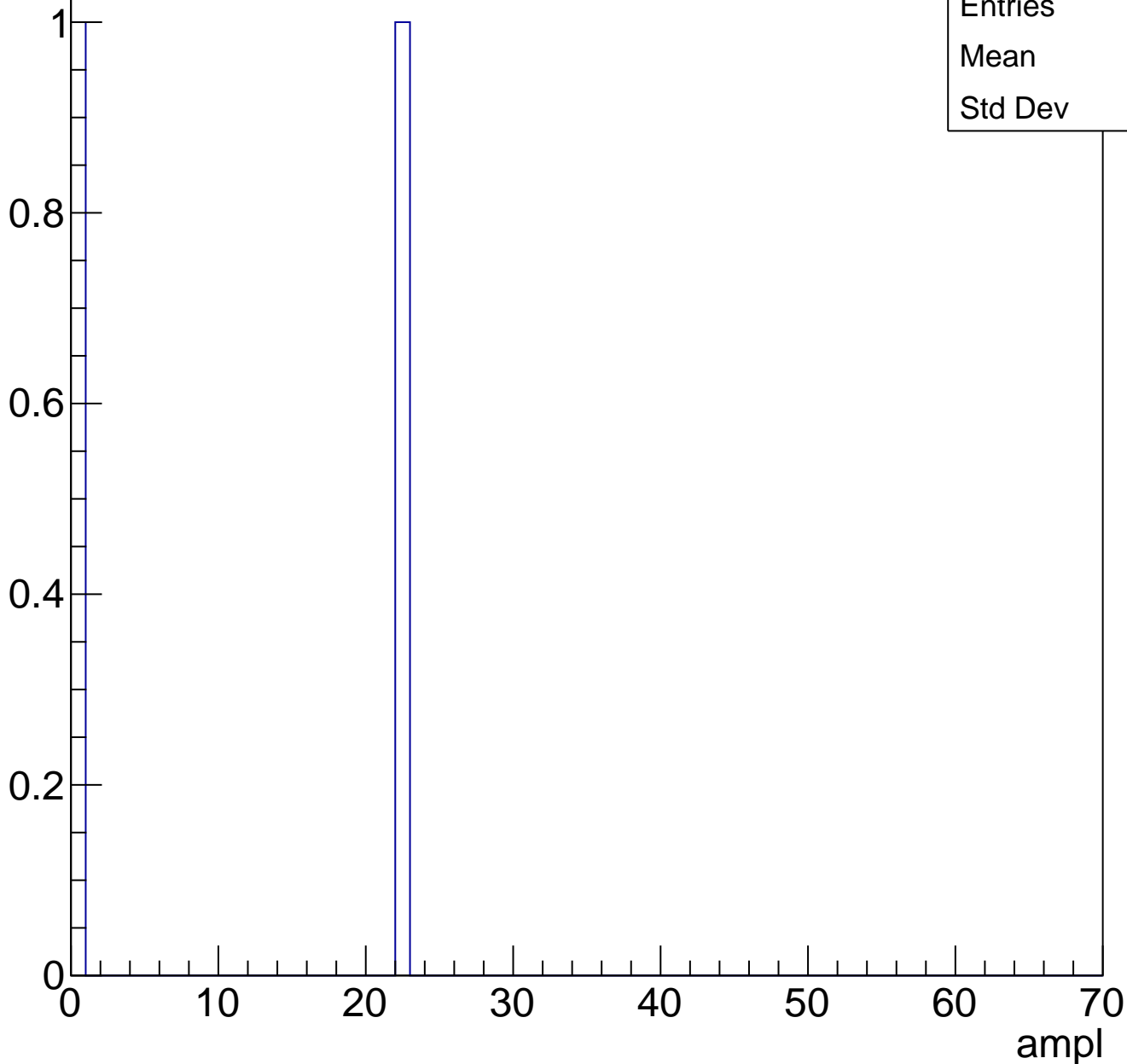
Entries	9
Mean	62.44
Std Dev	0.4969



B1L102S, U19-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch42, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	28.4
Std Dev	3.287

Gaus mean : 28.9648

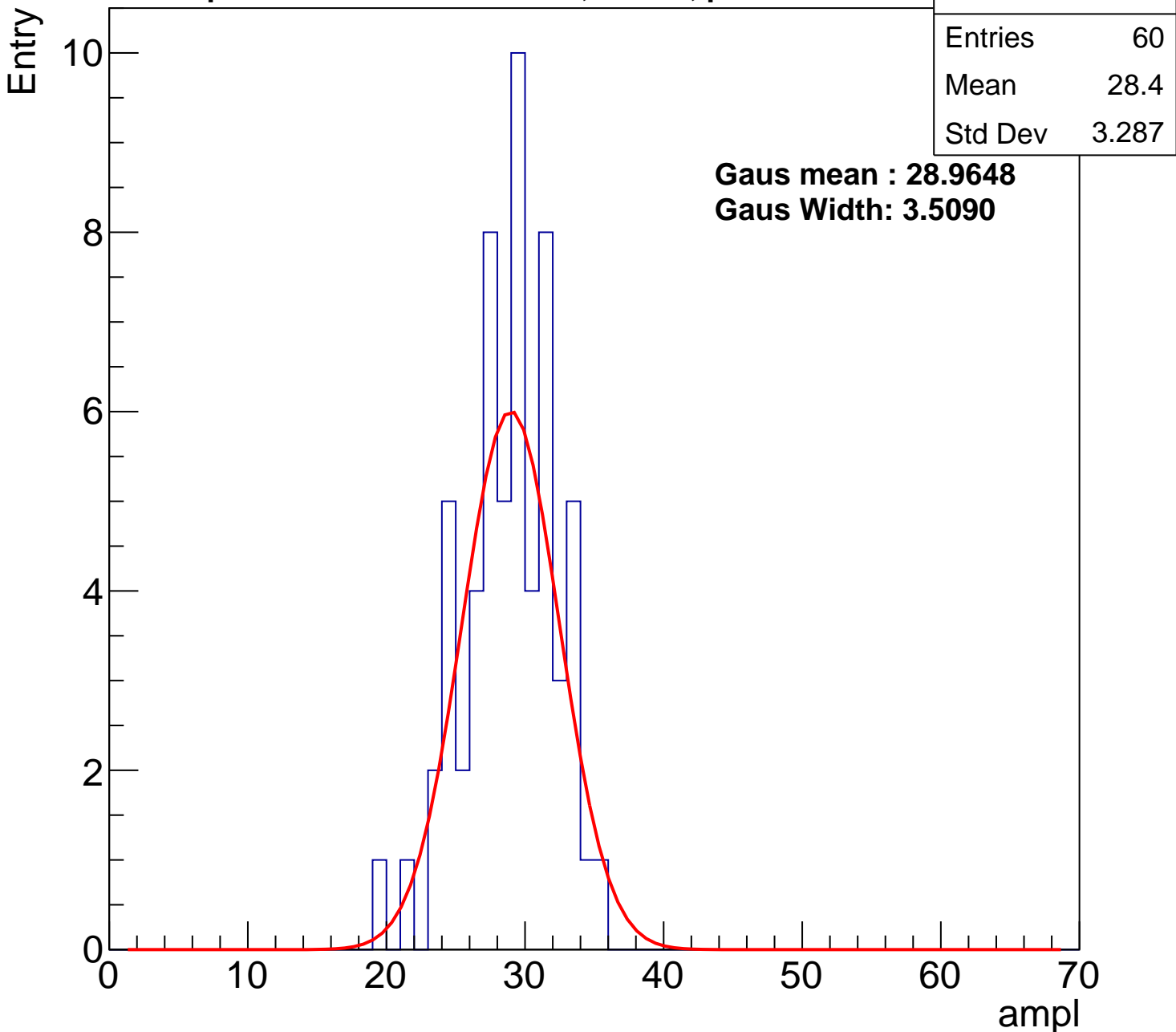
Gaus Width: 3.5090

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



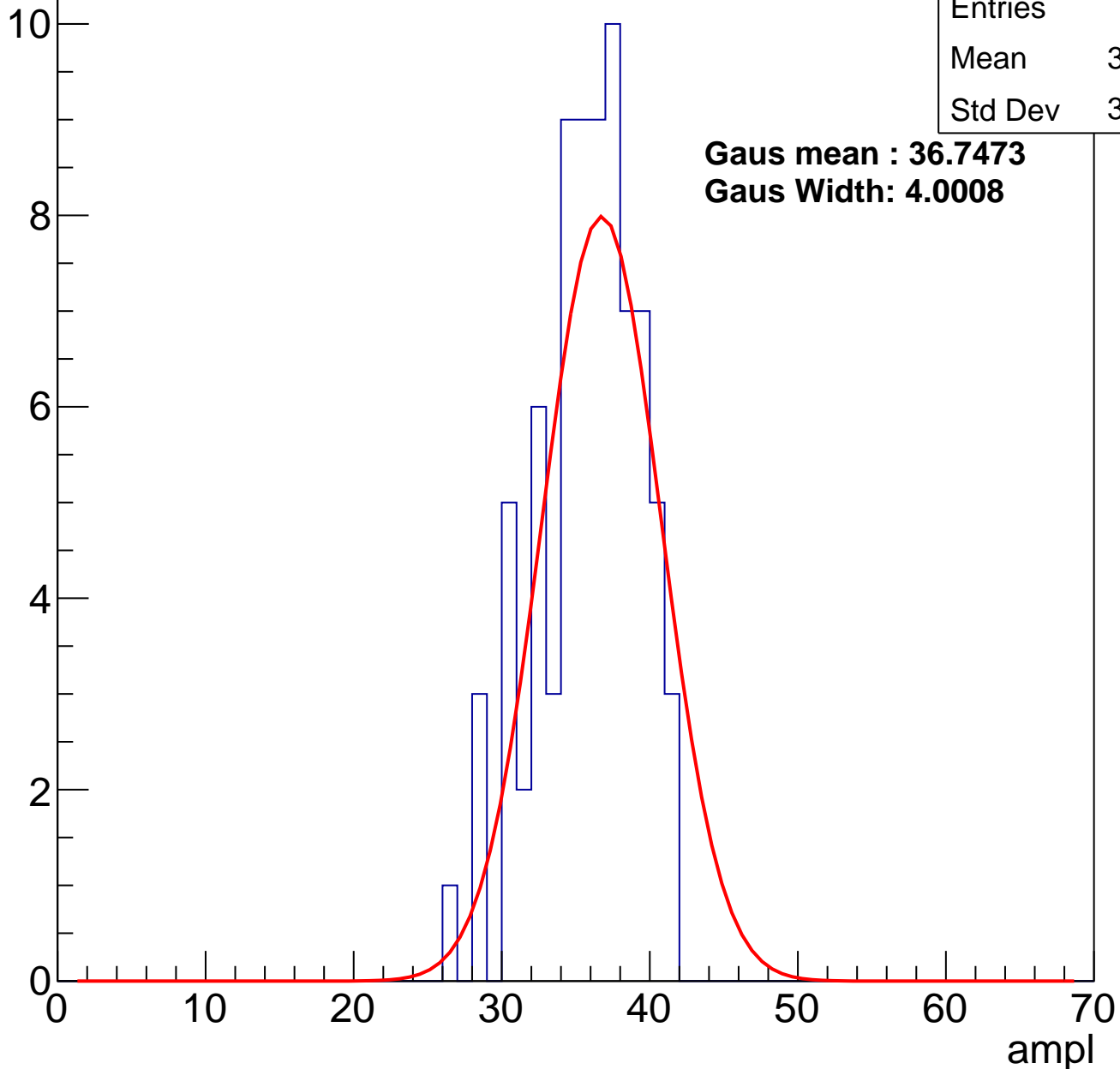
B1L102S, U19-ch42, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	35.32
Std Dev	3.392

Gaus mean : 36.7473
Gaus Width: 4.0008

Entry



B1L102S, U19-ch42, adc2

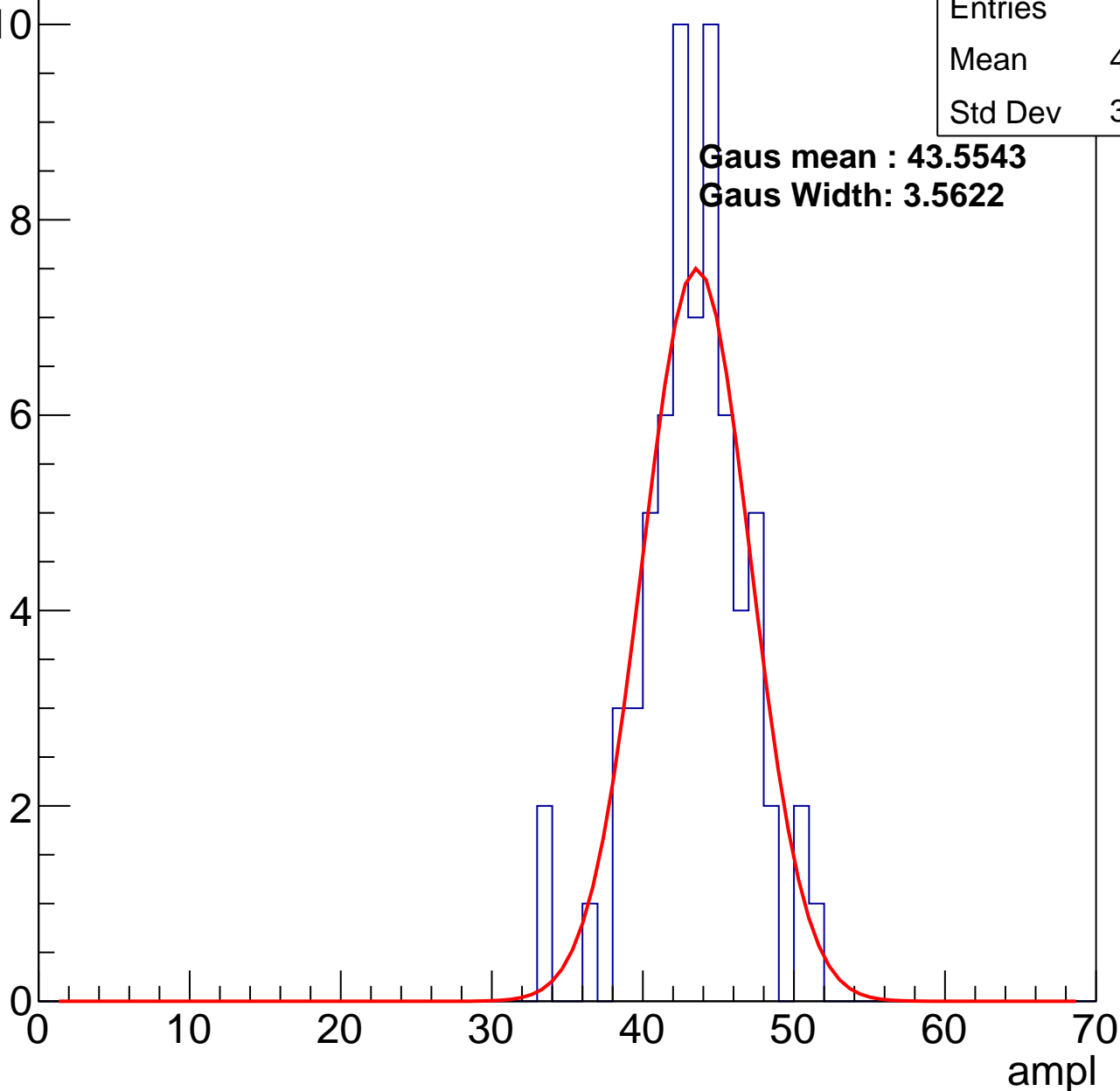
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.93
Std Dev	3.483

Gaus mean : 43.5543

Gaus Width: 3.5622

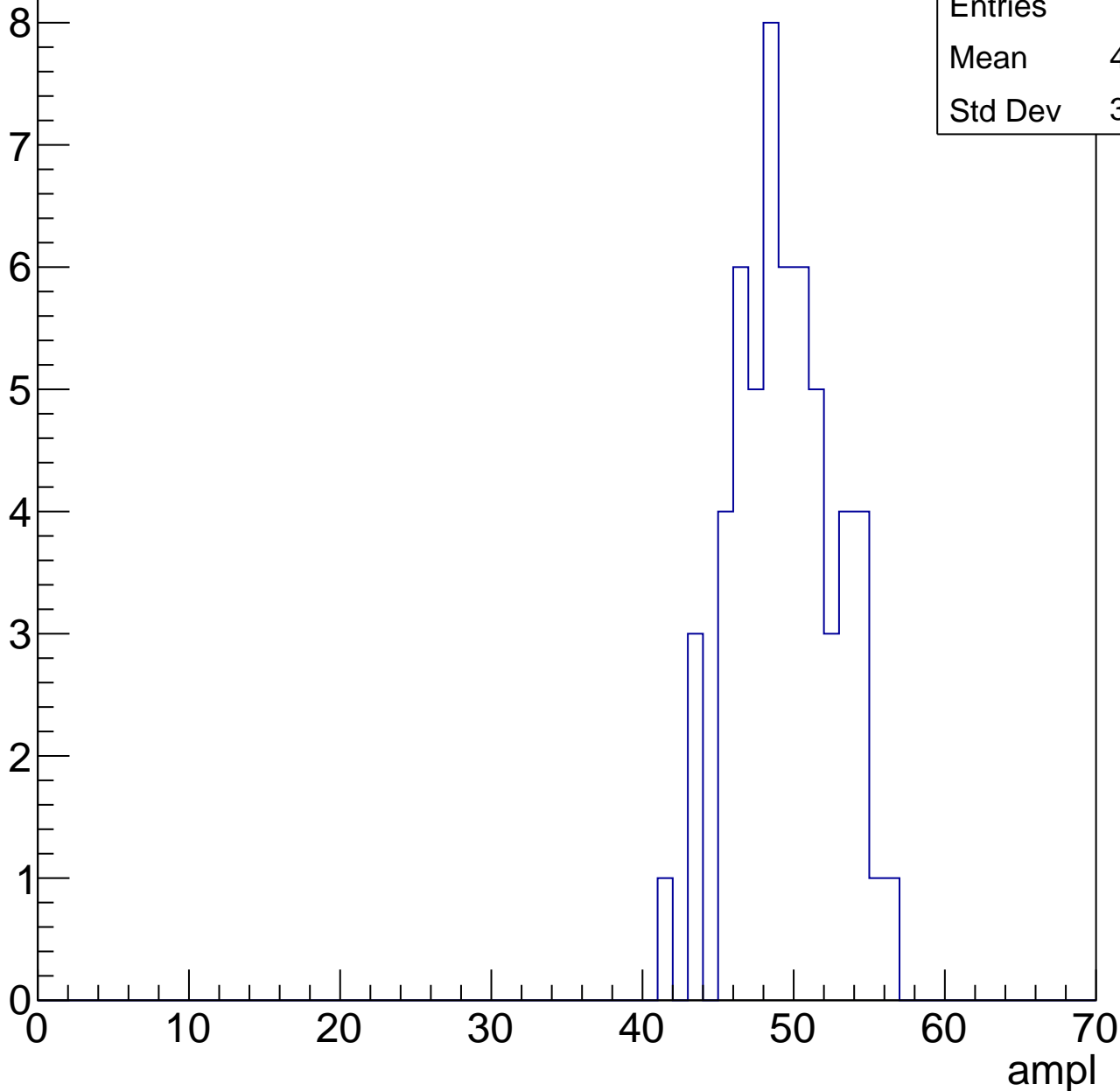


B1L102S, U19-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.93
Std Dev	3.297

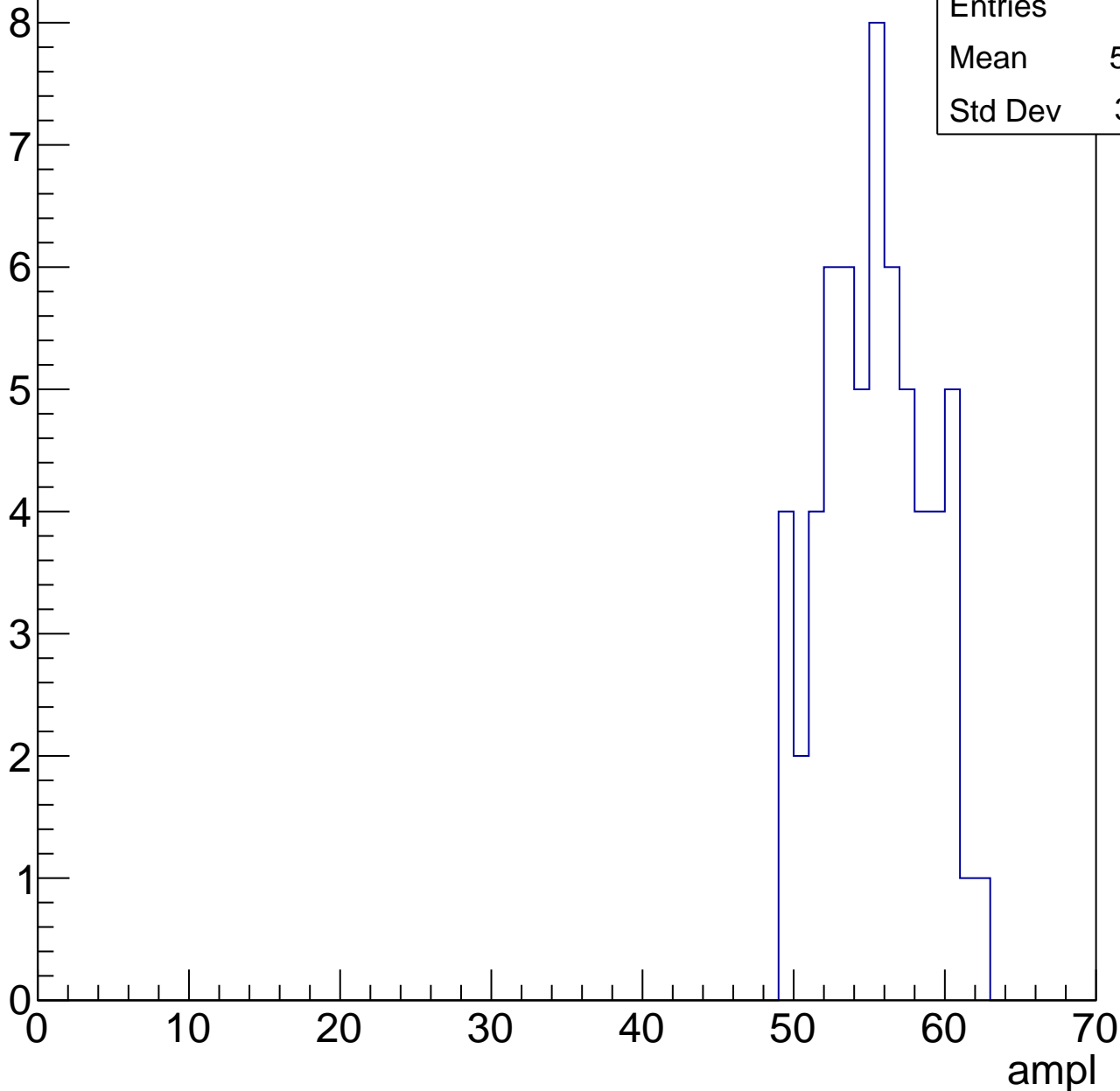


B1L102S, U19-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.95
Std Dev	3.331

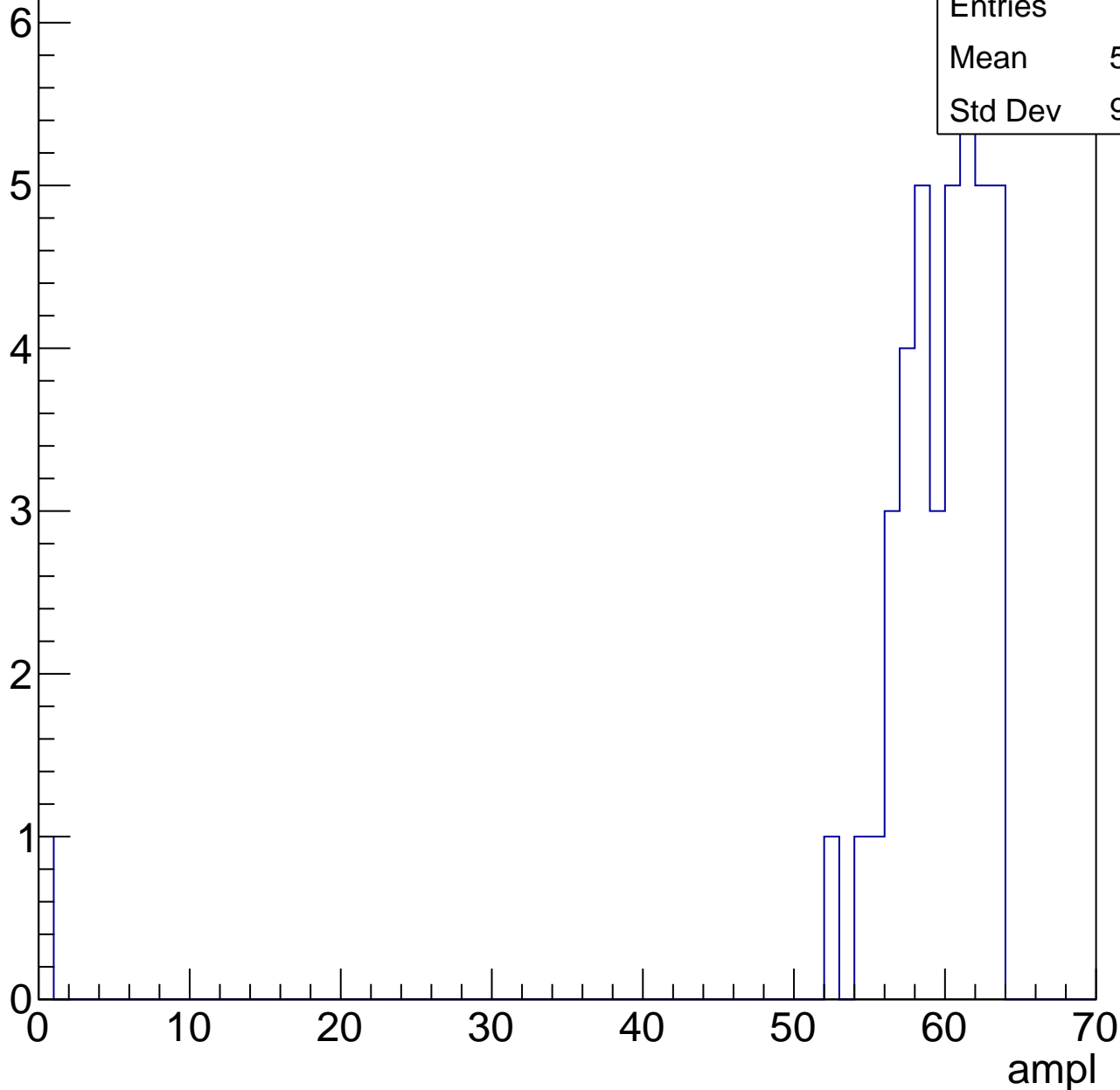


B1L102S, U19-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	57.88
Std Dev	9.647

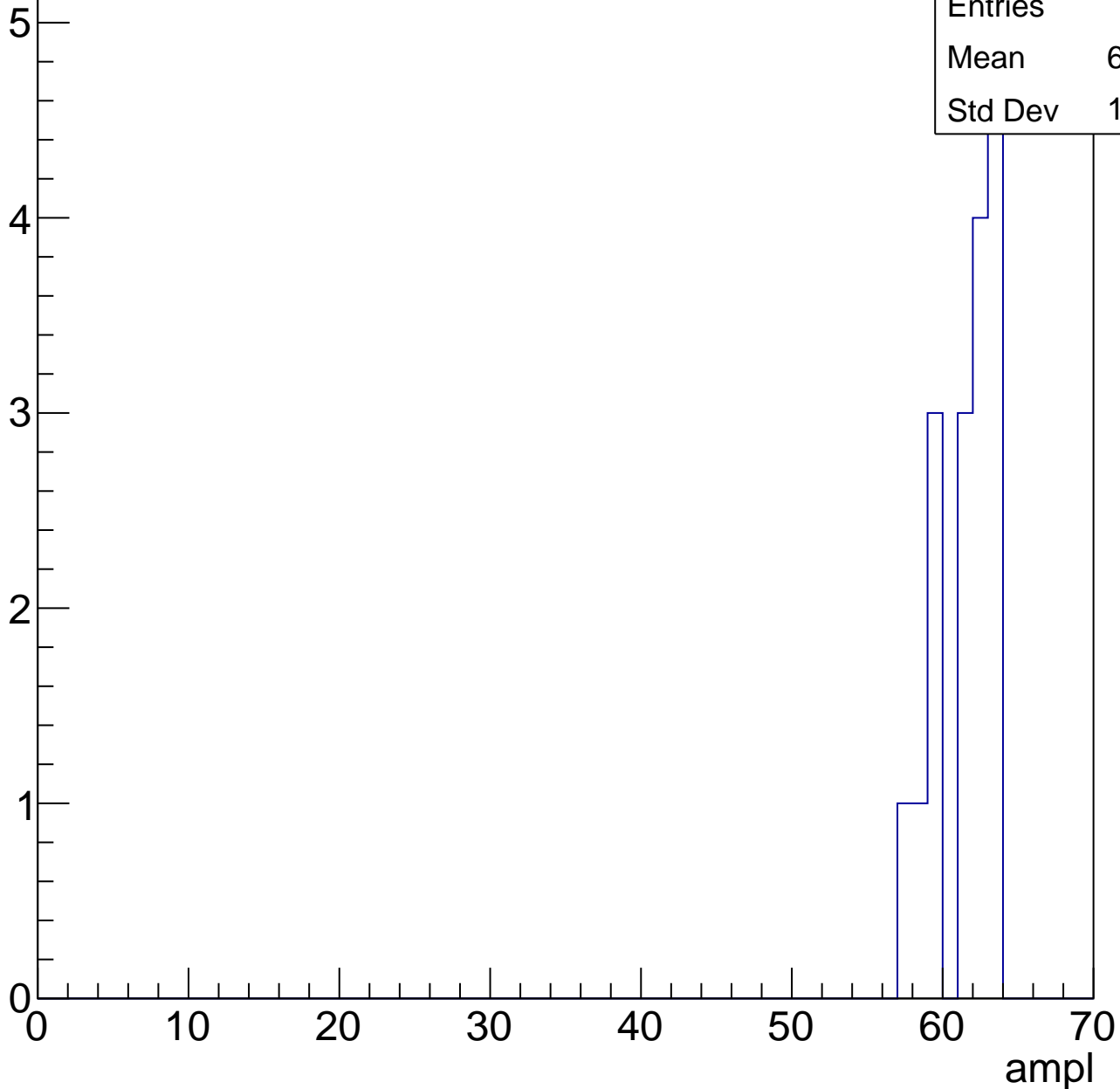


B1L102S, U19-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.06
Std Dev	1.893



B1L102S, U19-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch43, adc0

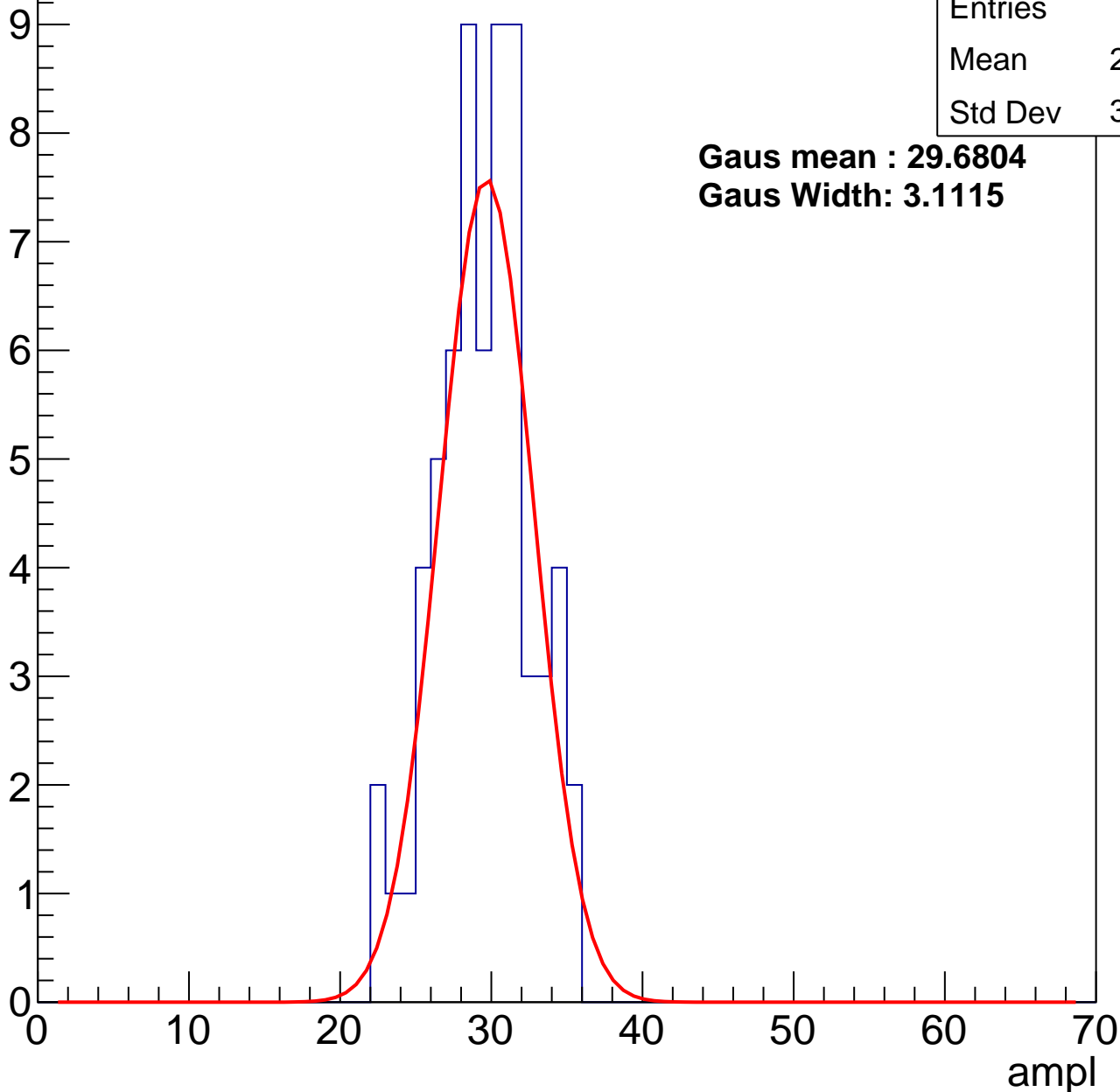
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.05
Std Dev	3.044

Gaus mean : 29.6804

Gaus Width: 3.1115



B1L102S, U19-ch43, adc1

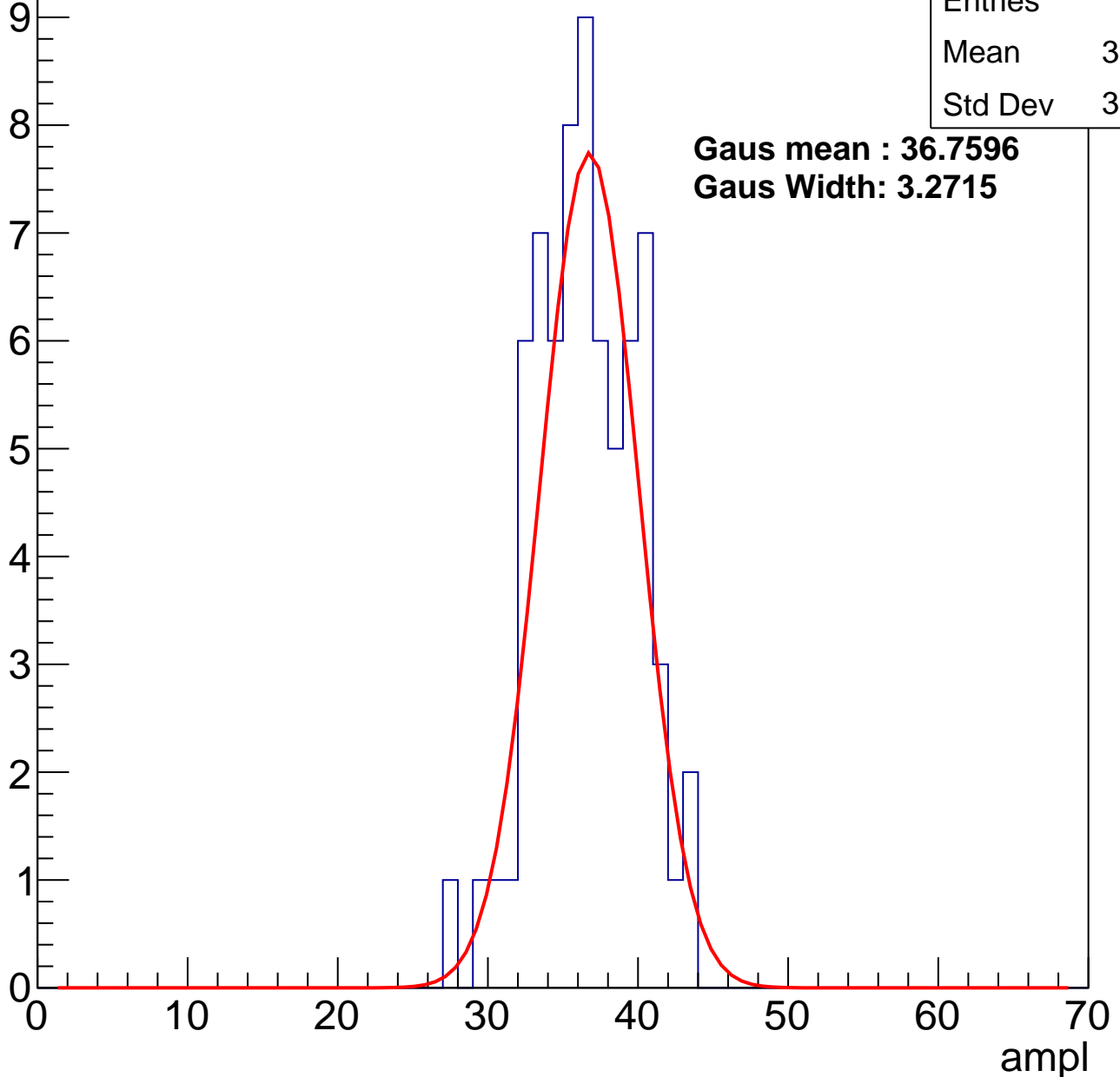
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.07
Std Dev	3.344

Gaus mean : 36.7596

Gaus Width: 3.2715



B1L102S, U19-ch43, adc2

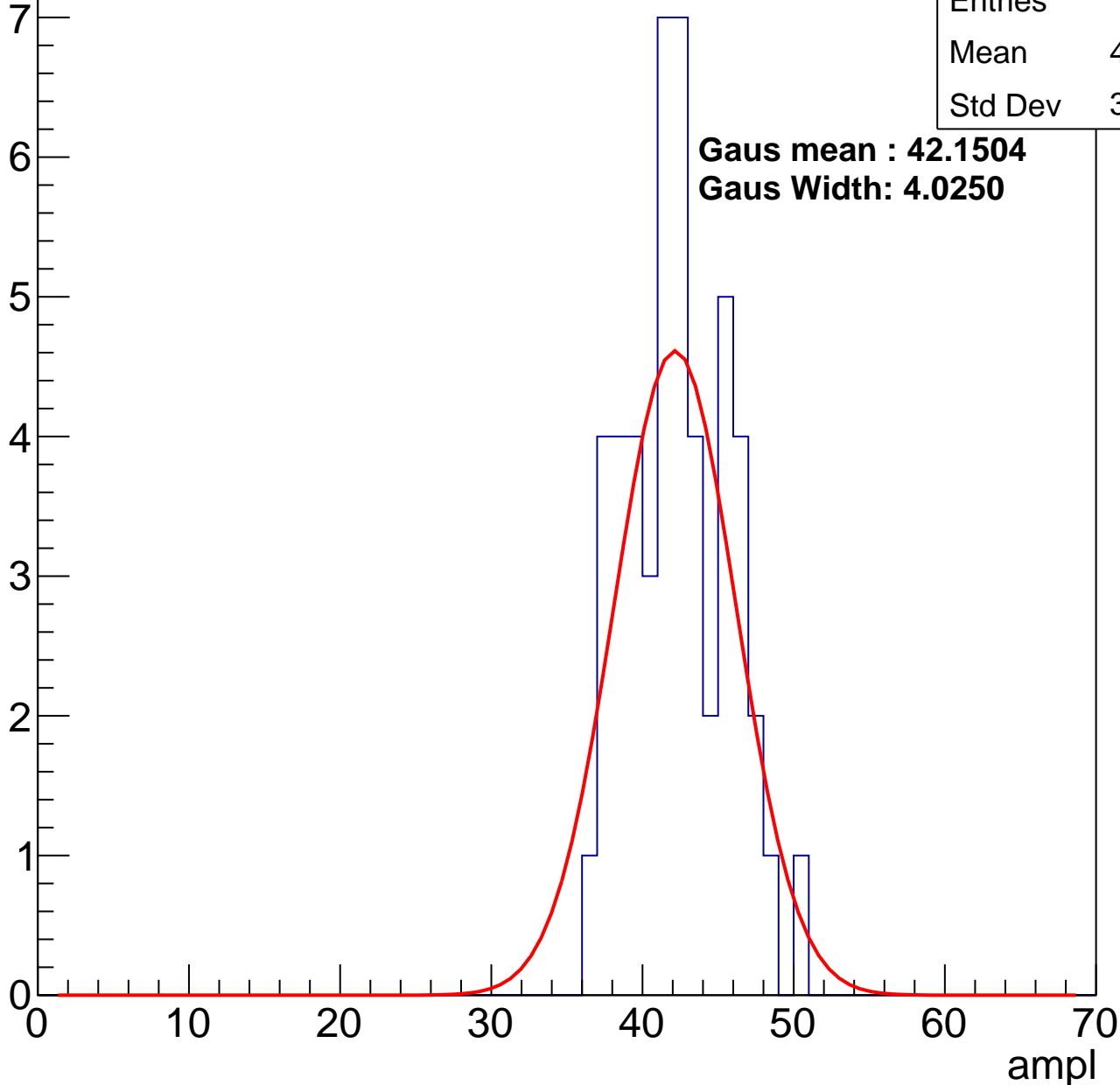
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.92
Std Dev	3.275

Gaus mean : 42.1504

Gaus Width: 4.0250



B1L102S, U19-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

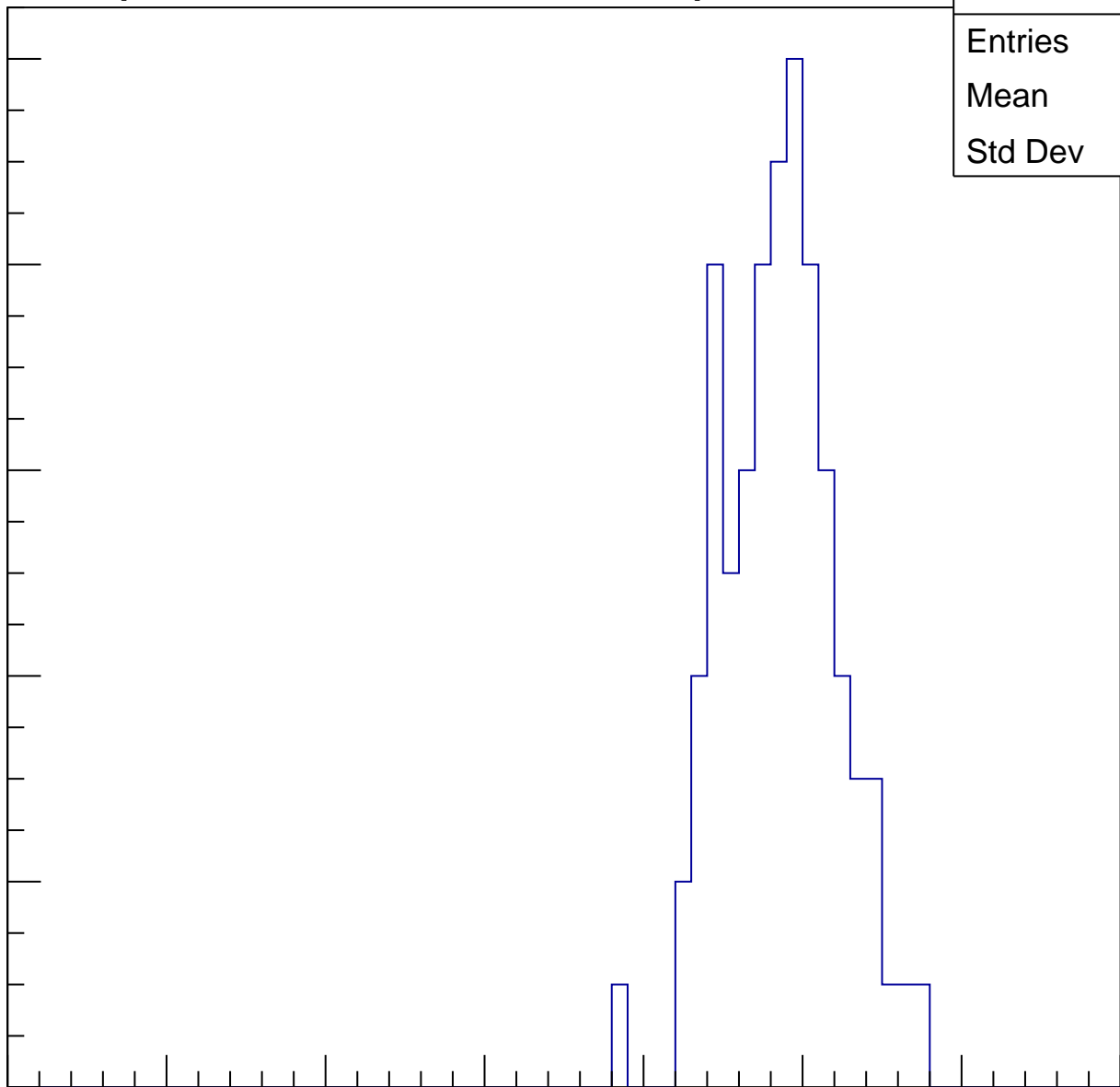
Entries	80
Mean	48.1
Std Dev	3.559

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

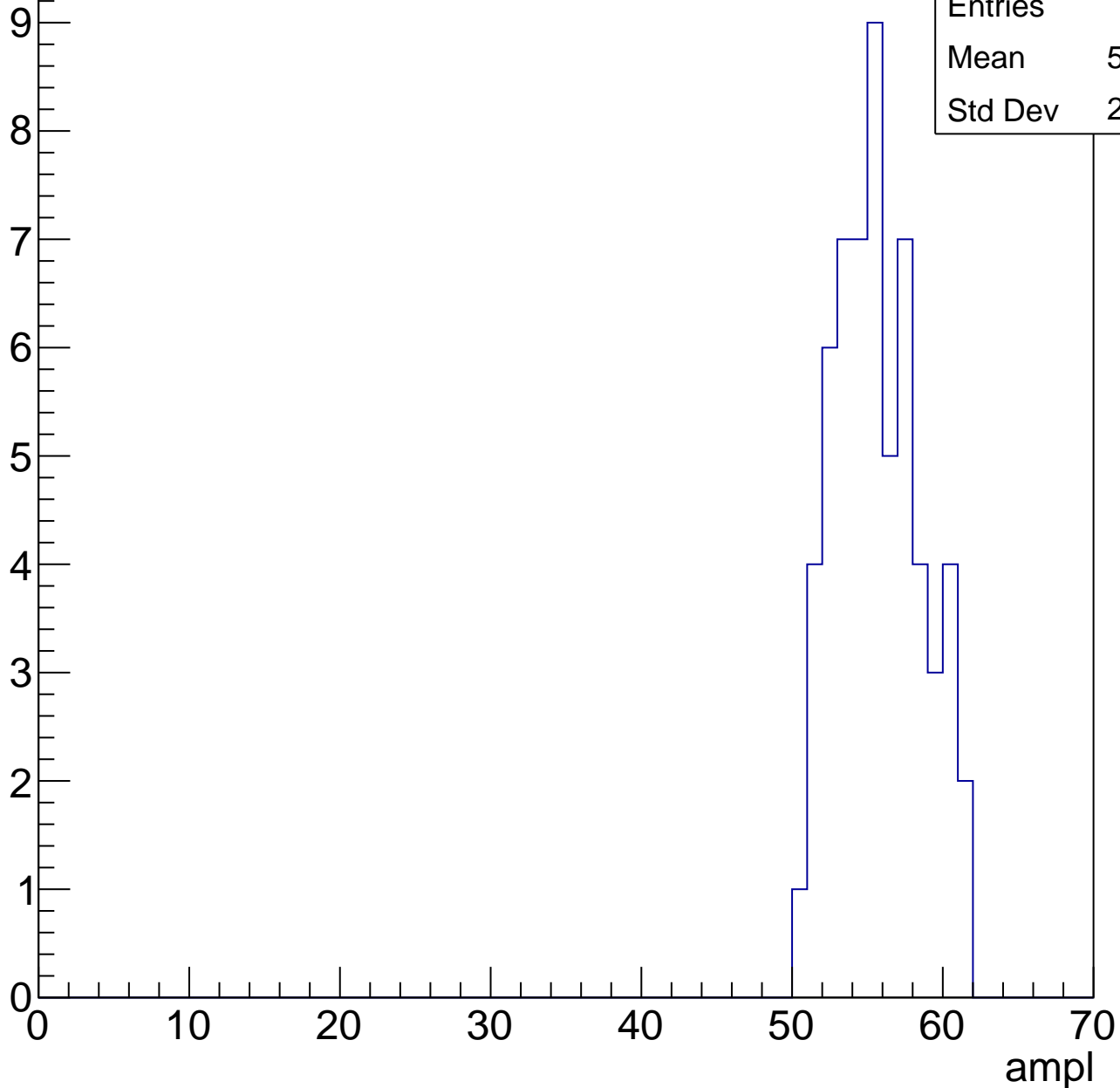
ampl



B1L102S, U19-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

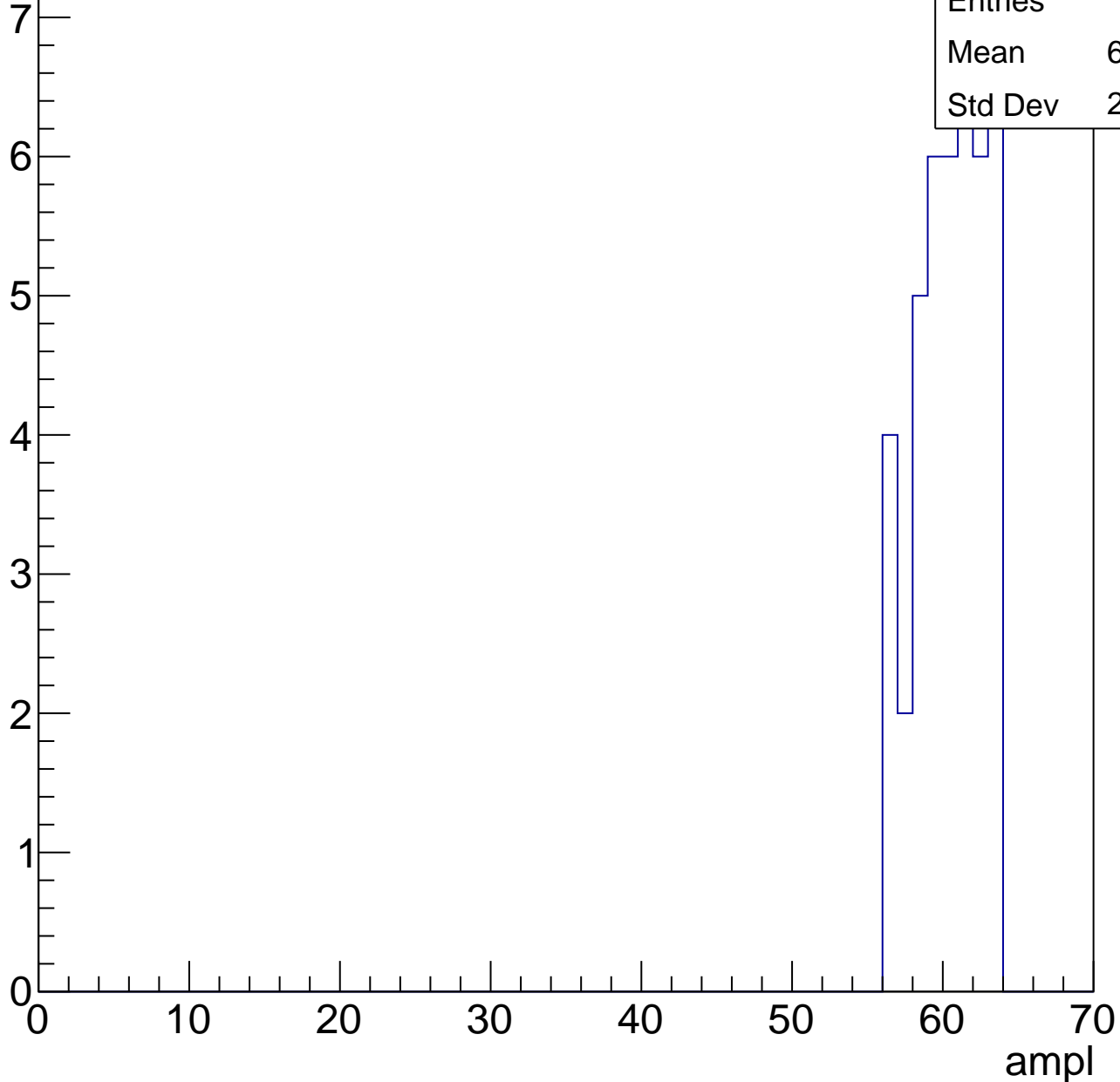


Entries	59
Mean	55.25
Std Dev	2.802

B1L102S, U19-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

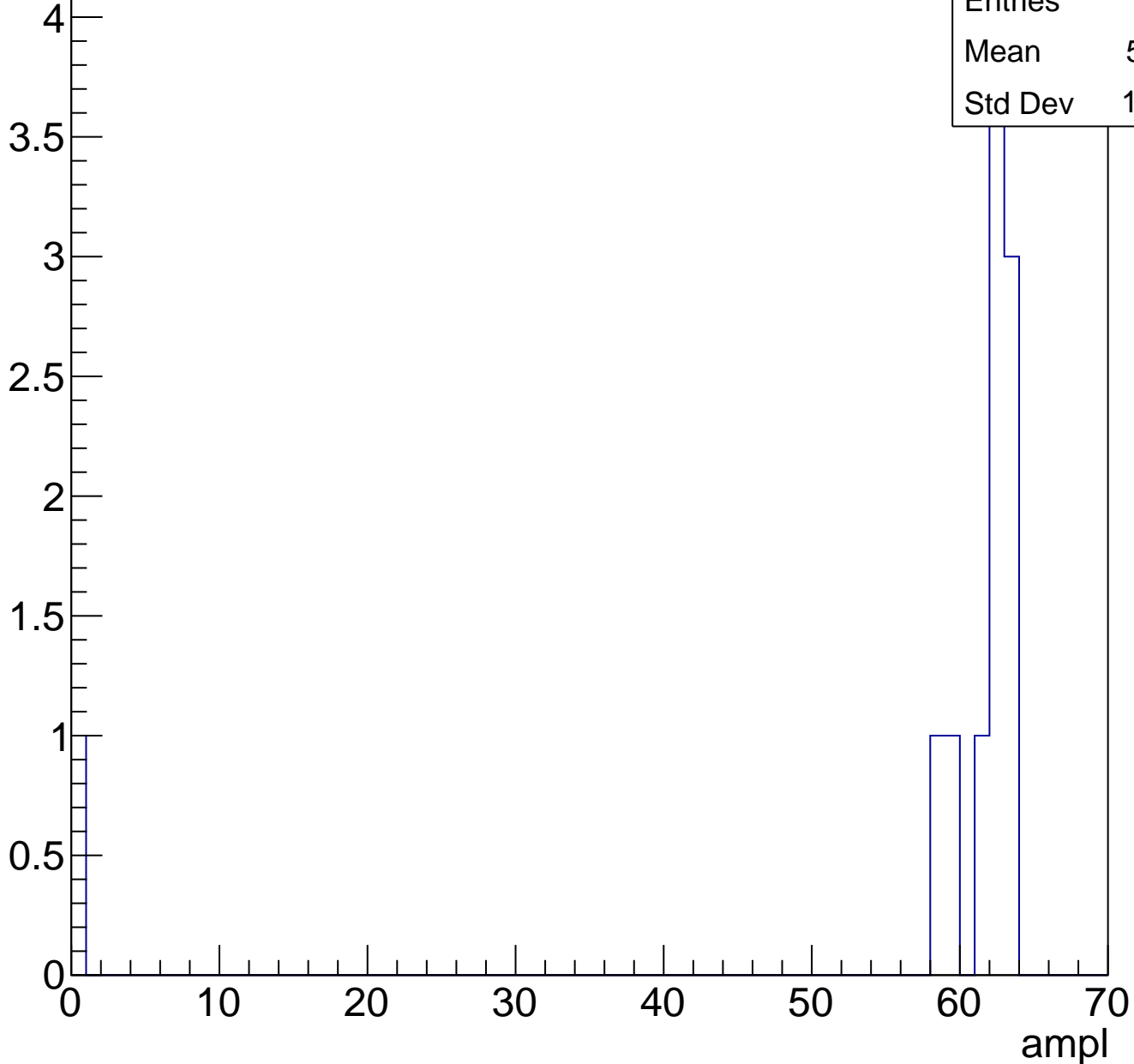


Entries	43
Mean	60.05
Std Dev	2.167

B1L102S, U19-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch44, adc0

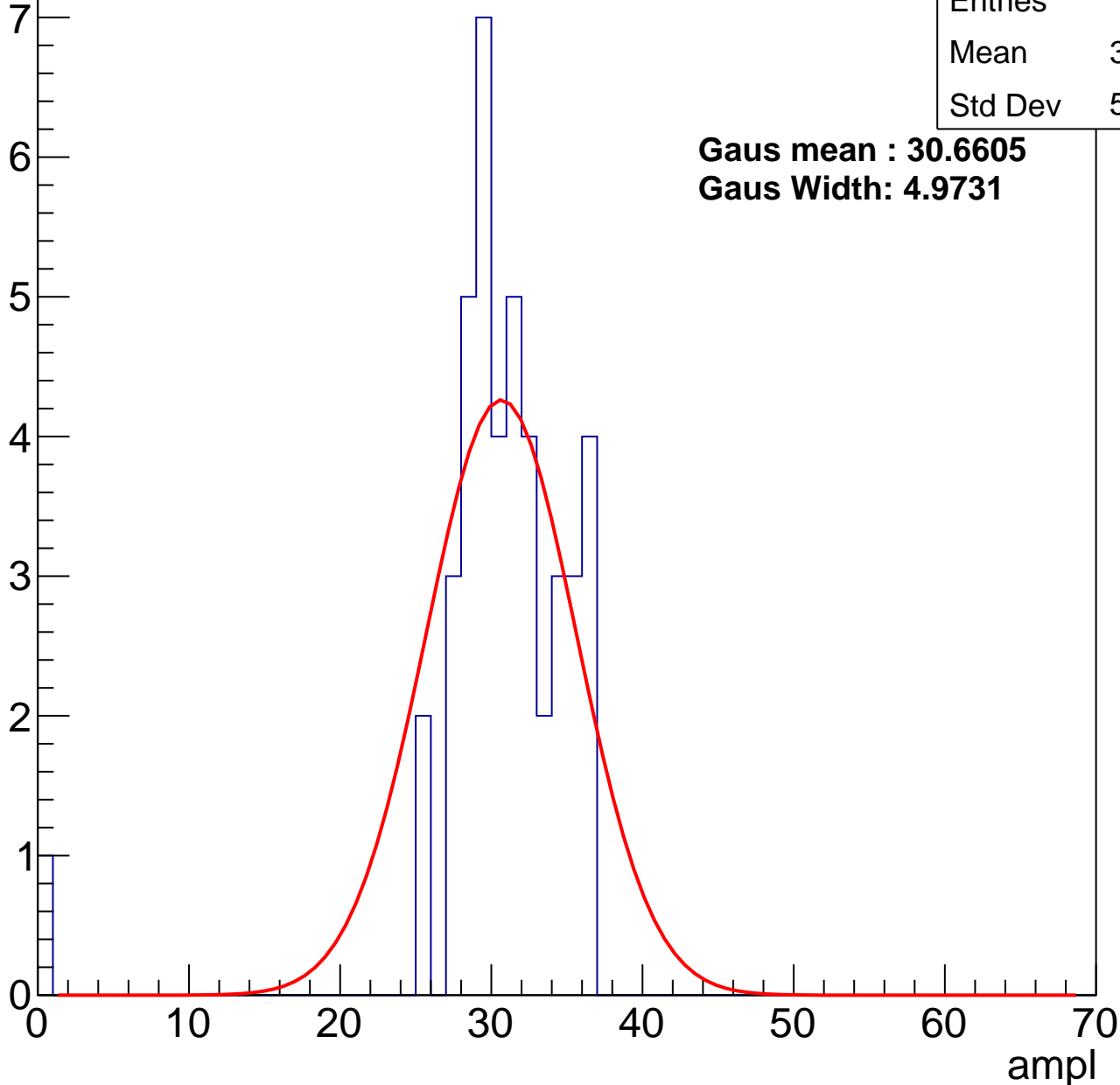
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	30.09
Std Dev	5.519

Gaus mean : 30.6605

Gaus Width: 4.9731



B1L102S, U19-ch44, adc1

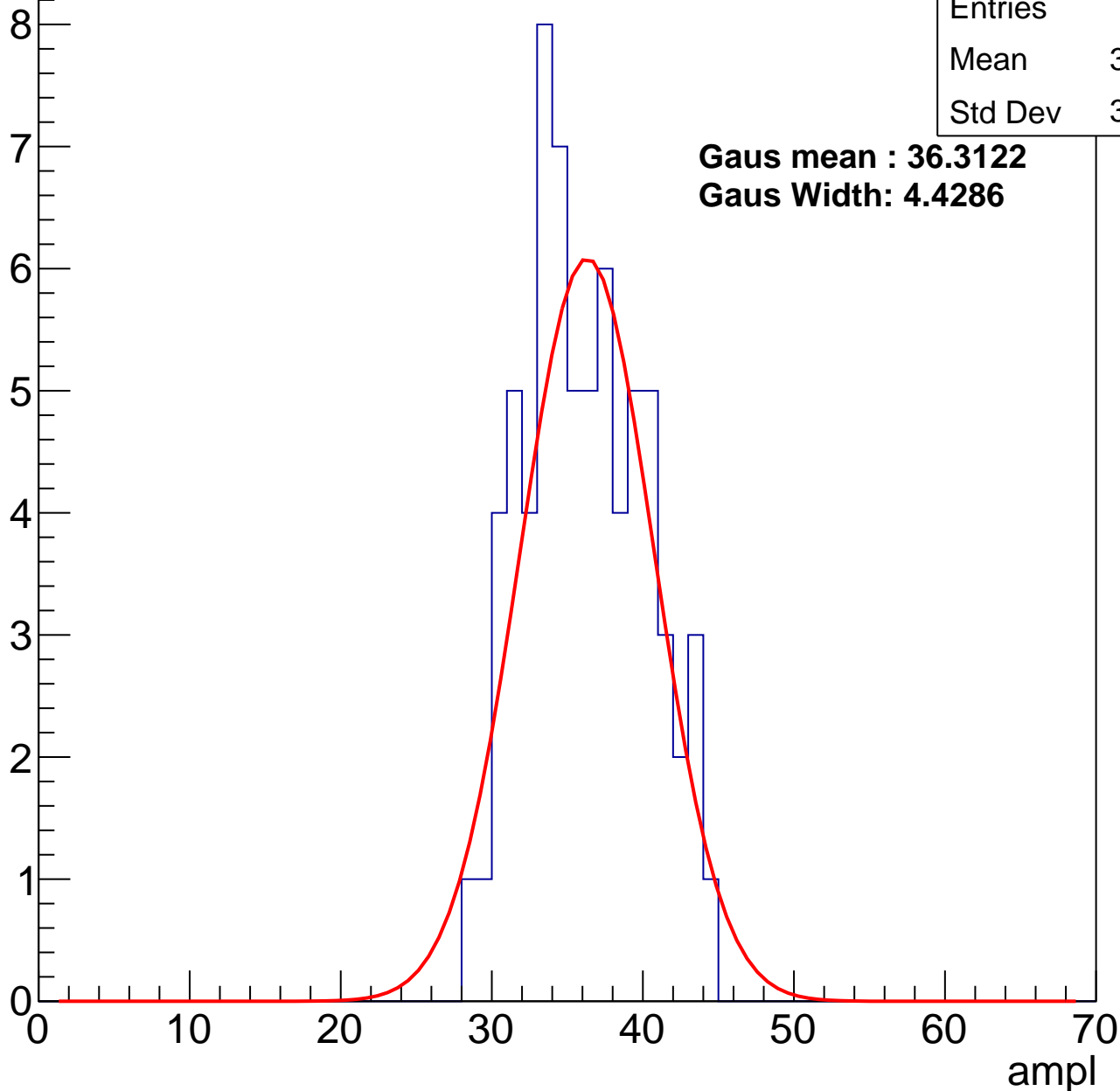
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.74
Std Dev	3.903

Gaus mean : 36.3122

Gaus Width: 4.4286



B1L102S, U19-ch44, adc2

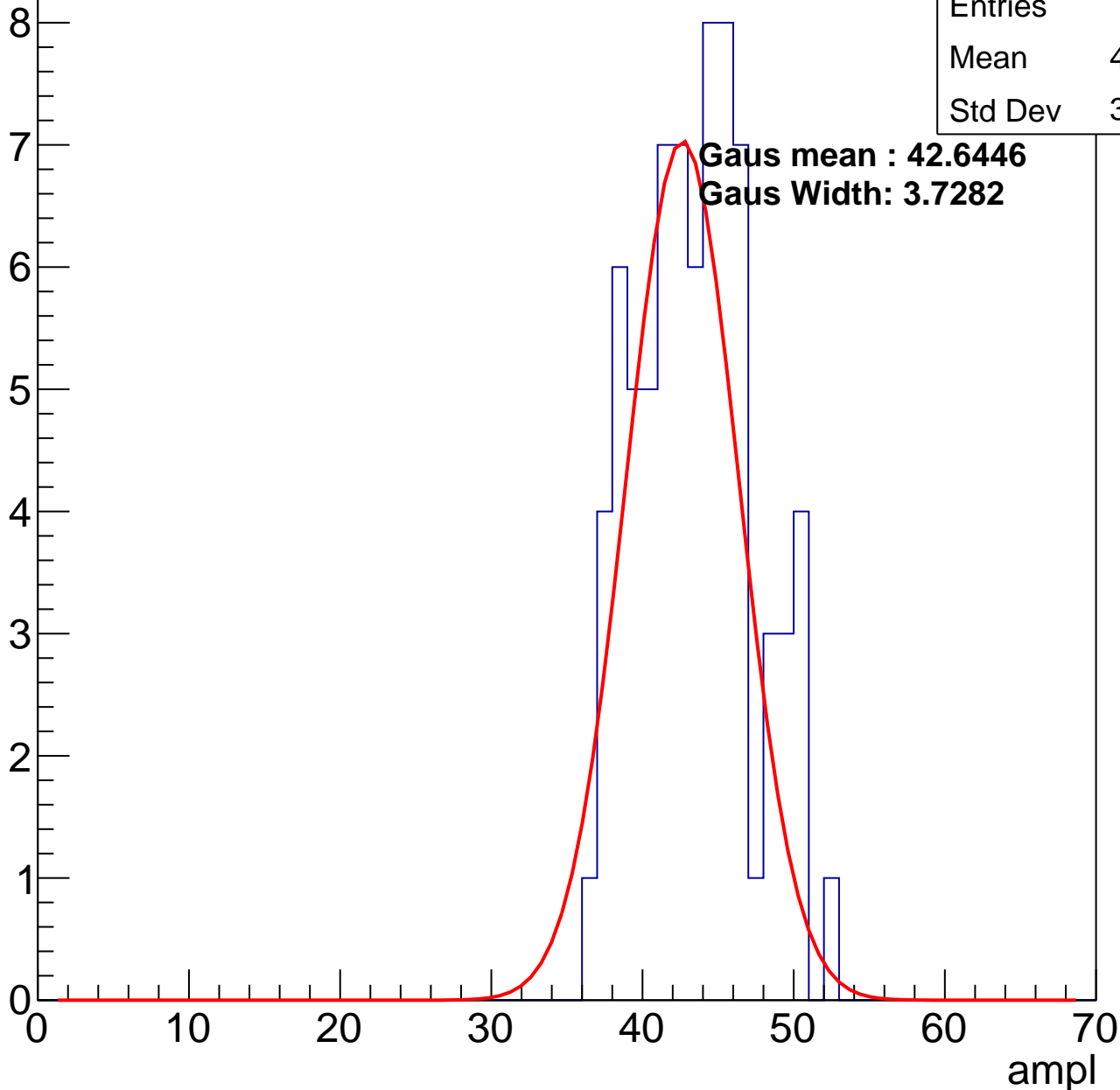
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.03
Std Dev	3.773

Gaus mean : 42.6446

Gaus Width: 3.7282

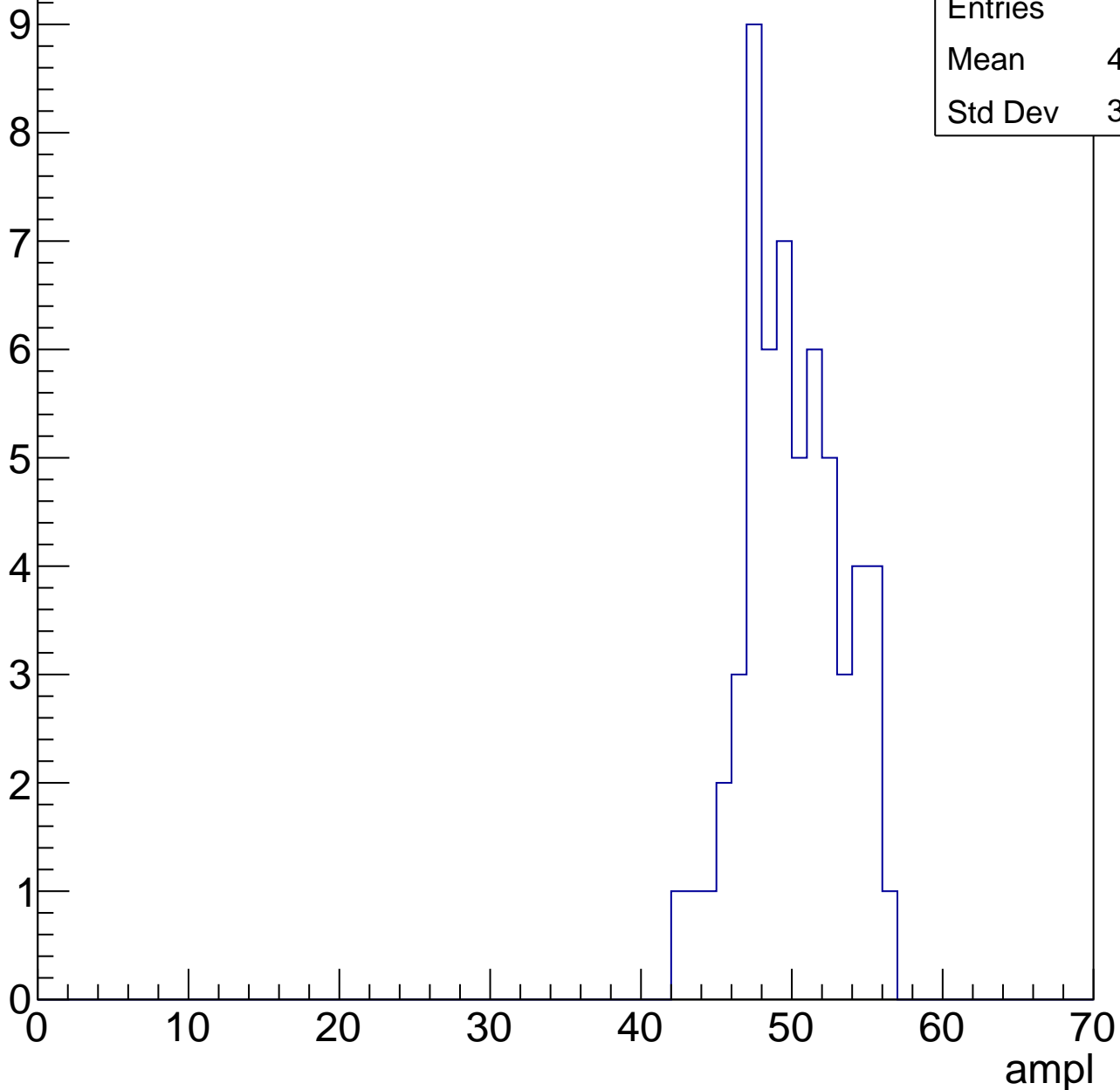


B1L102S, U19-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.62
Std Dev	3.226

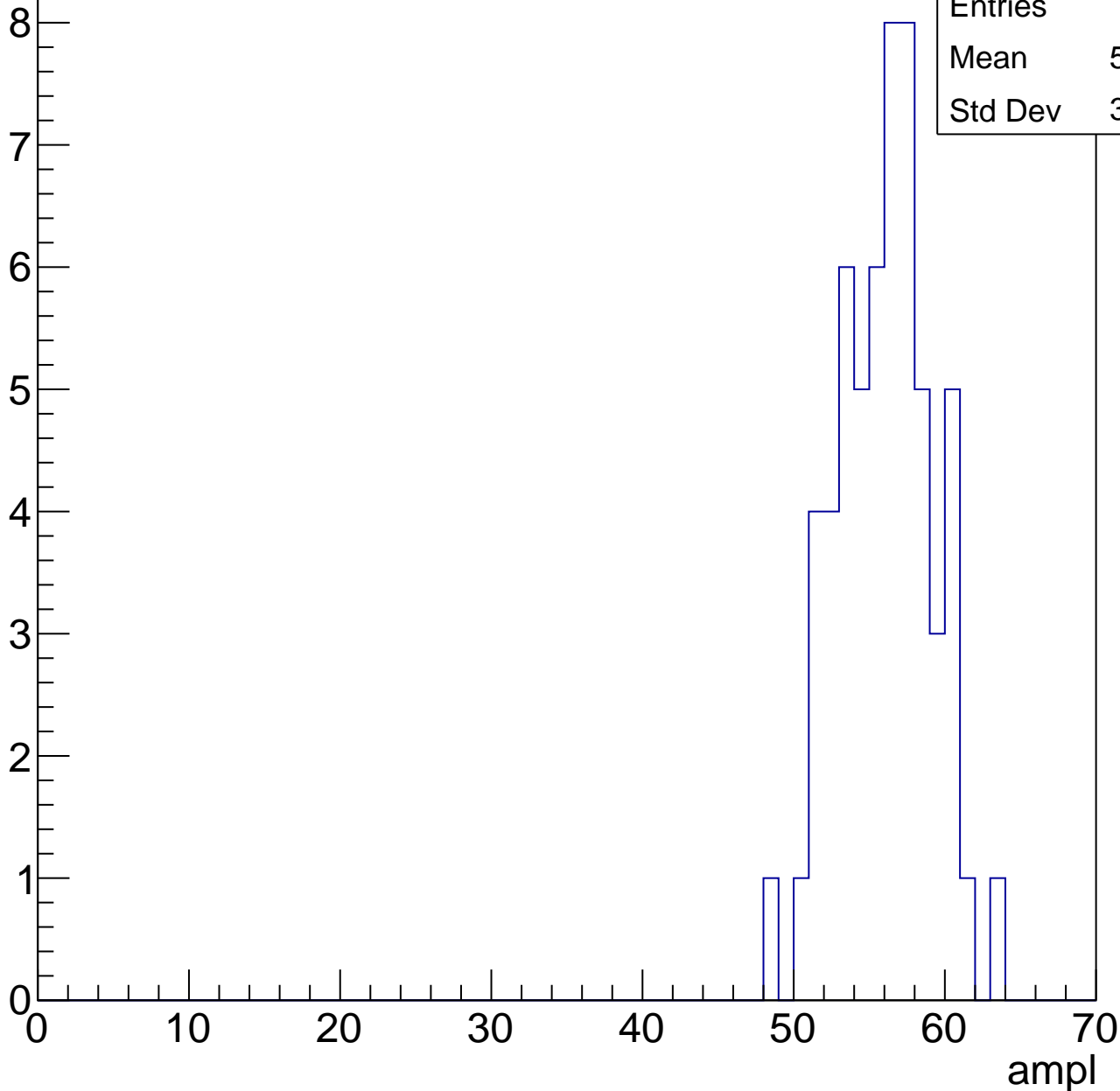


B1L102S, U19-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.57
Std Dev	3.058

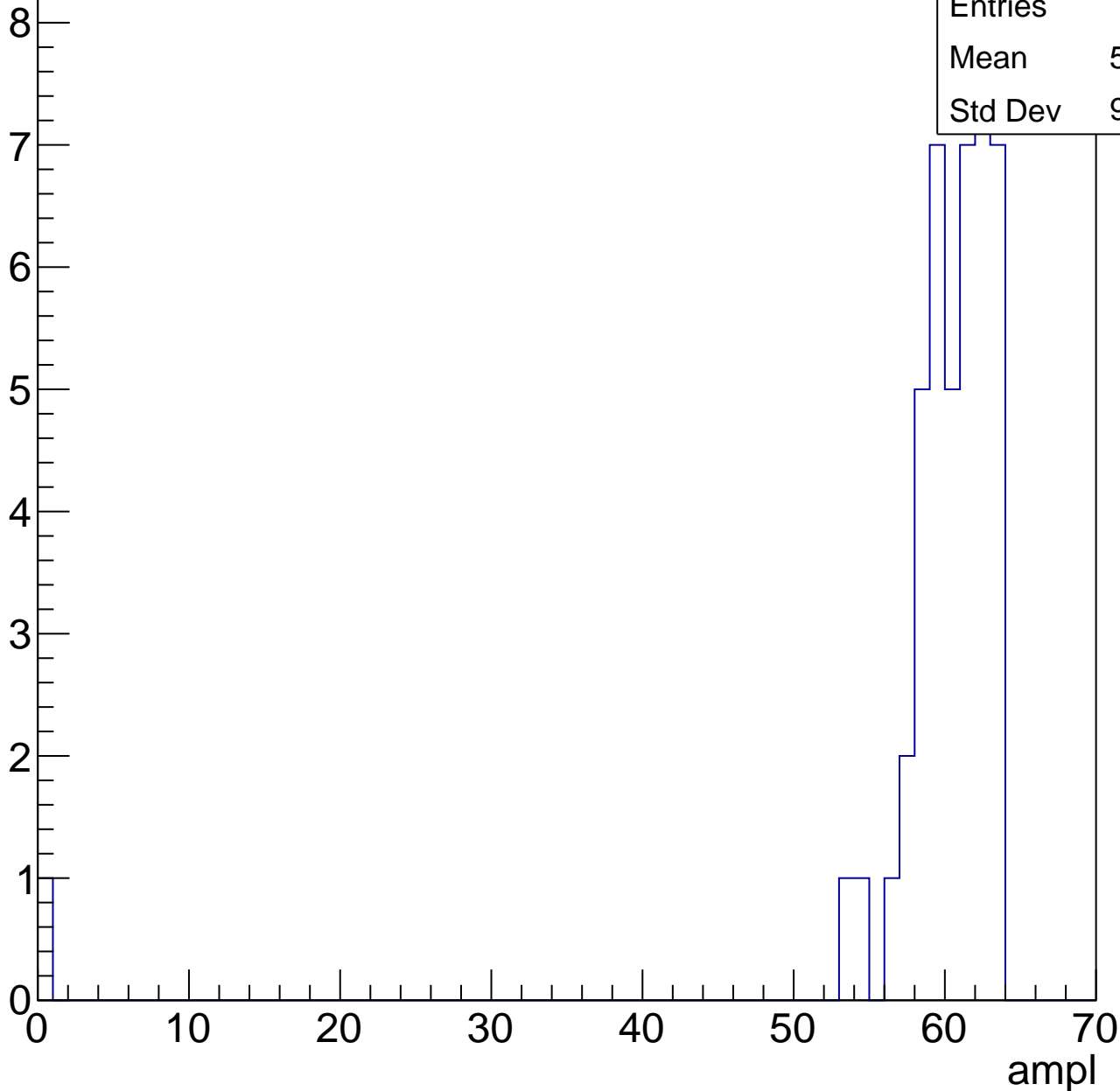


B1L102S, U19-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.76
Std Dev	9.163



B1L102S, U19-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	62
Std Dev	0.8165

0 10 20 30 40 50 60 70

ampl

B1L102S, U19-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch45, adc0

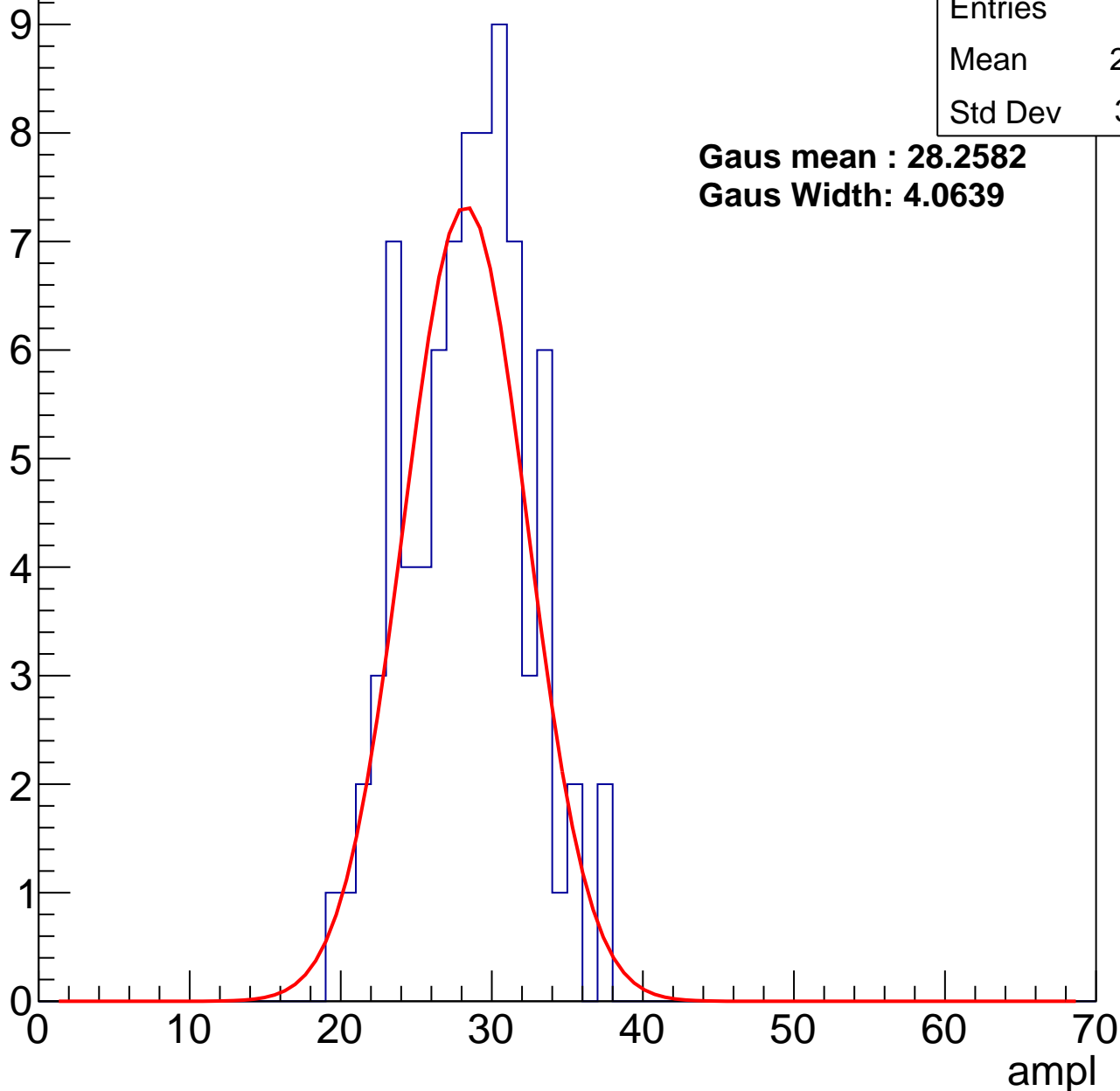
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.95
Std Dev	3.941

Gaus mean : 28.2582

Gaus Width: 4.0639



B1L102S, U19-ch45, adc1

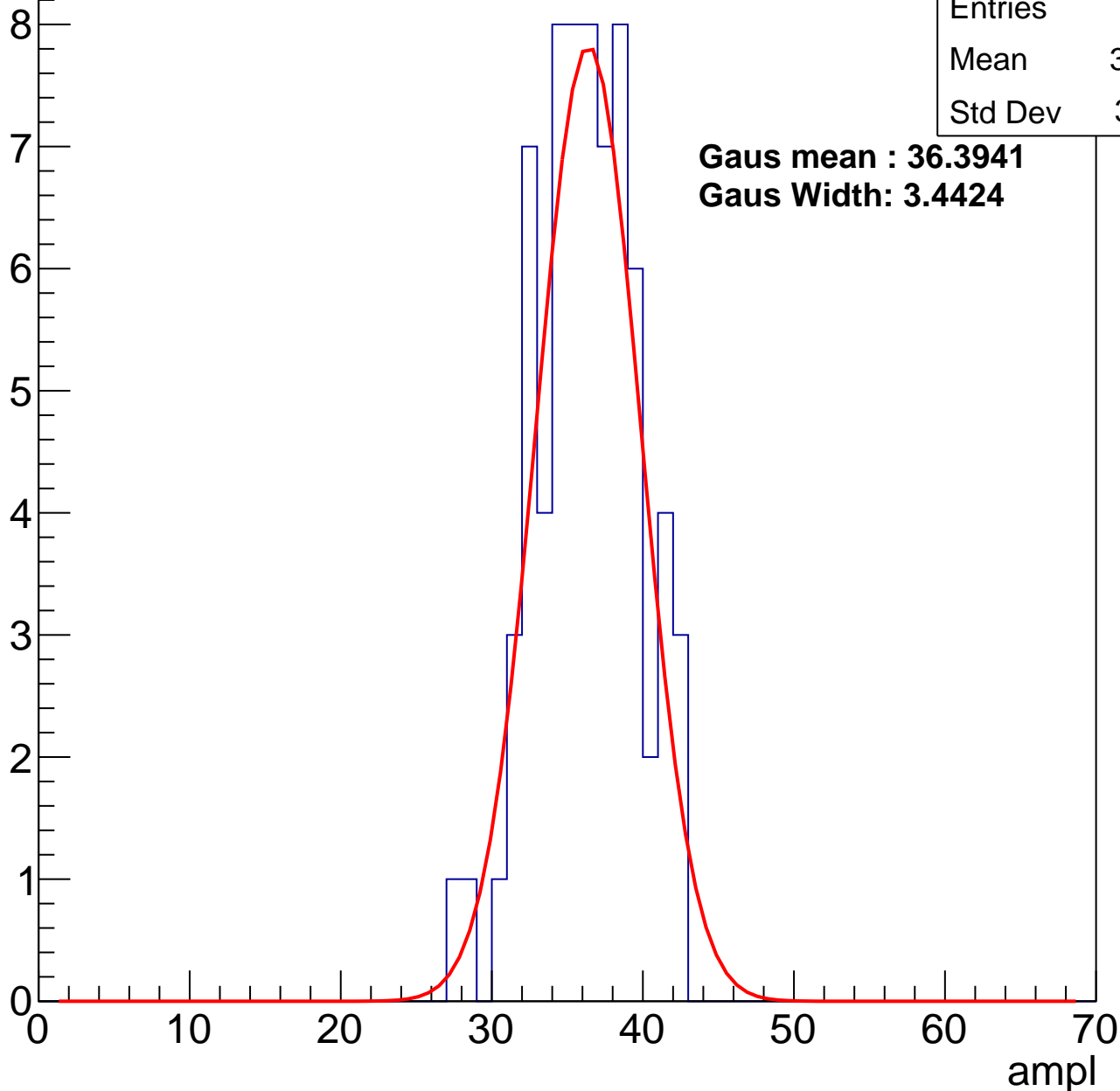
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.79
Std Dev	3.301

Gaus mean : 36.3941

Gaus Width: 3.4424



B1L102S, U19-ch45, adc2

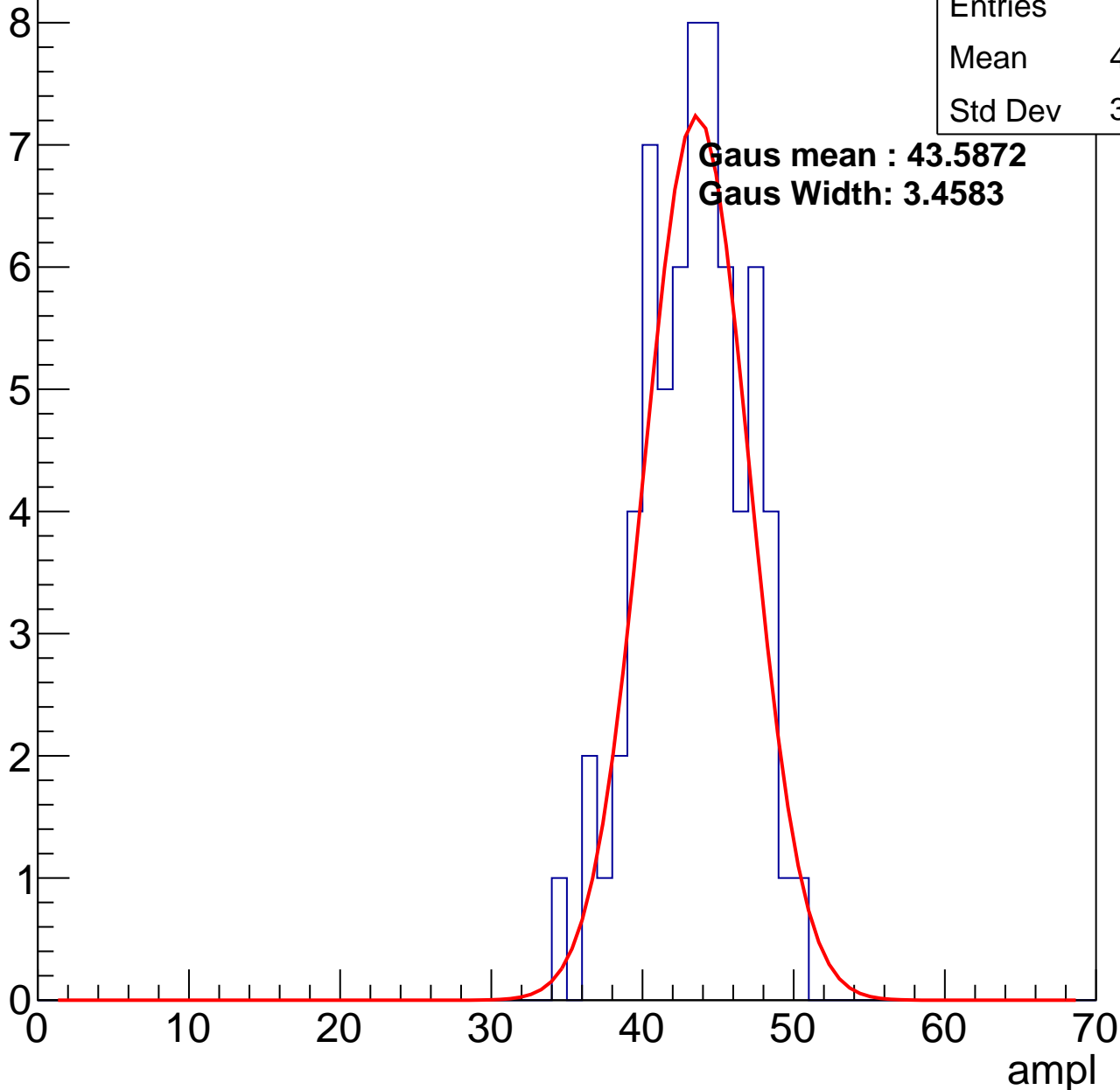
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.95
Std Dev	3.409

Gaus mean : 43.5872

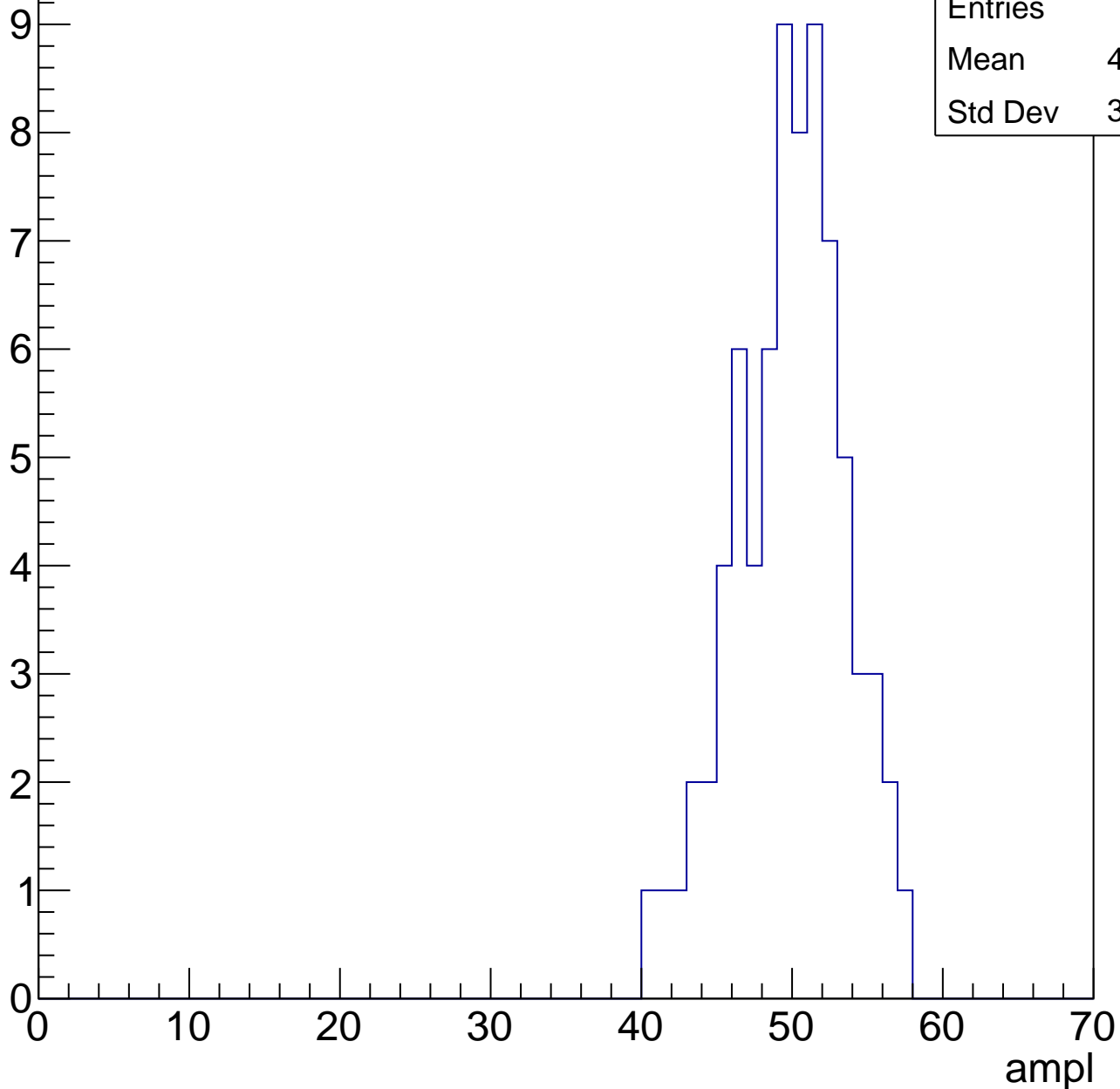
Gaus Width: 3.4583



B1L102S, U19-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

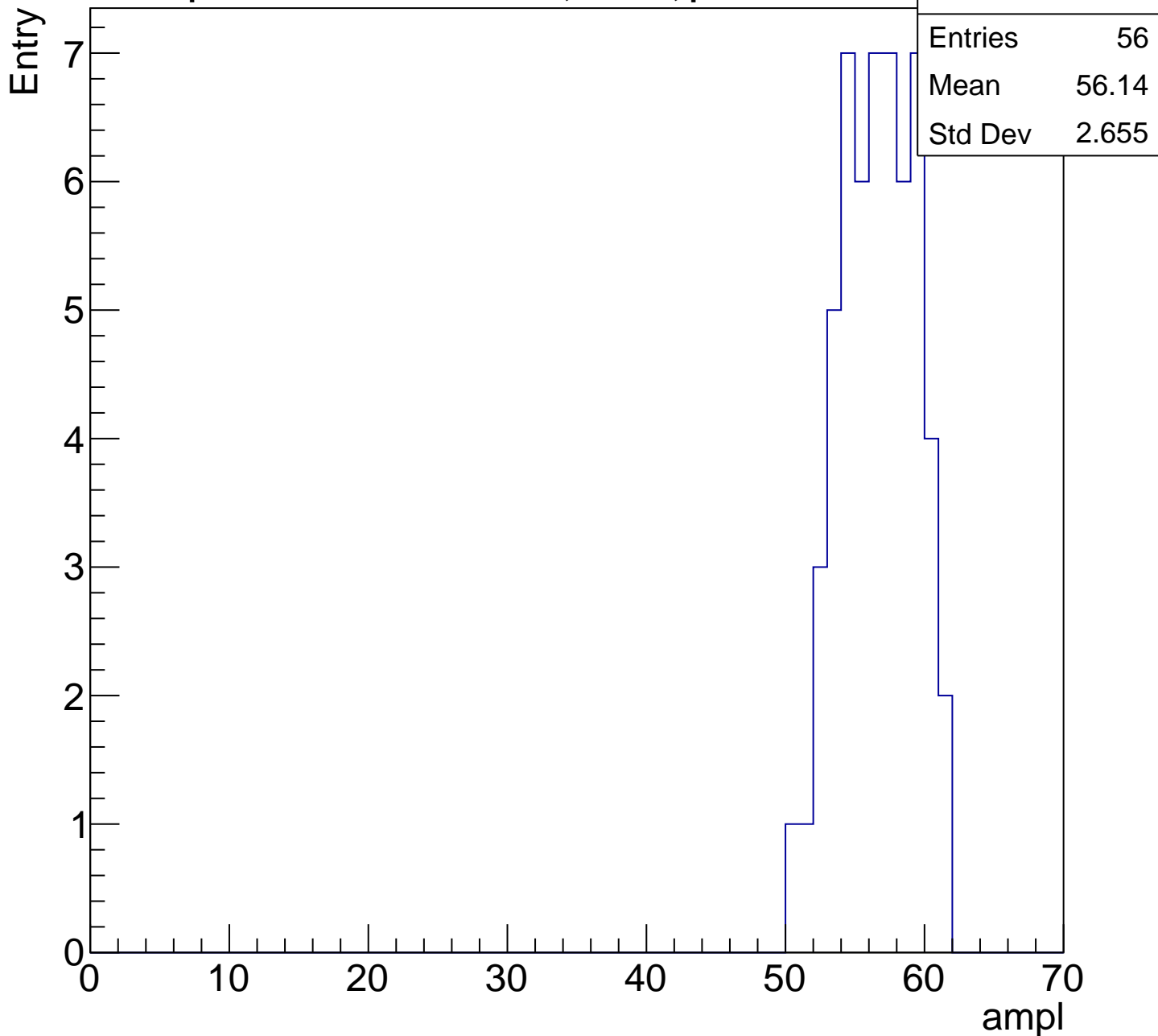
Entry



Entries	74
Mean	49.38
Std Dev	3.634

B1L102S, U19-ch45, adc4

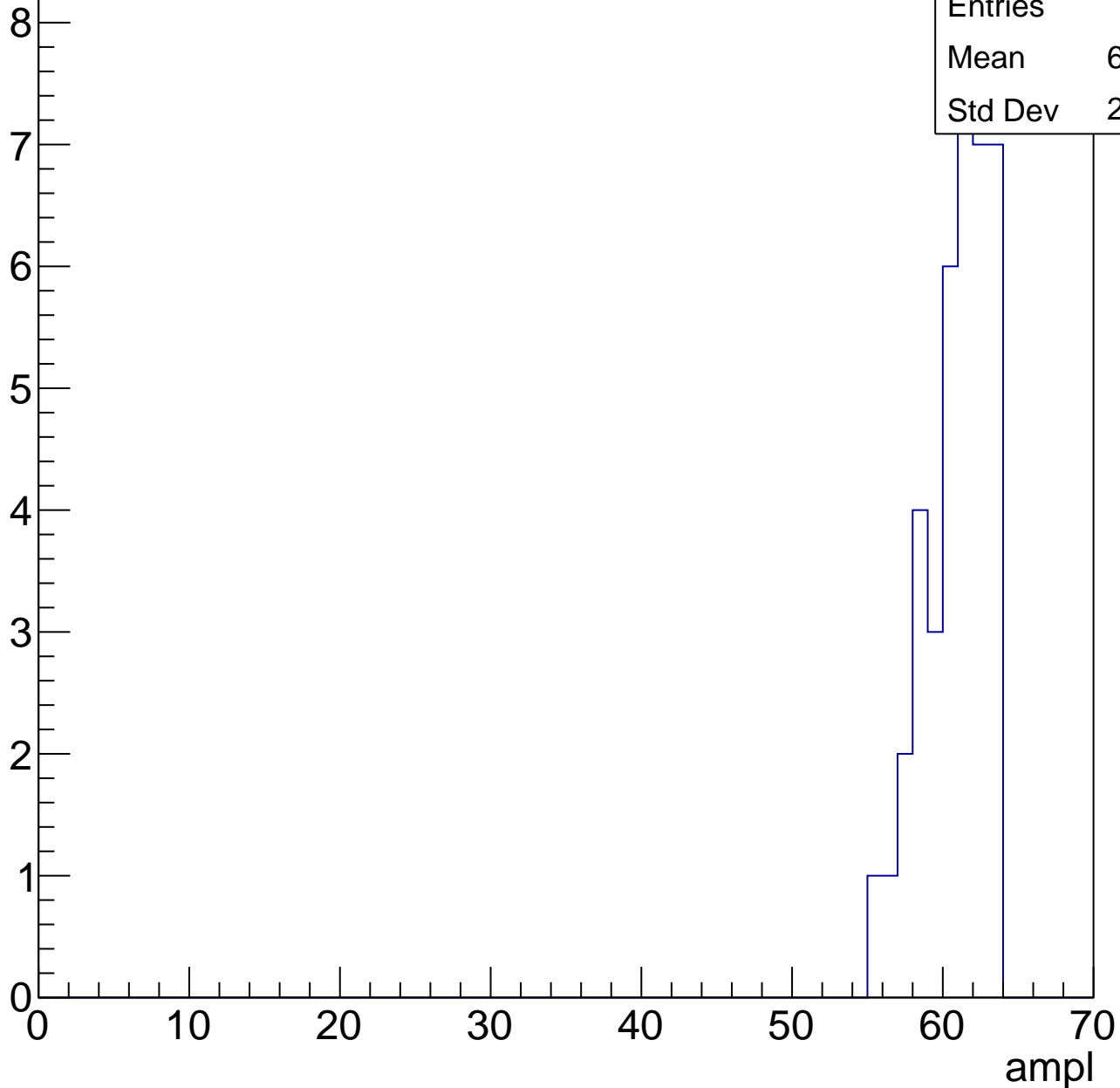
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U19-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

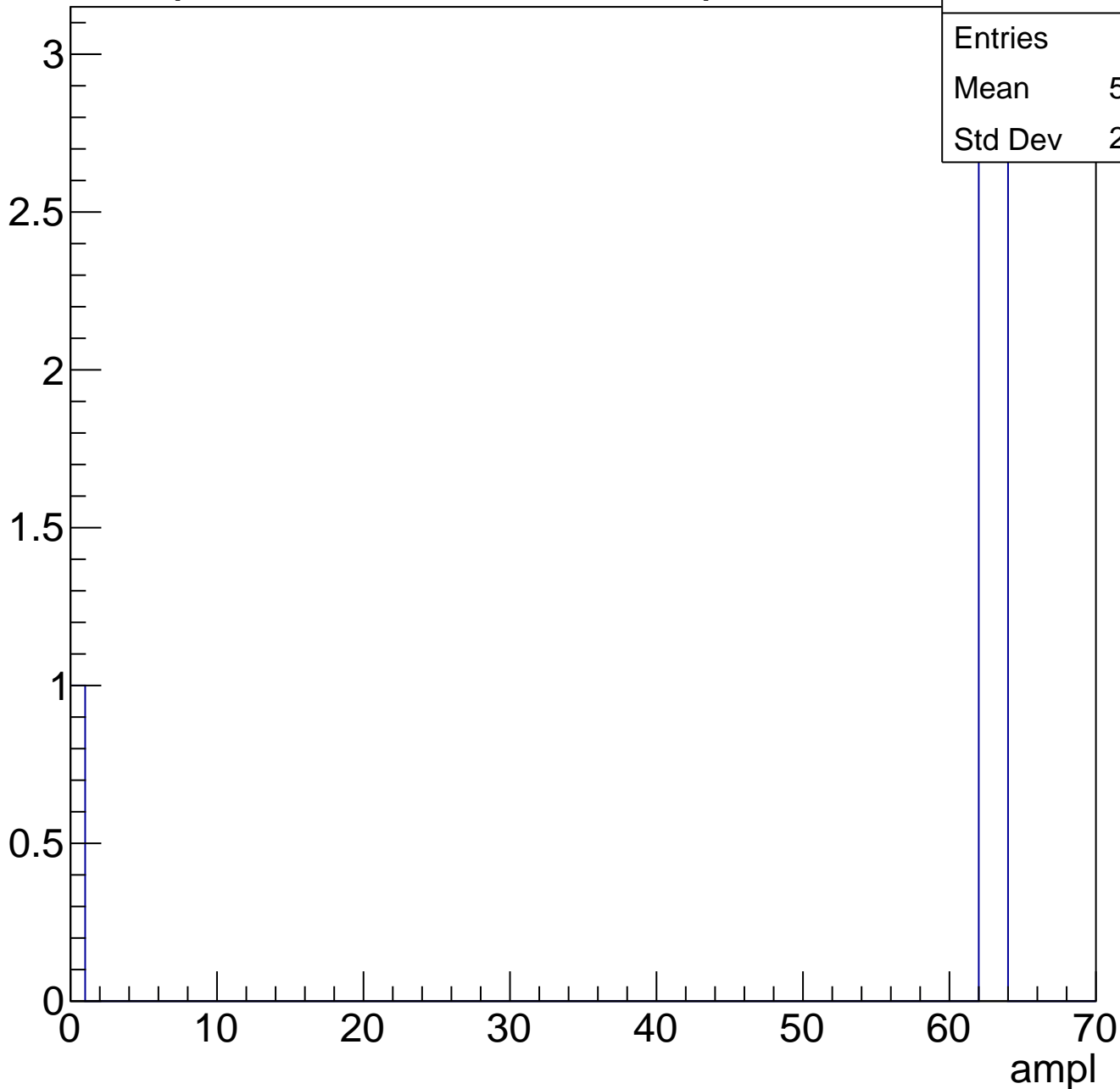
Entry



B1L102S, U19-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch46, adc0

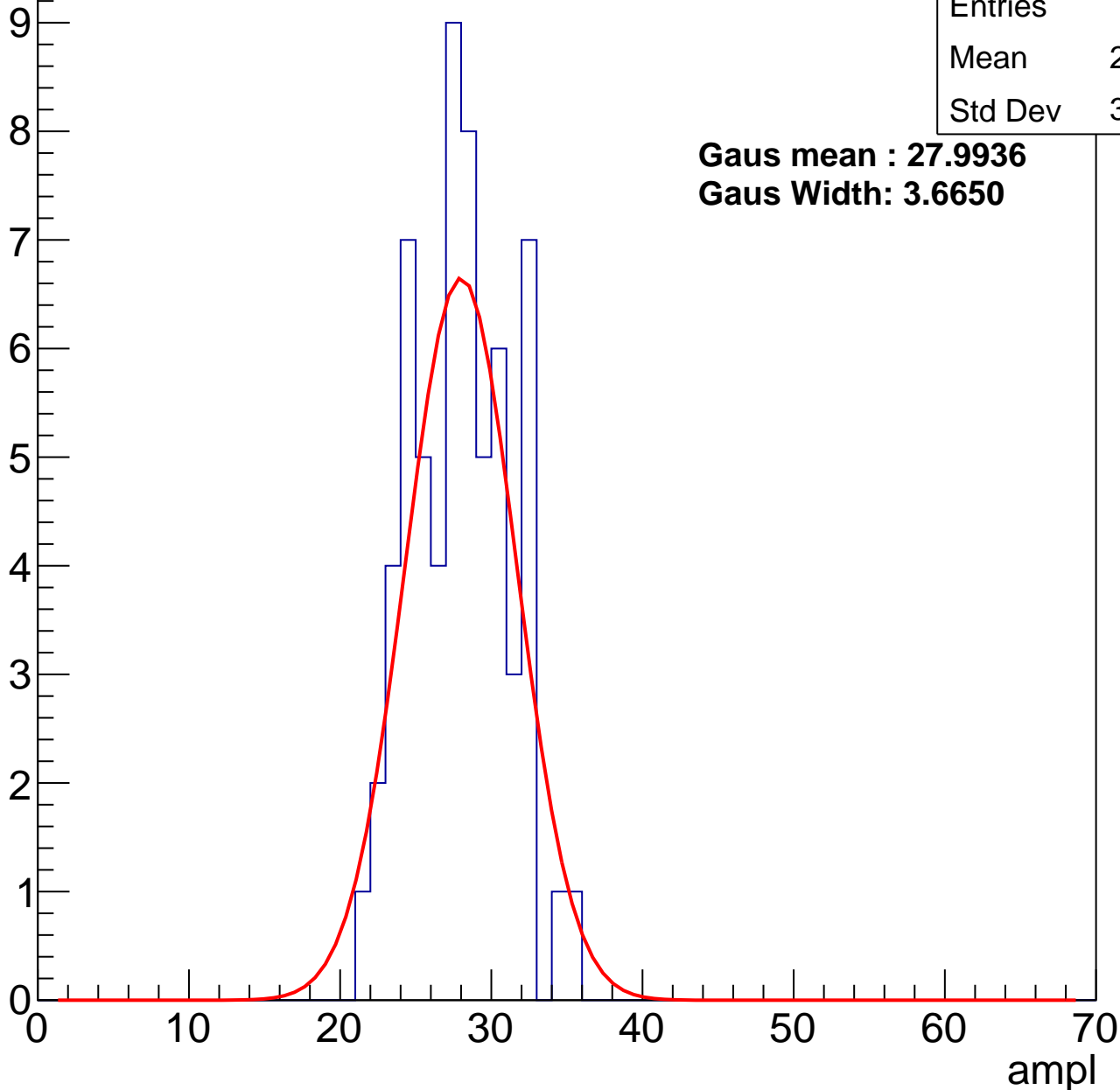
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.49
Std Dev	3.187

Gaus mean : 27.9936

Gaus Width: 3.6650



B1L102S, U19-ch46, adc1

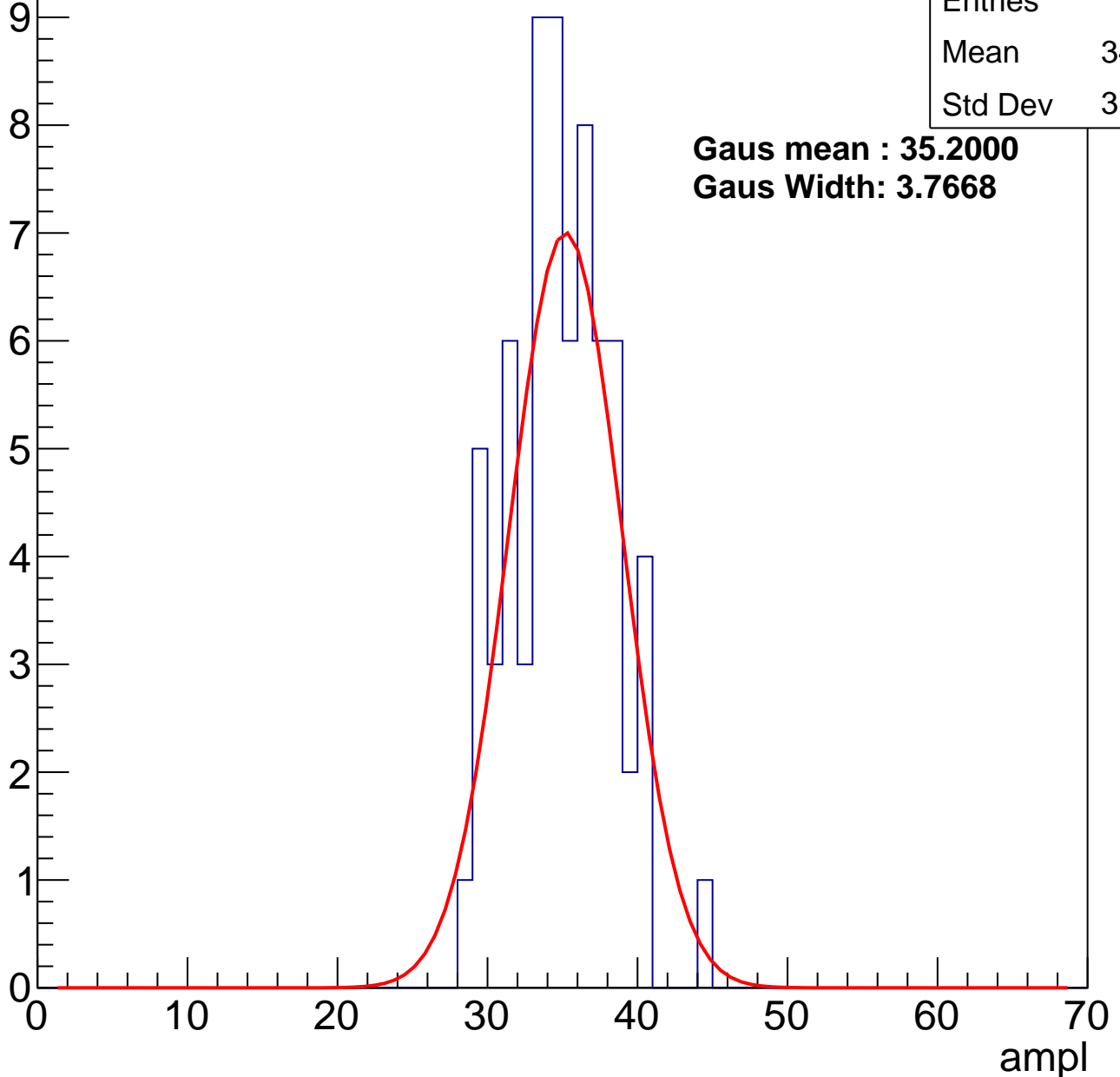
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.46
Std Dev	3.299

Gaus mean : 35.2000

Gaus Width: 3.7668



B1L102S, U19-ch46, adc2

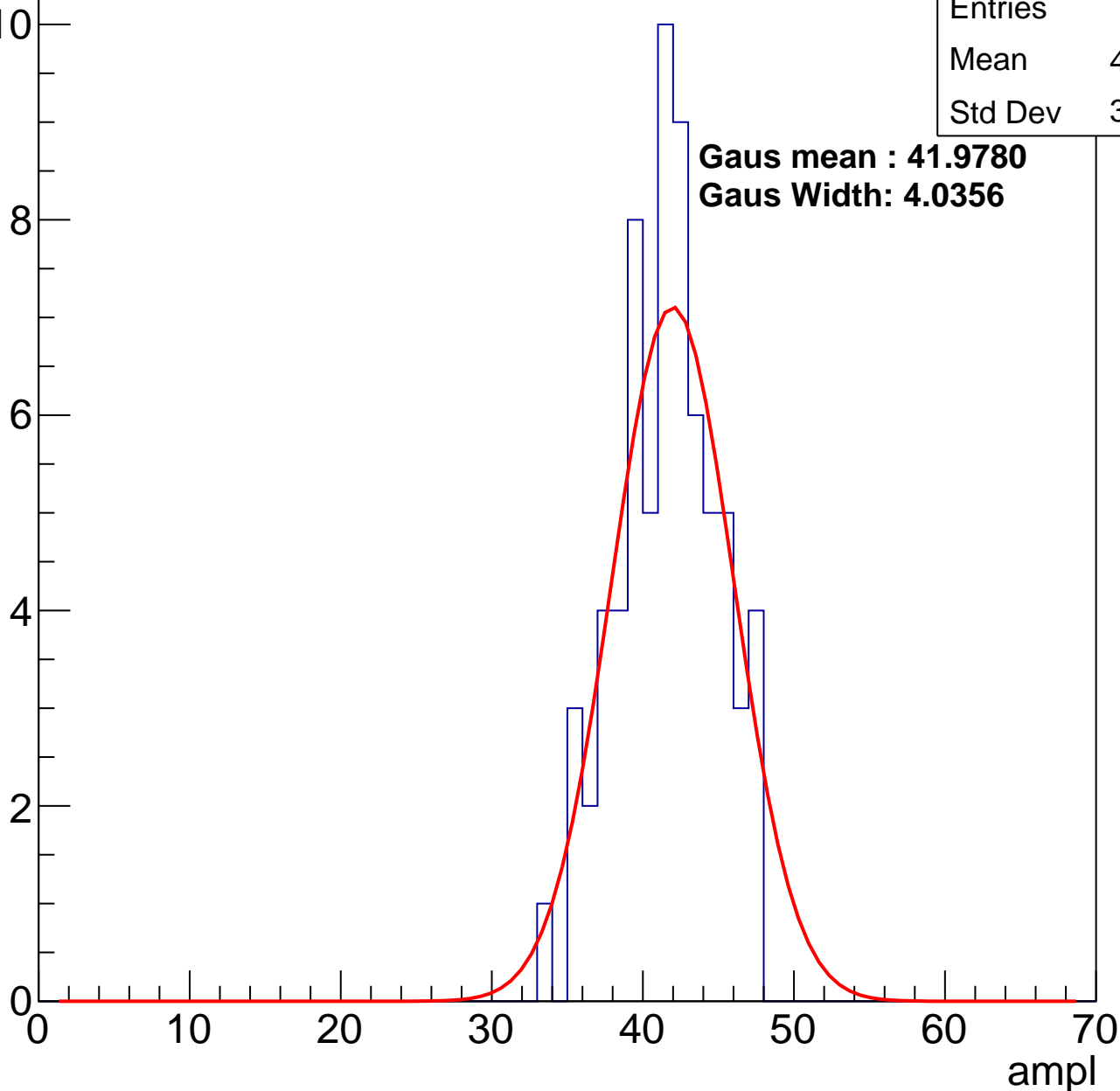
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.14
Std Dev	3.263

Gaus mean : 41.9780

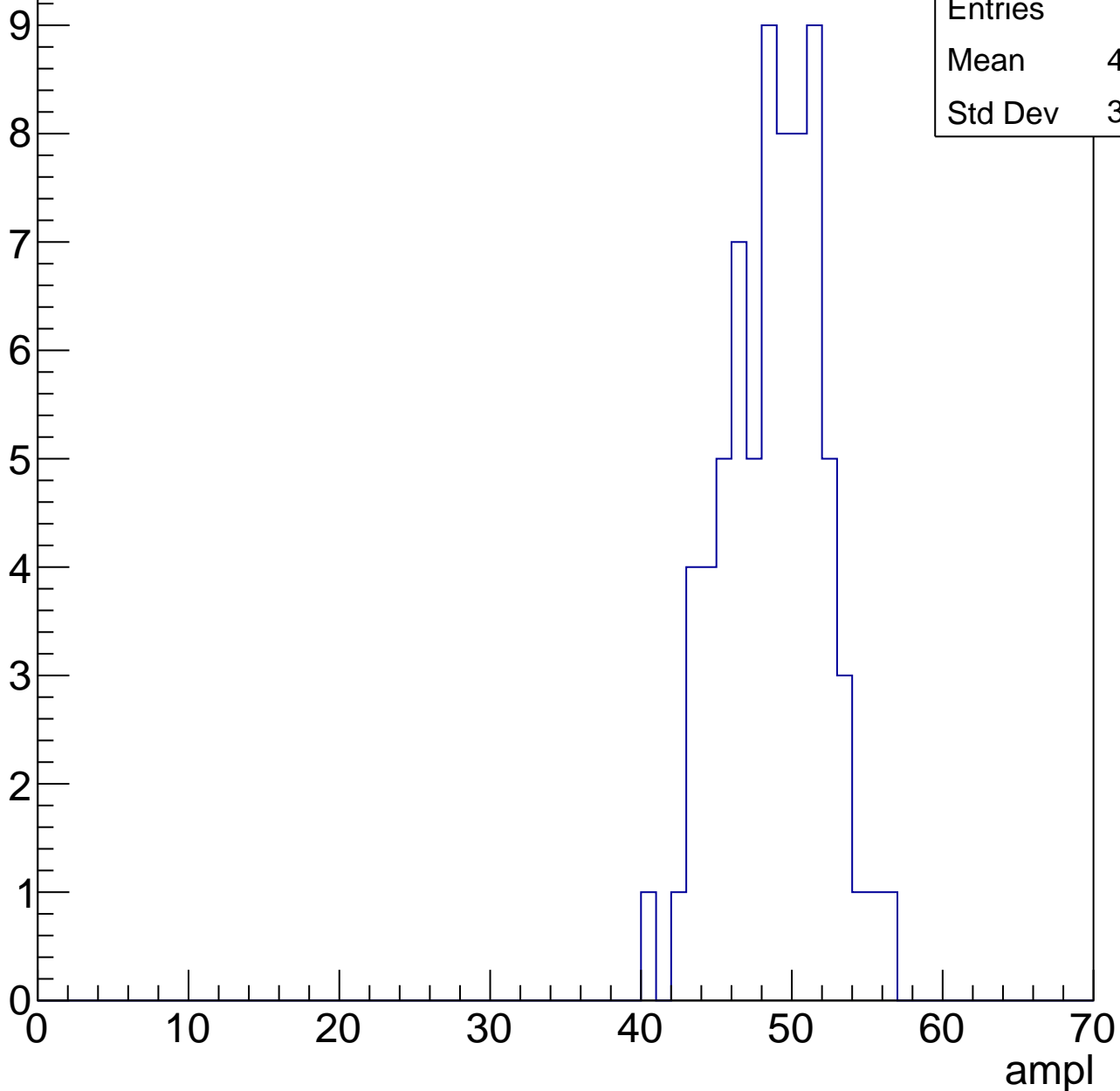
Gaus Width: 4.0356



B1L102S, U19-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



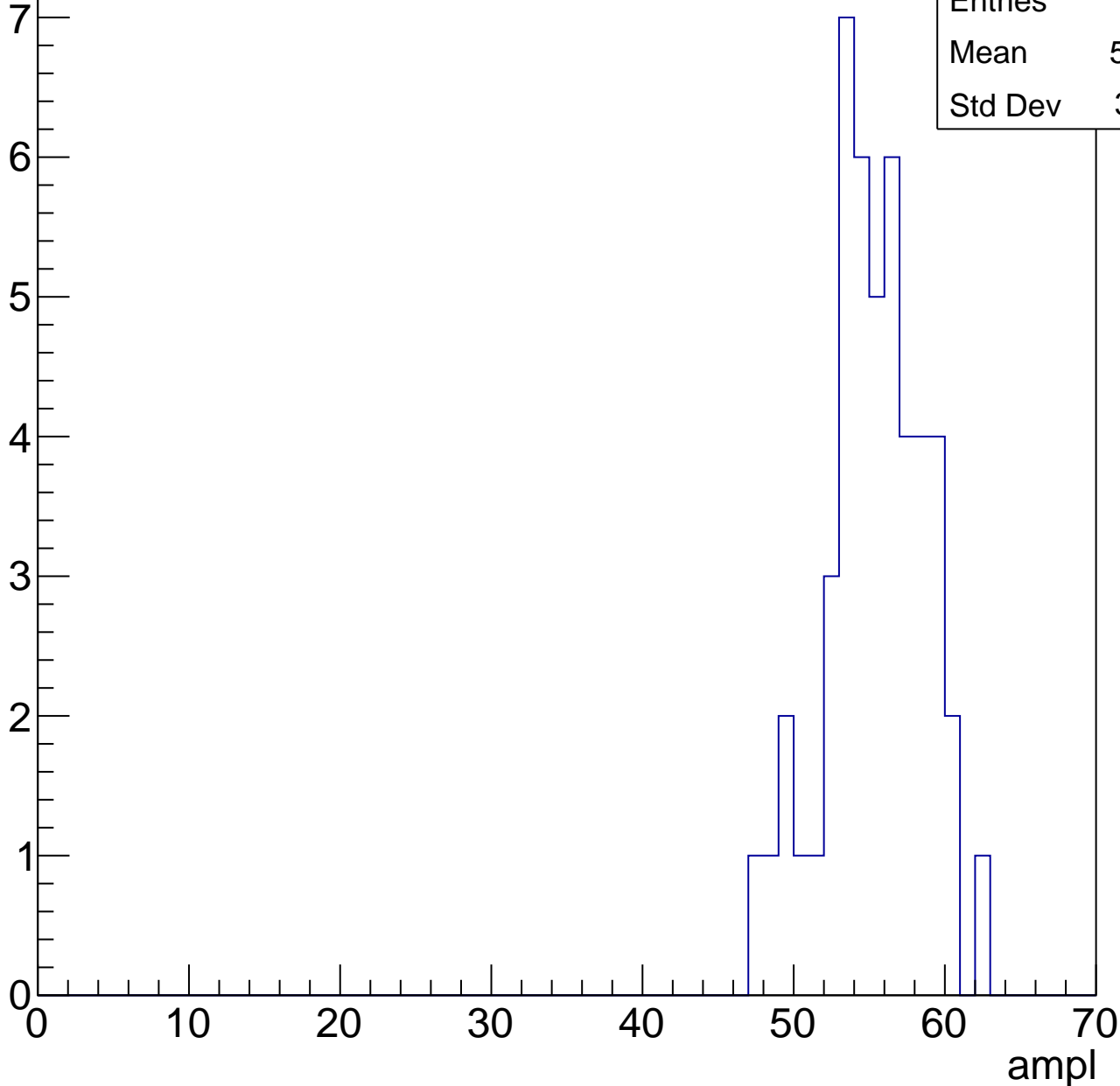
Entries	72
Mean	48.32
Std Dev	3.248

B1L102S, U19-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.88
Std Dev	3.251

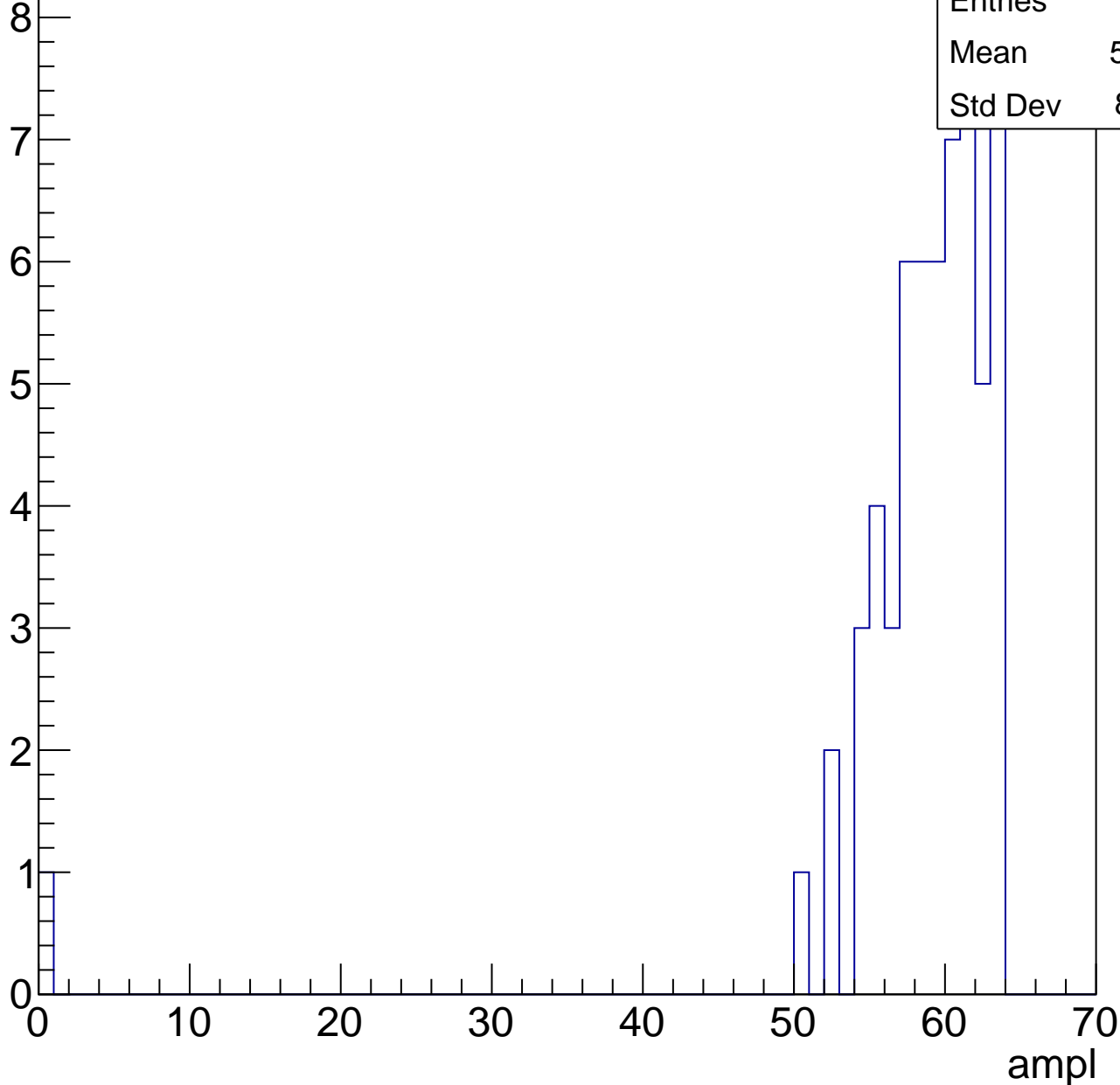


B1L102S, U19-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

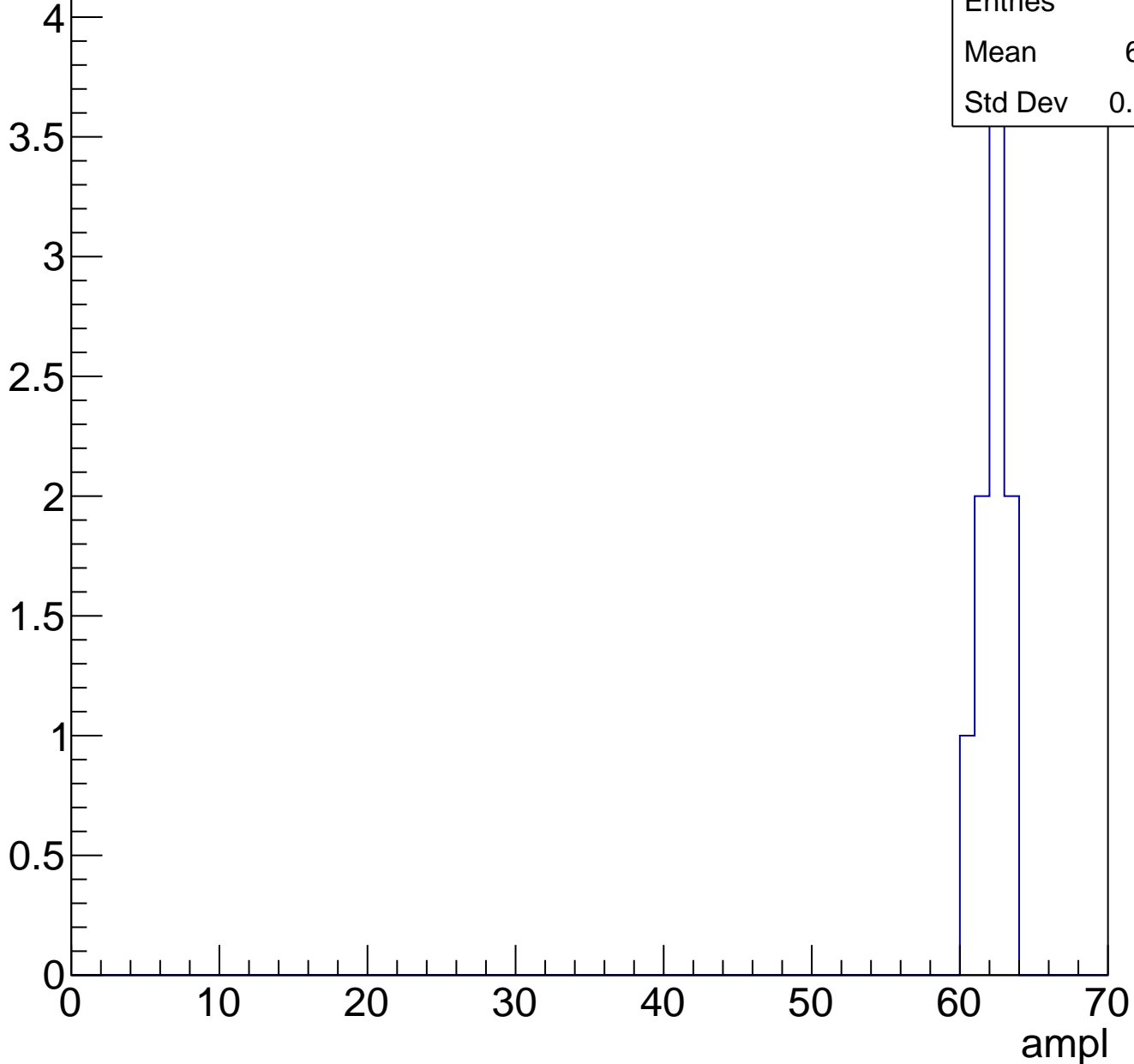
Entries	60
Mean	57.83
Std Dev	8.151



B1L102S, U19-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

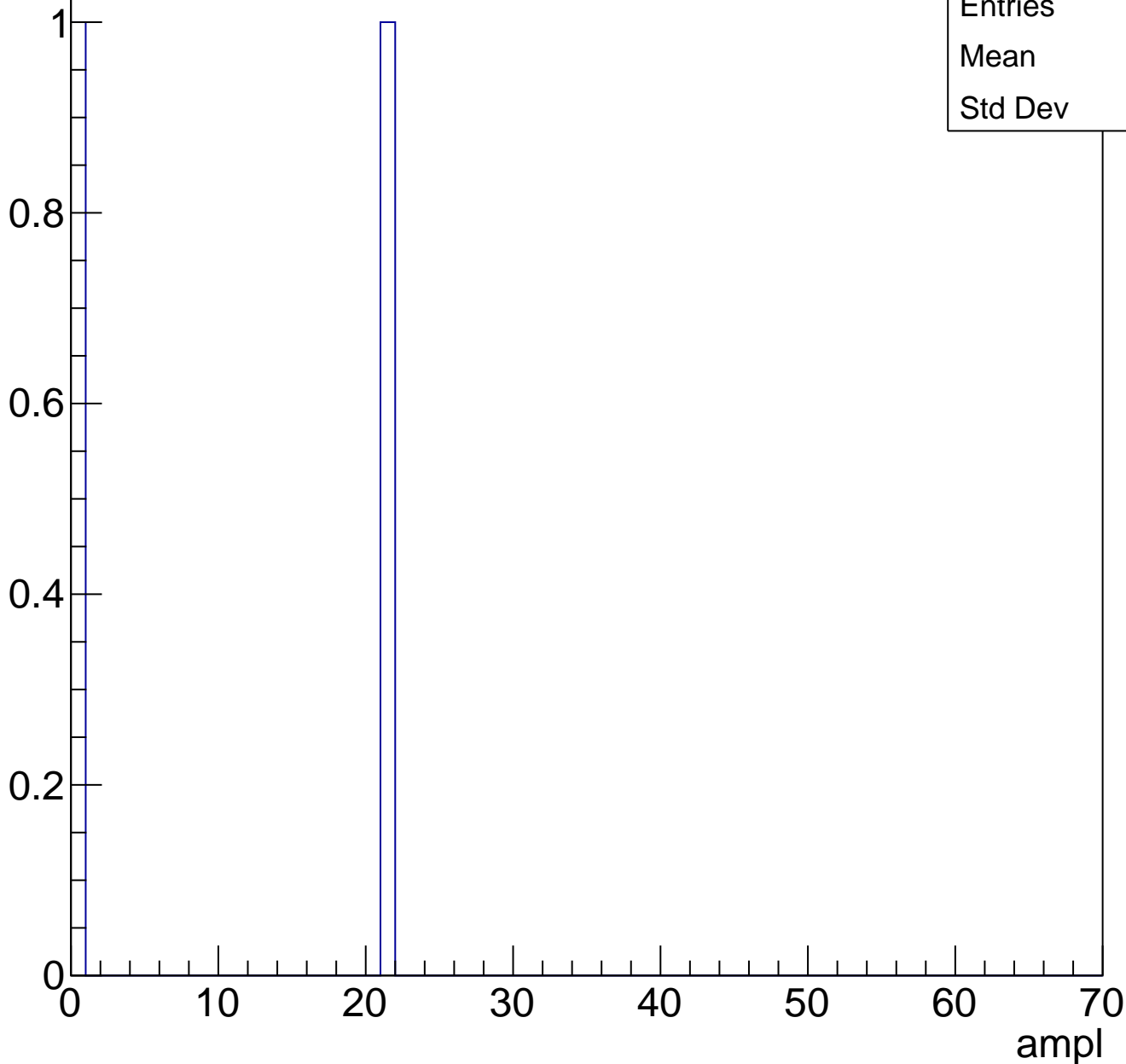
Entry



B1L102S, U19-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch47, adc0

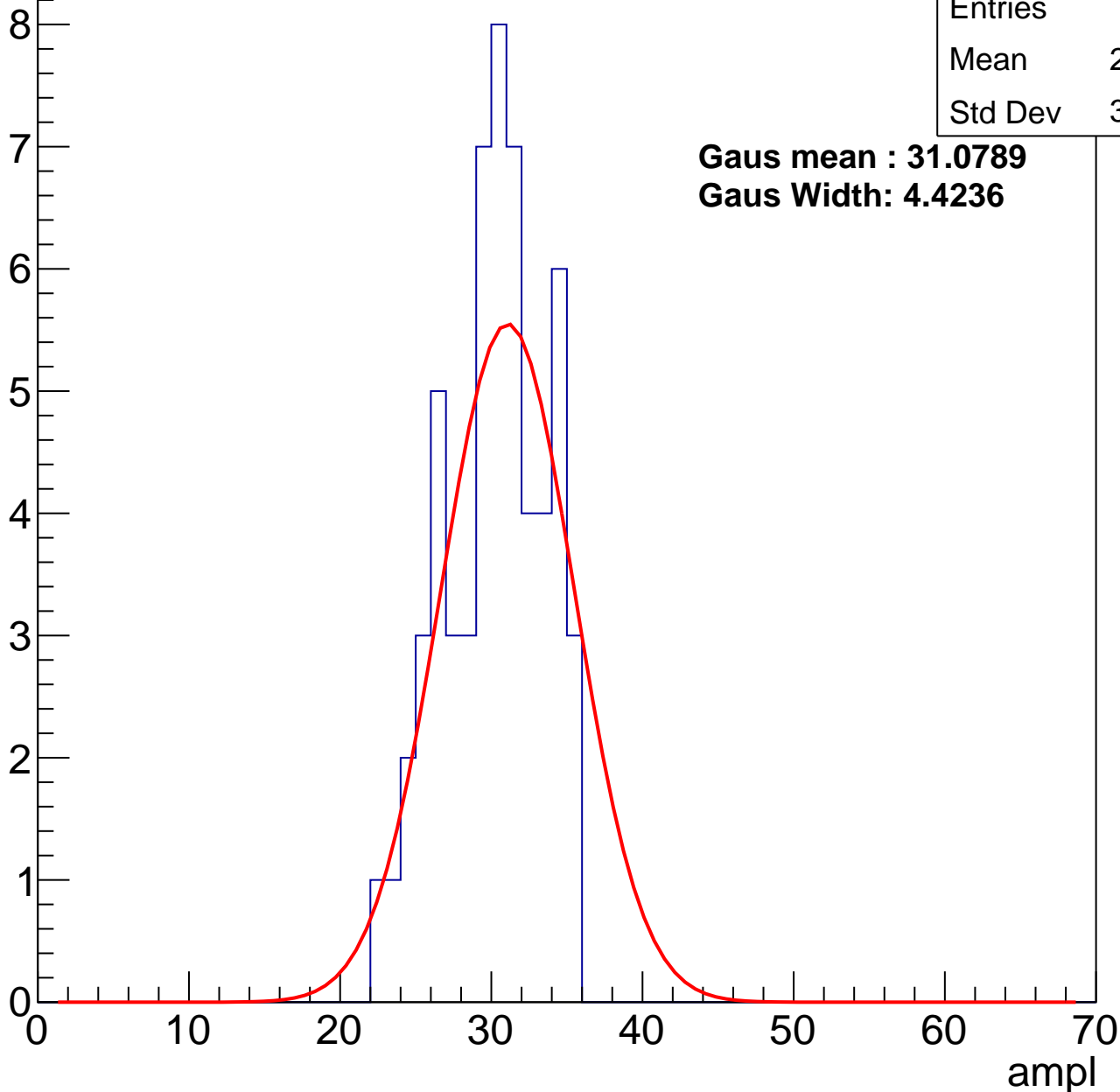
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	29.68
Std Dev	3.272

Gaus mean : 31.0789

Gaus Width: 4.4236



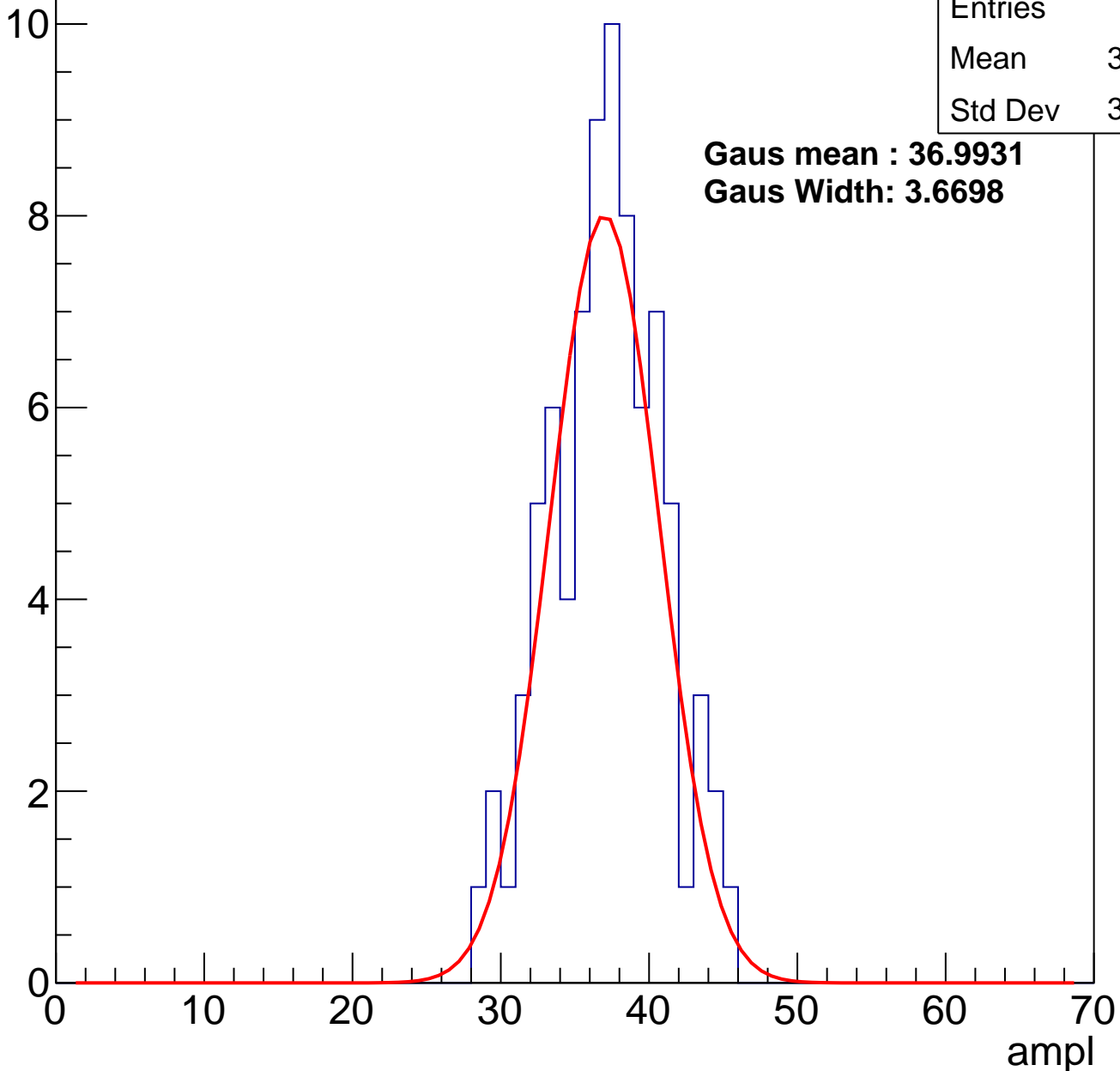
B1L102S, U19-ch47, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	36.65
Std Dev	3.719

Gaus mean : 36.9931
Gaus Width: 3.6698

Entry



B1L102S, U19-ch47, adc2

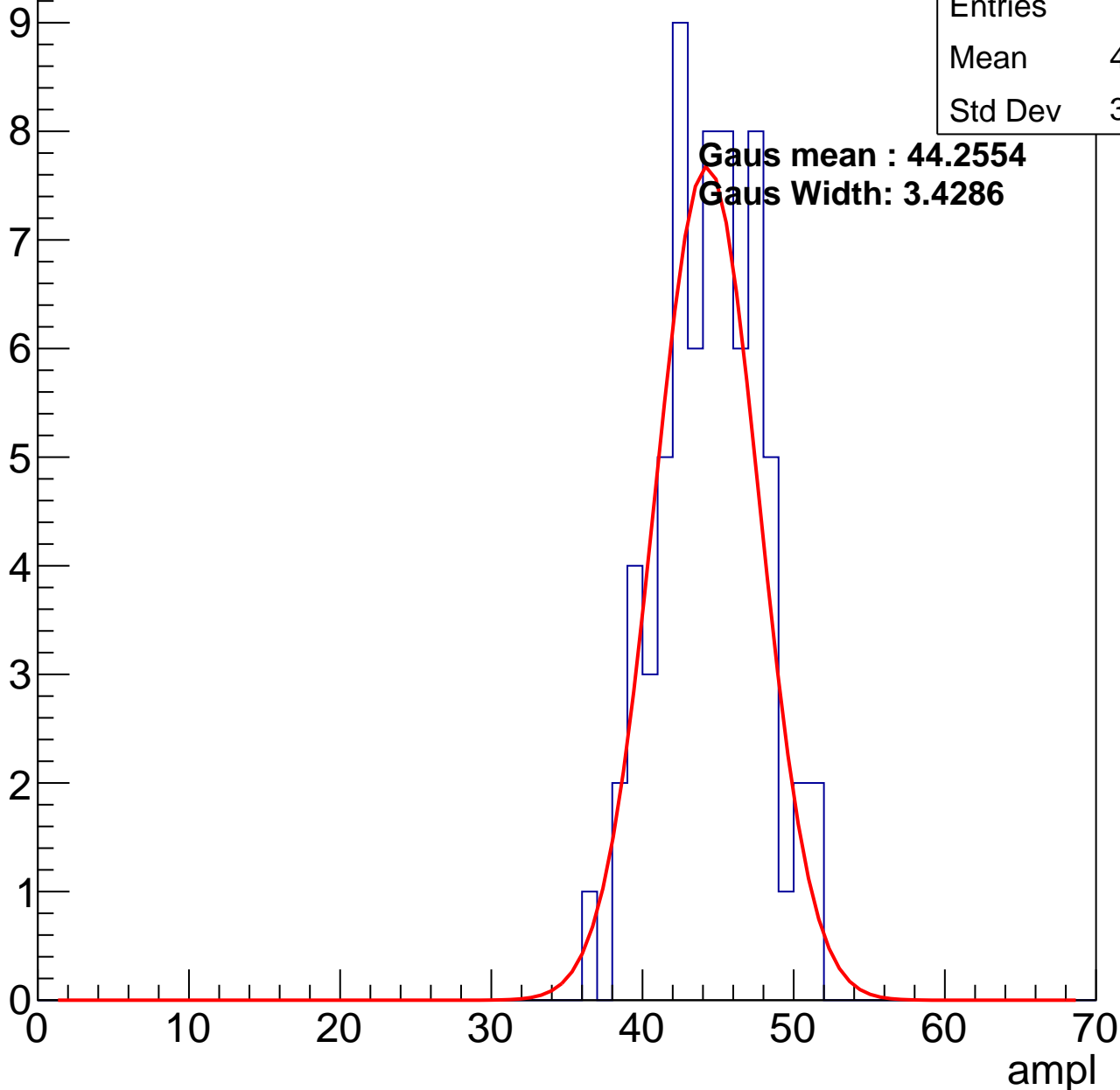
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	44.06
Std Dev	3.273

Gaus mean : 44.2554

Gaus Width: 3.4286

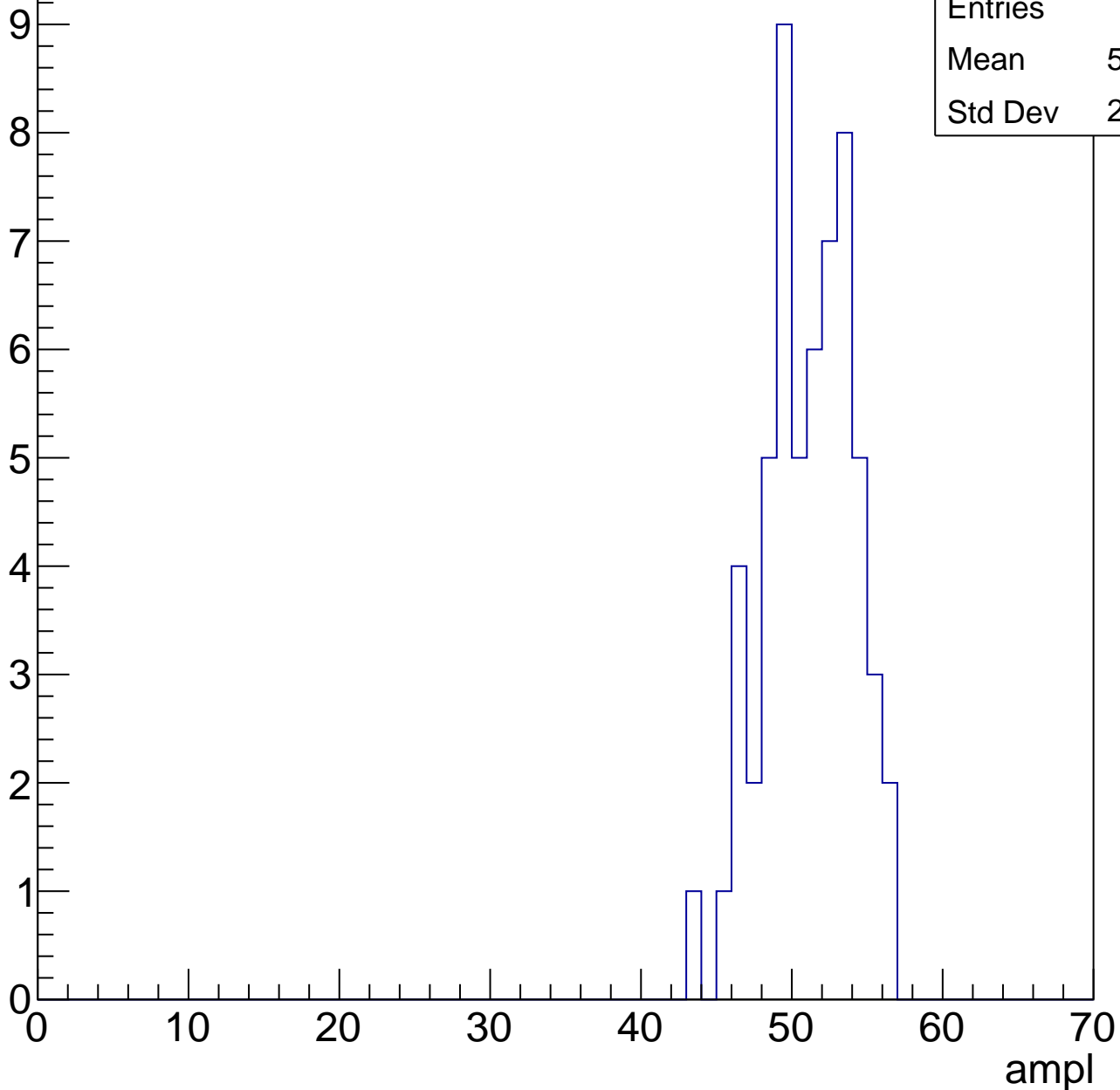


B1L102S, U19-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.66
Std Dev	2.916

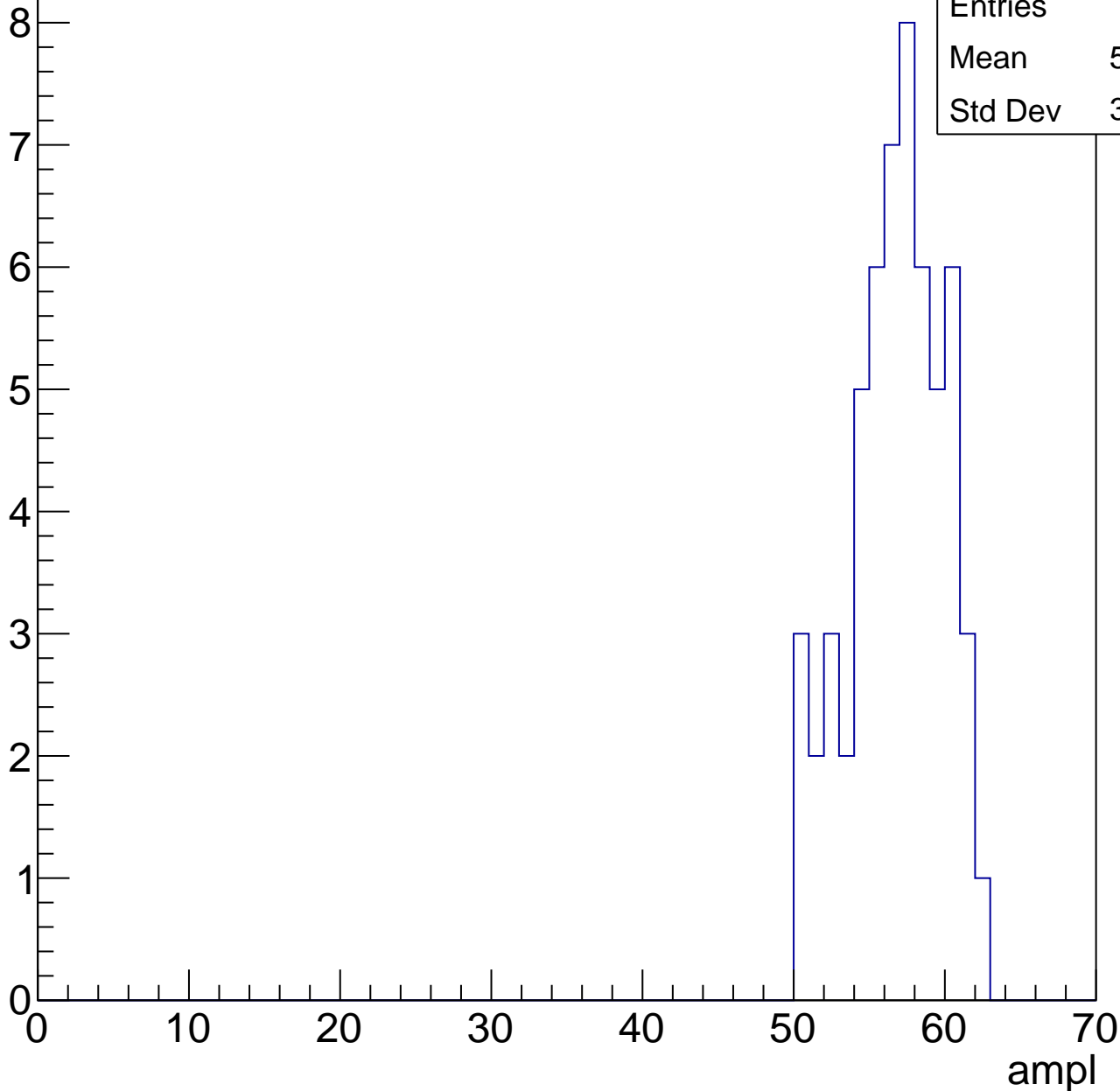


B1L102S, U19-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.32
Std Dev	3.044

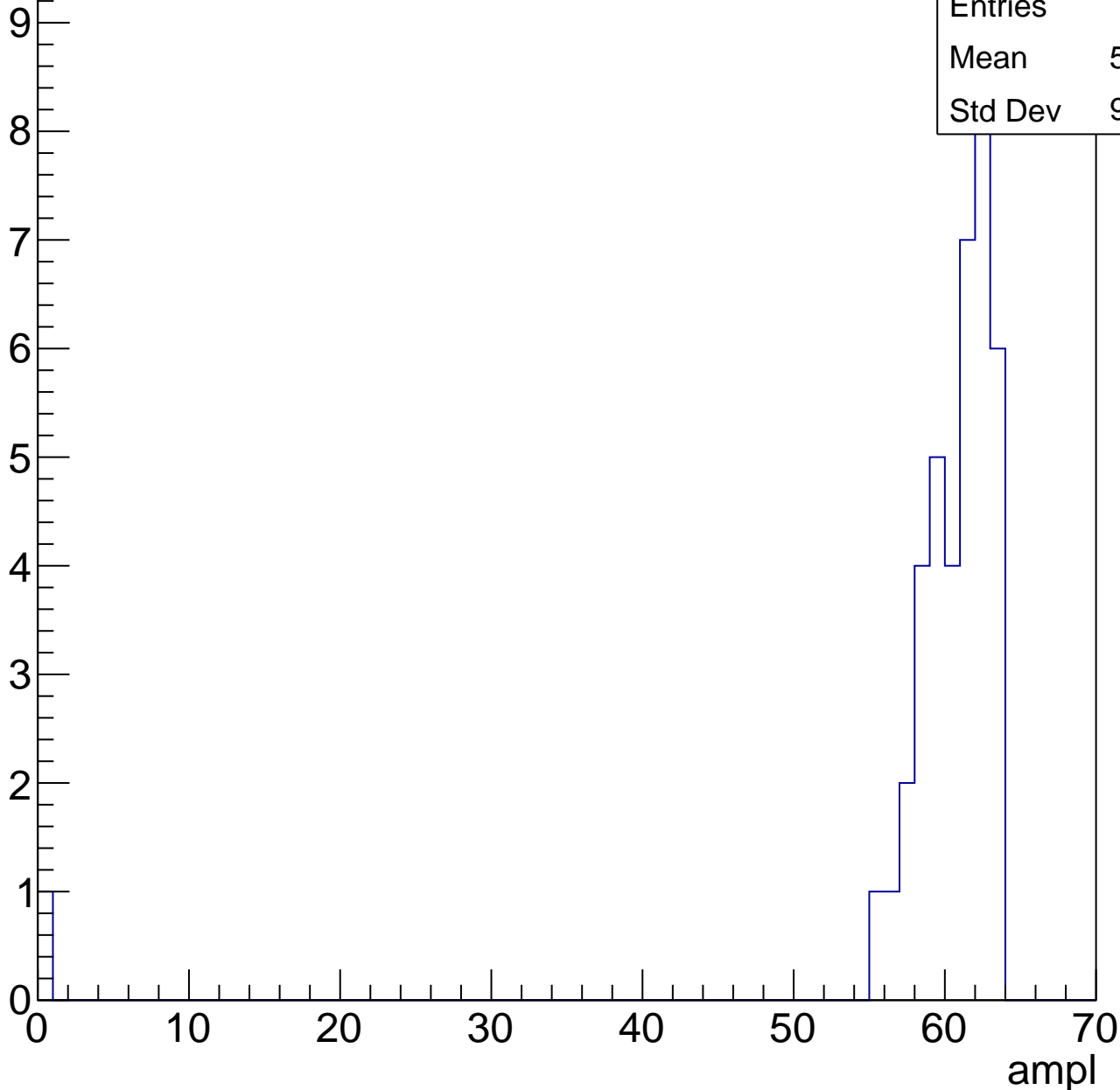


B1L102S, U19-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.88
Std Dev	9.652



B1L102S, U19-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch48, adc0

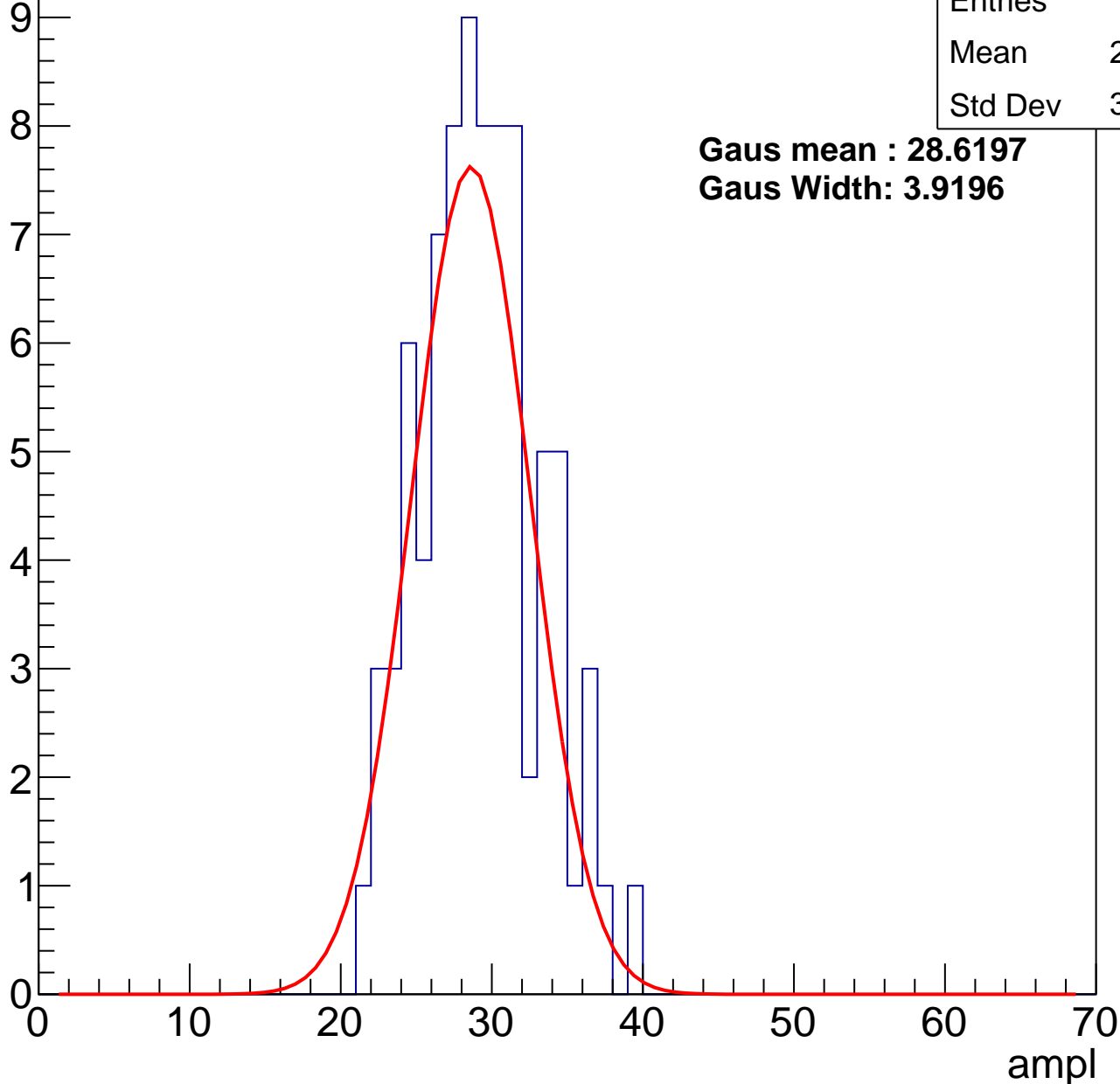
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.77
Std Dev	3.869

Gaus mean : 28.6197

Gaus Width: 3.9196



B1L102S, U19-ch48, adc1

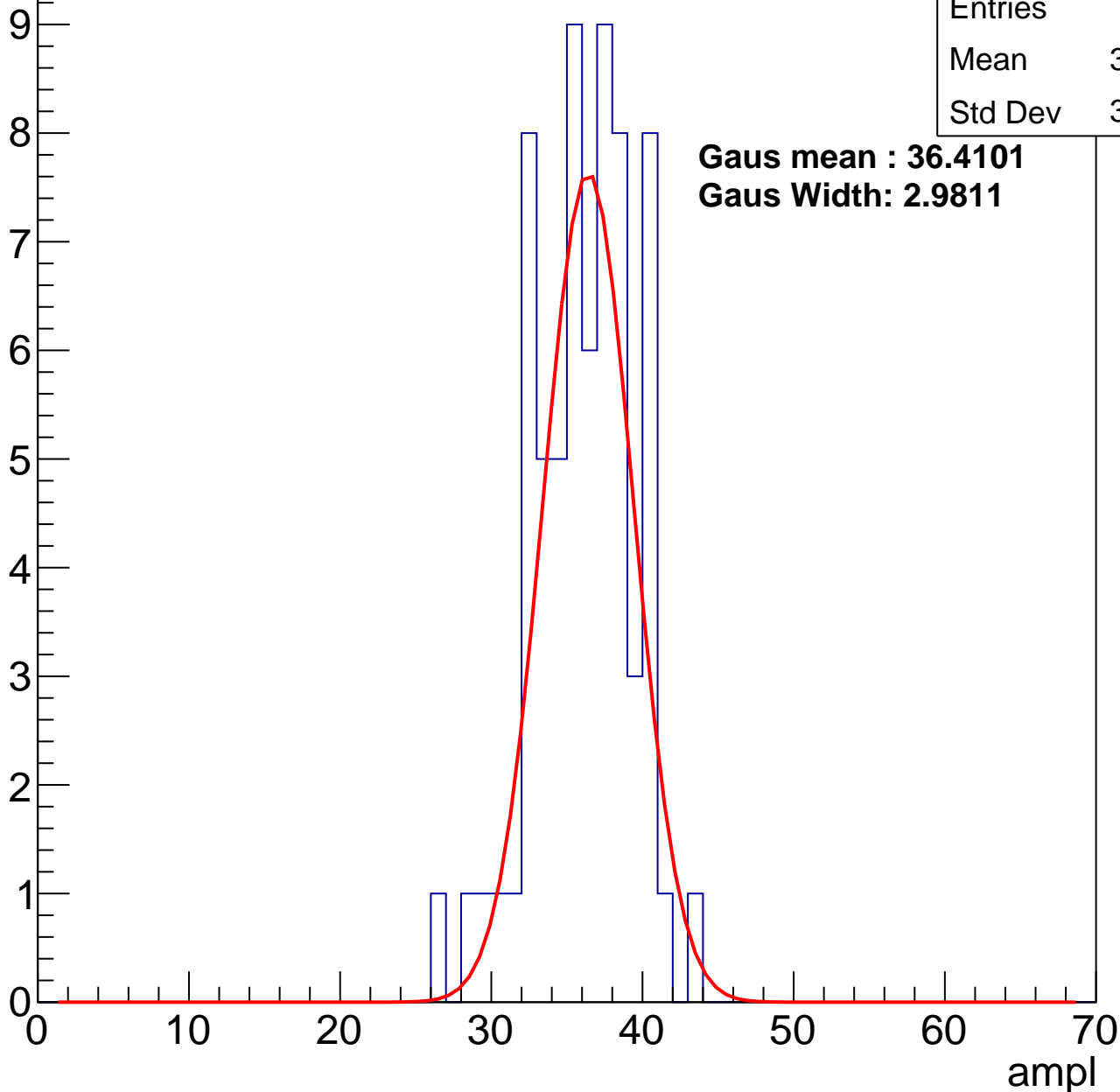
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.65
Std Dev	3.293

Gaus mean : 36.4101

Gaus Width: 2.9811



B1L102S, U19-ch48, adc2

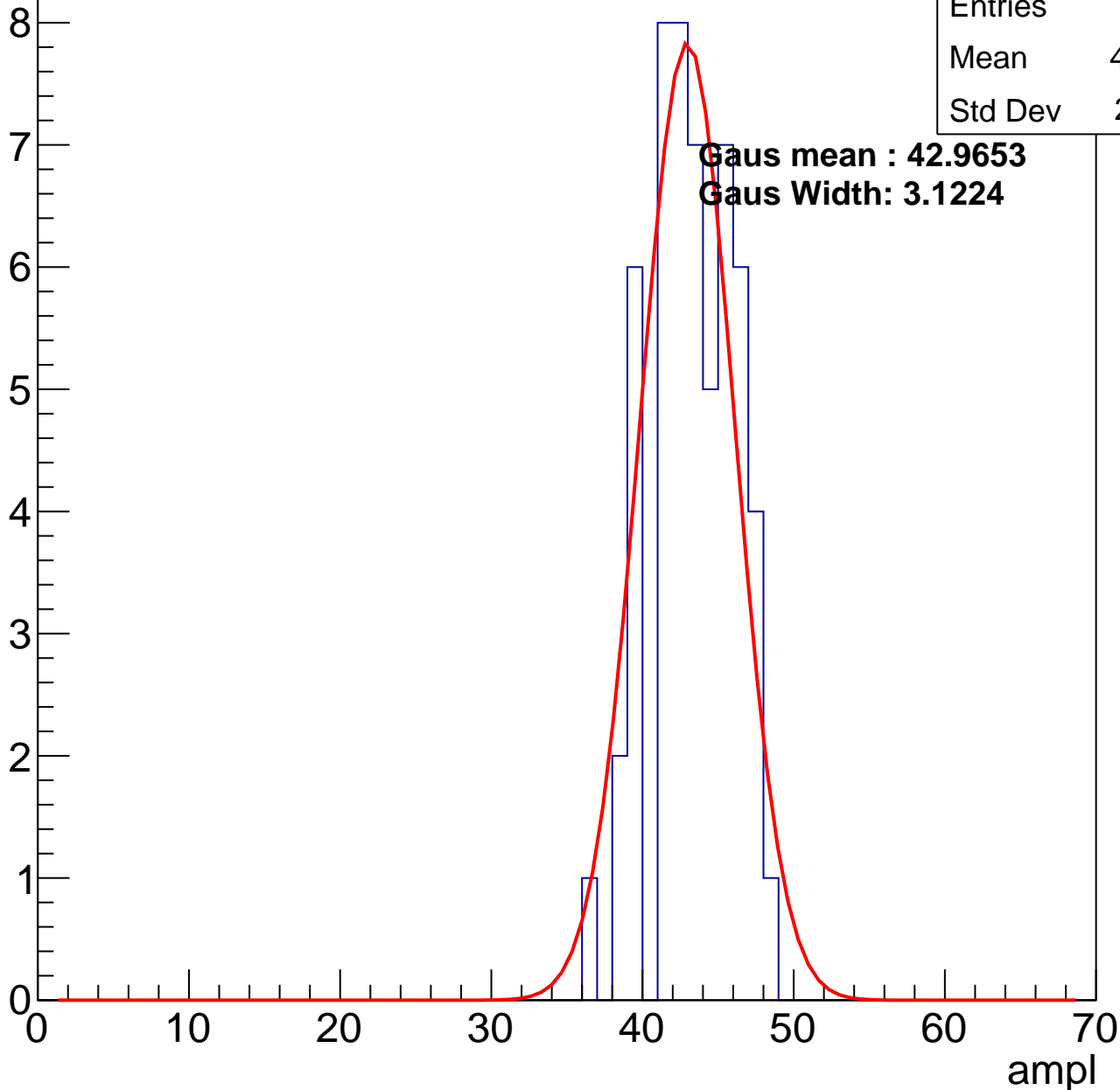
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.87
Std Dev	2.731

Gaus mean : 42.9653

Gaus Width: 3.1224

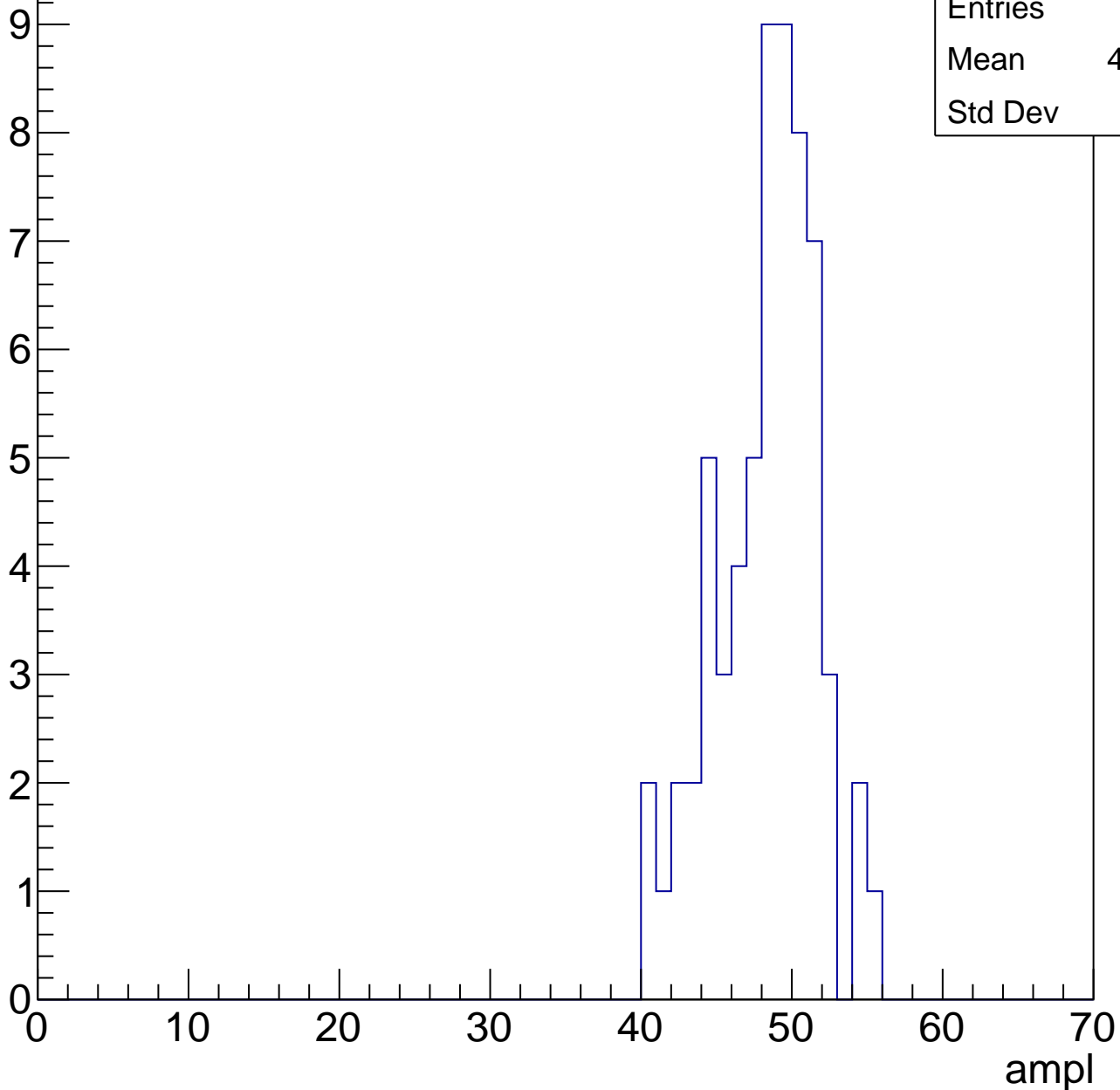


B1L102S, U19-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.84
Std Dev	3.33

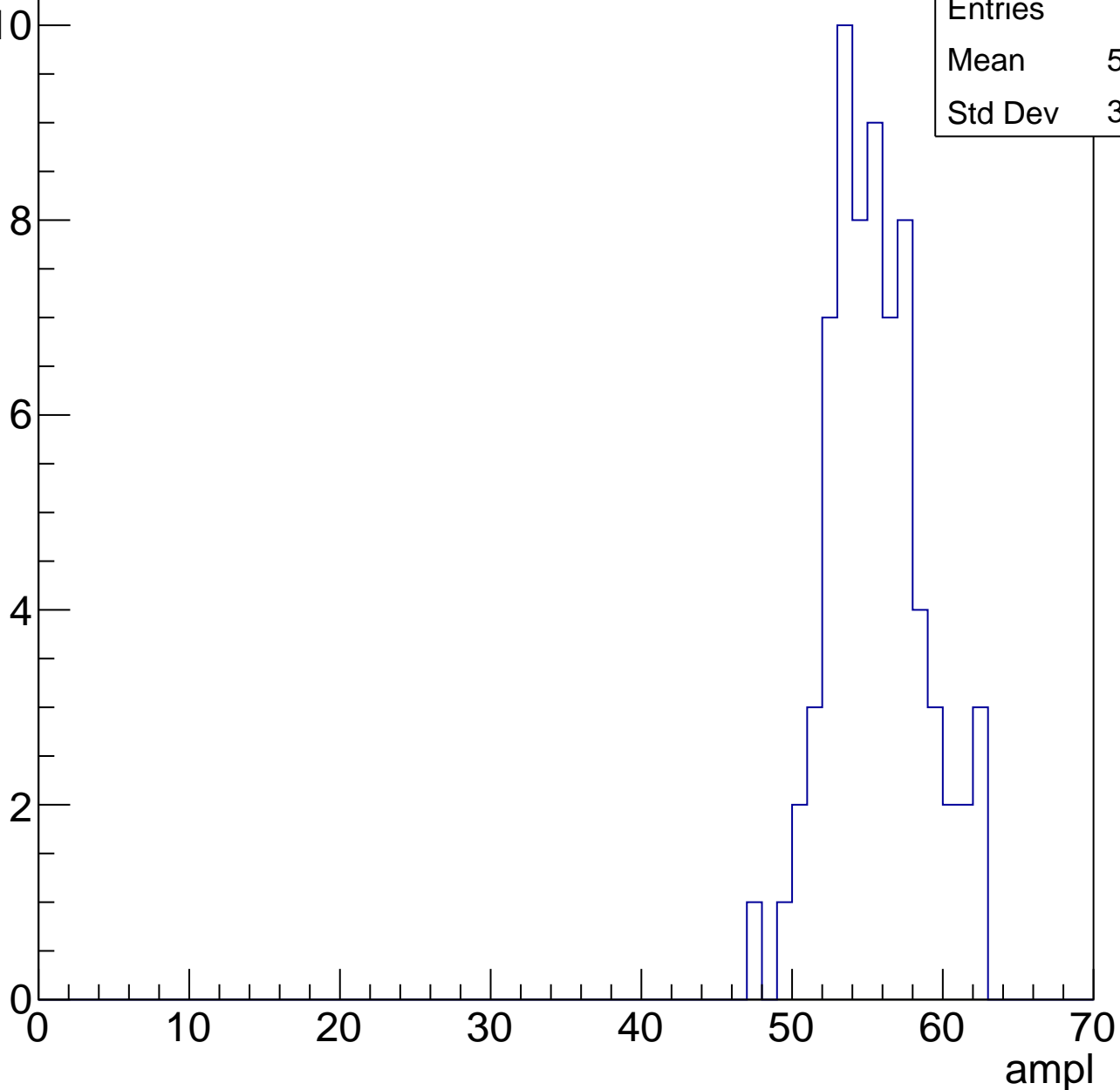


B1L102S, U19-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.07
Std Dev	3.164



B1L102S, U19-ch48, adc5

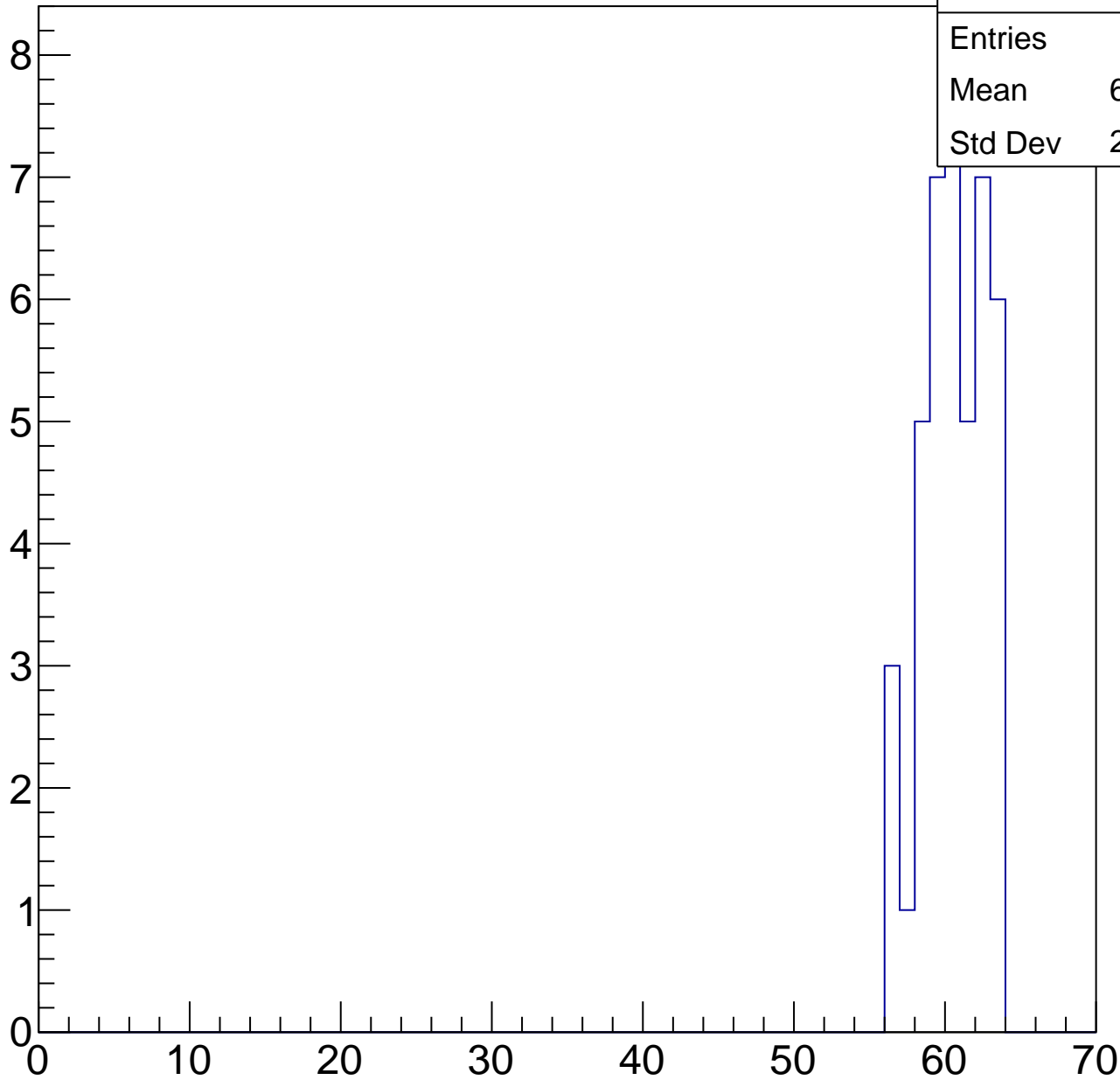
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.12
Std Dev	2.014

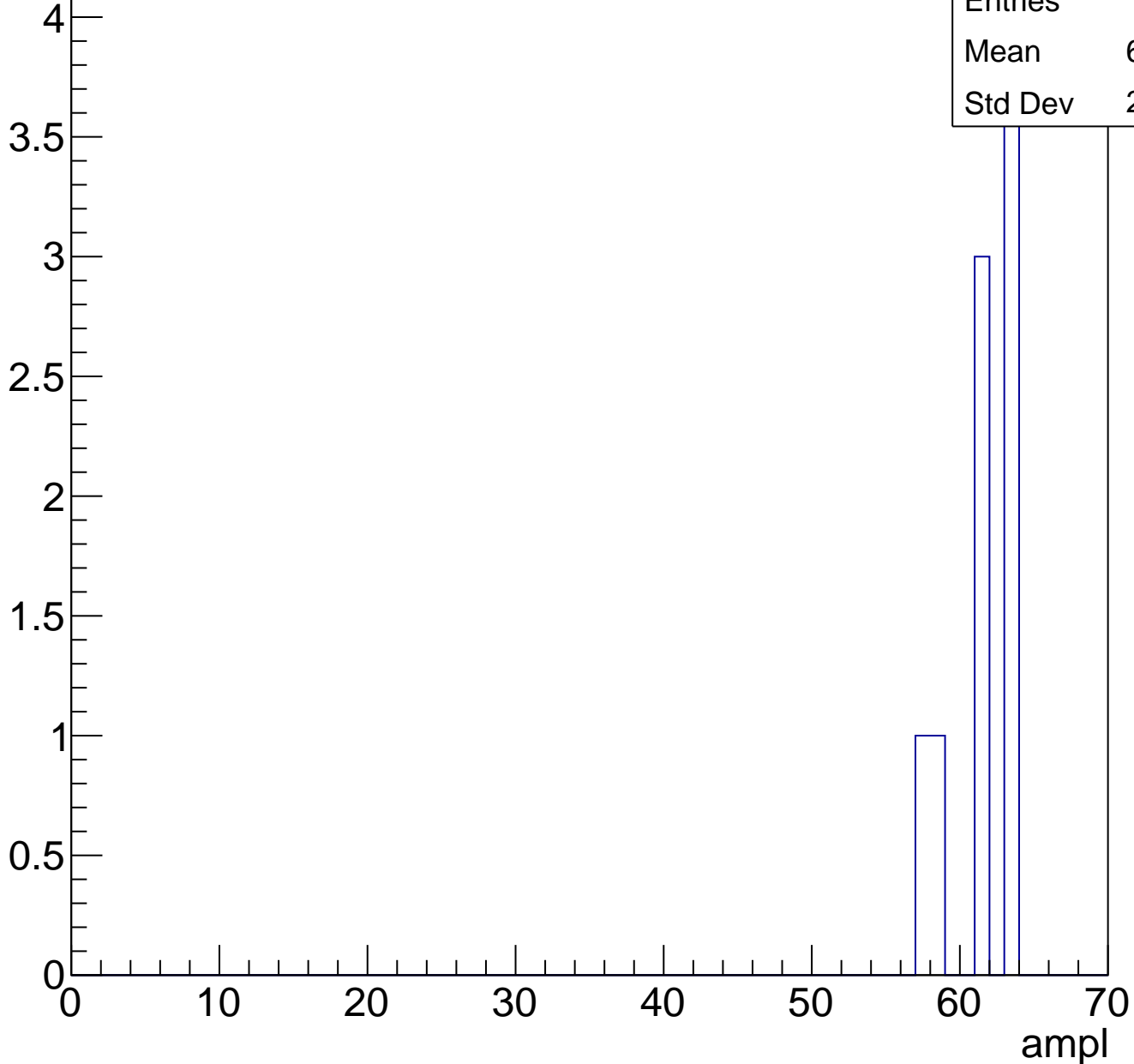
ampl



B1L102S, U19-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch49, adc0

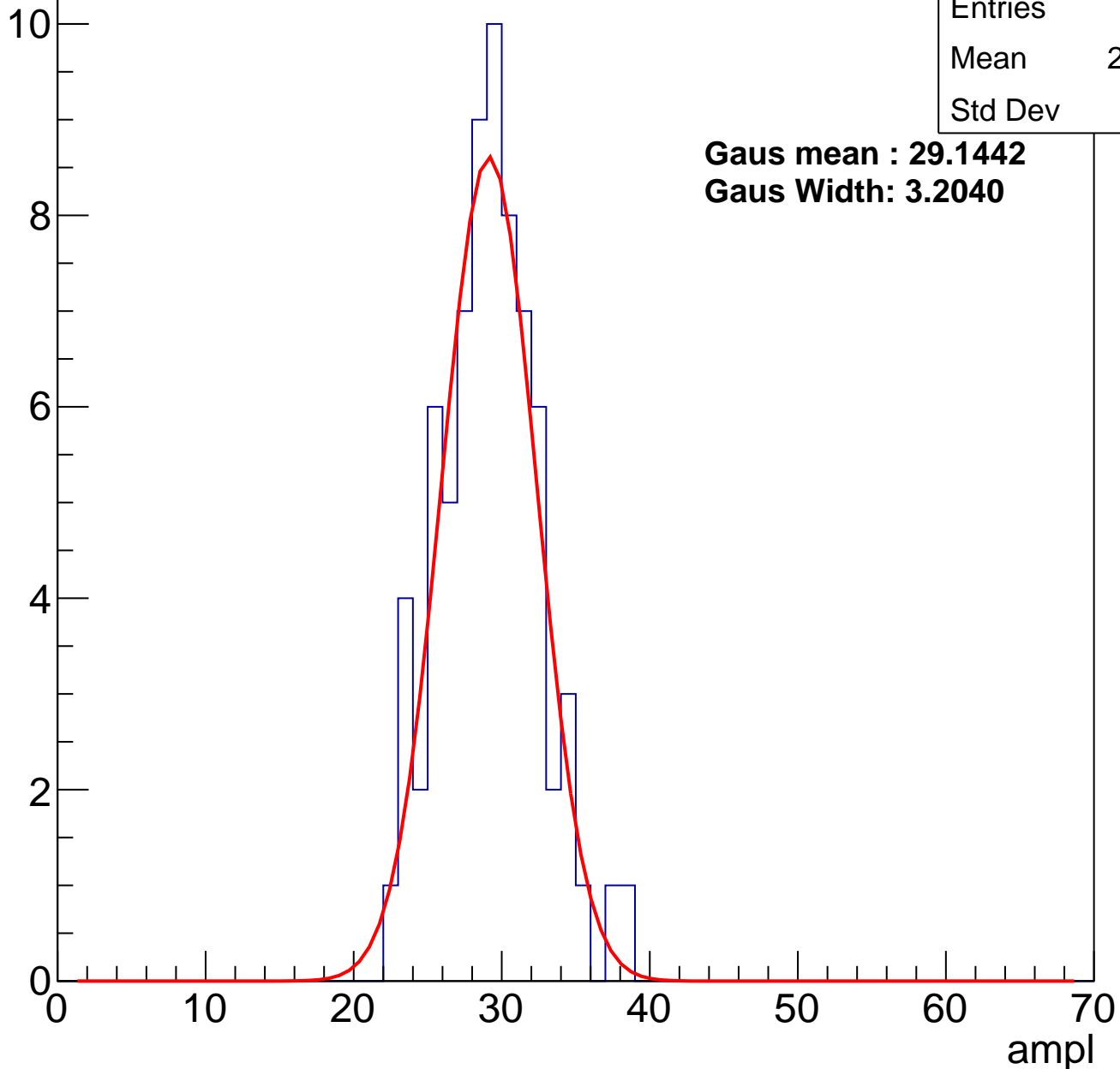
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	28.77
Std Dev	3.3

Gaus mean : 29.1442

Gaus Width: 3.2040

Entry



B1L102S, U19-ch49, adc1

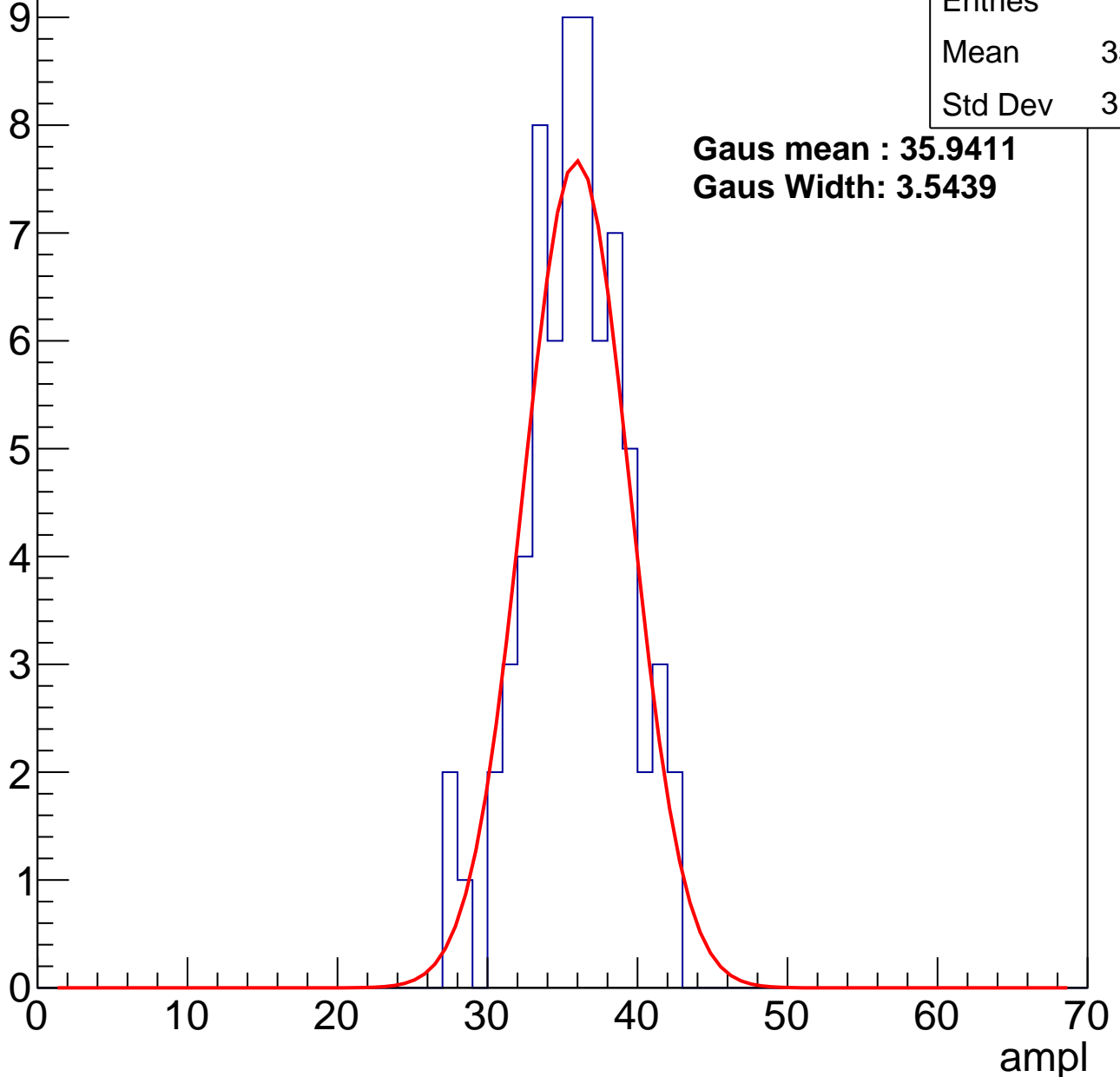
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.36
Std Dev	3.345

Gaus mean : 35.9411

Gaus Width: 3.5439



B1L102S, U19-ch49, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.38
Std Dev	3.153

Gaus mean : 43.1022

Gaus Width: 3.3376

10

8

6

4

2

0

0

10

20

30

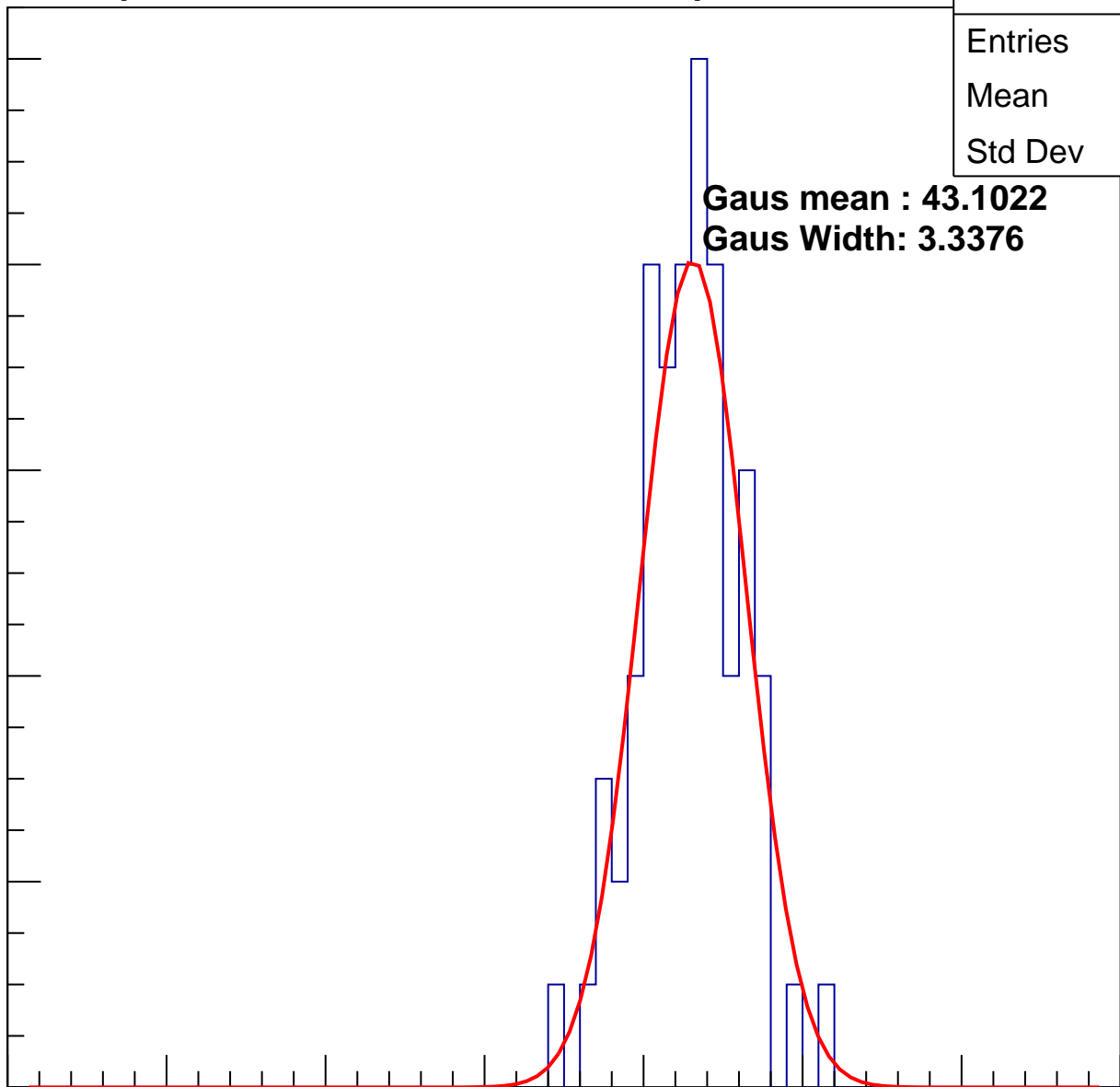
40

50

60

70

ampl



B1L102S, U19-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	49
Std Dev	2.986

Entry

10

8

6

4

2

0

0

10

20

30

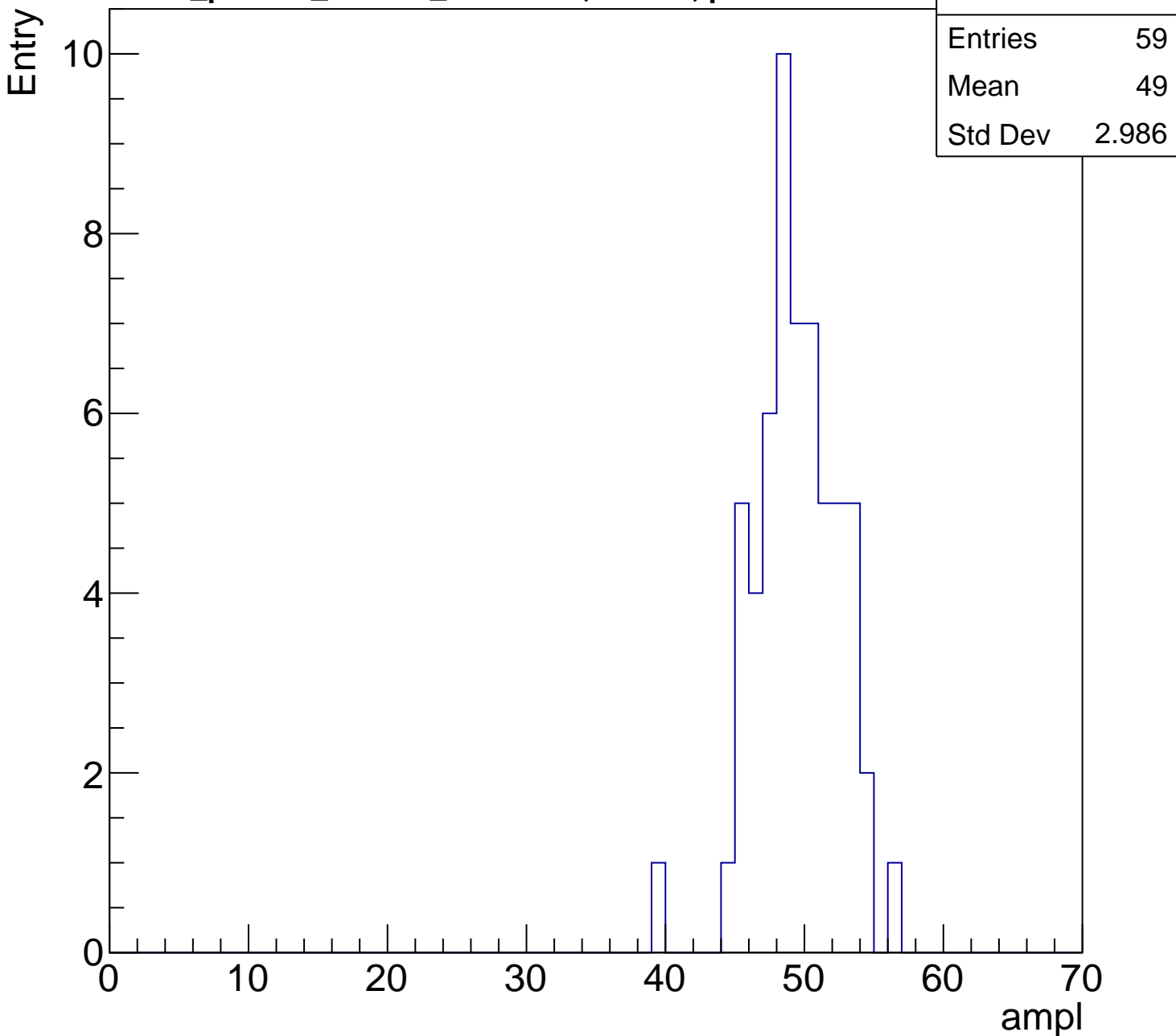
40

50

60

70

ampl

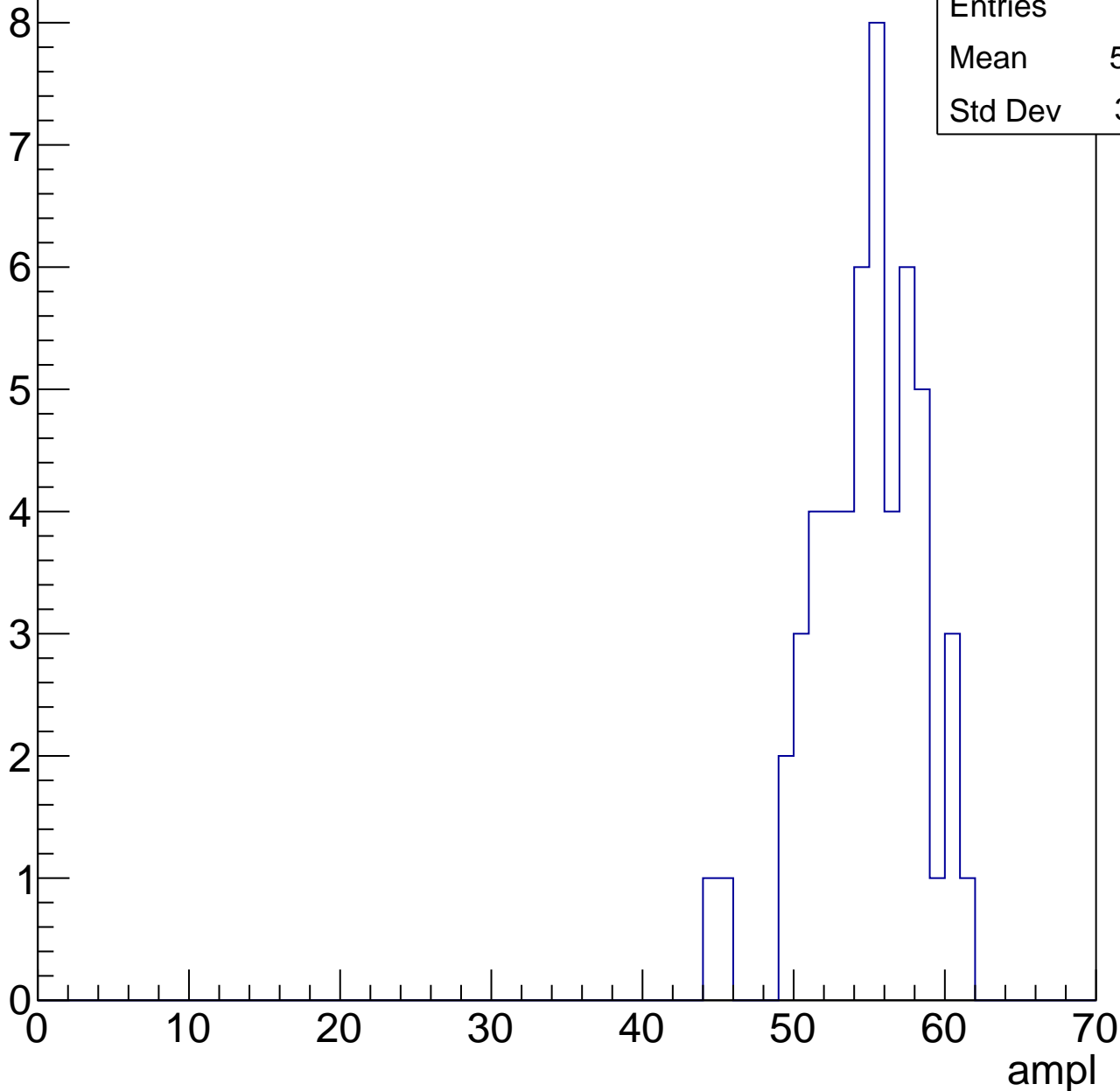


B1L102S, U19-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.36
Std Dev	3.551

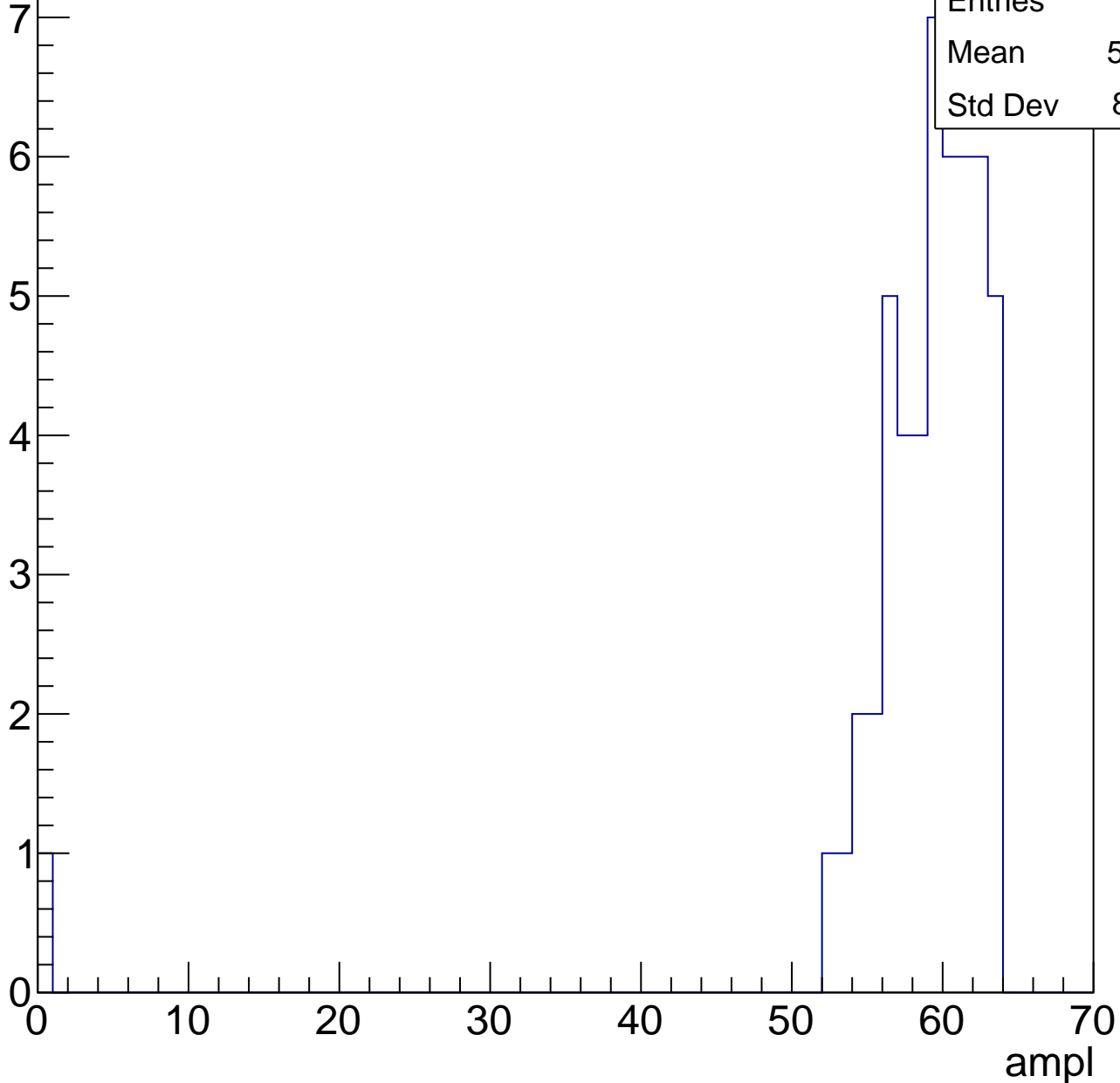


B1L102S, U19-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.78
Std Dev	8.721

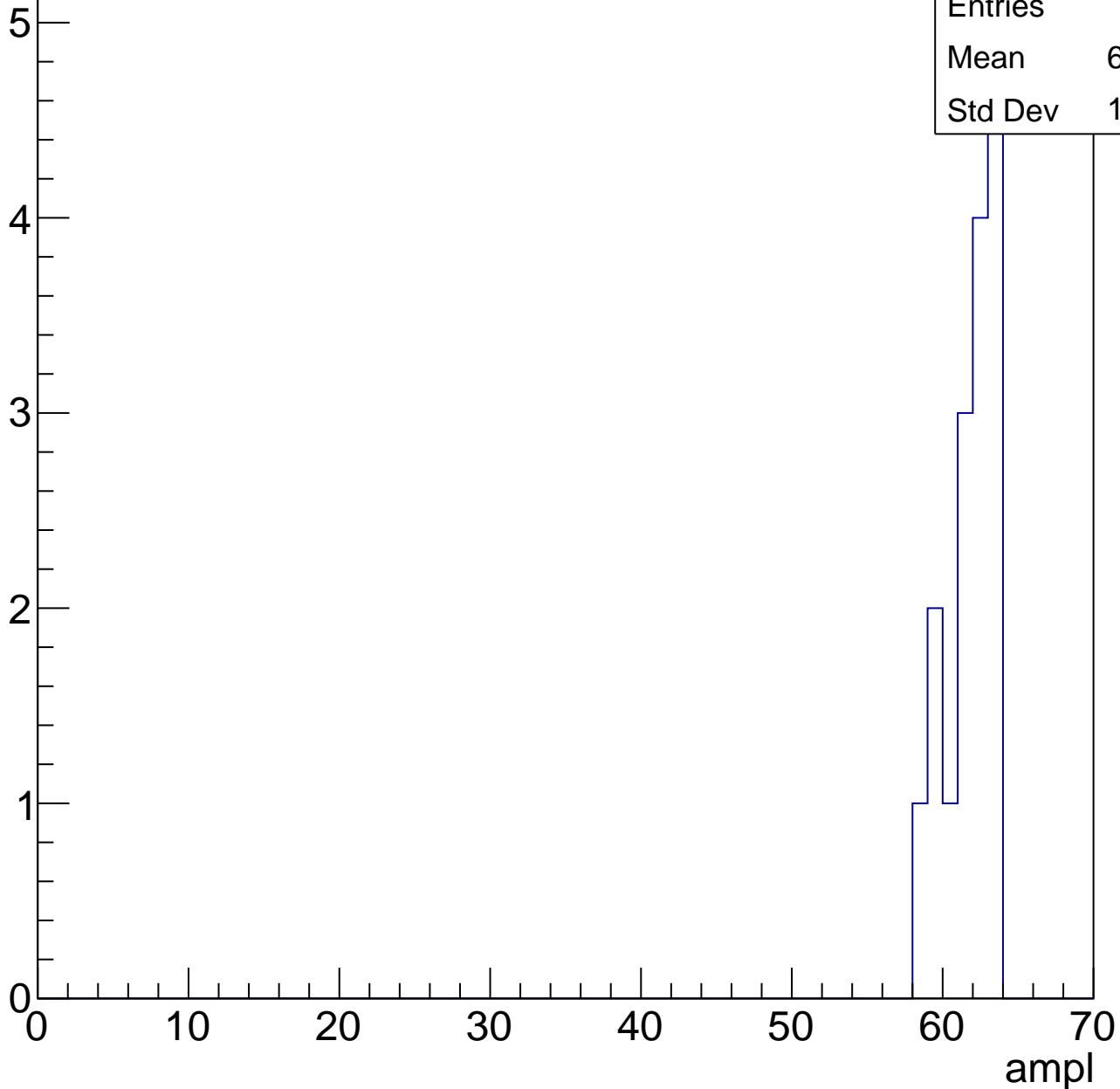


B1L102S, U19-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.38
Std Dev	1.576



B1L102S, U19-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch50, adc0

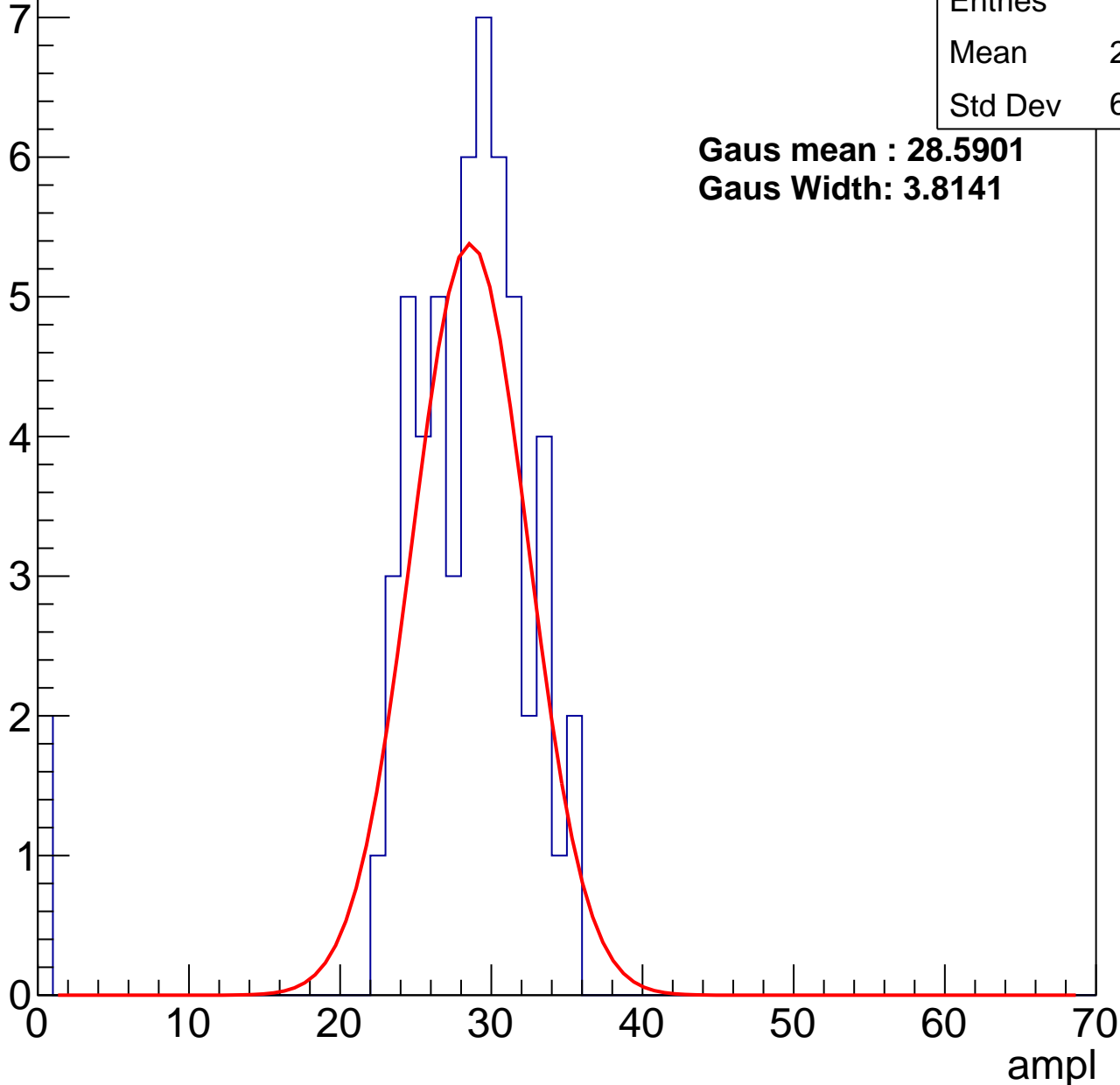
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.29
Std Dev	6.169

Gaus mean : 28.5901

Gaus Width: 3.8141



B1L102S, U19-ch50, adc1

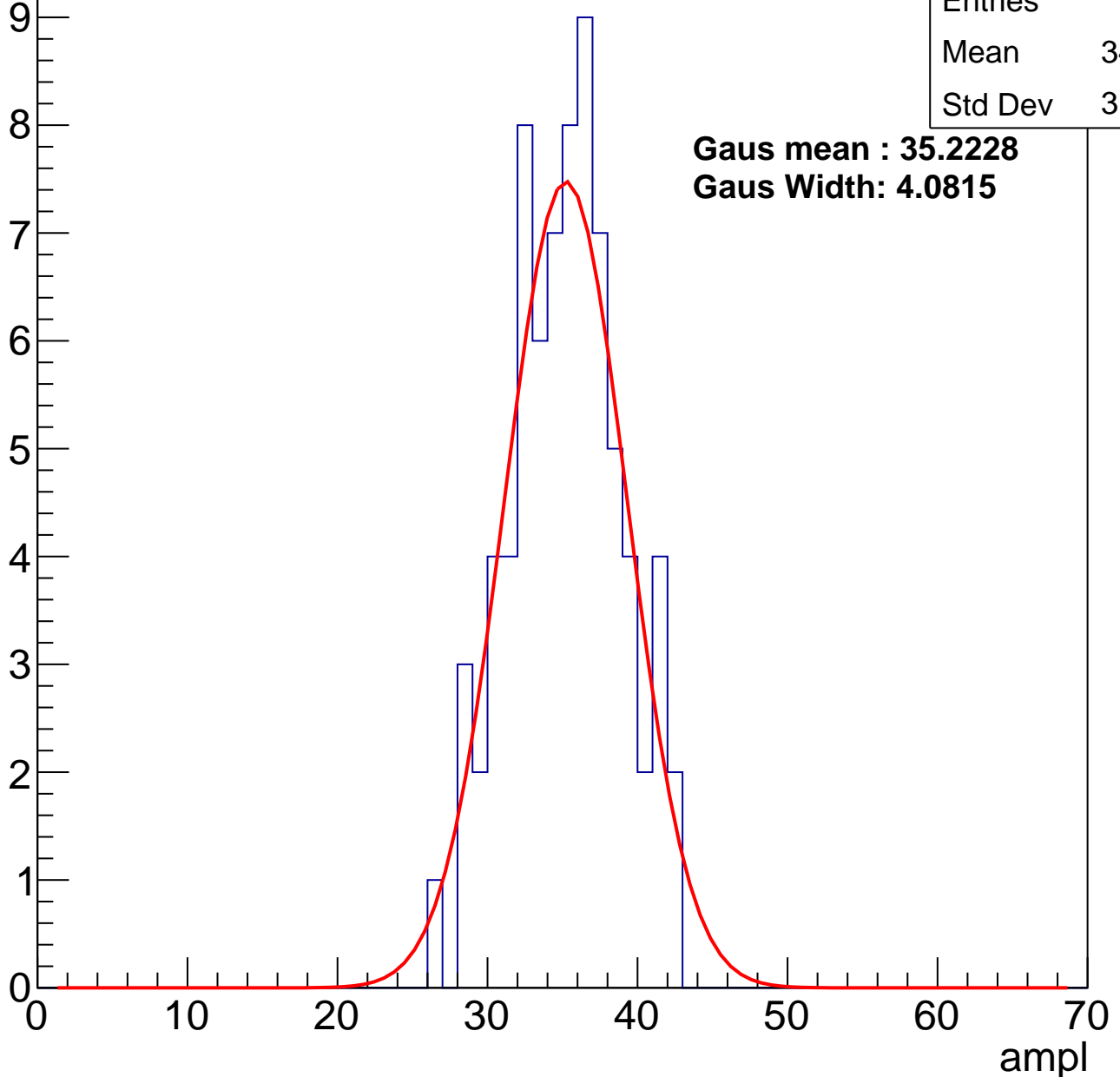
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.75
Std Dev	3.628

Gaus mean : 35.2228

Gaus Width: 4.0815



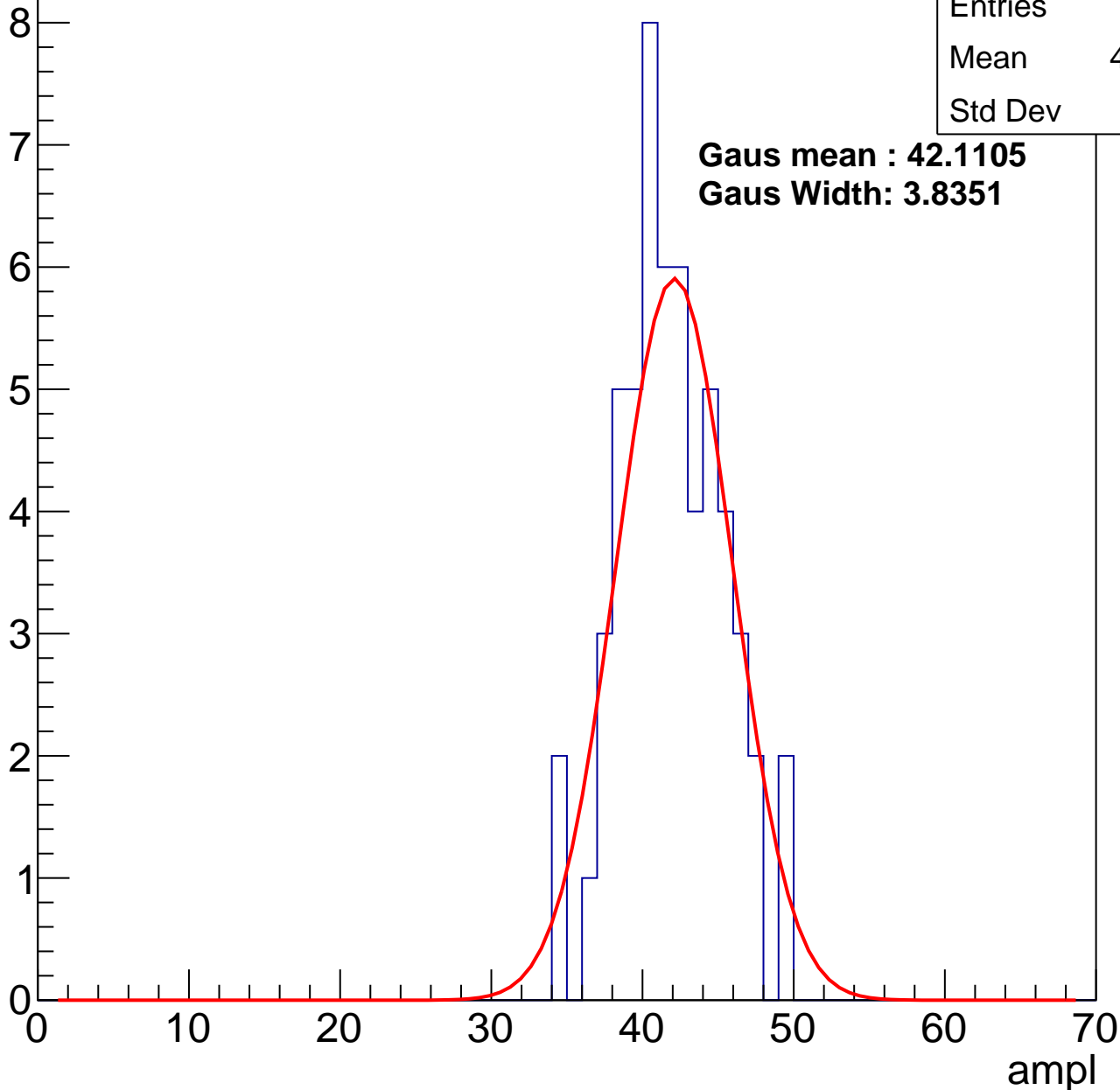
B1L102S, U19-ch50, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.43
Std Dev	3.38

Gaus mean : 42.1105
Gaus Width: 3.8351

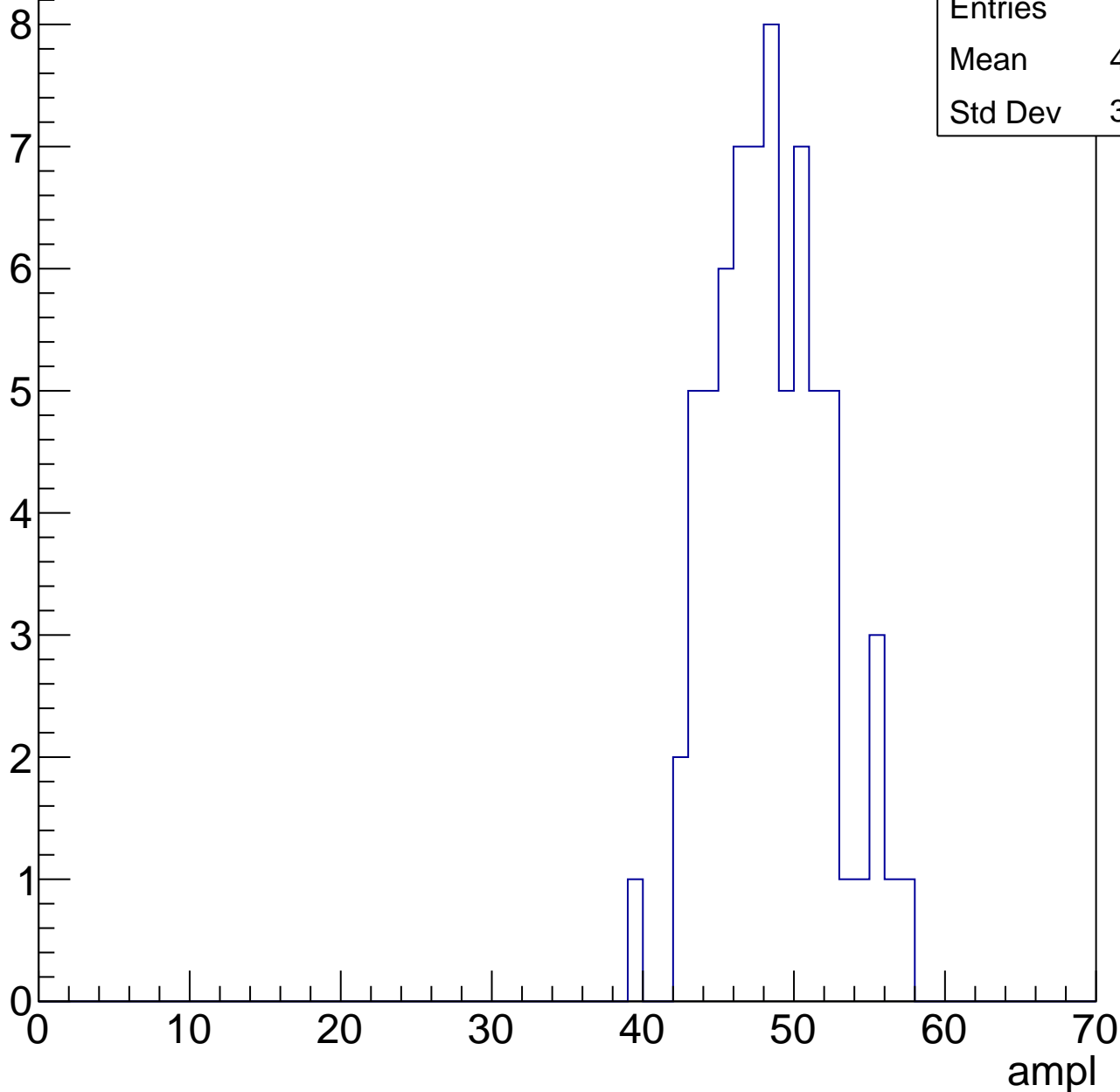


B1L102S, U19-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.97
Std Dev	3.699

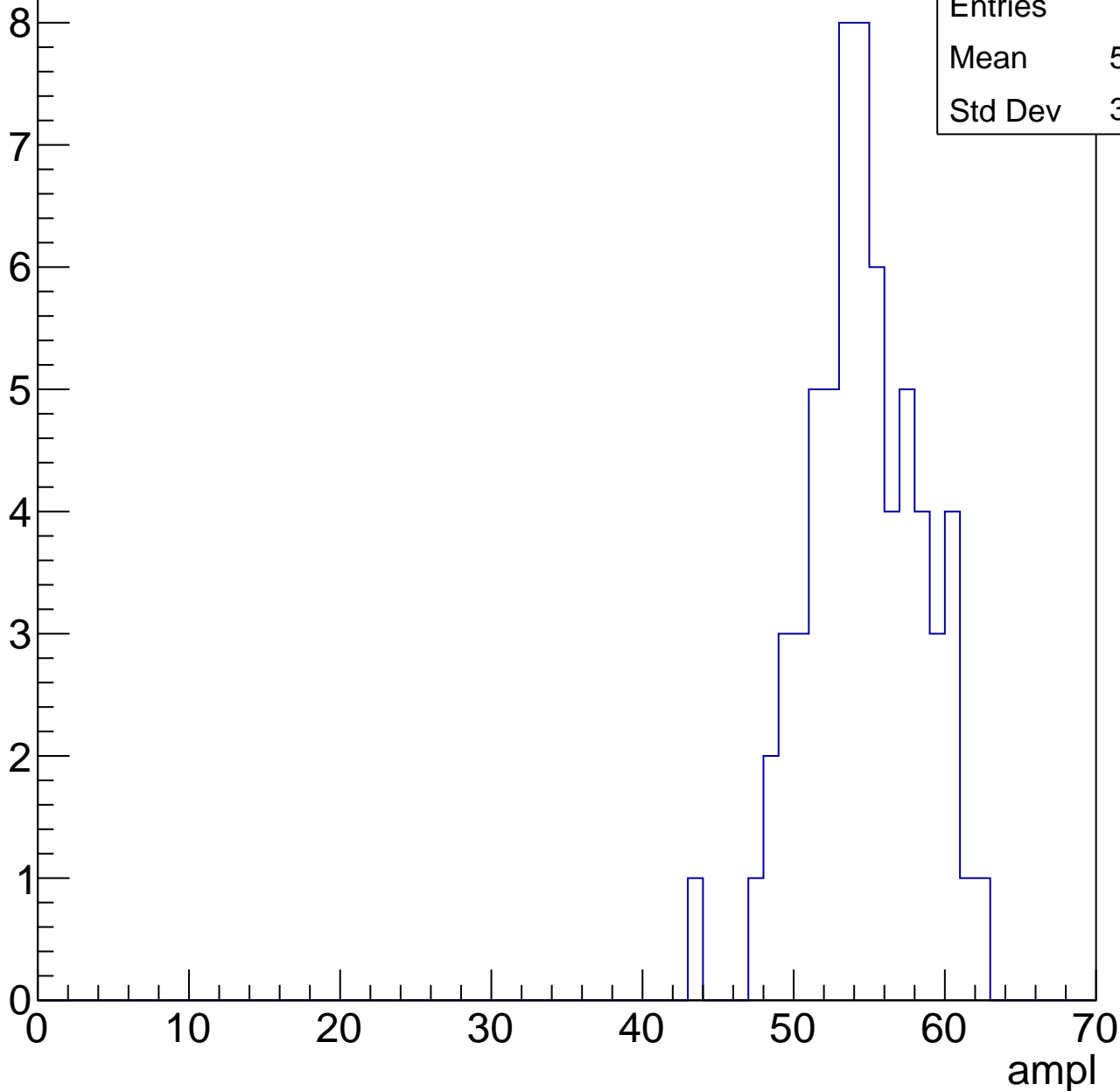


B1L102S, U19-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.14
Std Dev	3.749

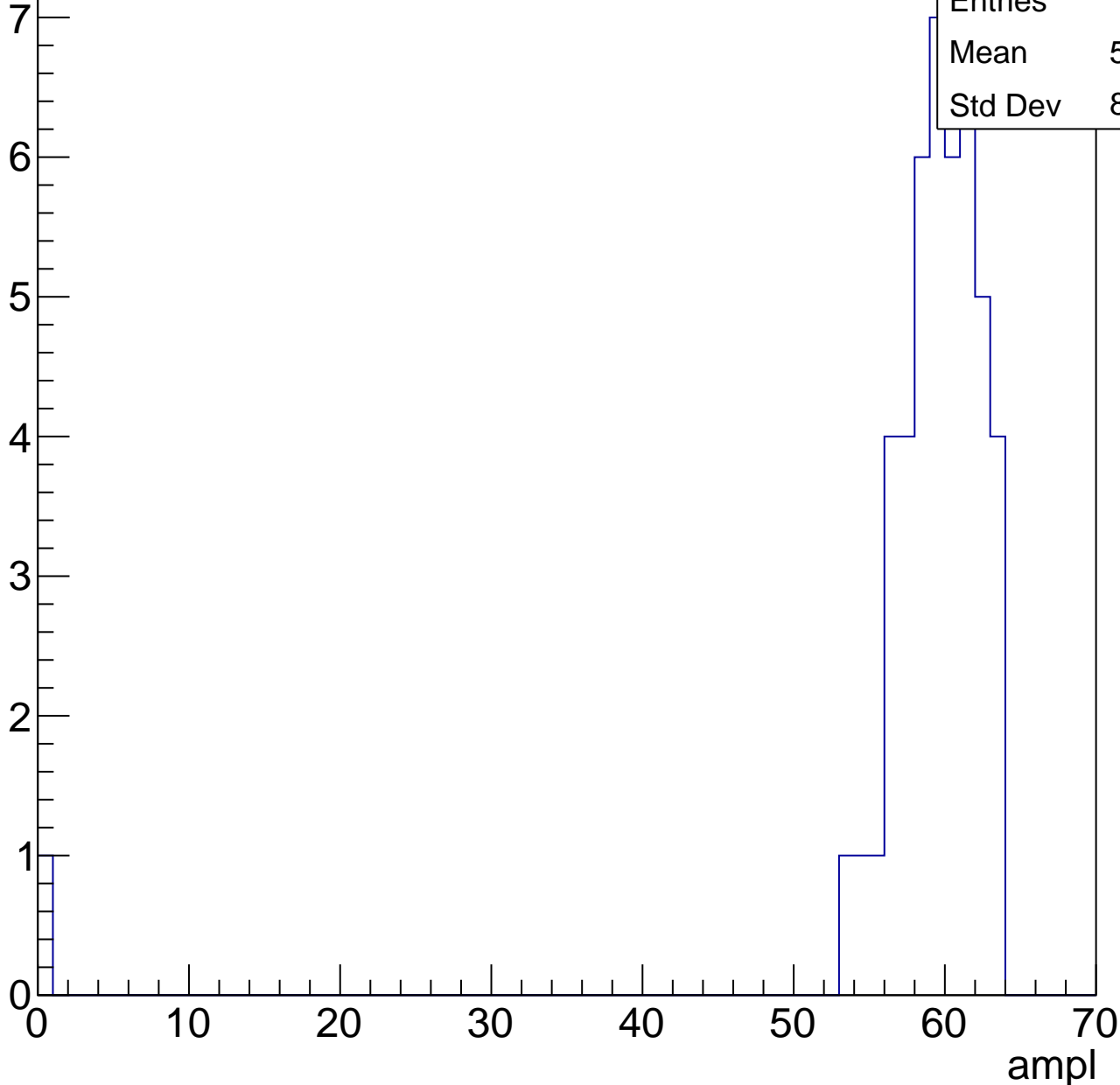


B1L102S, U19-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.96
Std Dev	8.882

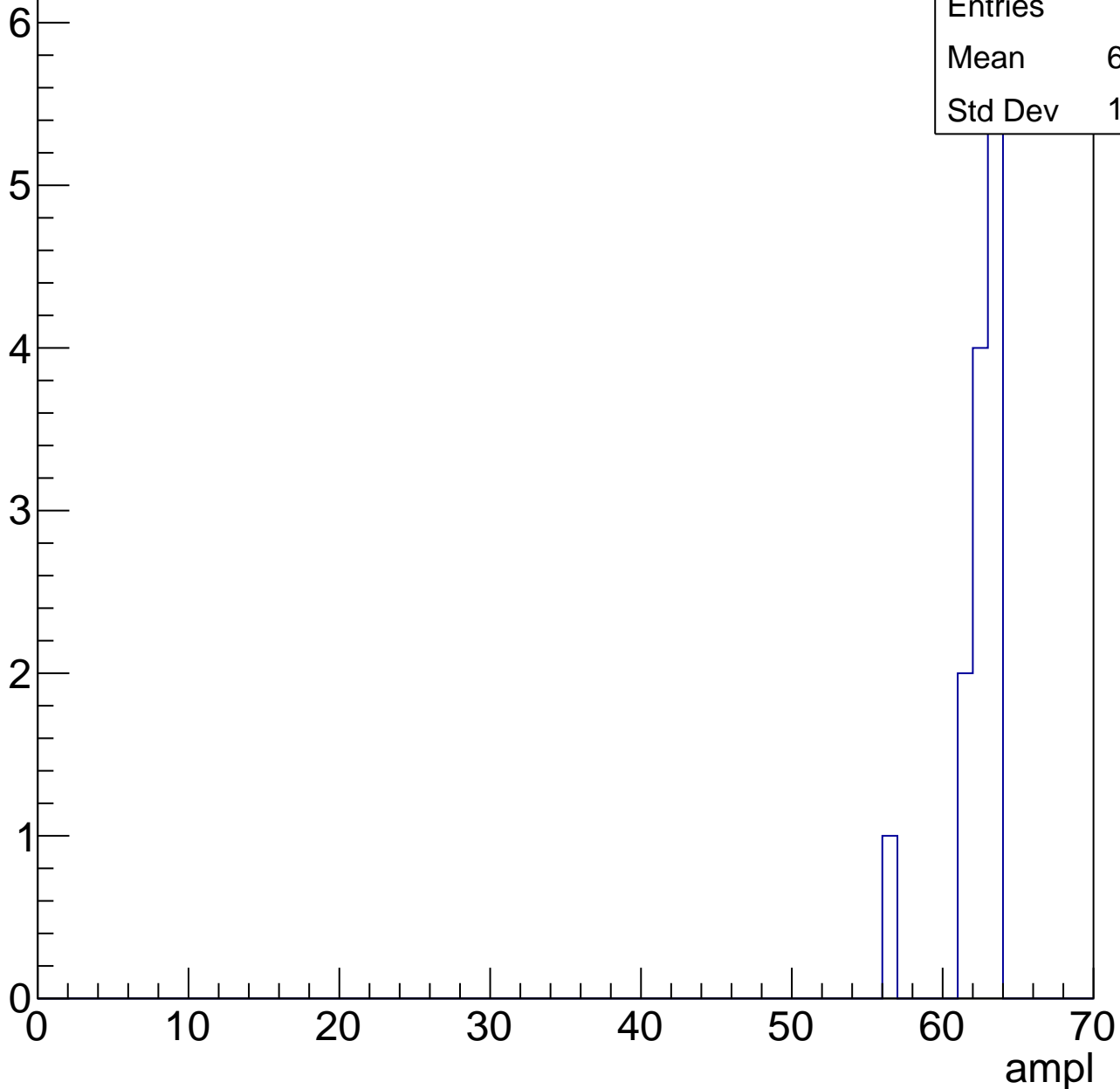


B1L102S, U19-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.85
Std Dev	1.833



B1L102S, U19-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch51, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	30.89
Std Dev	3.204

Gaus mean : 31.2975

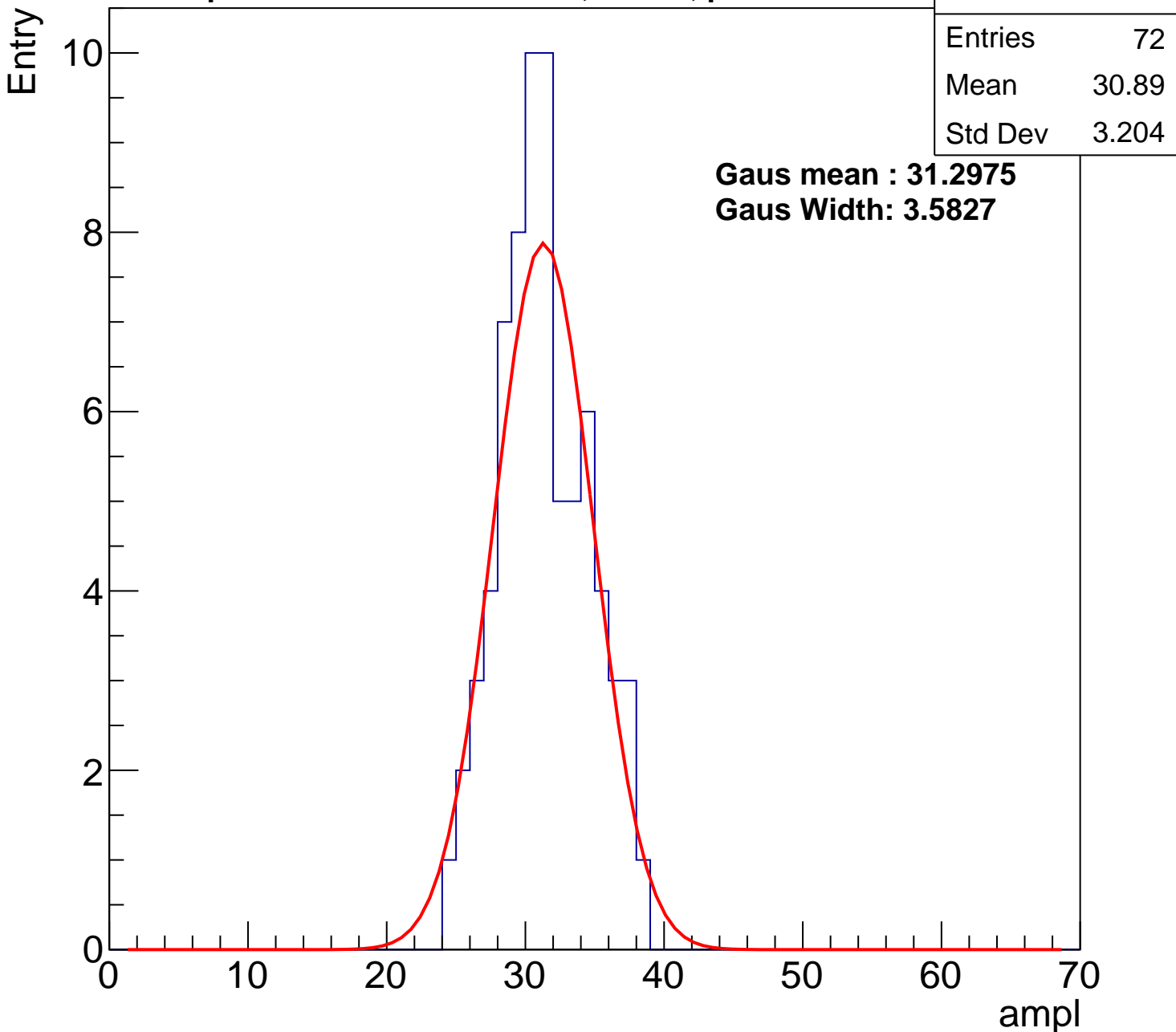
Gaus Width: 3.5827

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



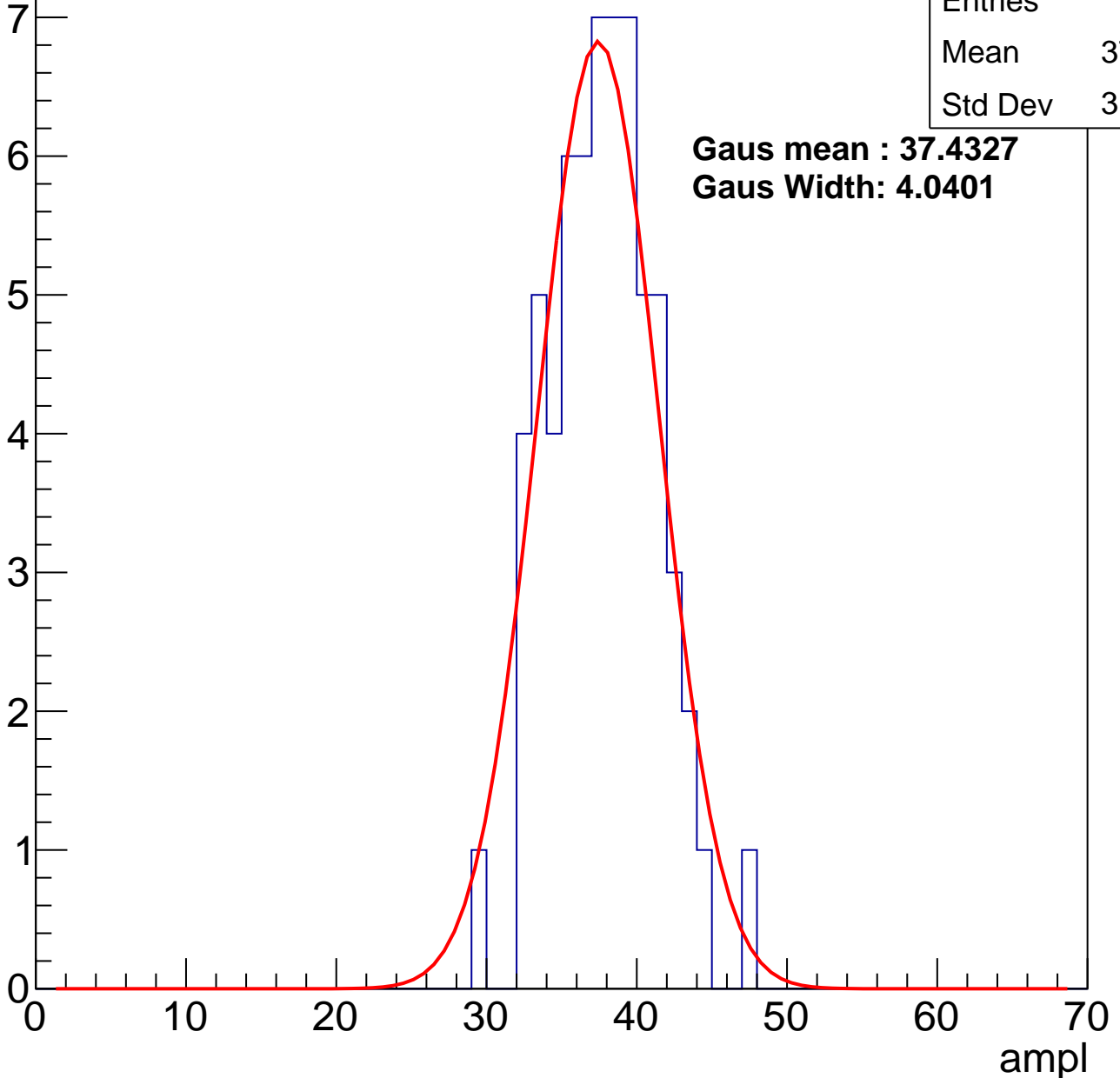
B1L102S, U19-ch51, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.34
Std Dev	3.442

Gaus mean : 37.4327
Gaus Width: 4.0401

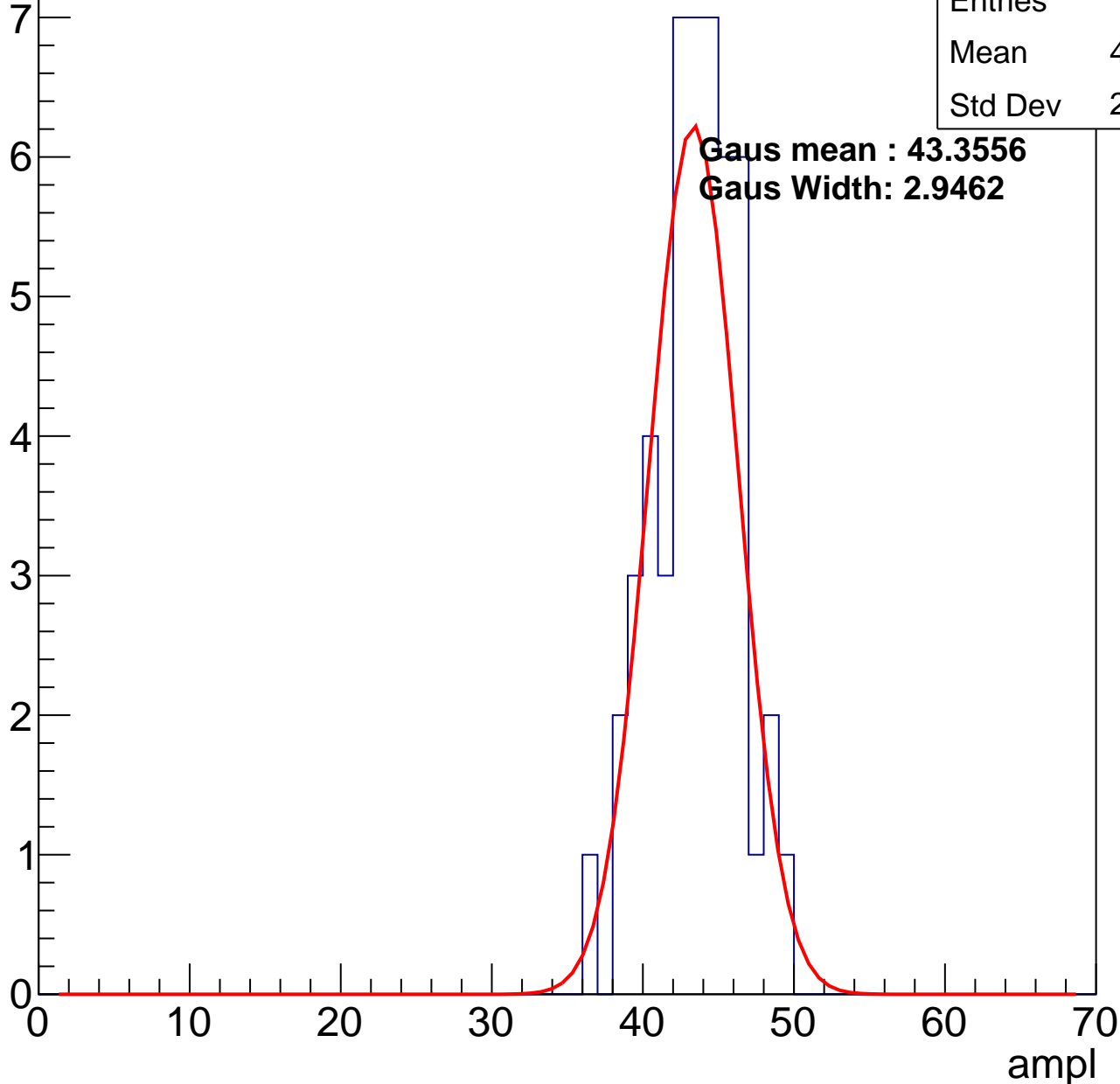


B1L102S, U19-ch51, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.06
Std Dev	2.789

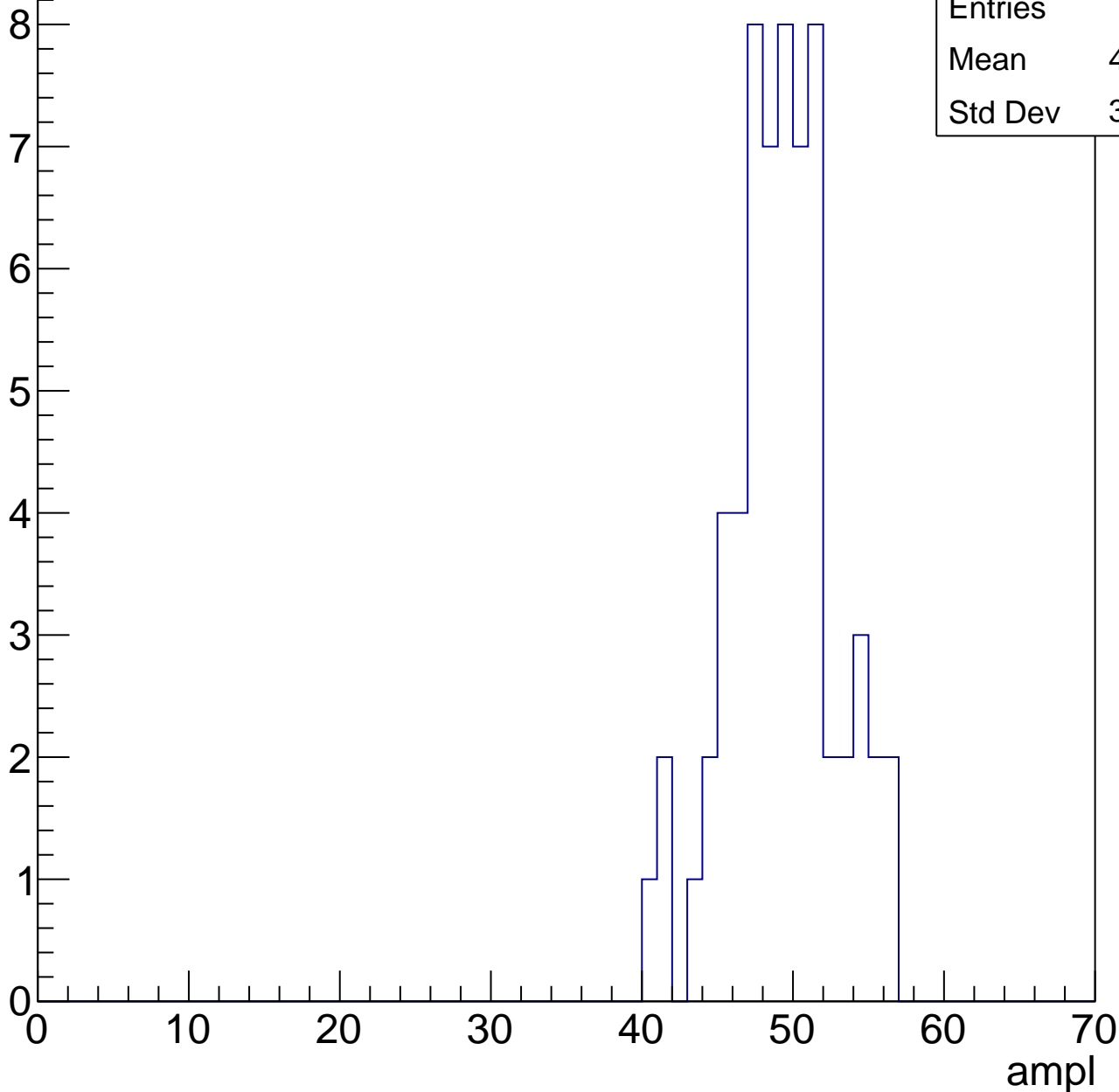


B1L102S, U19-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.78
Std Dev	3.484



B1L102S, U19-ch51, adc4

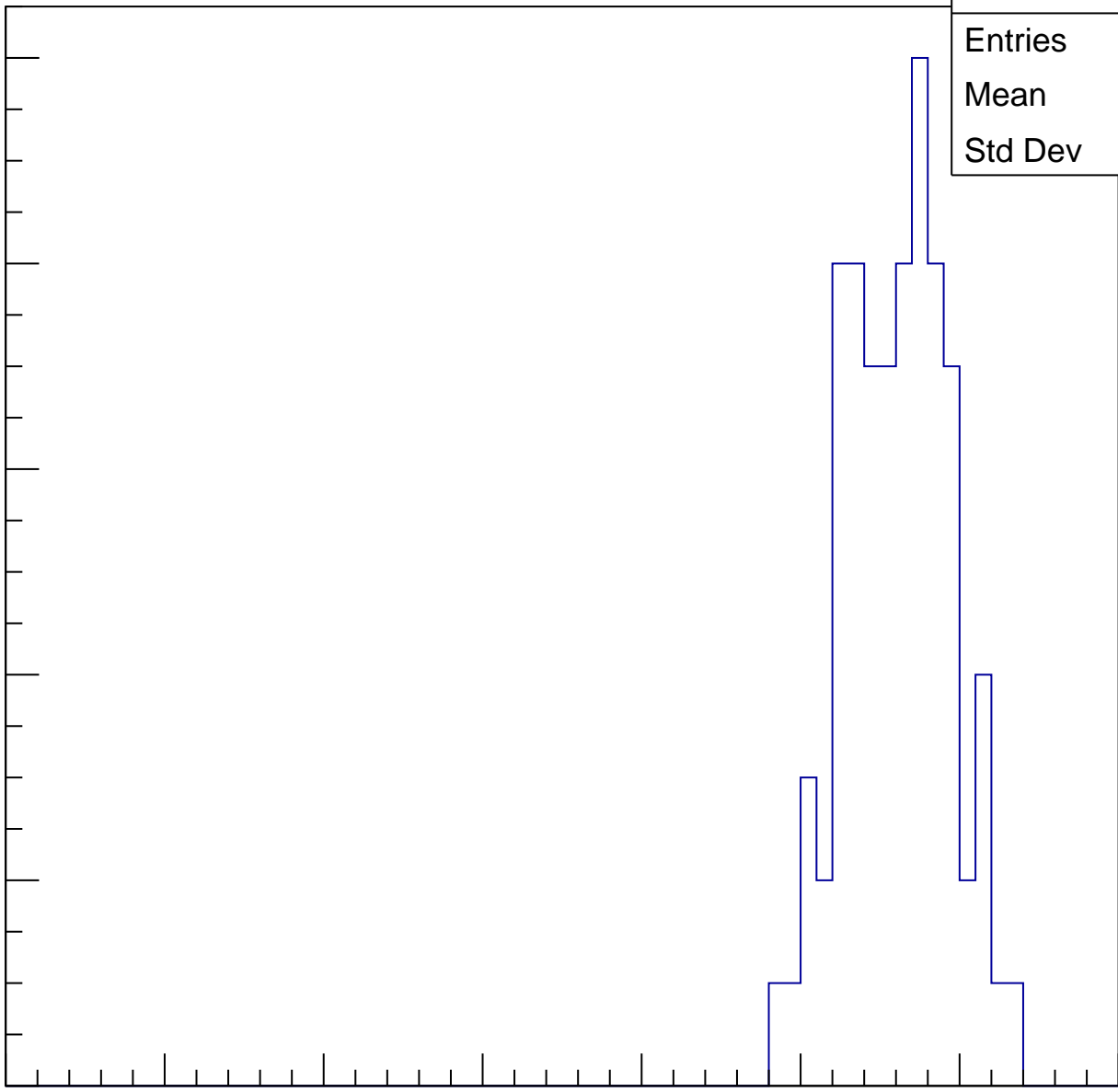
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	55.59
Std Dev	3.232

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl



B1L102S, U19-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 31

Mean 58.87

Std Dev 10.89

ampl

0

10

20

30

40

50

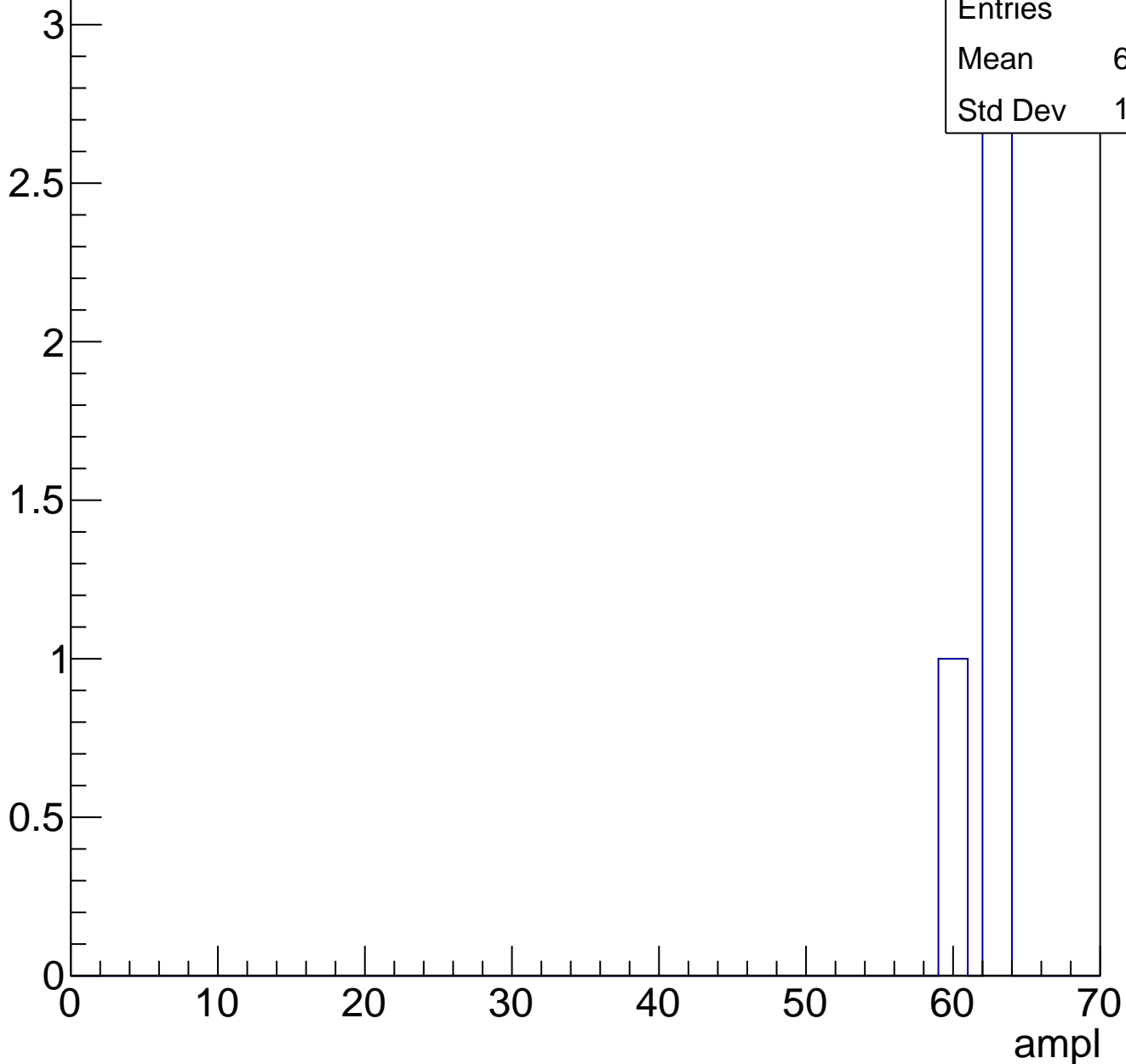
60

70

B1L102S, U19-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L102S, U19-ch52, adc0

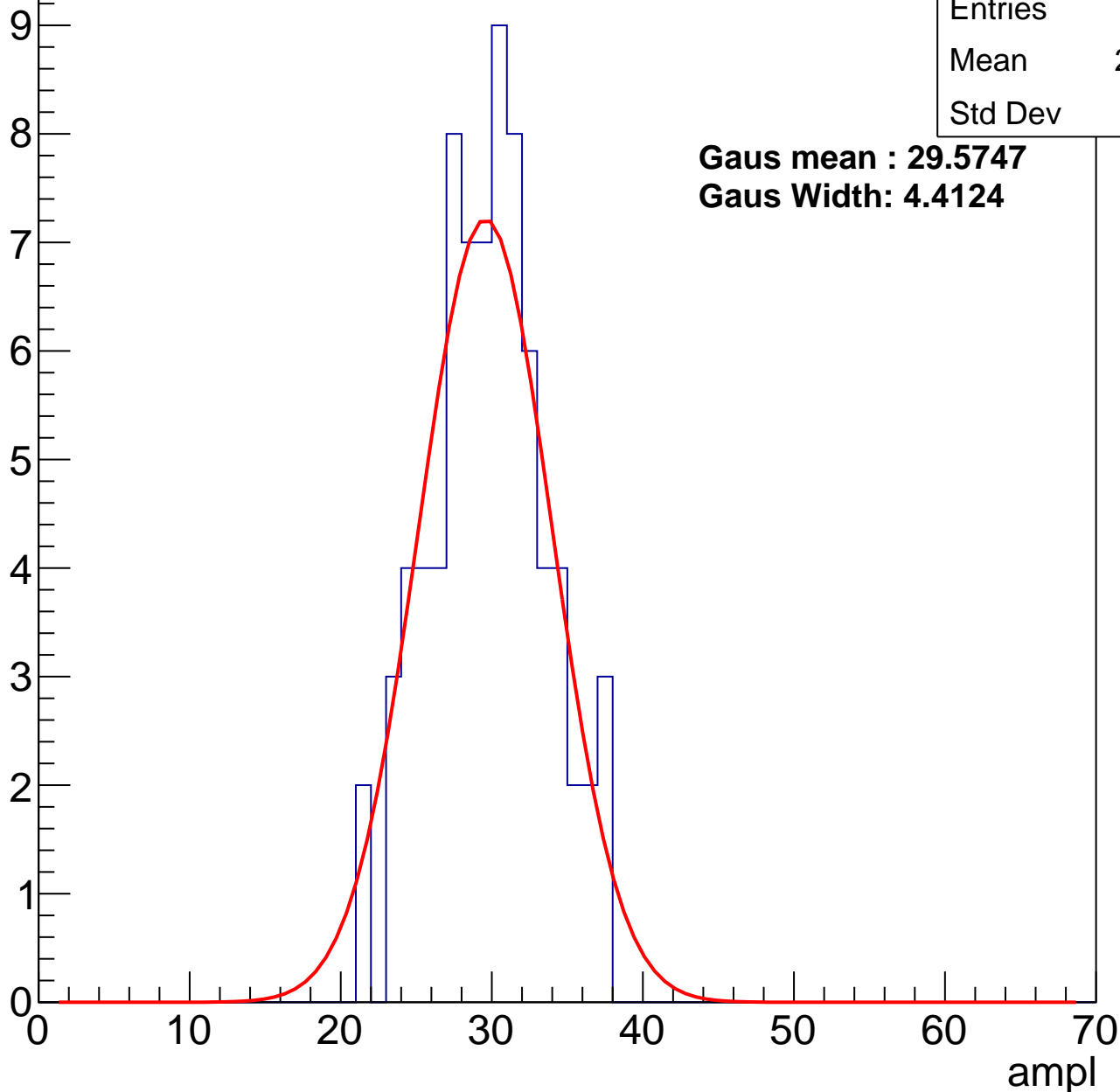
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.31
Std Dev	3.76

Gaus mean : 29.5747

Gaus Width: 4.4124



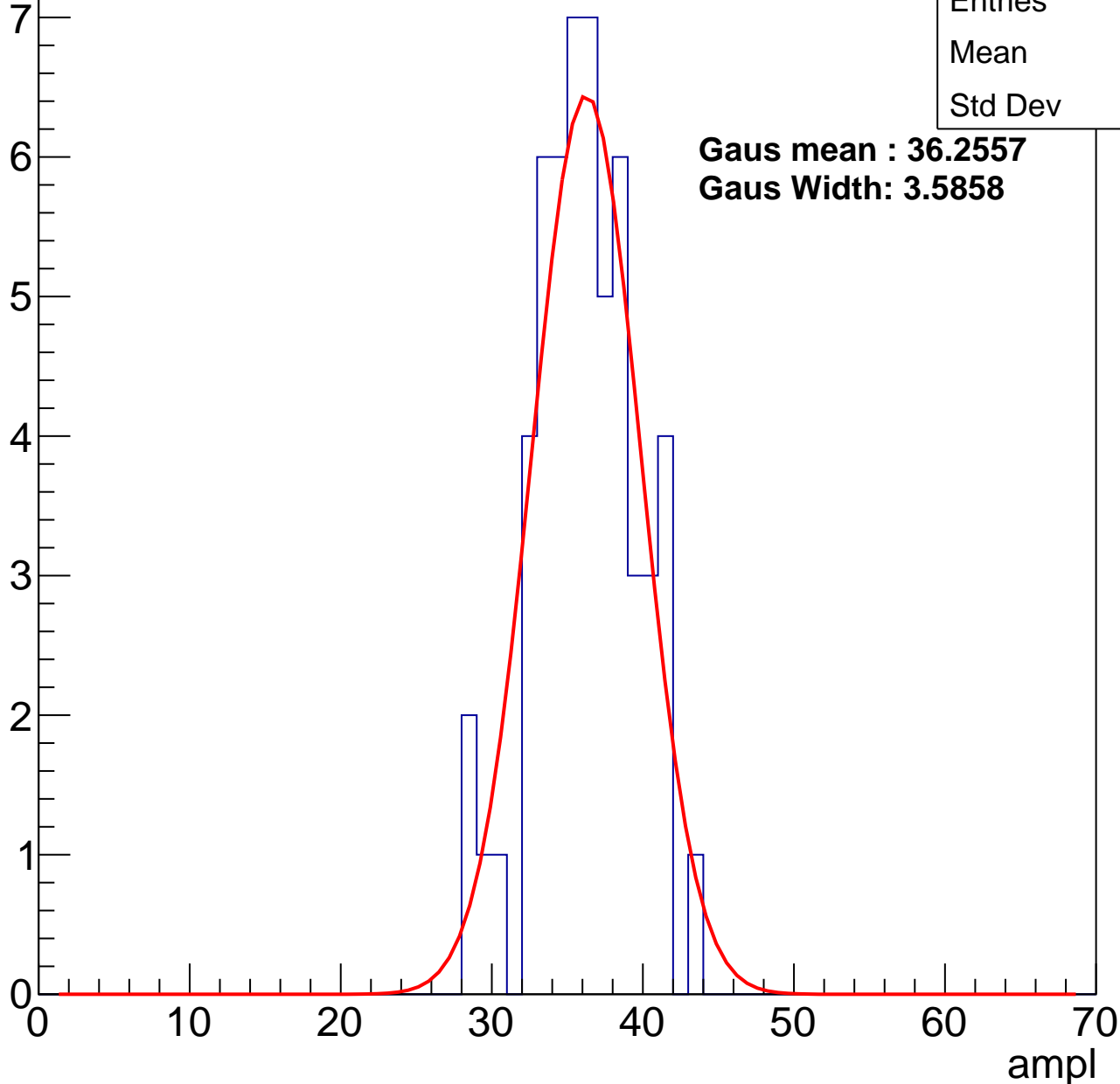
B1L102S, U19-ch52, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.7
Std Dev	3.3

Gaus mean : 36.2557
Gaus Width: 3.5858



B1L102S, U19-ch52, adc2

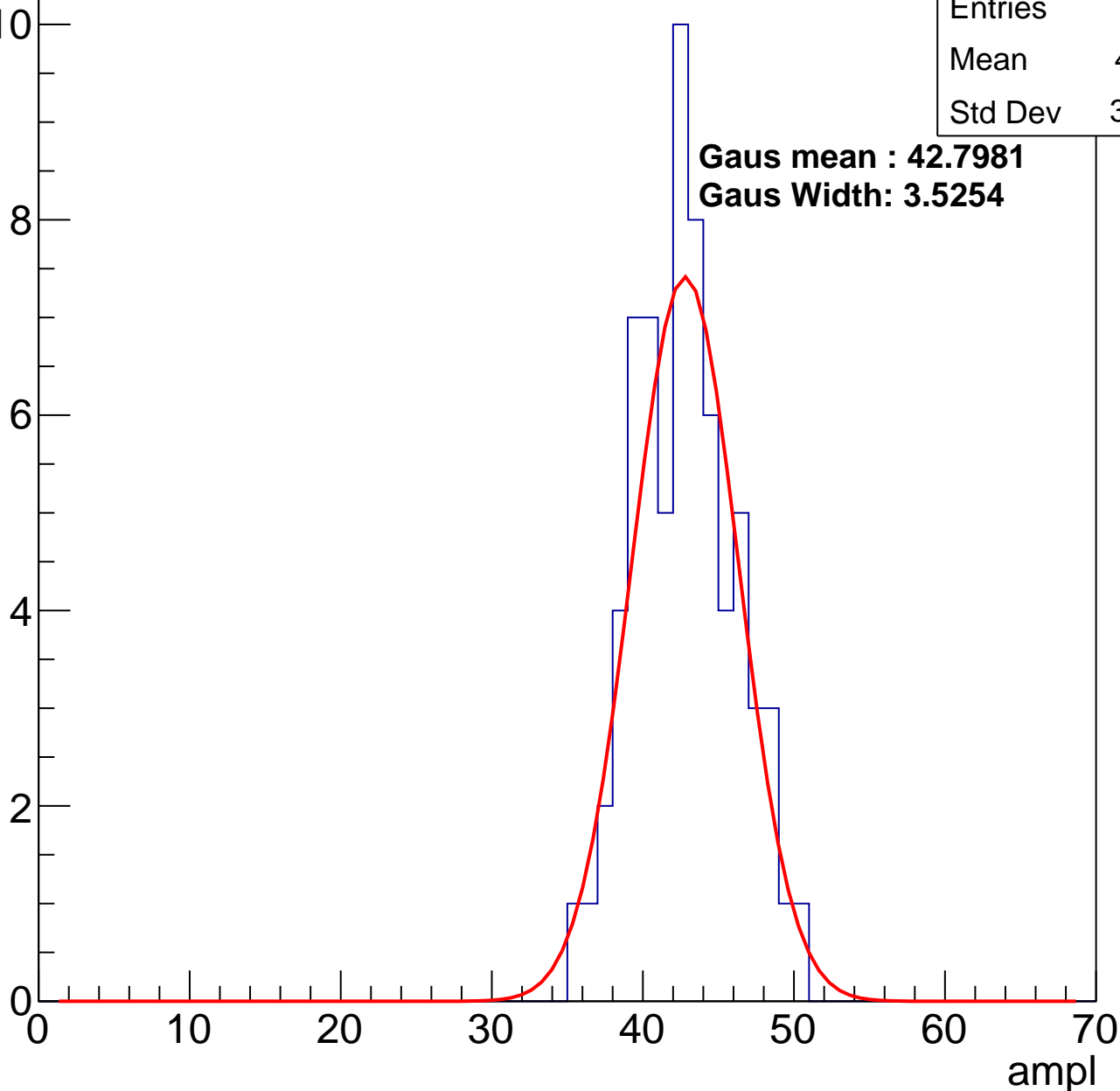
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.31
Std Dev	3.282

Gaus mean : 42.7981

Gaus Width: 3.5254

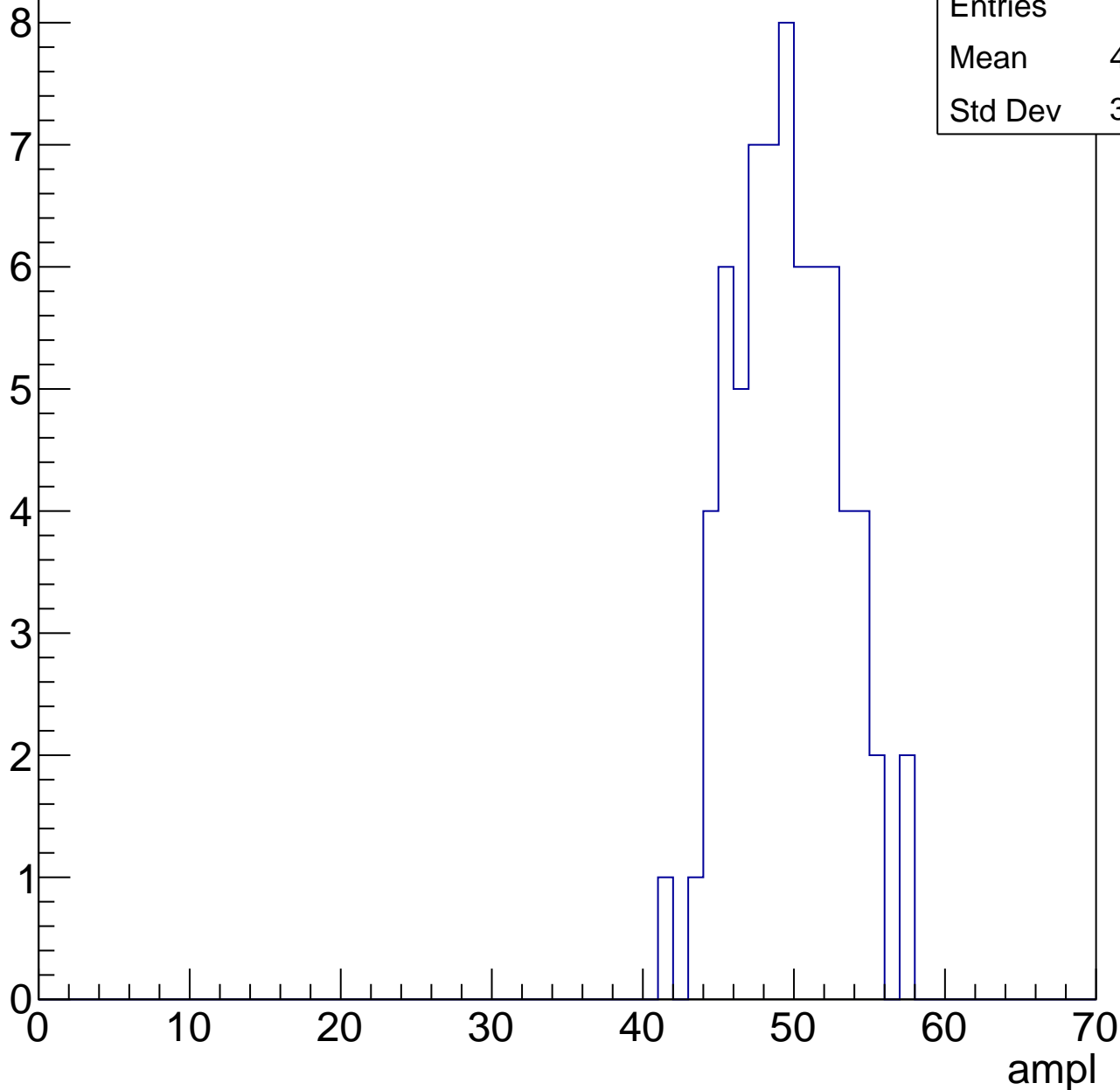


B1L102S, U19-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

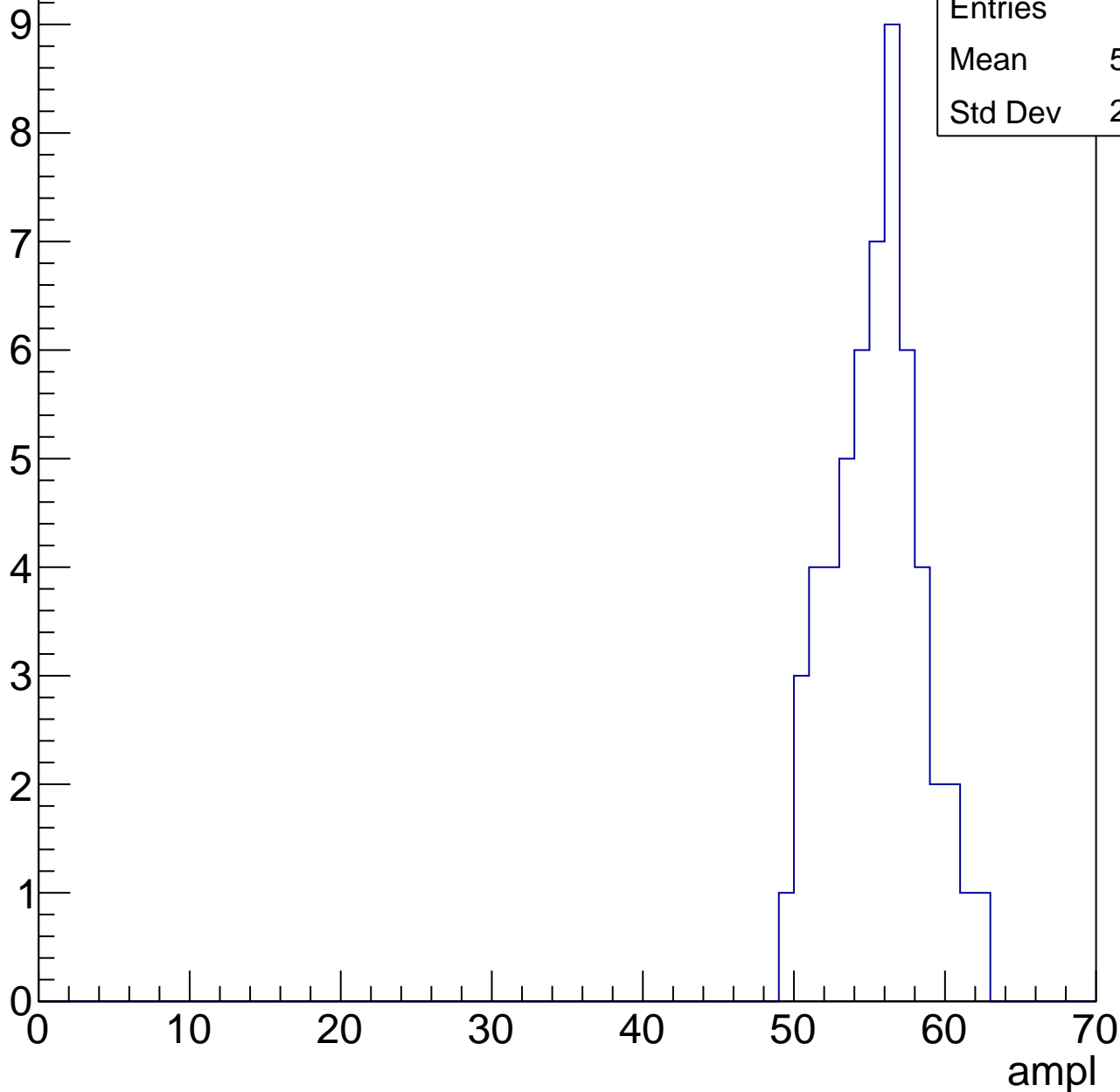
Entries	69
Mean	49.09
Std Dev	3.455



B1L102S, U19-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



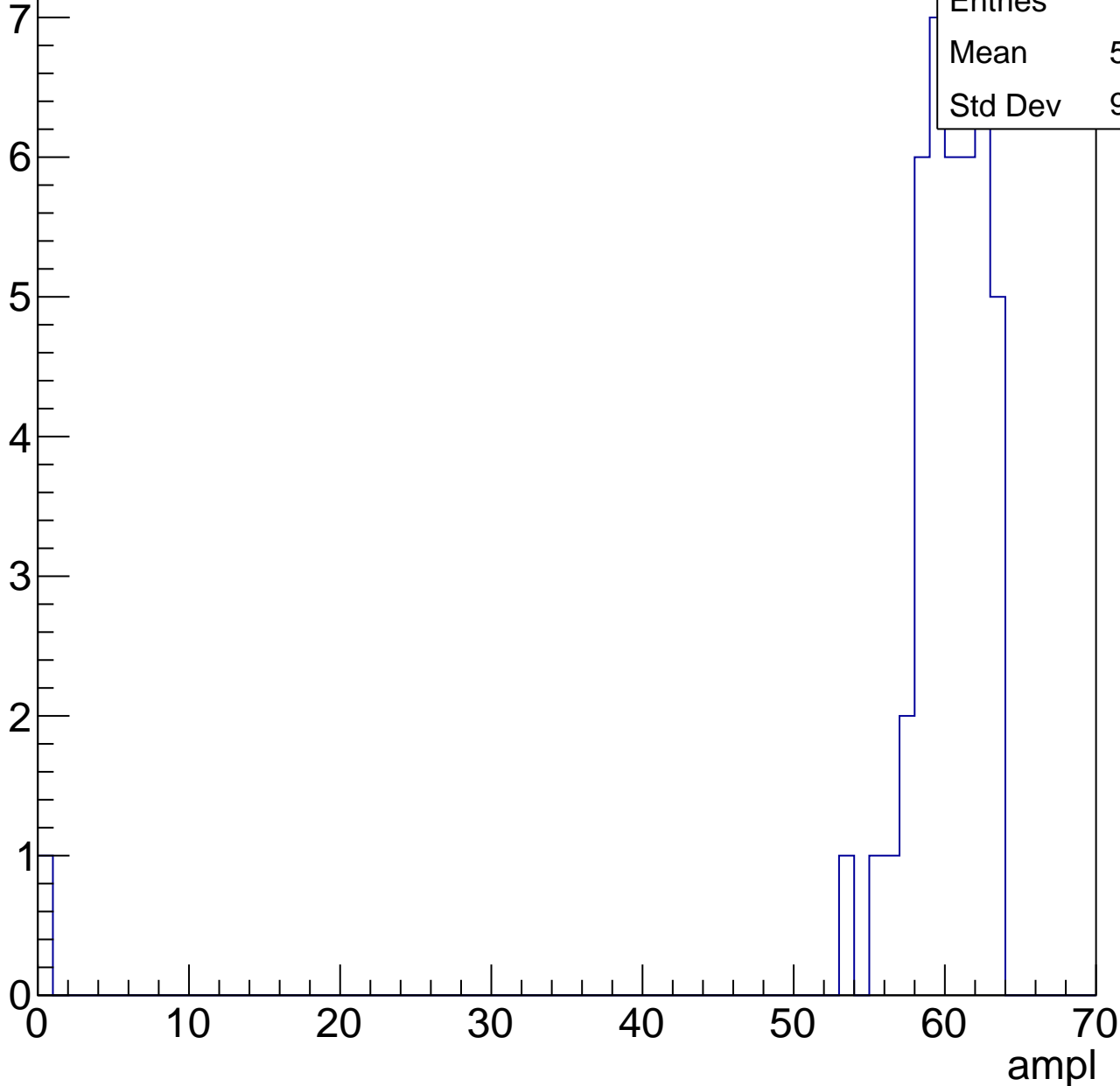
Entries	55
Mean	54.98
Std Dev	2.933

B1L102S, U19-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.47
Std Dev	9.297

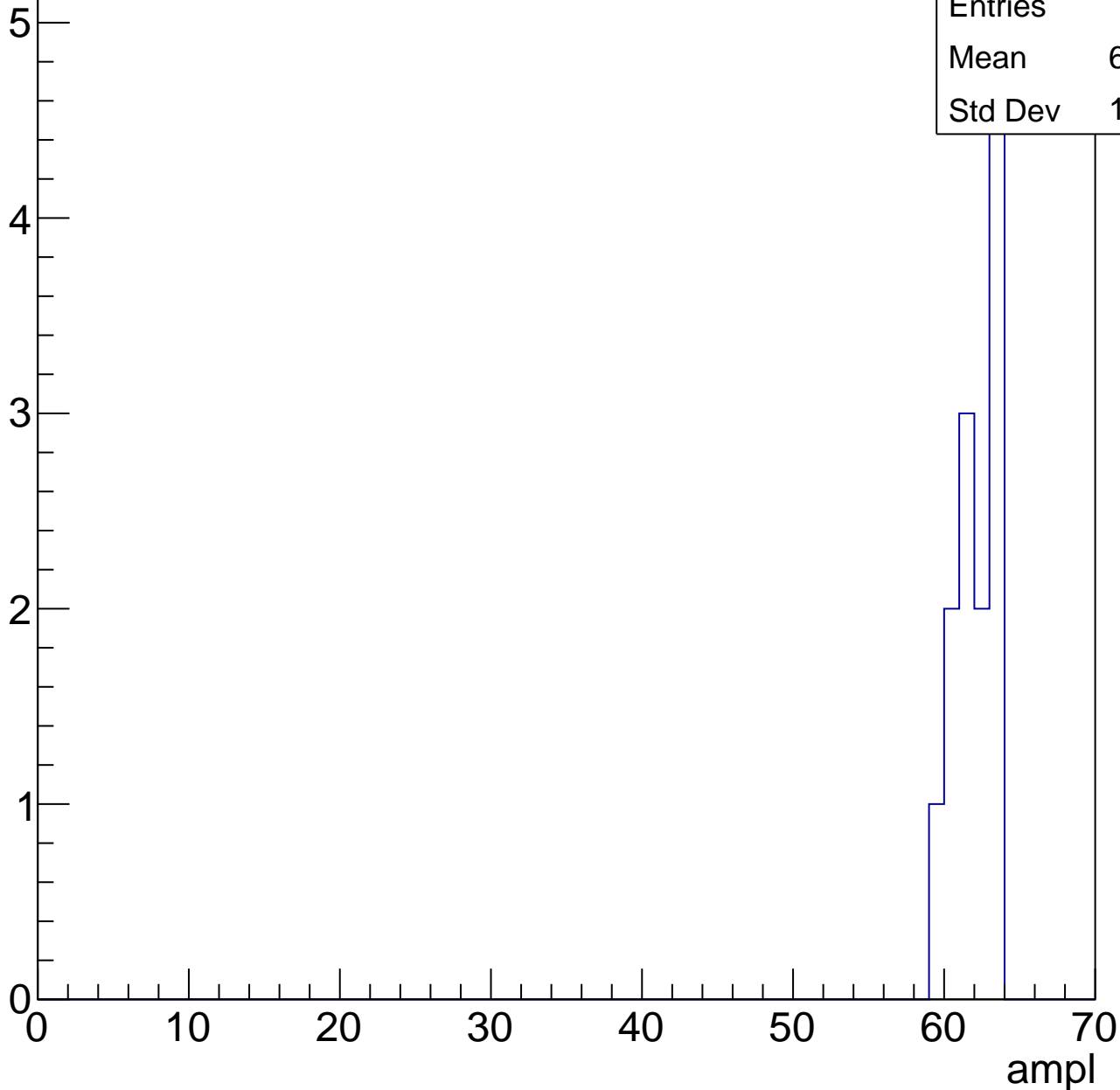


B1L102S, U19-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.62
Std Dev	1.332



B1L102S, U19-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch53, adc0

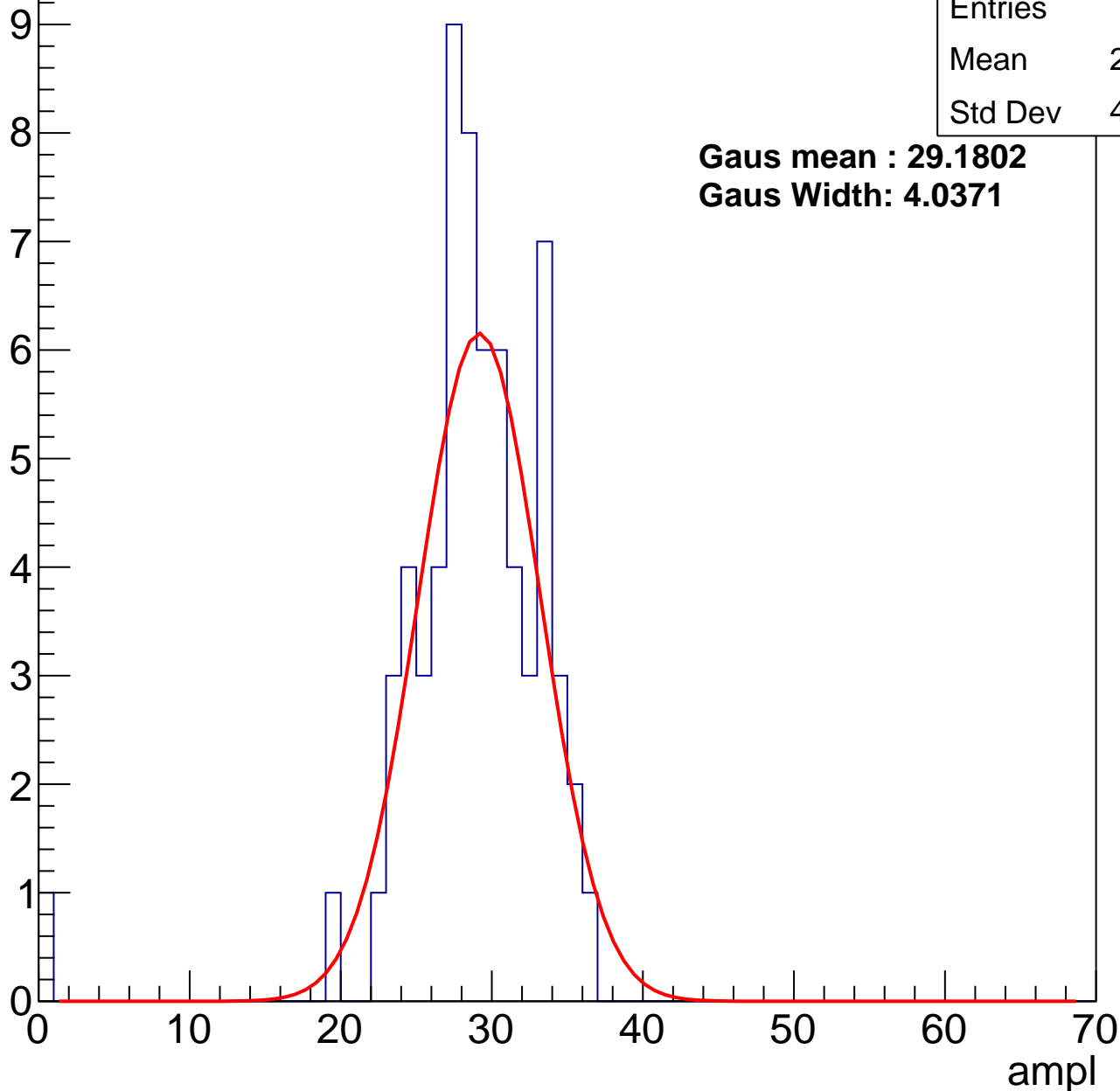
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.26
Std Dev	4.995

Gaus mean : 29.1802

Gaus Width: 4.0371



B1L102S, U19-ch53, adc1

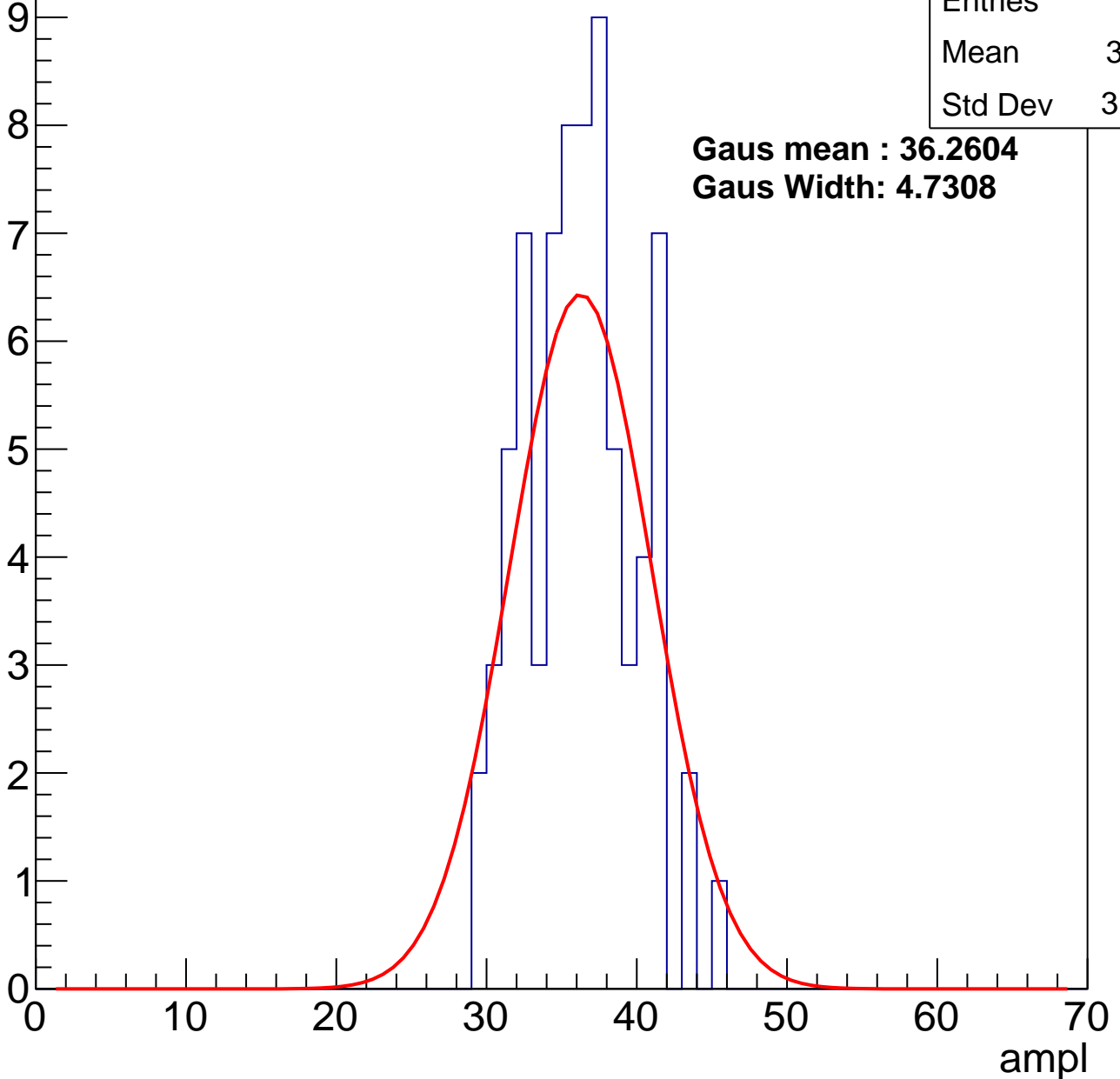
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.81
Std Dev	3.634

Gaus mean : 36.2604

Gaus Width: 4.7308



B1L102S, U19-ch53, adc2

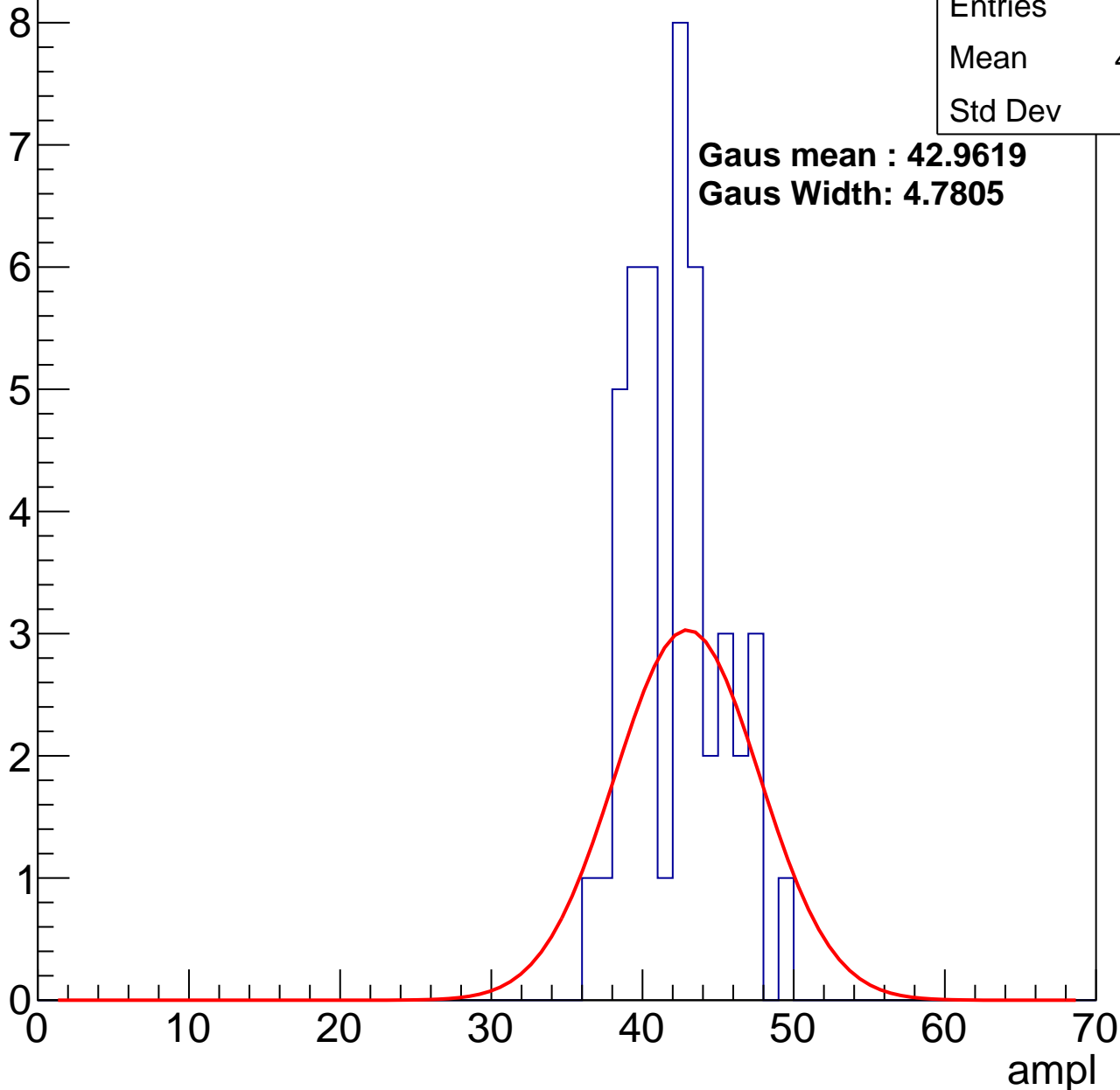
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	41.71
Std Dev	3.03

Gaus mean : 42.9619

Gaus Width: 4.7805

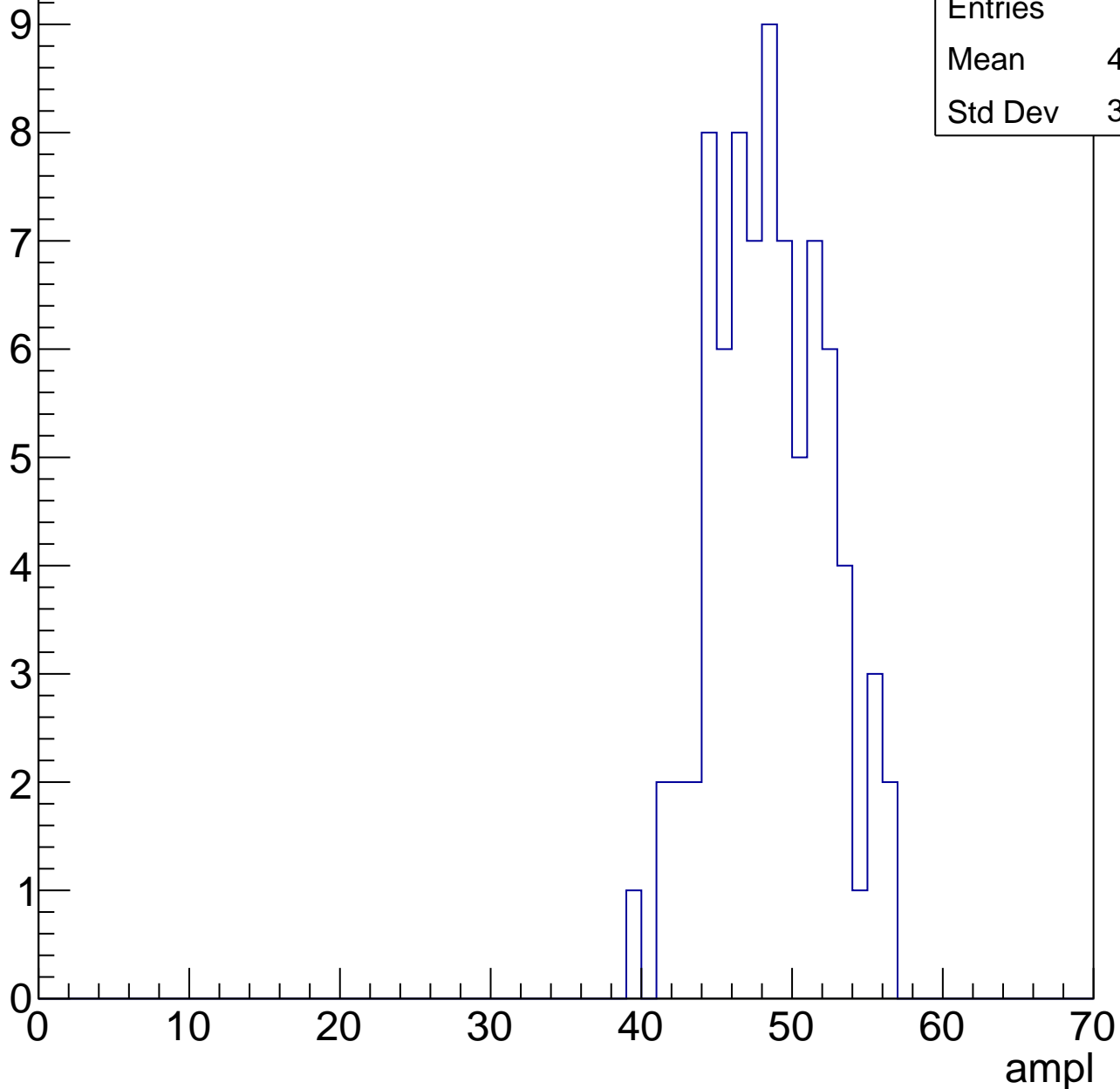


B1L102S, U19-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	48.09
Std Dev	3.739

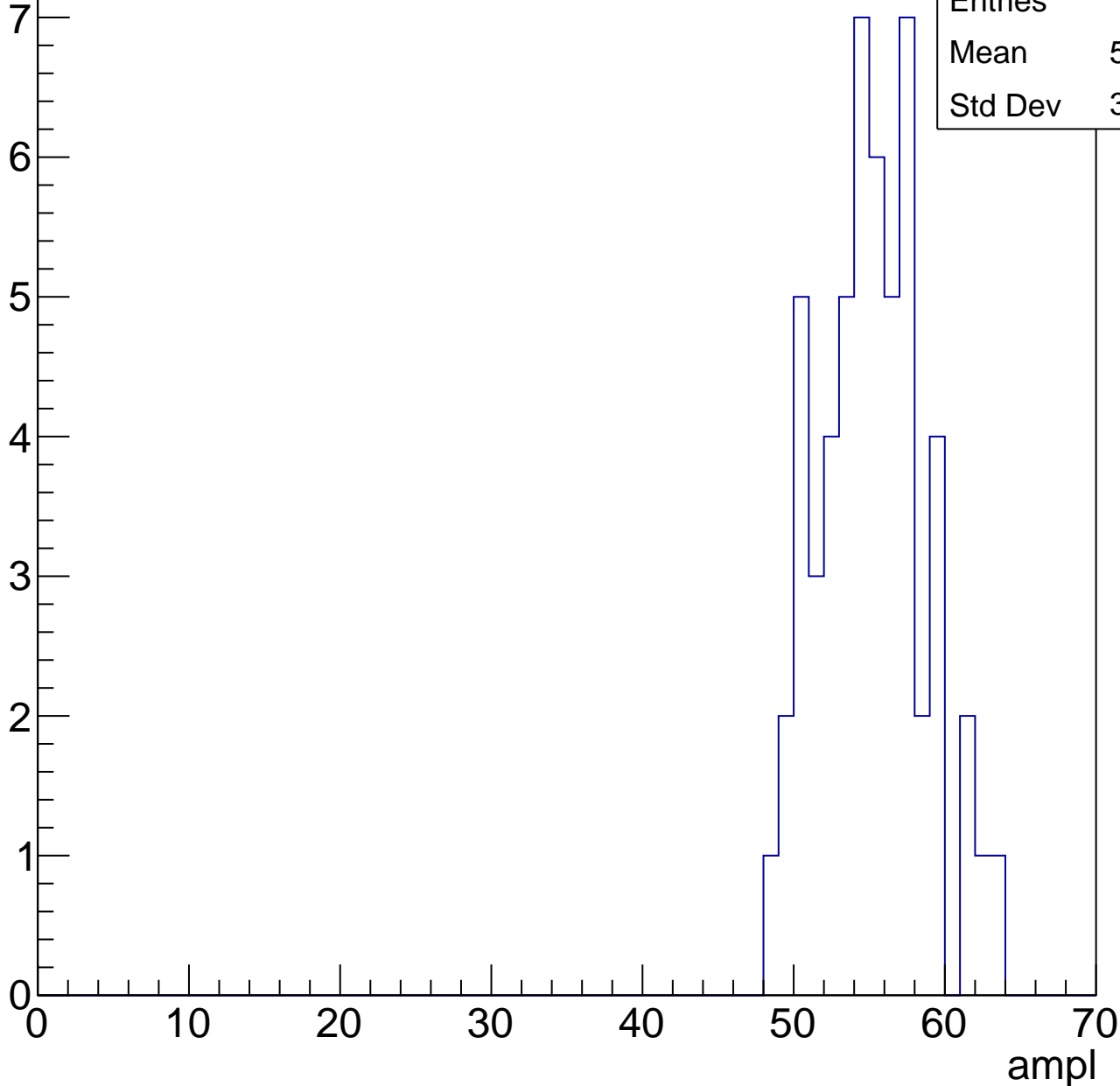


B1L102S, U19-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.69
Std Dev	3.442

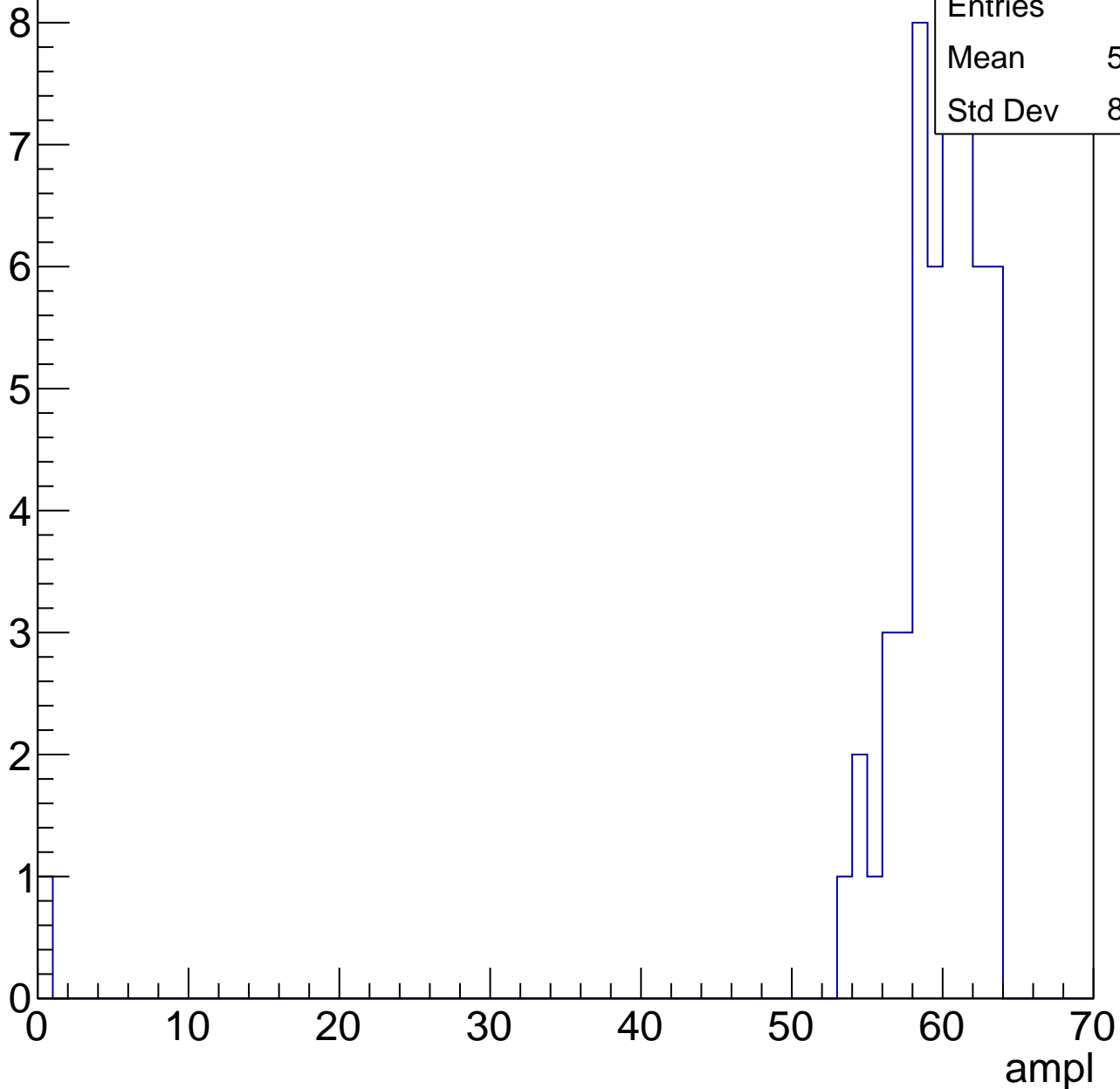


B1L102S, U19-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

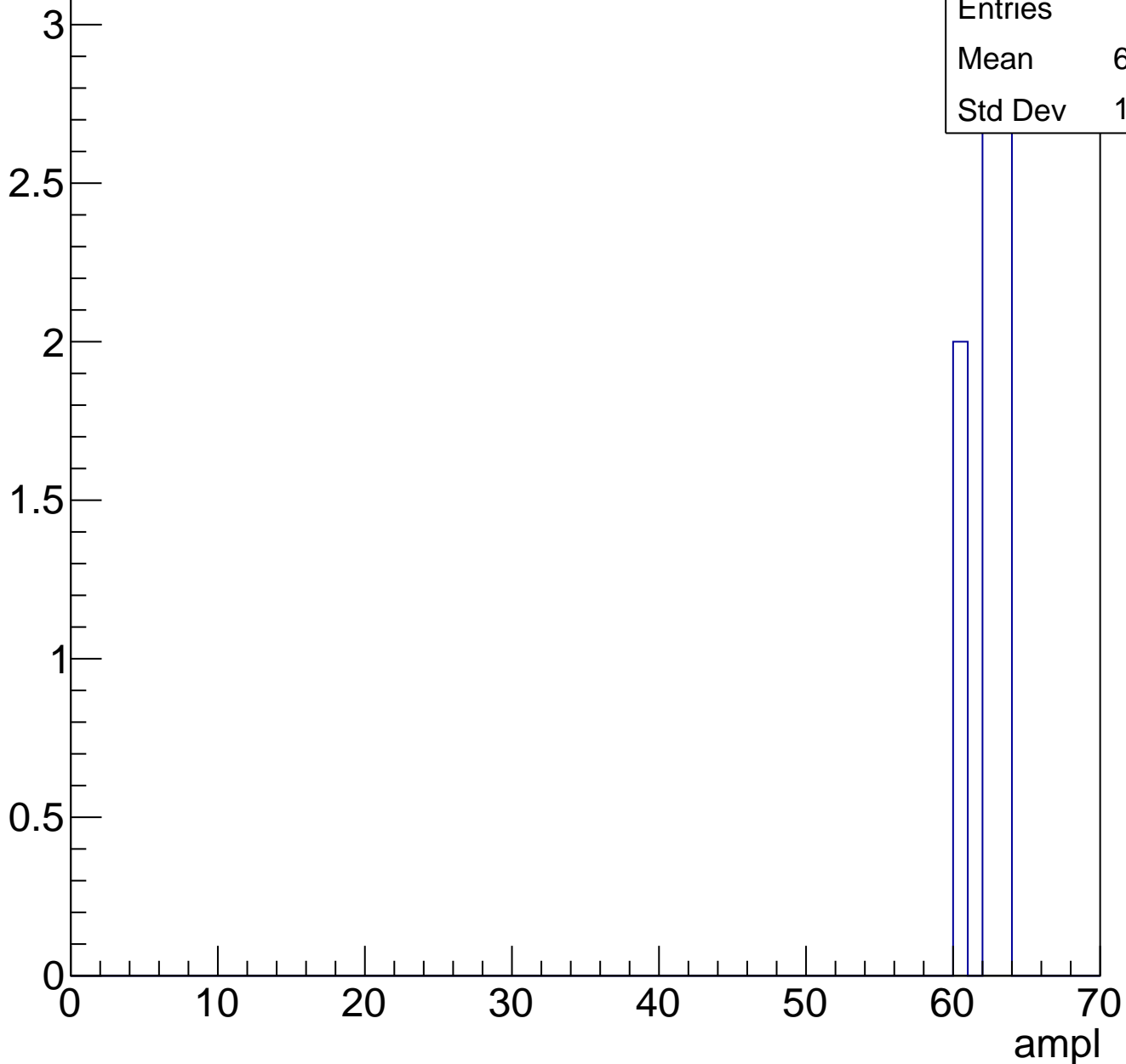
Entries	53
Mean	58.32
Std Dev	8.463



B1L102S, U19-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

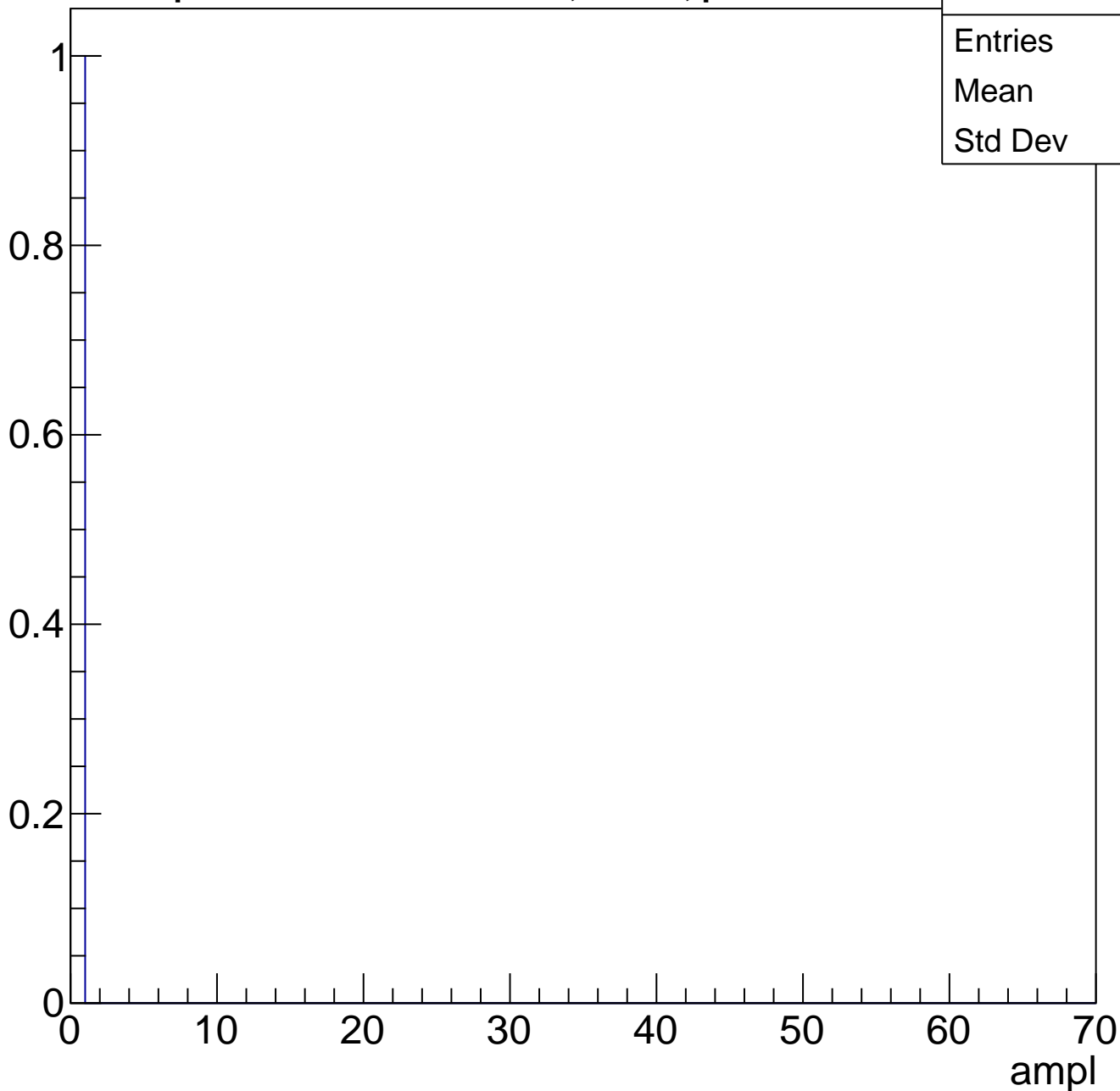
Entry



B1L102S, U19-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch54, adc0

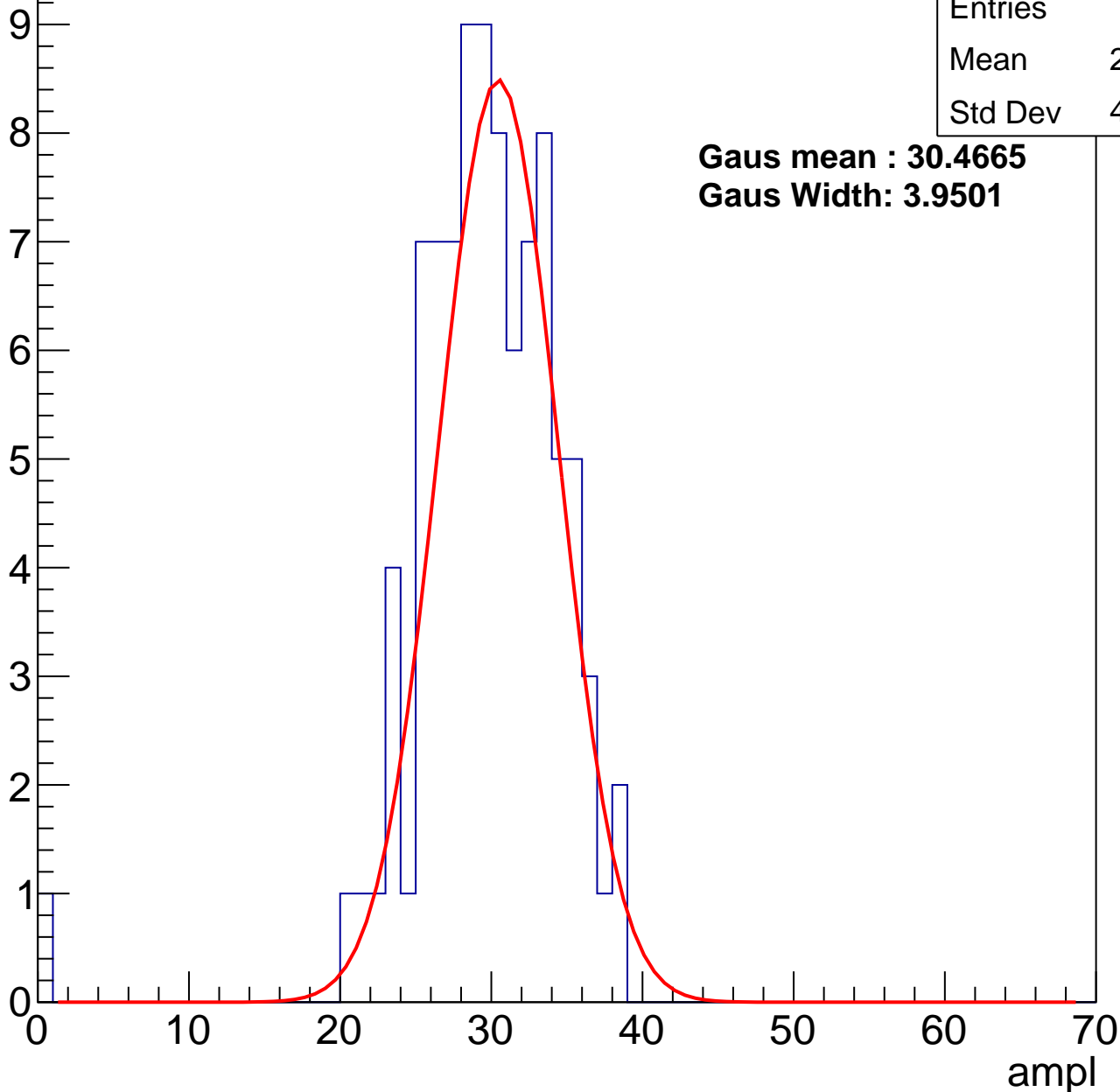
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	29.23
Std Dev	4.986

Gaus mean : 30.4665

Gaus Width: 3.9501



B1L102S, U19-ch54, adc1

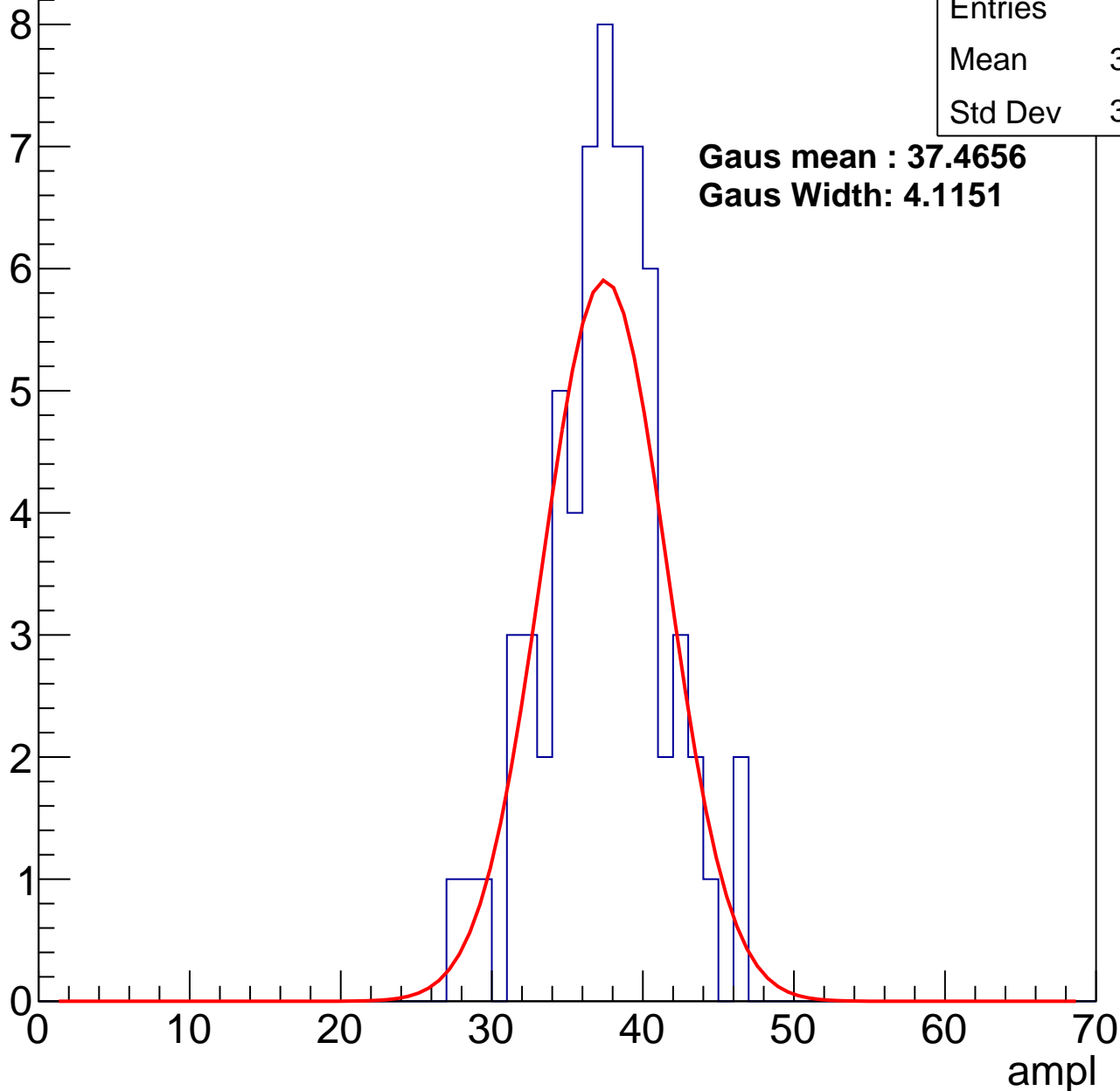
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.02
Std Dev	3.936

Gaus mean : 37.4656

Gaus Width: 4.1151



B1L102S, U19-ch54, adc2

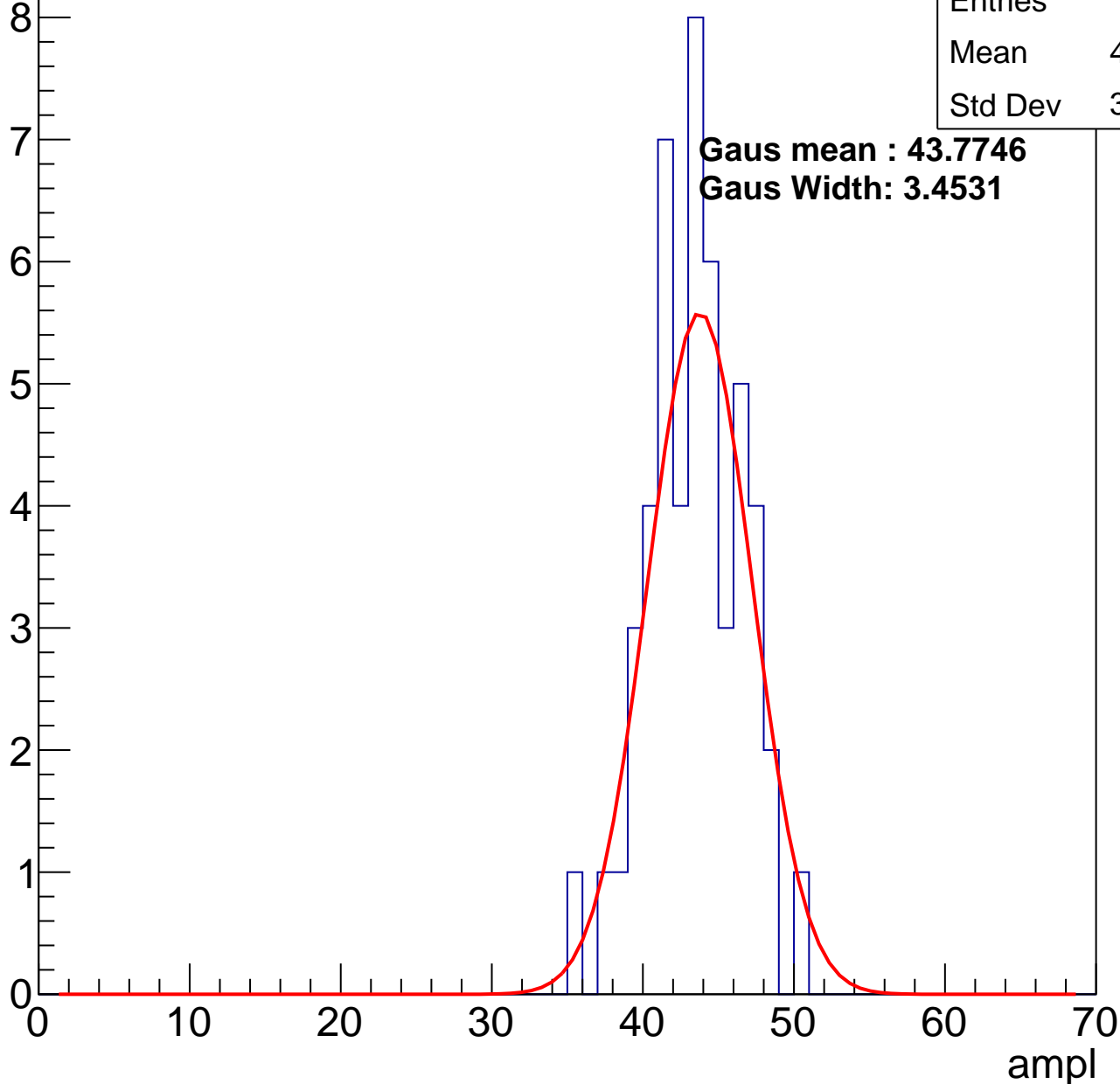
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	42.98
Std Dev	3.056

Gaus mean : 43.7746

Gaus Width: 3.4531

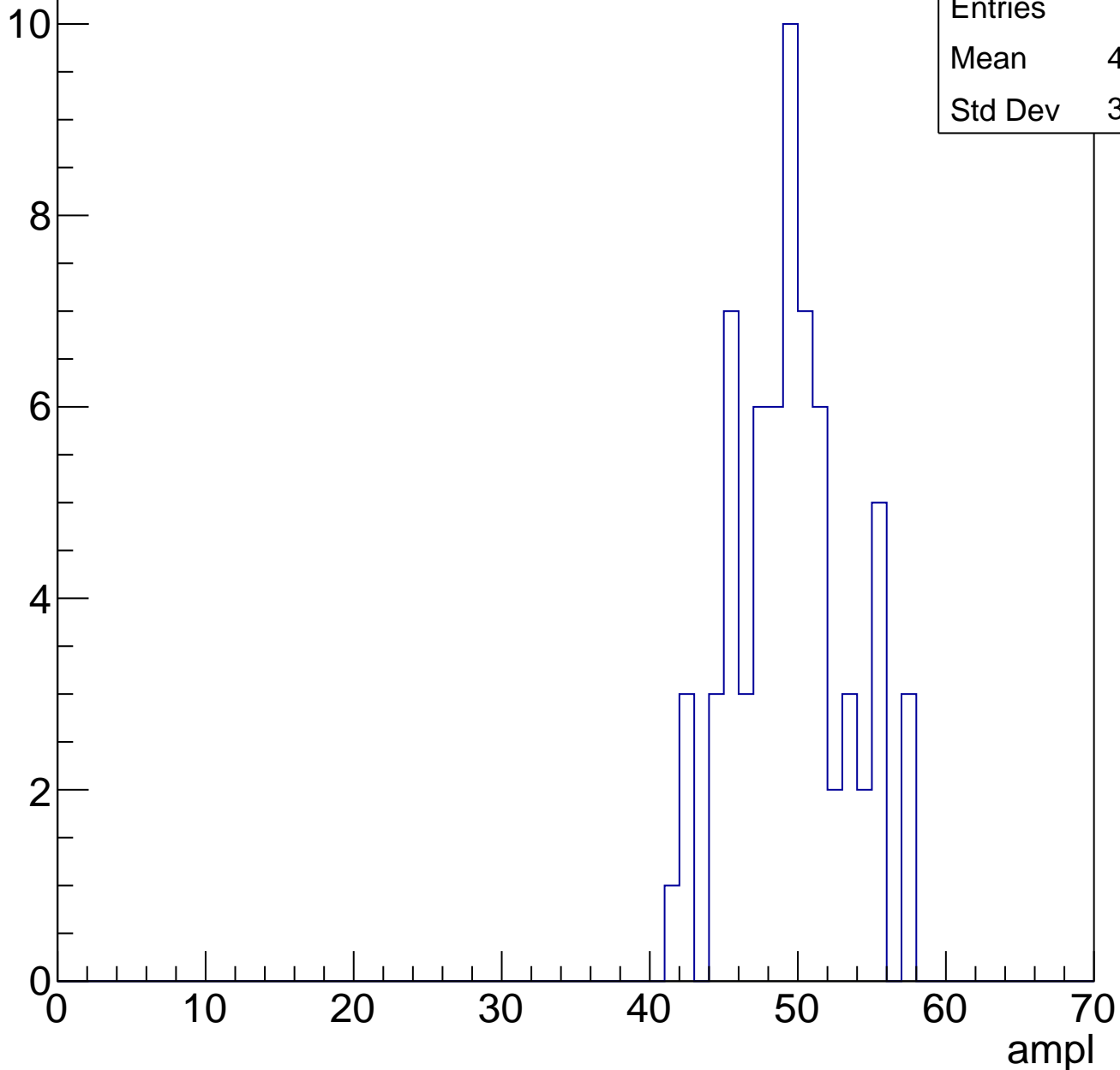


B1L102S, U19-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	49.03
Std Dev	3.813

Entry

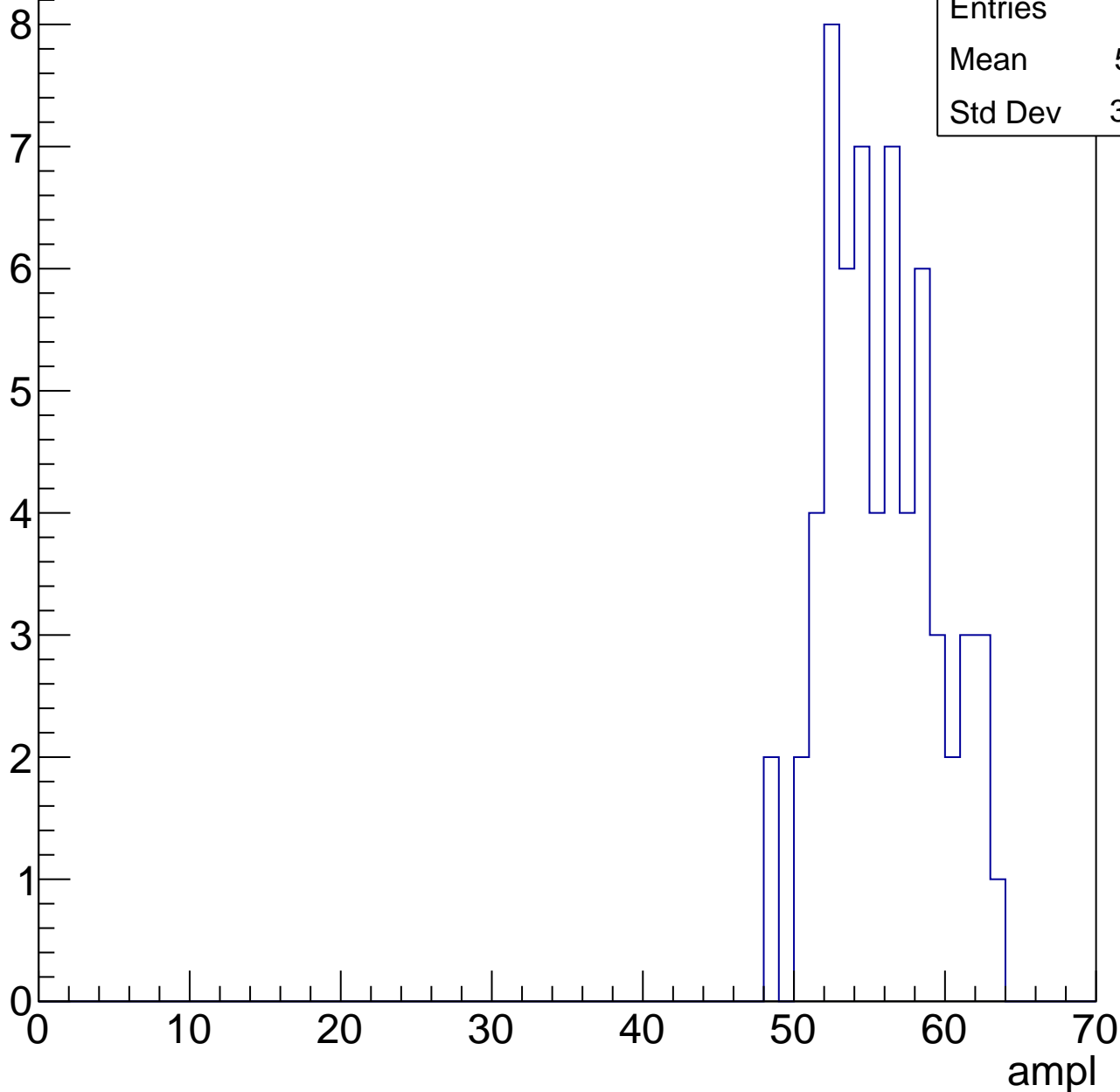


B1L102S, U19-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.31
Std Dev	3.599



B1L102S, U19-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	44
Mean	59.55
Std Dev	2.518

0

10

20

30

40

50

60

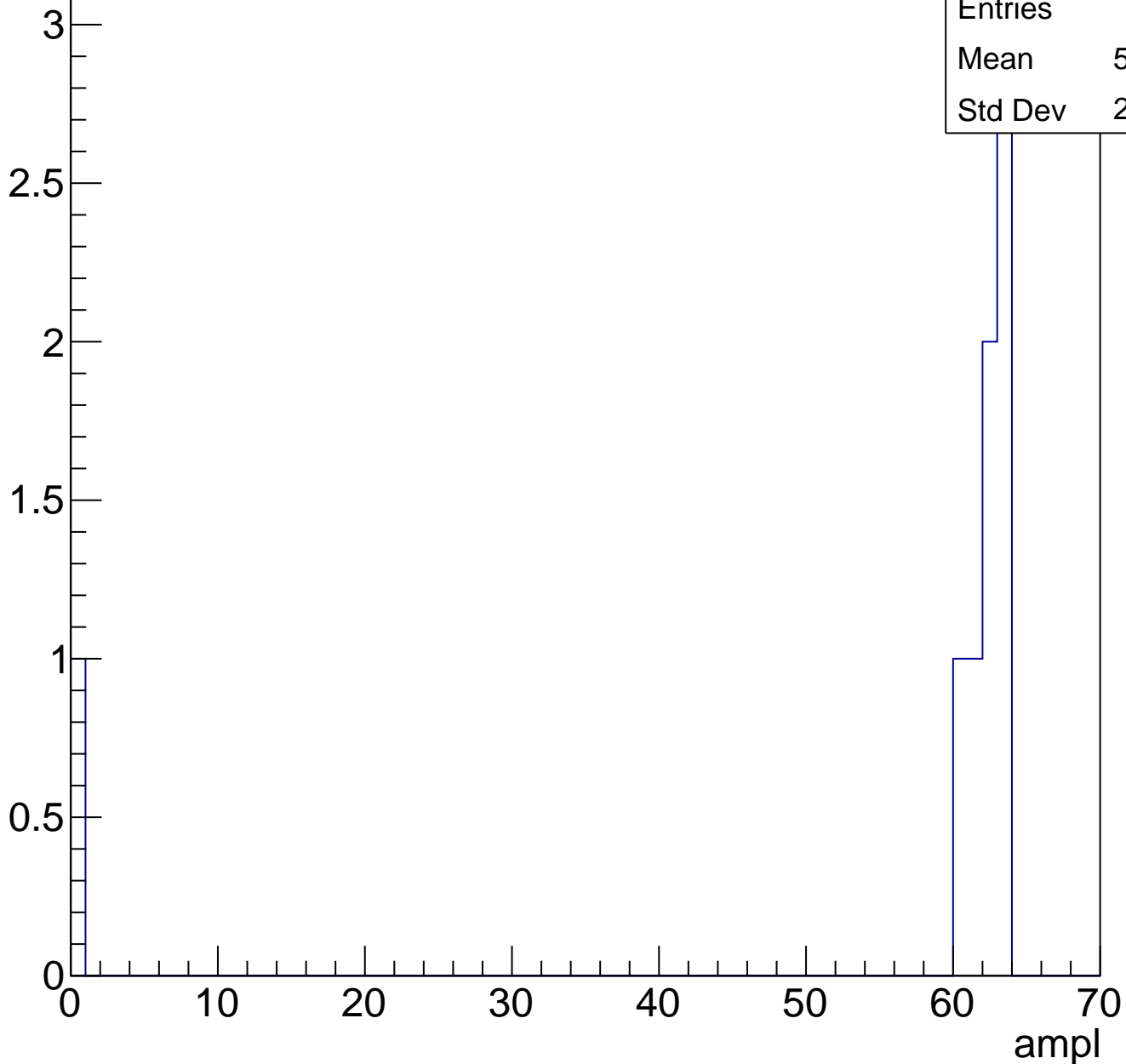
70

ampl

B1L102S, U19-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch55, adc0

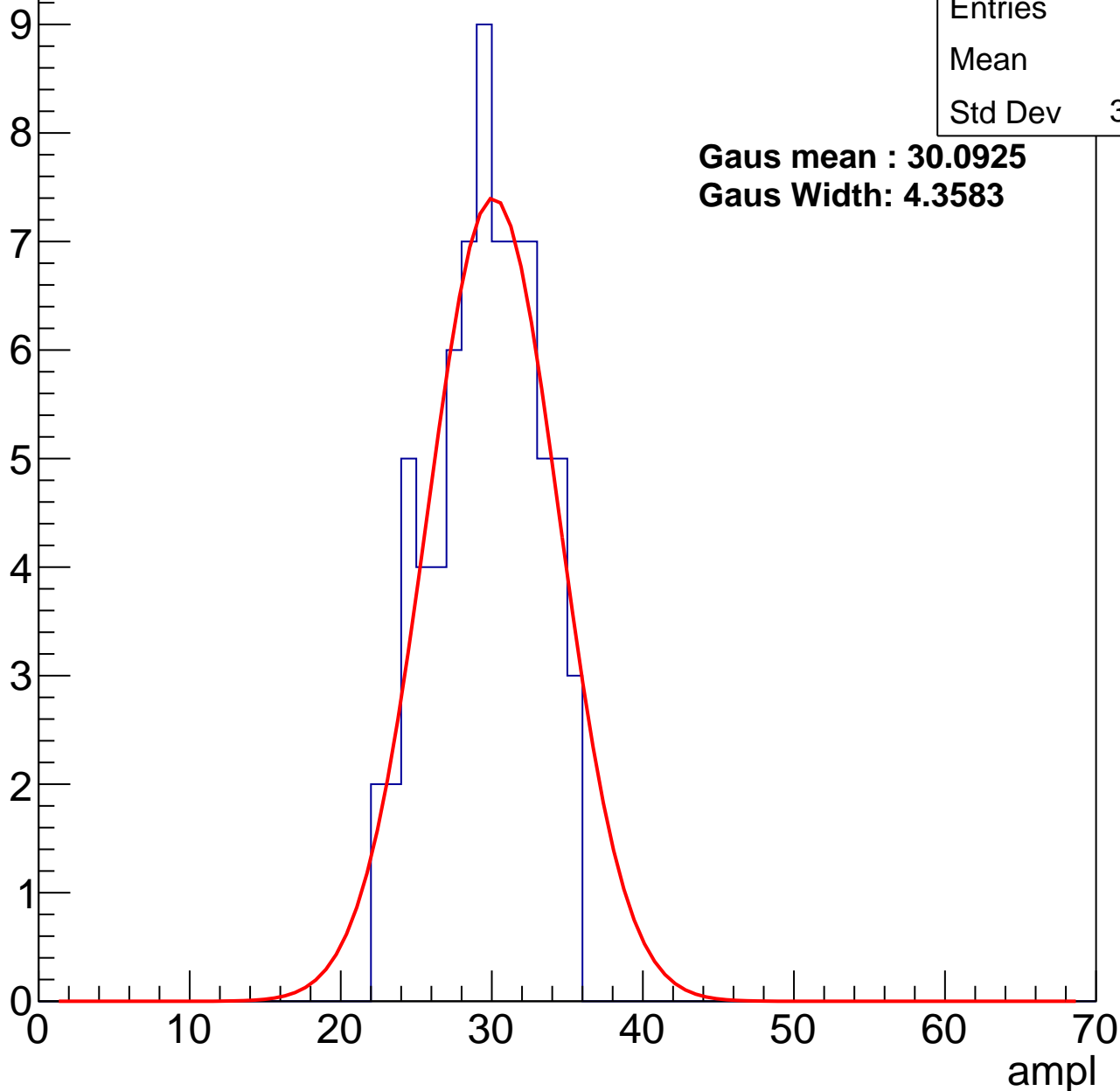
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.1
Std Dev	3.385

Gaus mean : 30.0925

Gaus Width: 4.3583



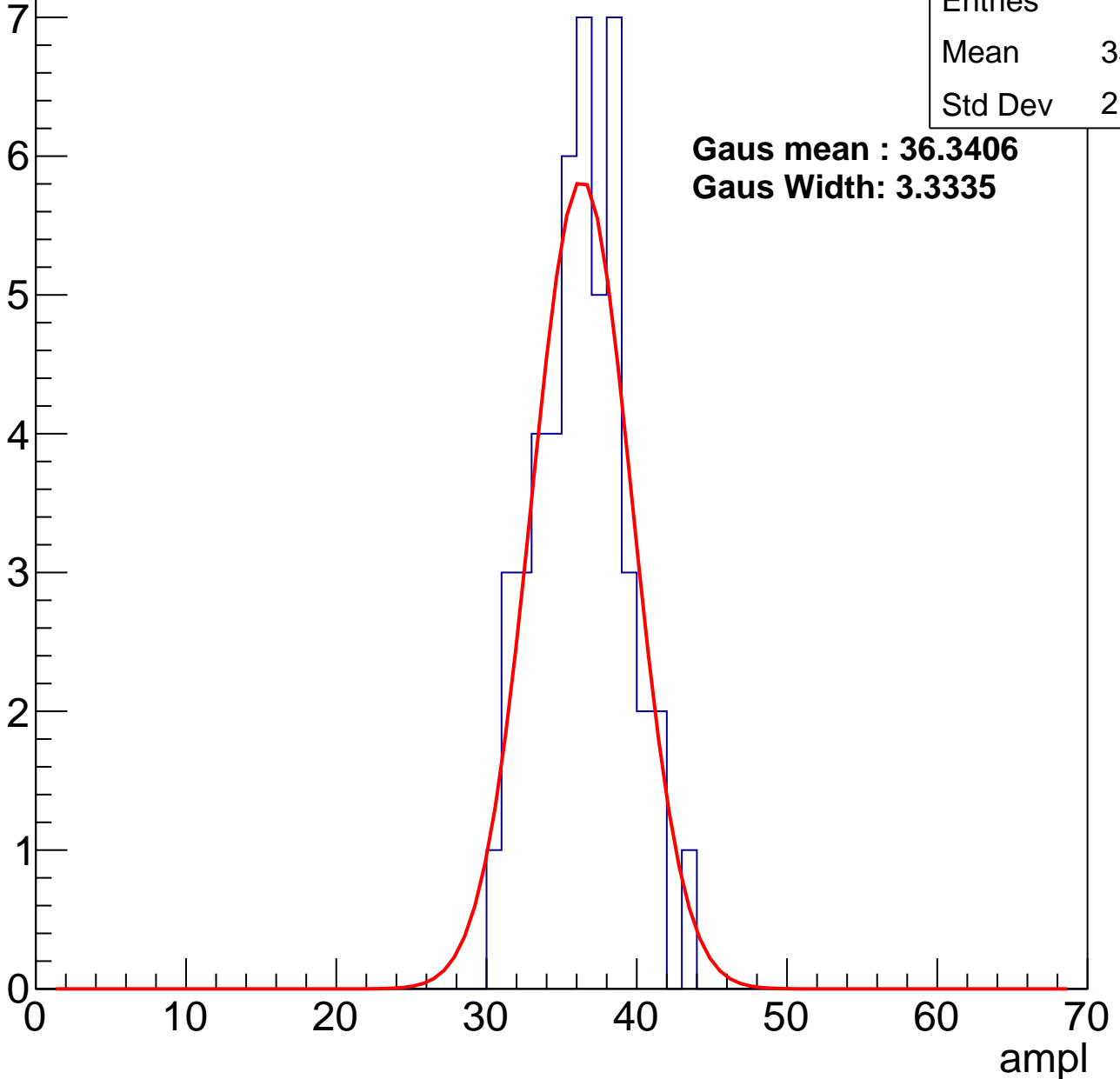
B1L102S, U19-ch55, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	35.88
Std Dev	2.913

Gaus mean : 36.3406
Gaus Width: 3.3335



B1L102S, U19-ch55, adc2

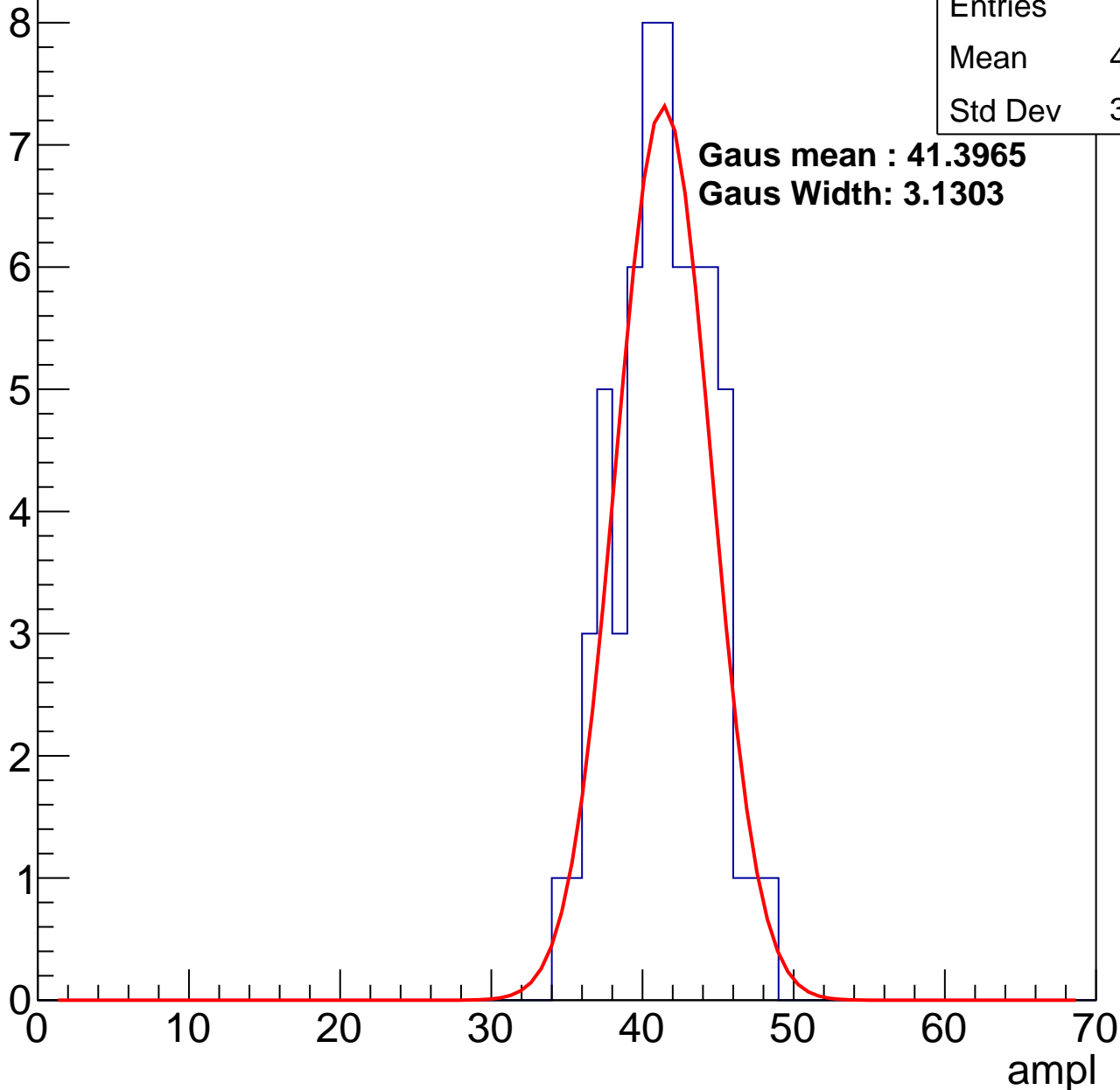
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	40.95
Std Dev	3.064

Gaus mean : 41.3965

Gaus Width: 3.1303

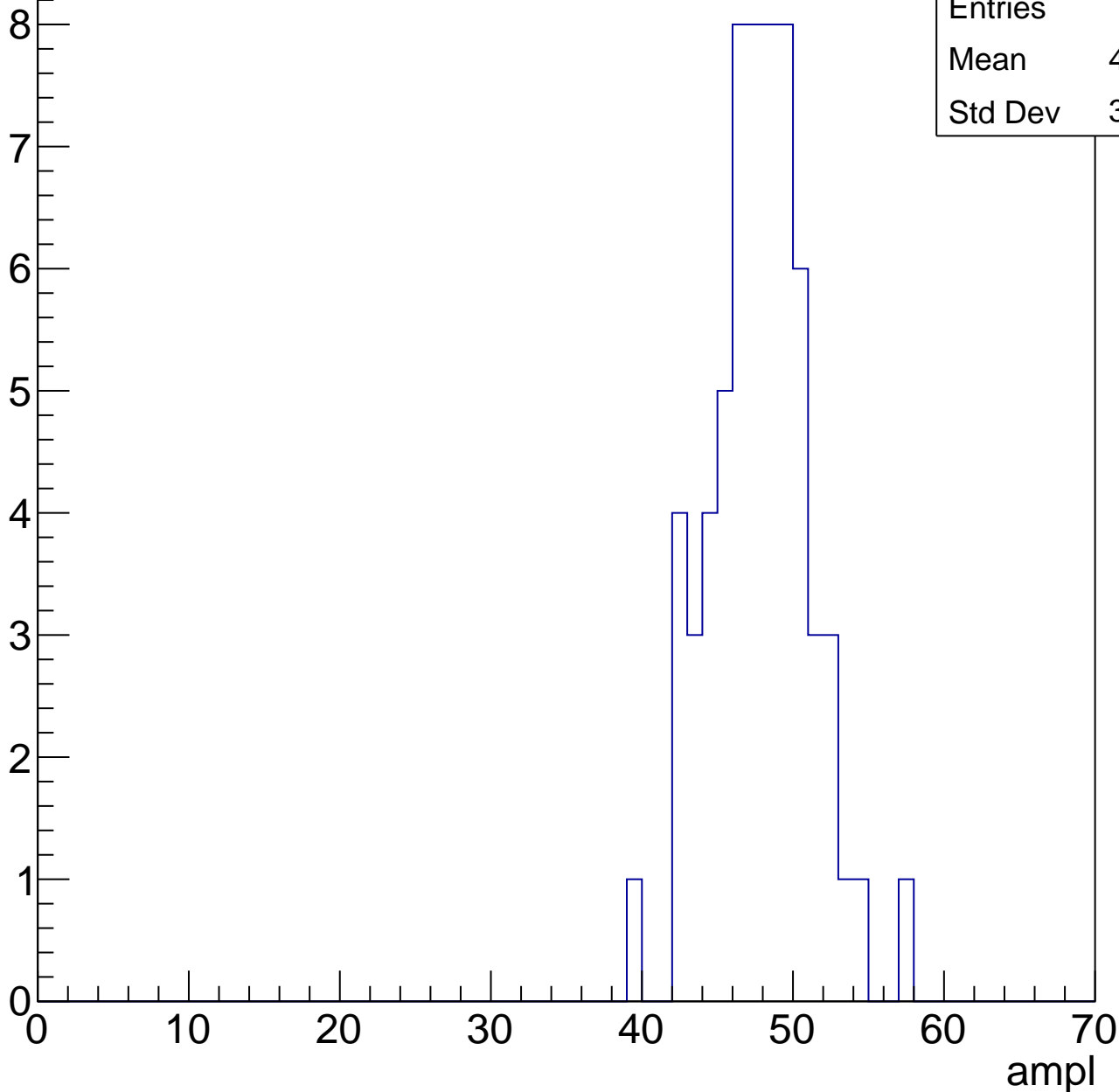


B1L102S, U19-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.34
Std Dev	3.237

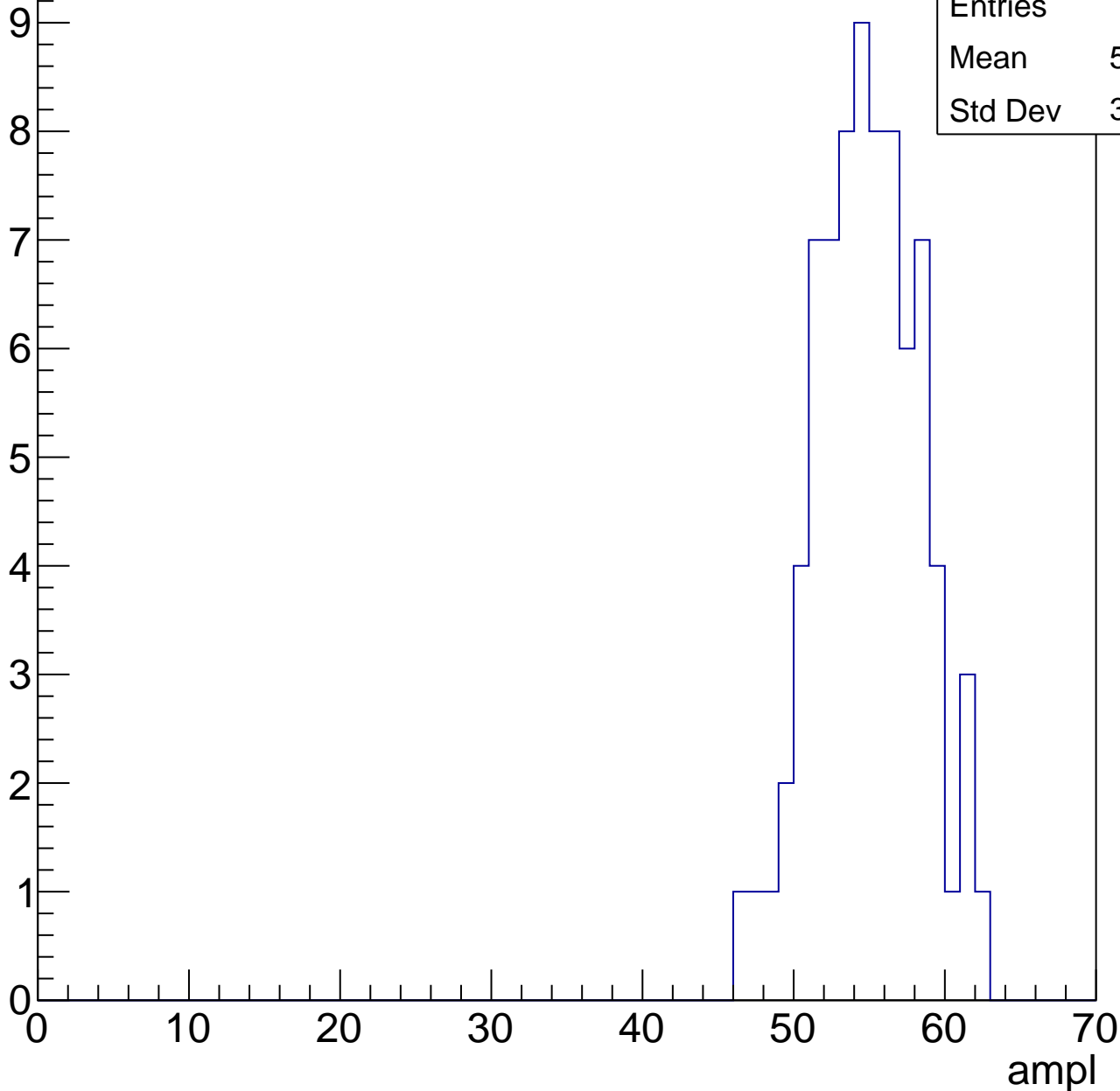


B1L102S, U19-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	54.45
Std Dev	3.396

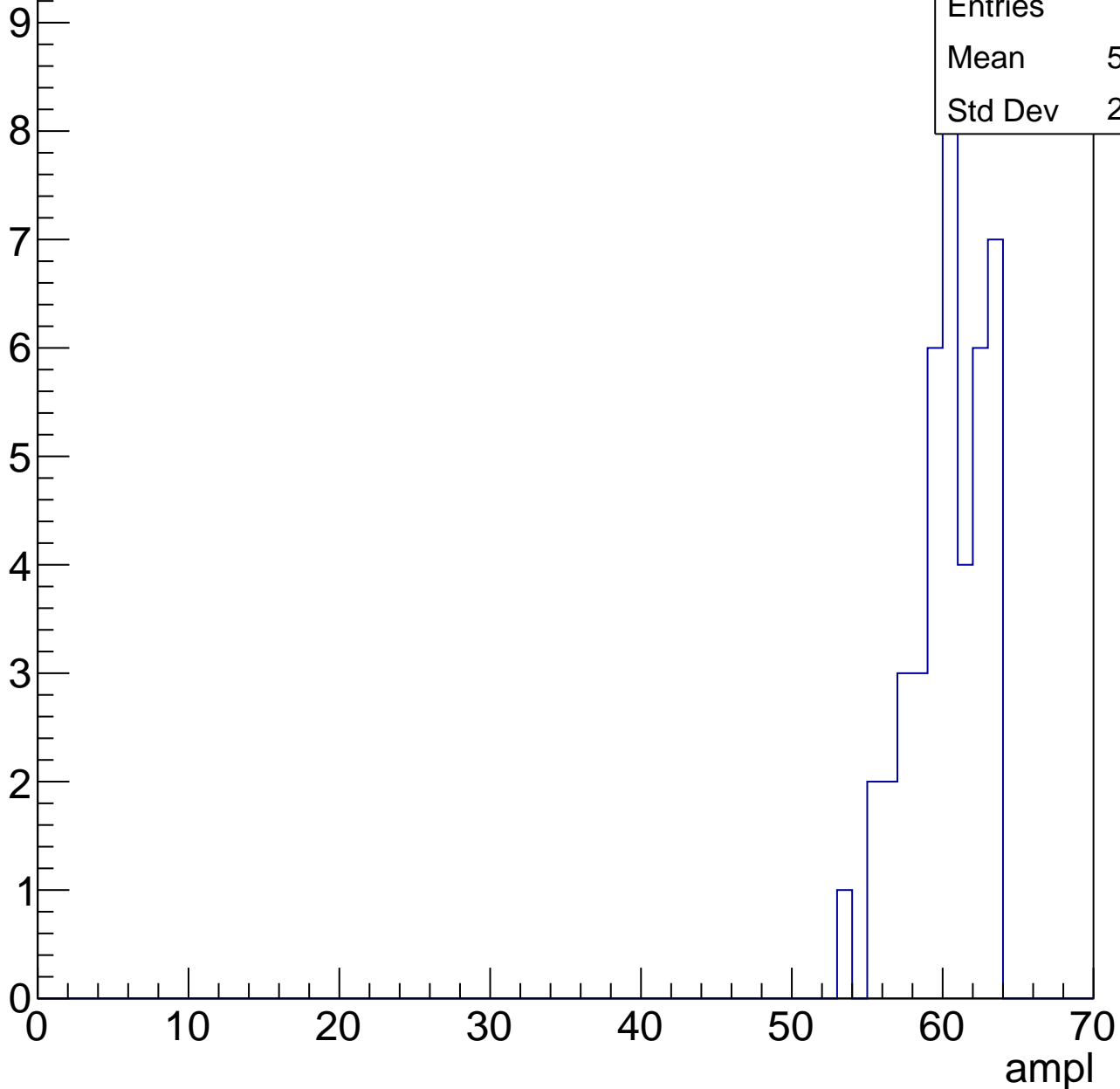


B1L102S, U19-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

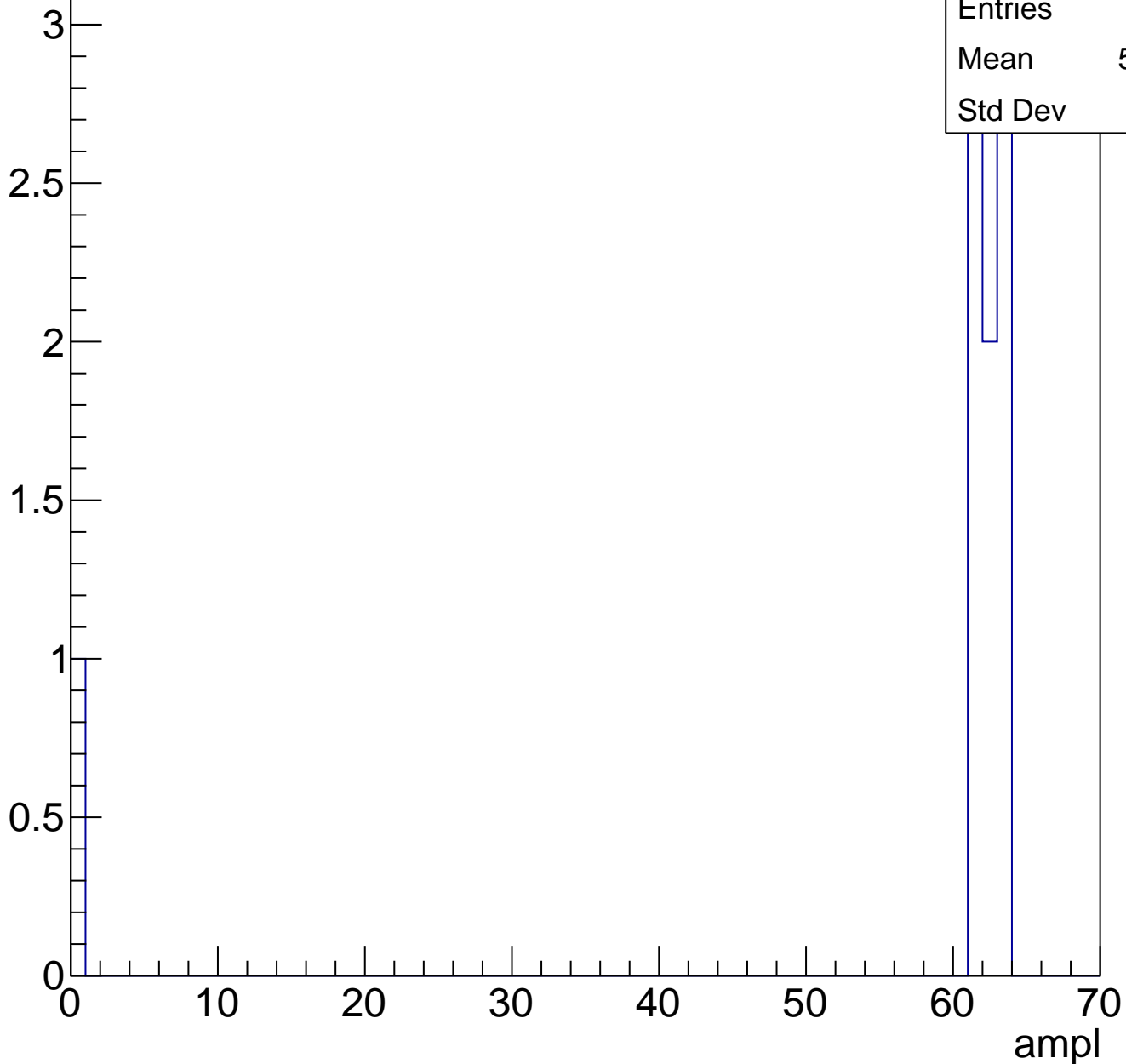
Entries	43
Mean	59.79
Std Dev	2.483



B1L102S, U19-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

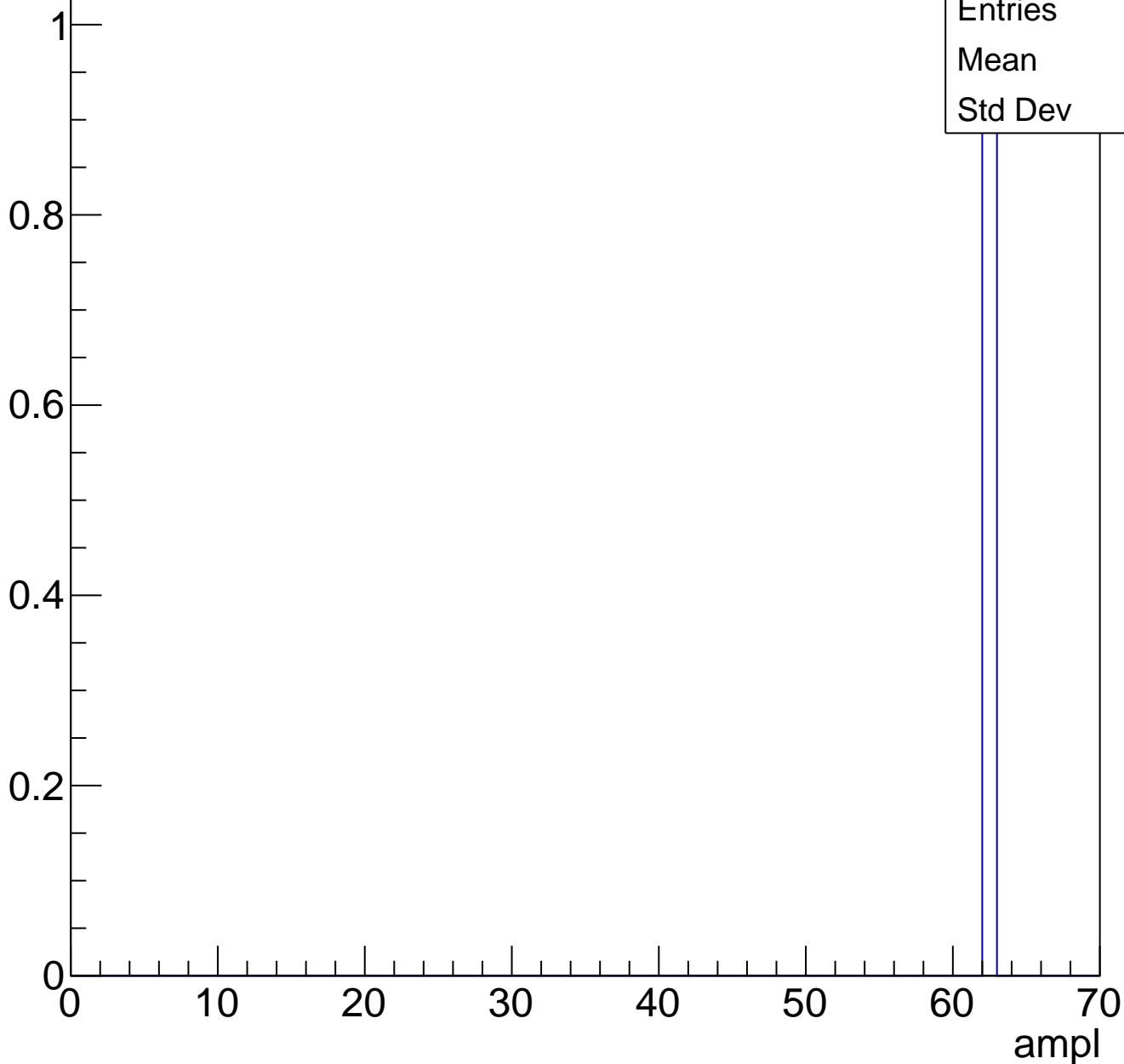


Entries	9
Mean	55.11
Std Dev	19.5

B1L102S, U19-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch56, adc0

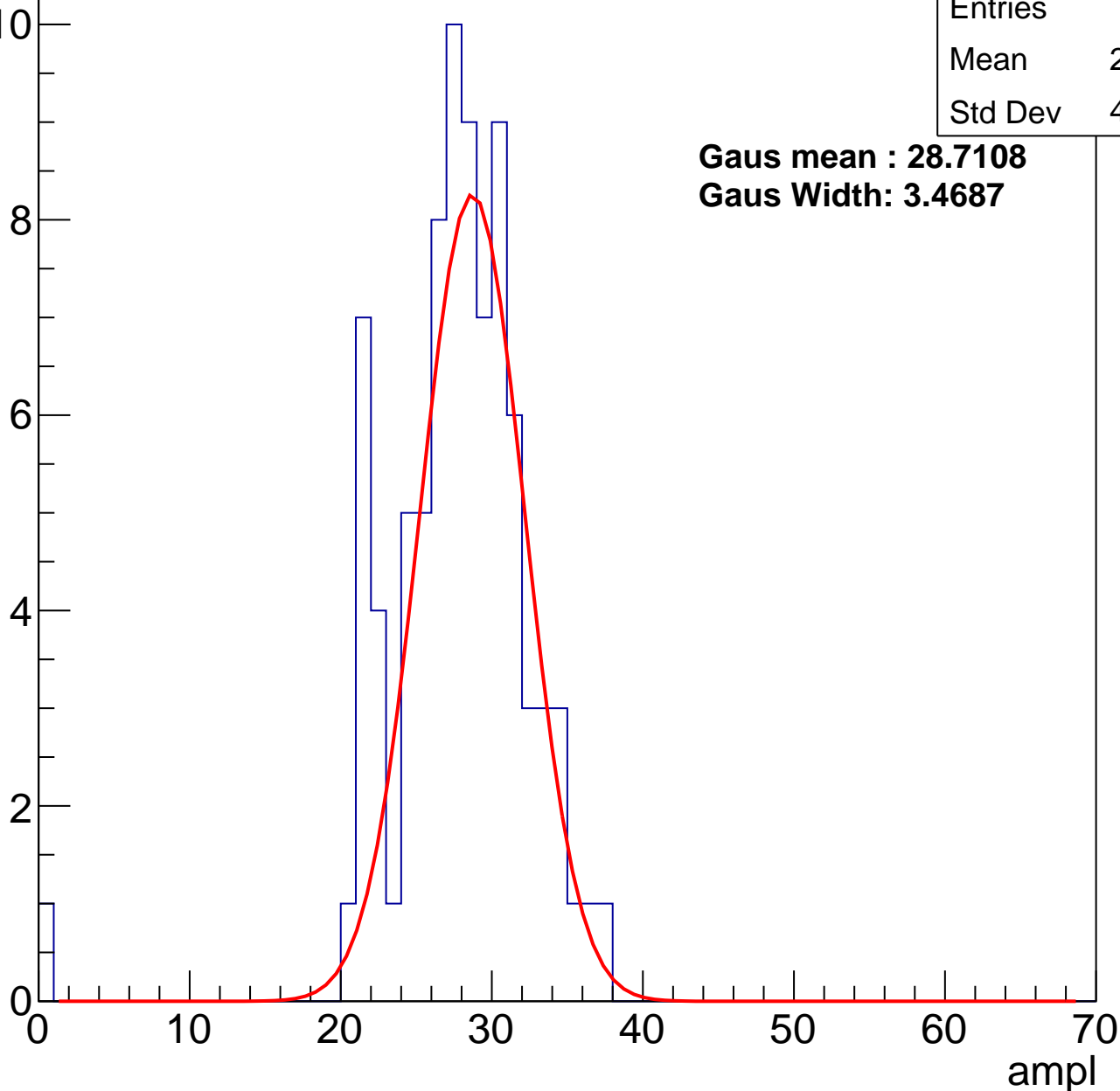
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	27.26
Std Dev	4.854

Gaus mean : 28.7108

Gaus Width: 3.4687



B1L102S, U19-ch56, adc1

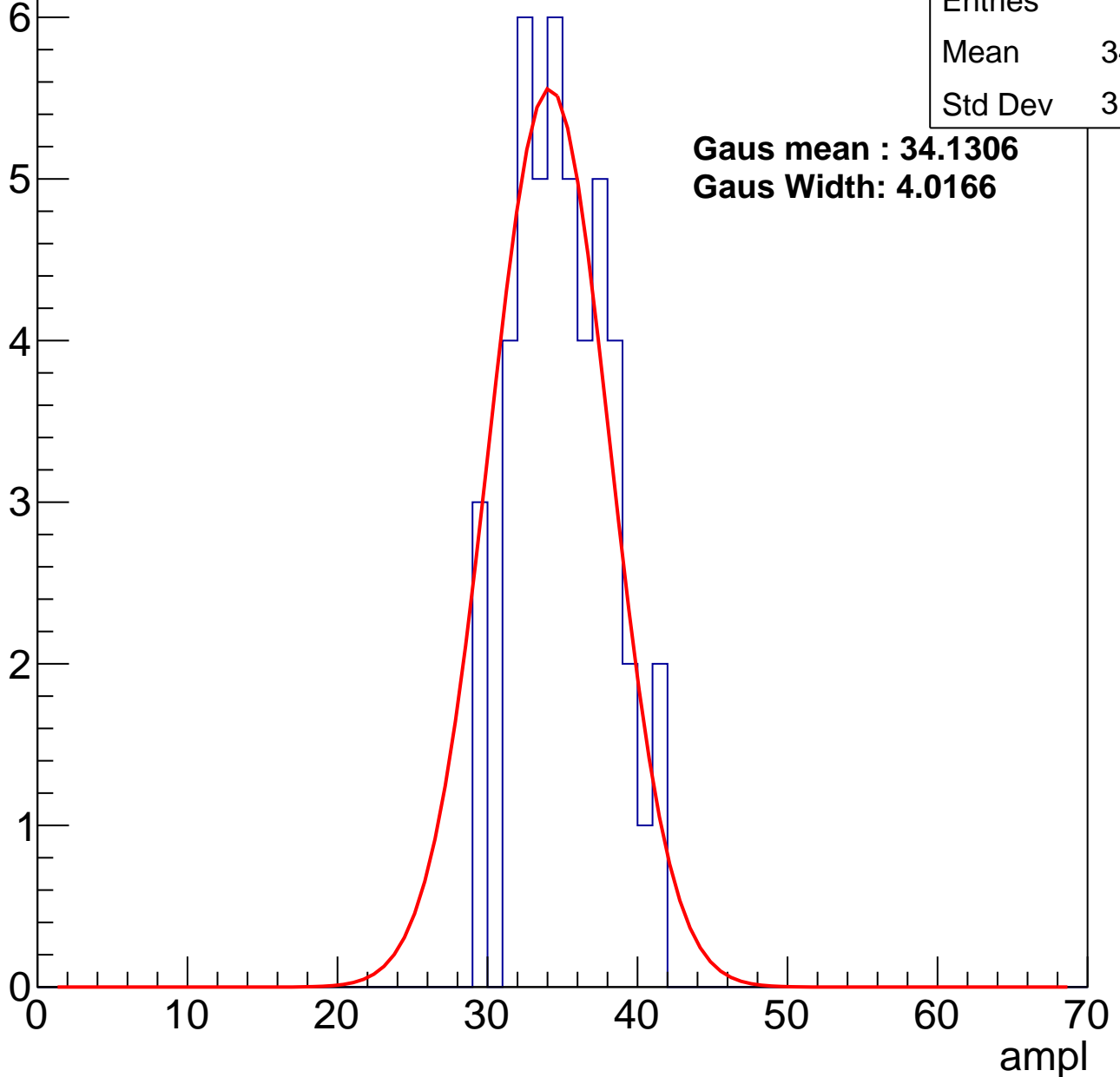
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	34.64
Std Dev	3.042

Gaus mean : 34.1306

Gaus Width: 4.0166



B1L102S, U19-ch56, adc2

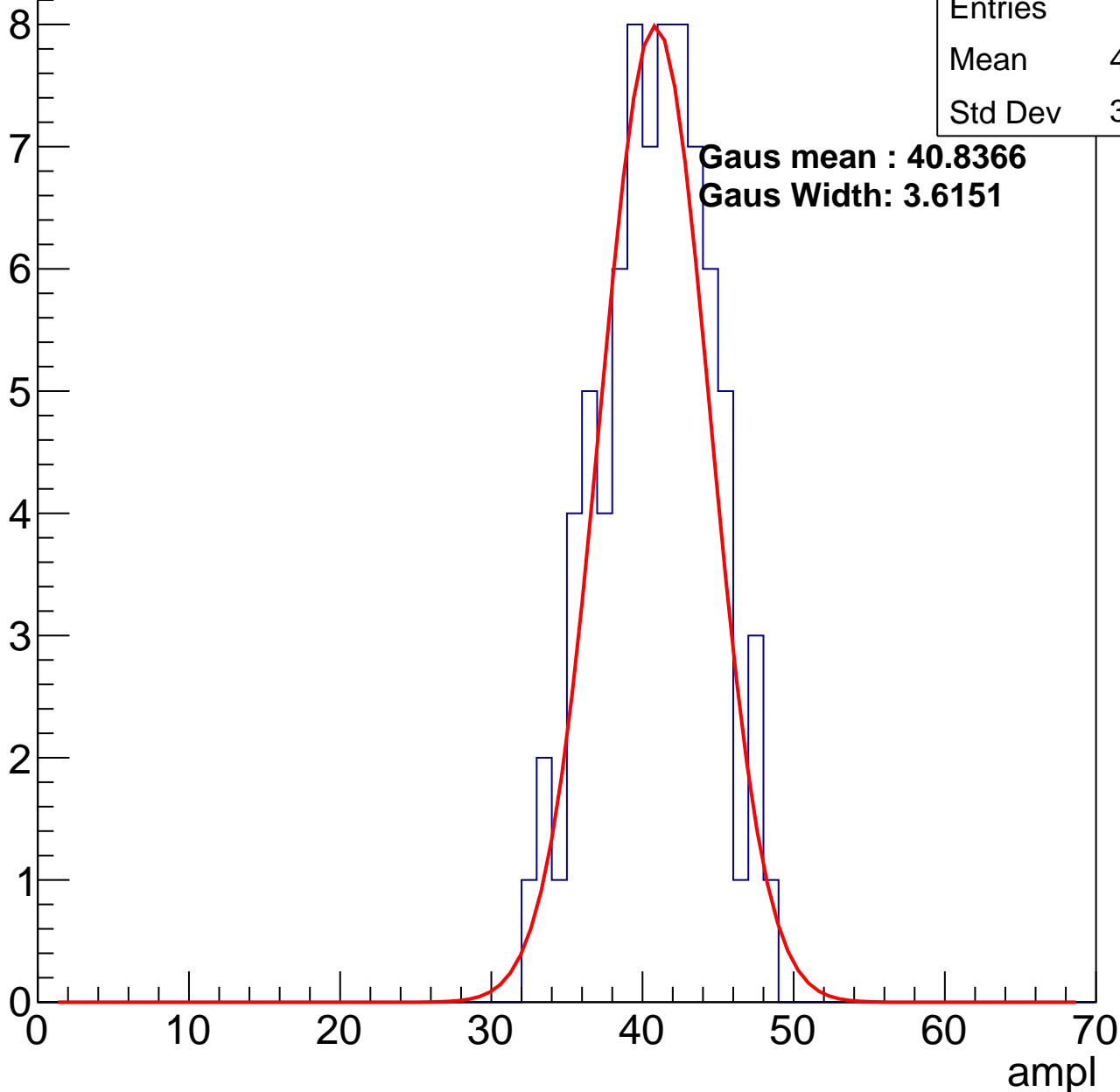
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	40.38
Std Dev	3.615

Gaus mean : 40.8366

Gaus Width: 3.6151

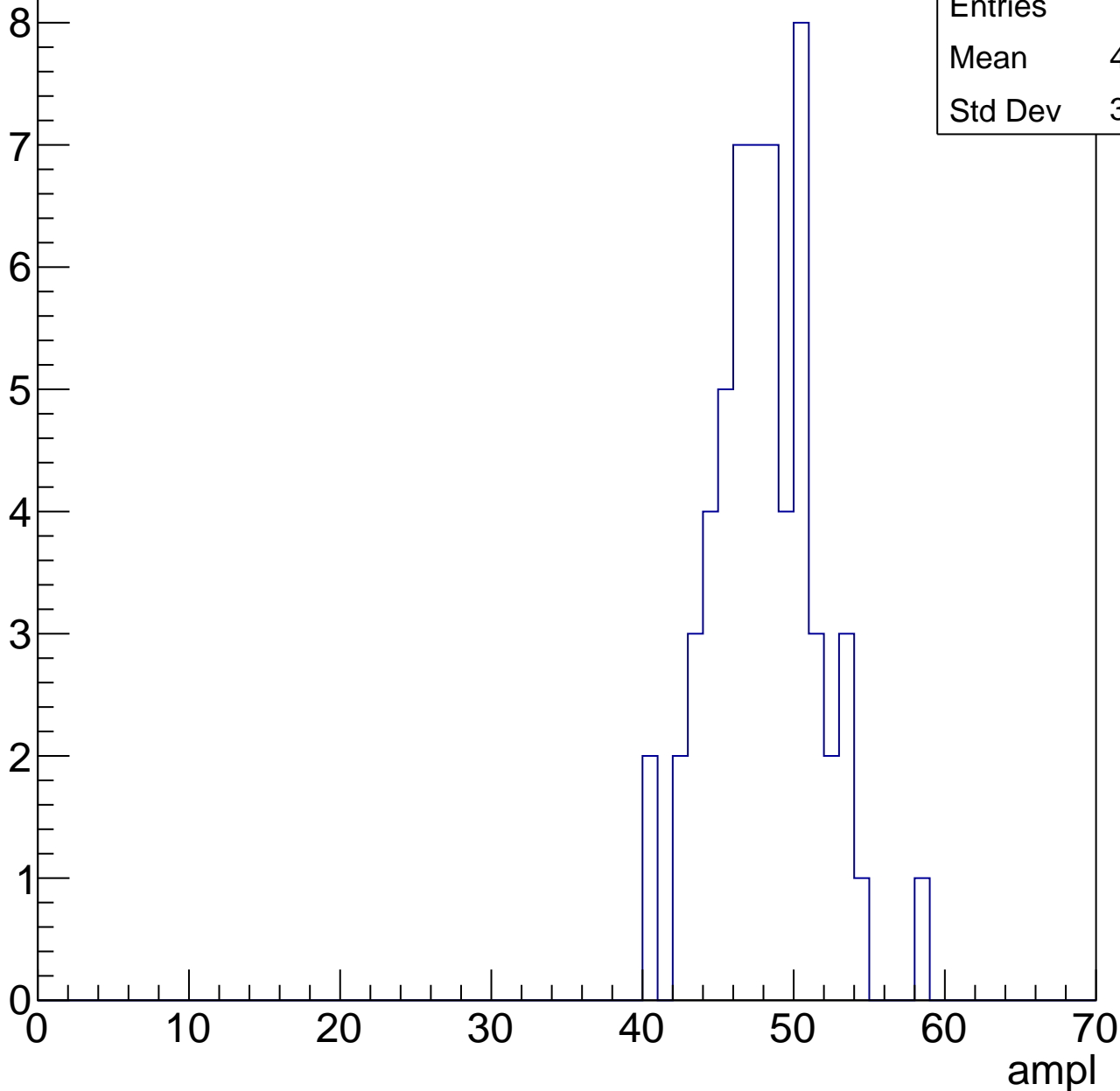


B1L102S, U19-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

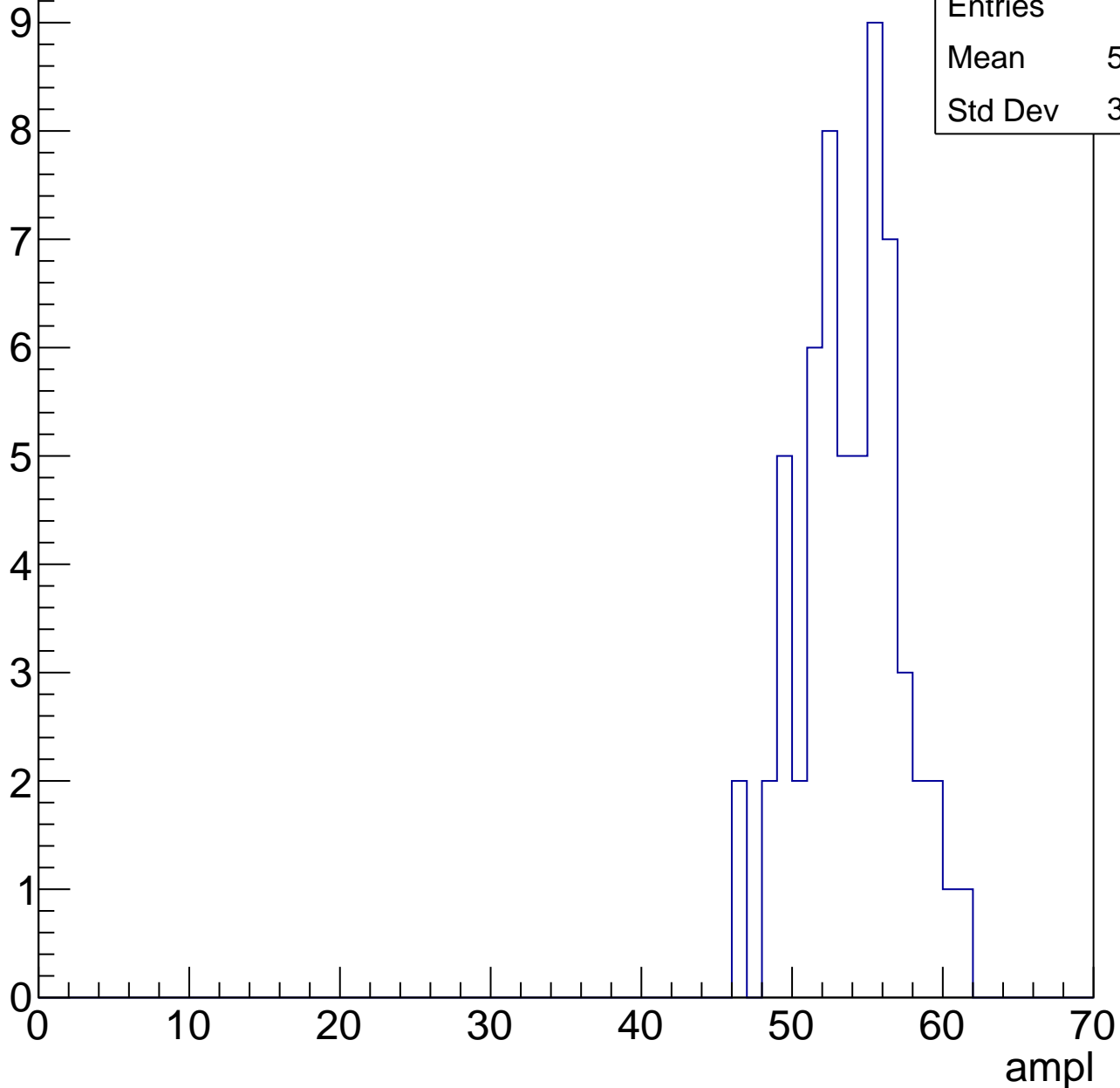
Entries	59
Mean	47.54
Std Dev	3.475



B1L102S, U19-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

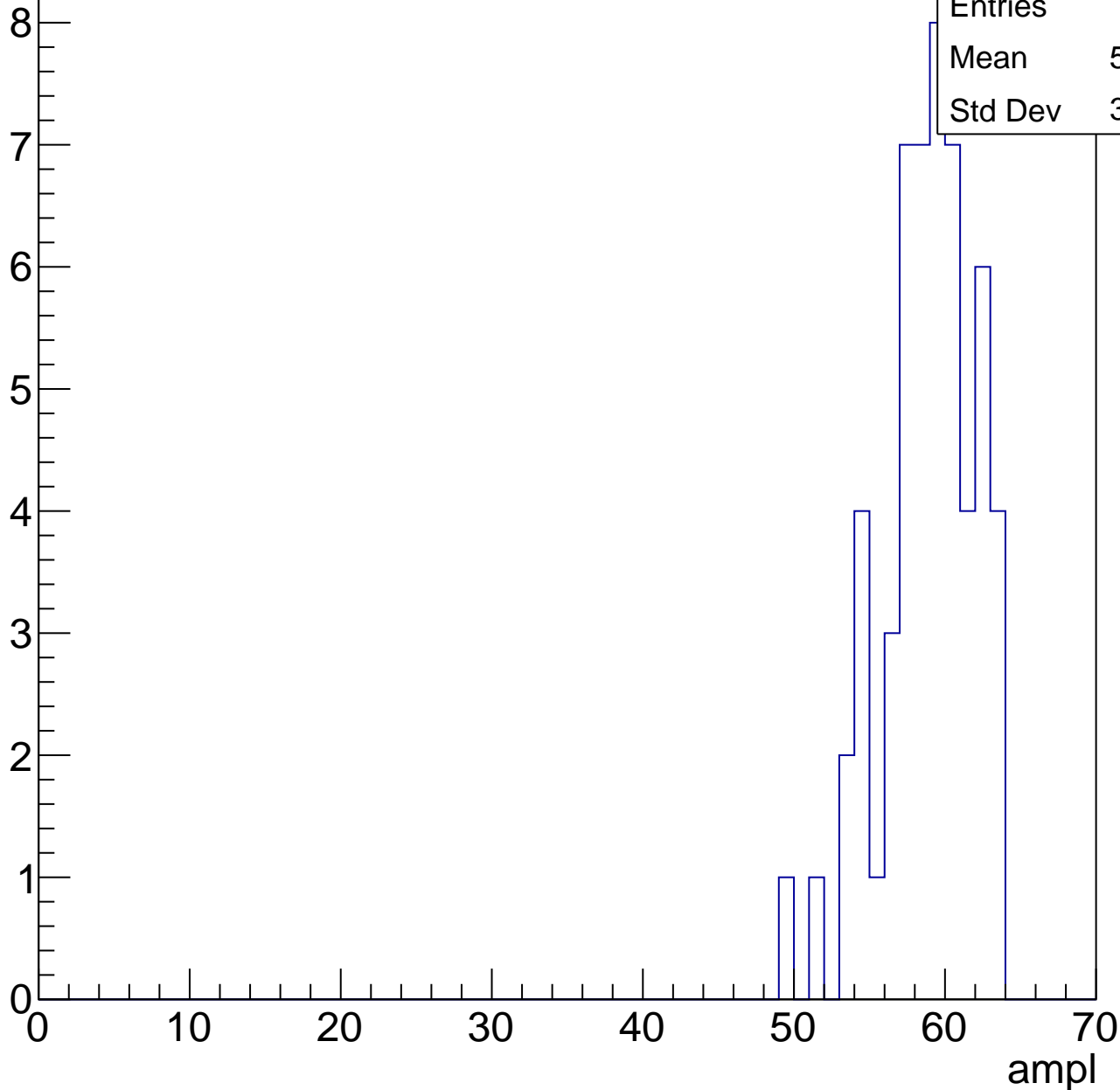


B1L102S, U19-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.36
Std Dev	3.112

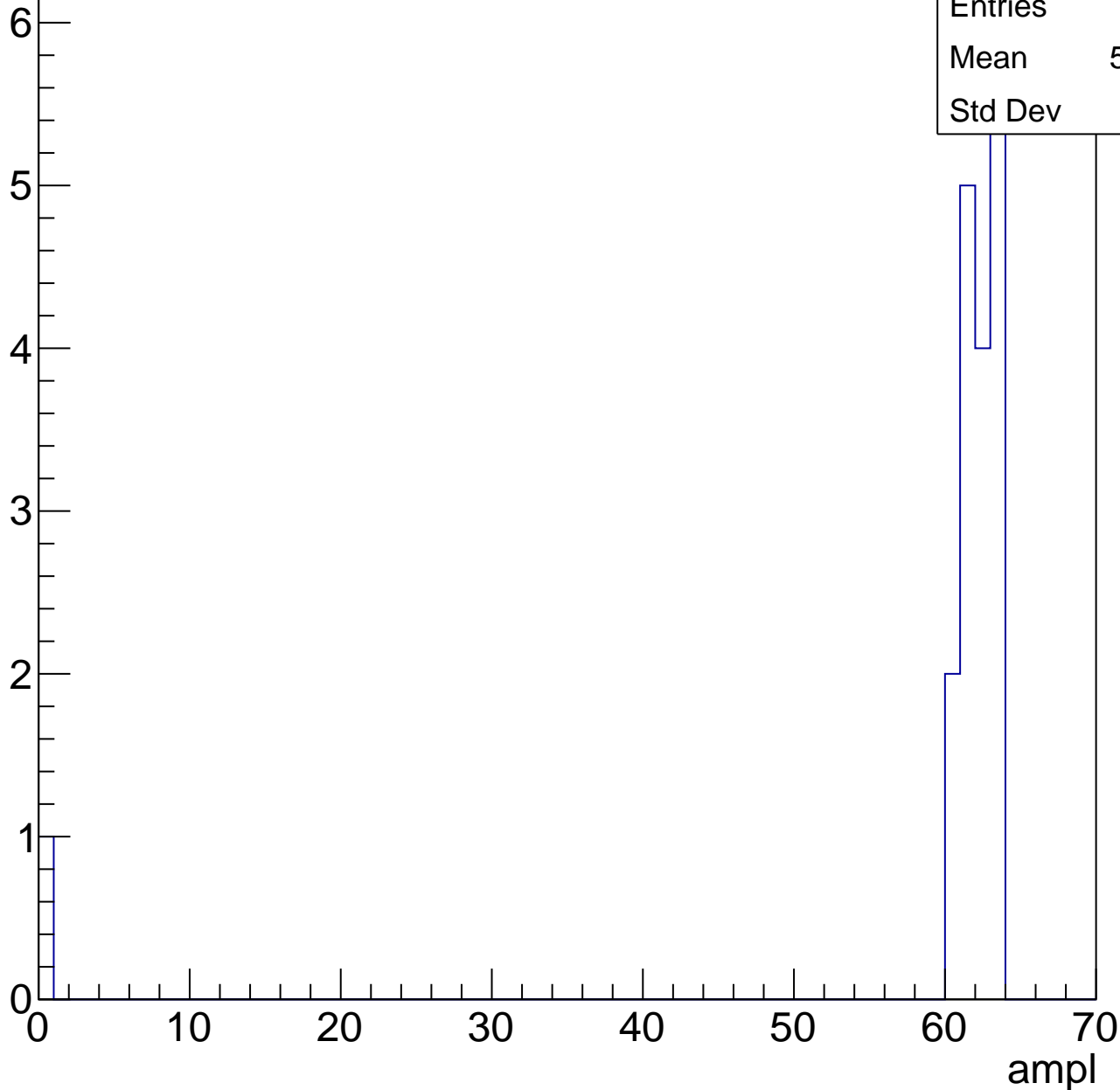


B1L102S, U19-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

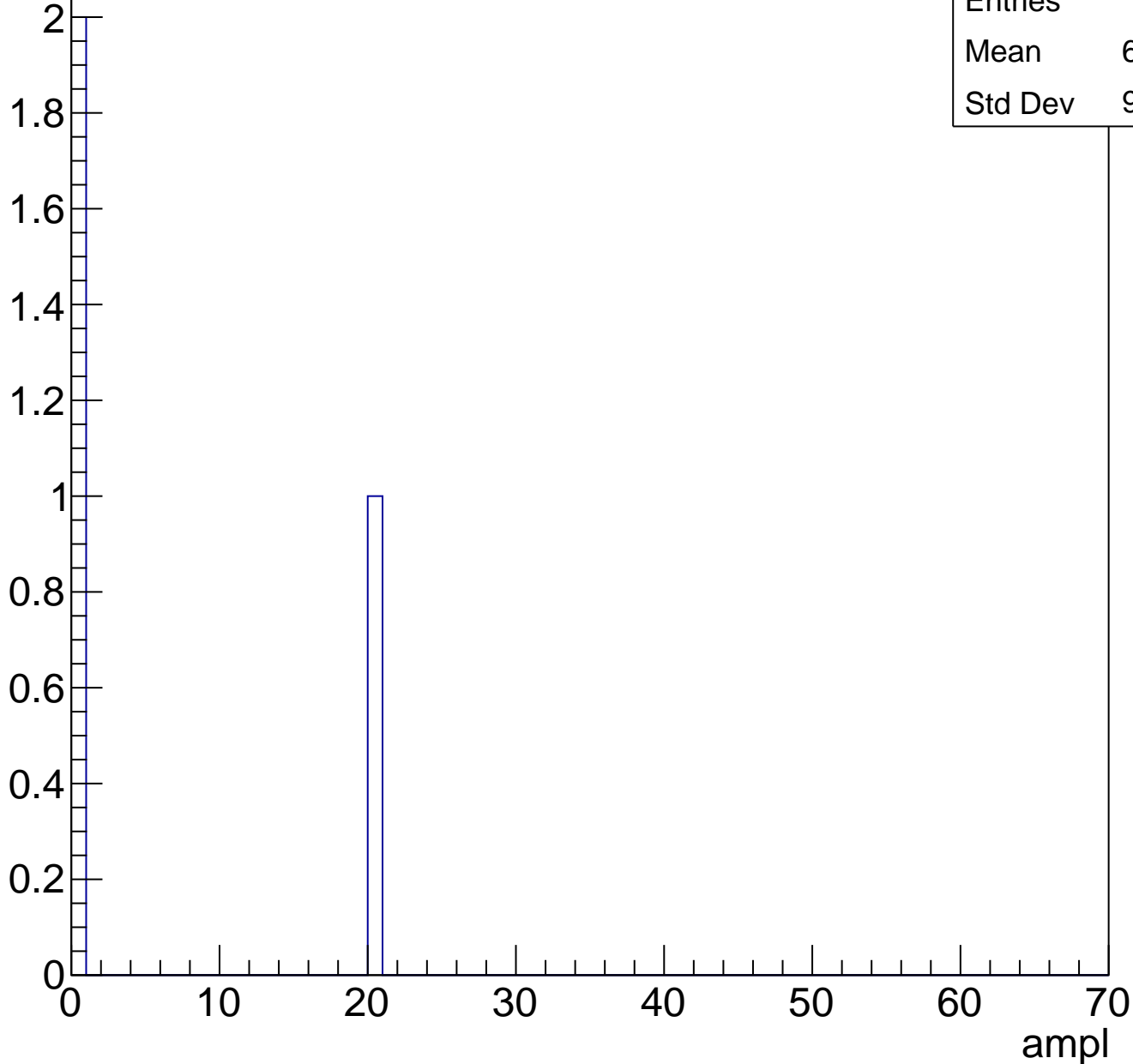
Entries	18
Mean	58.39
Std Dev	14.2



B1L102S, U19-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L102S, U19-ch57, adc0

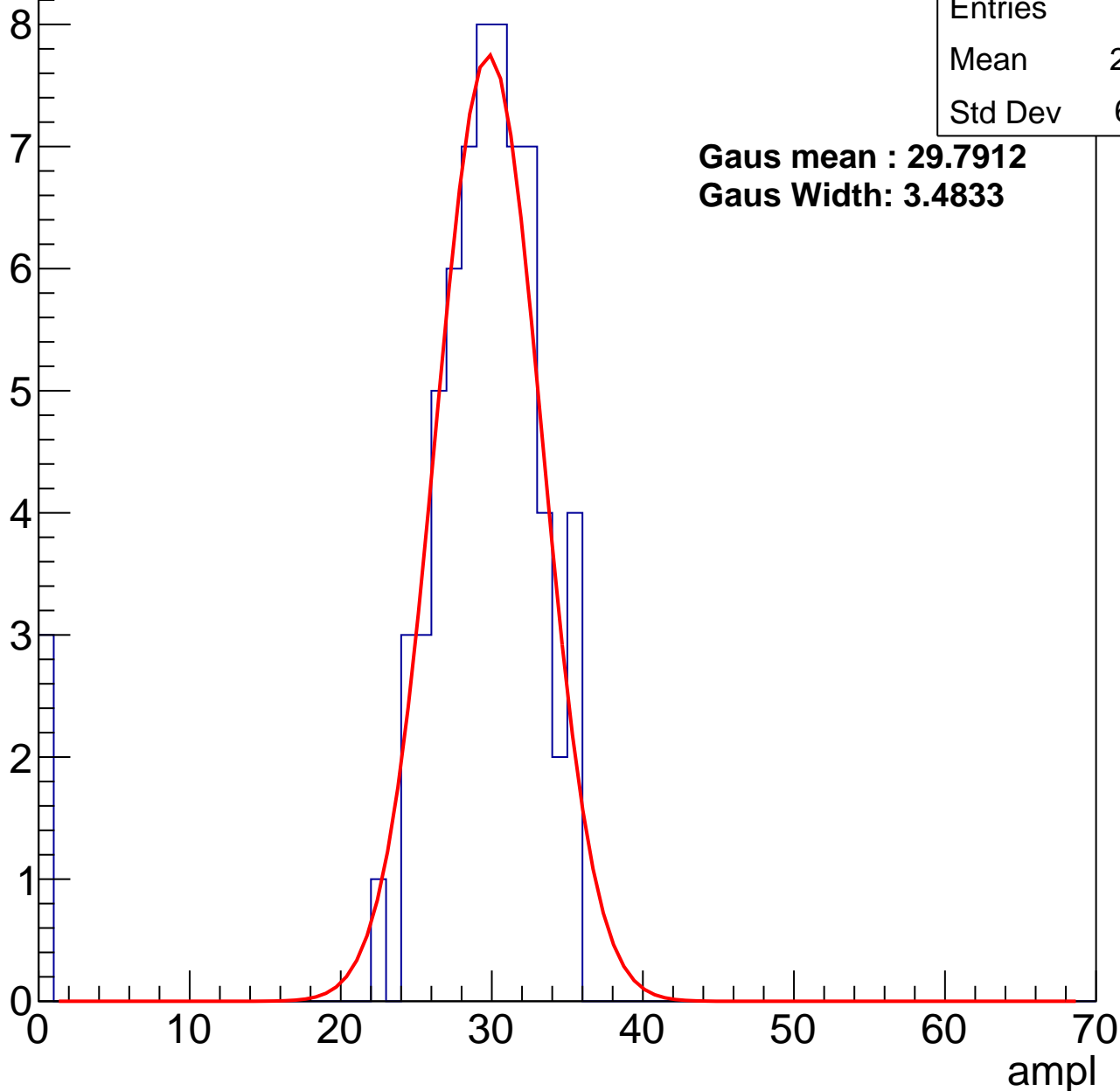
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.09
Std Dev	6.721

Gaus mean : 29.7912

Gaus Width: 3.4833



B1L102S, U19-ch57, adc1

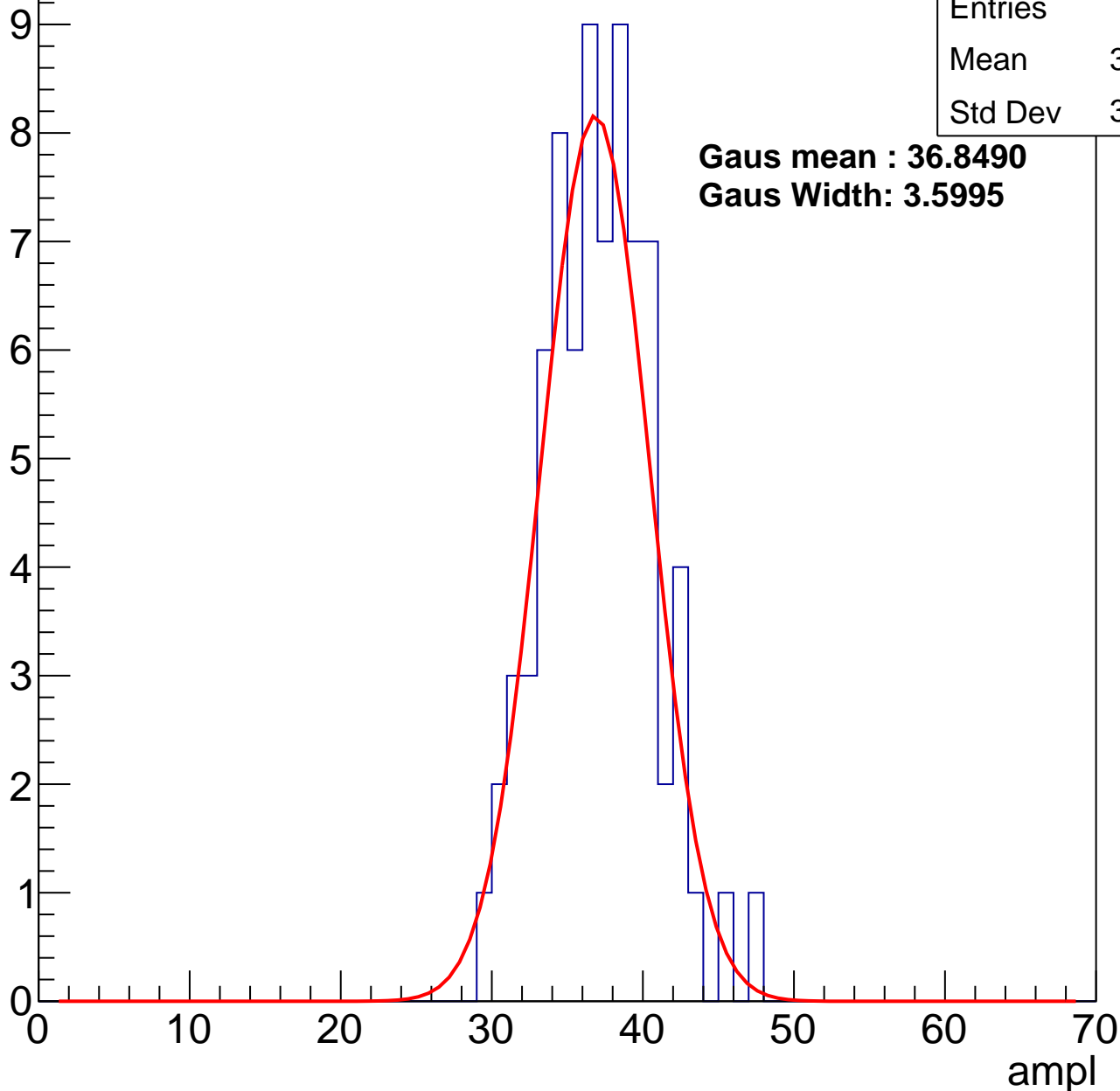
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.64
Std Dev	3.545

Gaus mean : 36.8490

Gaus Width: 3.5995

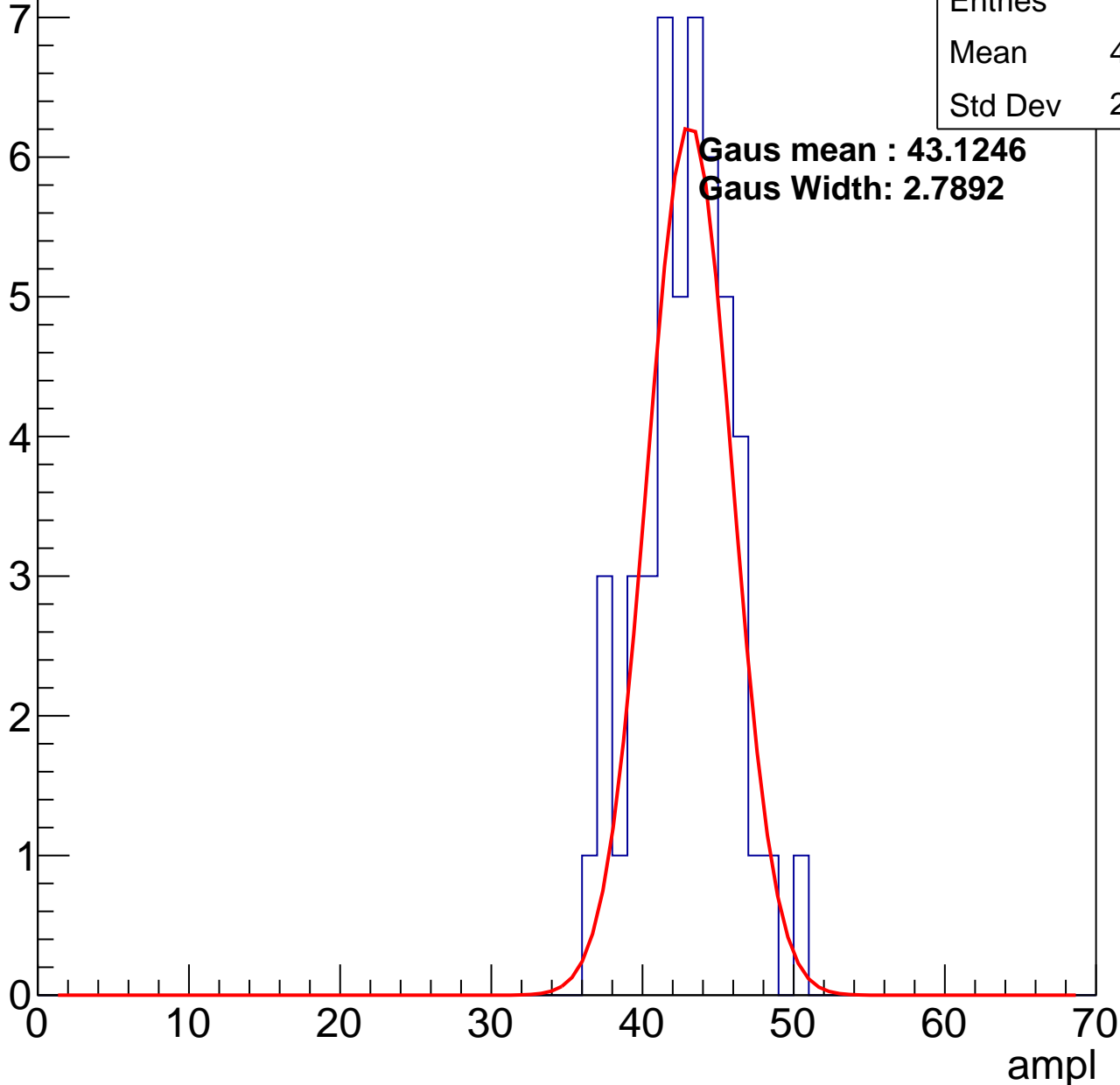


B1L102S, U19-ch57, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.46
Std Dev	2.986

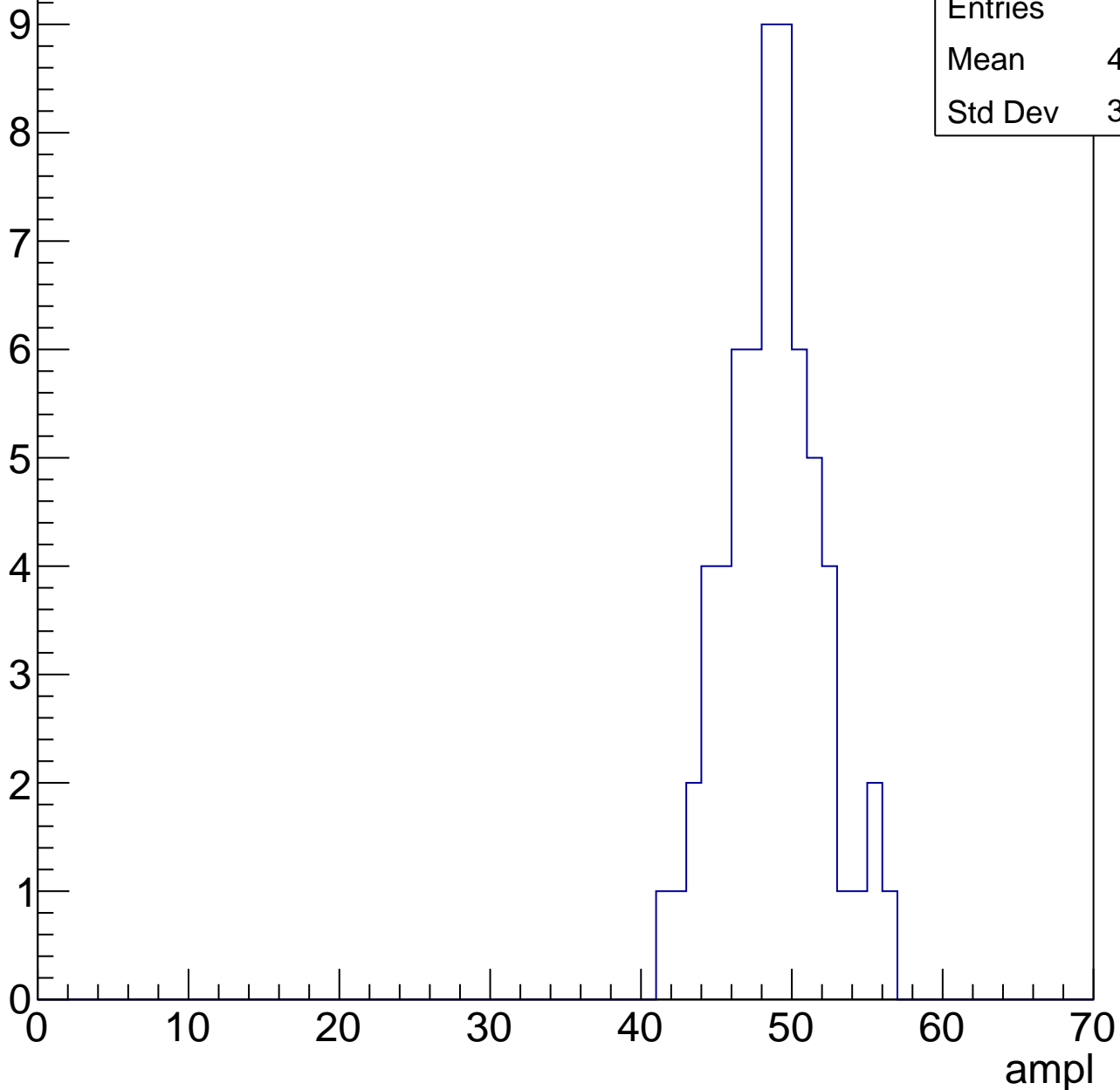


B1L102S, U19-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.26
Std Dev	3.177

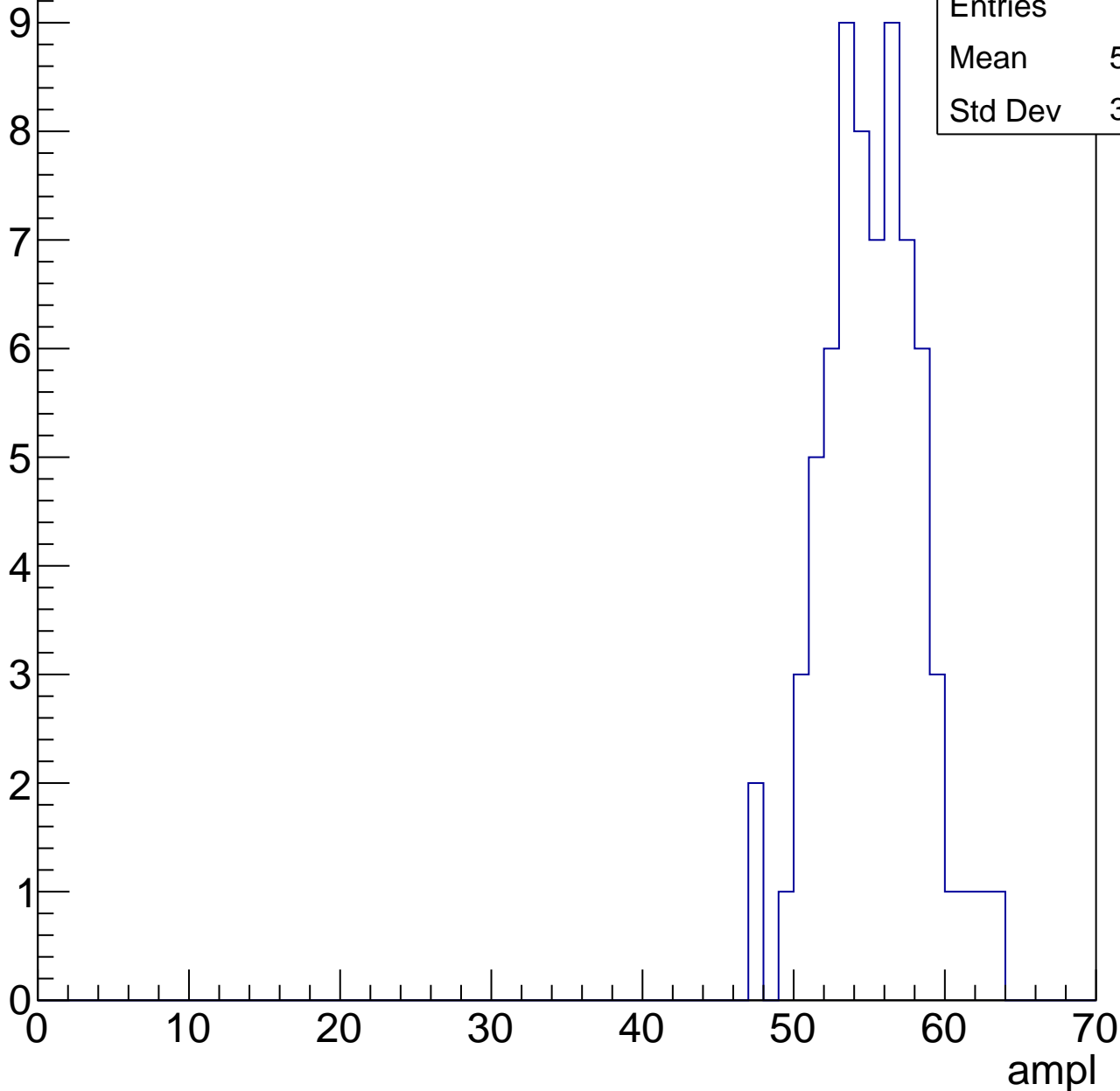


B1L102S, U19-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.69
Std Dev	3.214

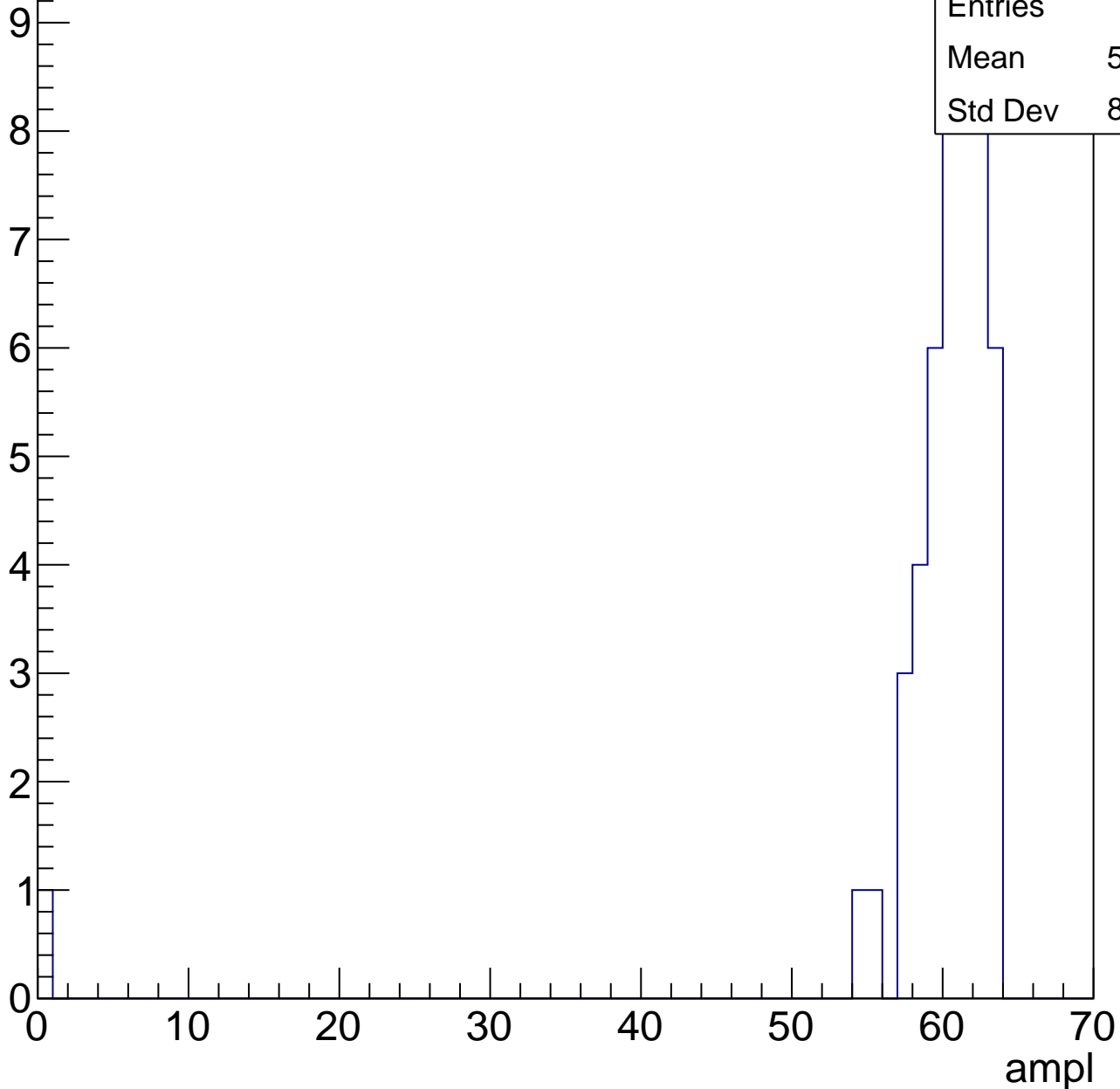


B1L102S, U19-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.89
Std Dev	8.928



B1L102S, U19-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch58, adc0

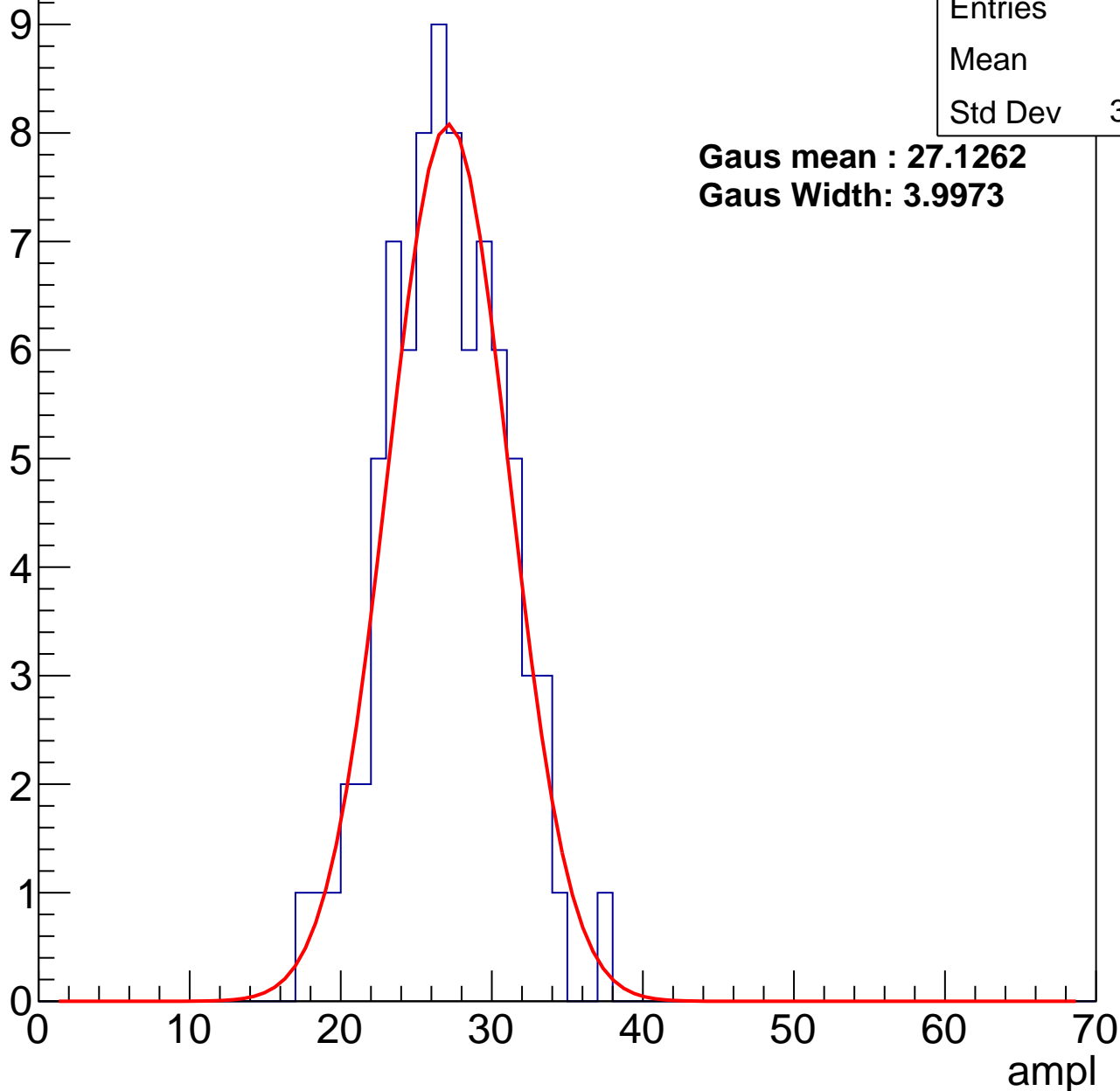
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	26.5
Std Dev	3.867

Gaus mean : 27.1262

Gaus Width: 3.9973



B1L102S, U19-ch58, adc1

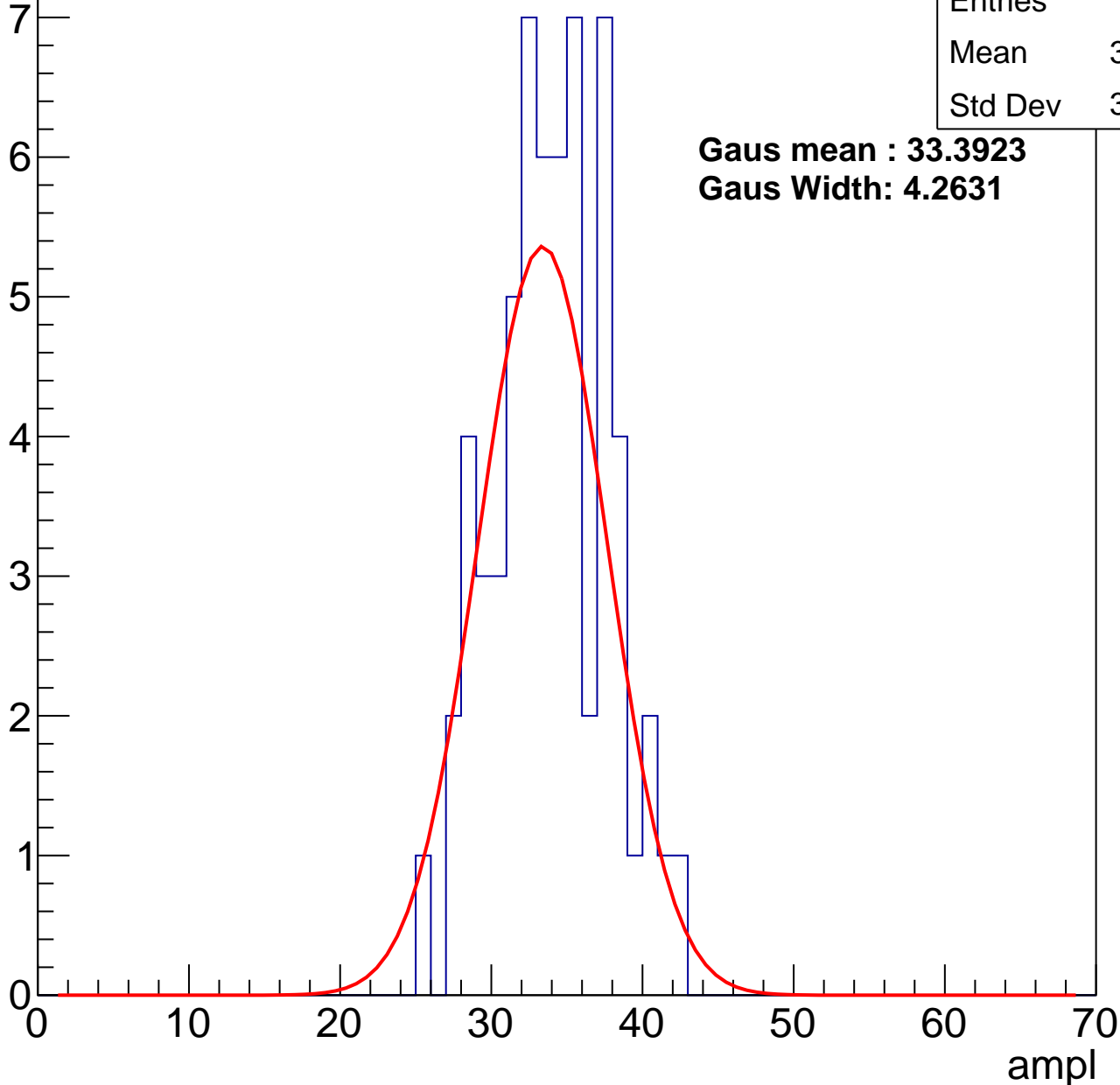
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.53
Std Dev	3.732

Gaus mean : 33.3923

Gaus Width: 4.2631



B1L102S, U19-ch58, adc2

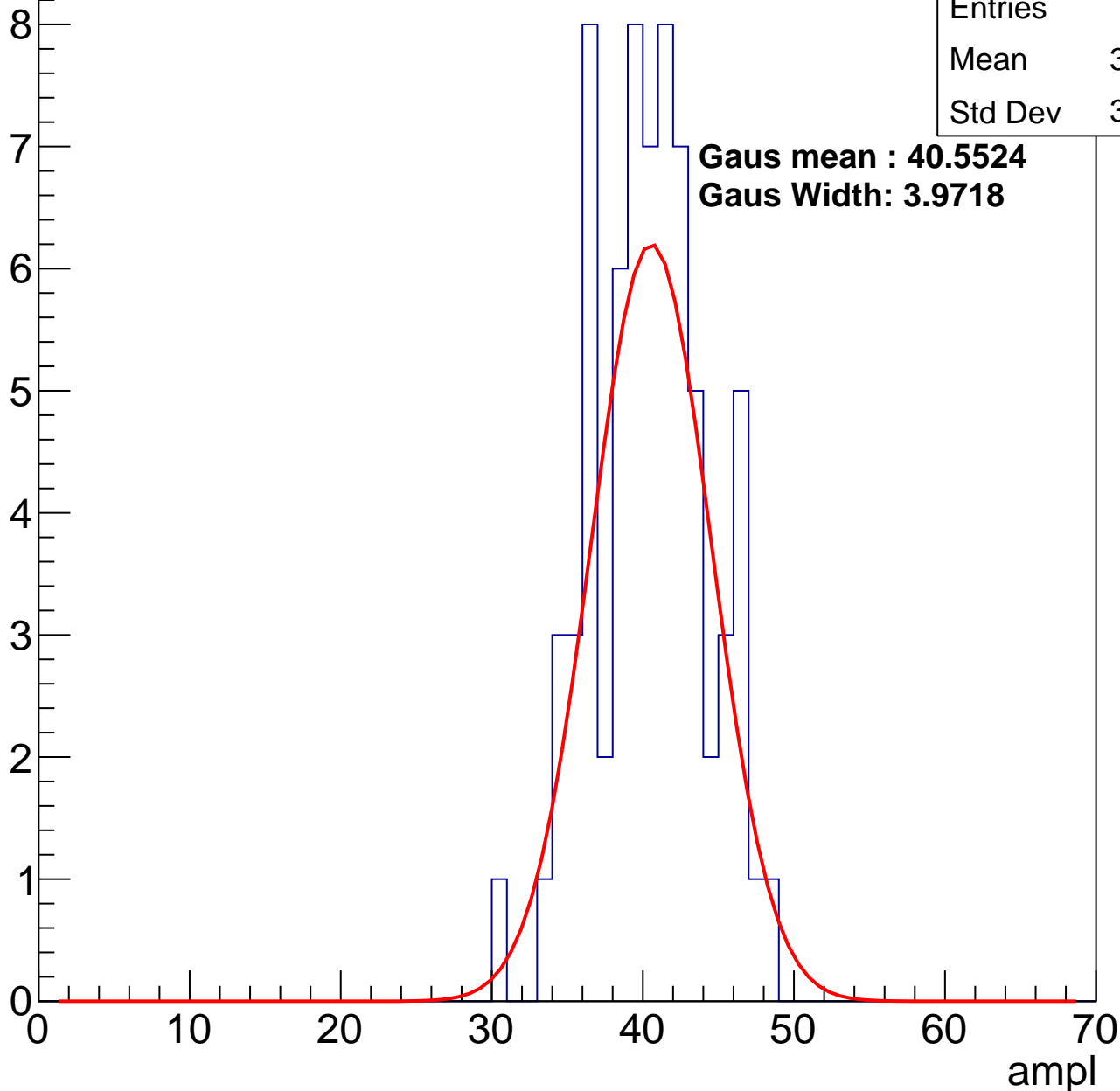
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	39.96
Std Dev	3.736

Gaus mean : 40.5524

Gaus Width: 3.9718



B1L102S, U19-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	47.22
Std Dev	3.527

Entry

10

8

6

4

2

0

0

10

20

30

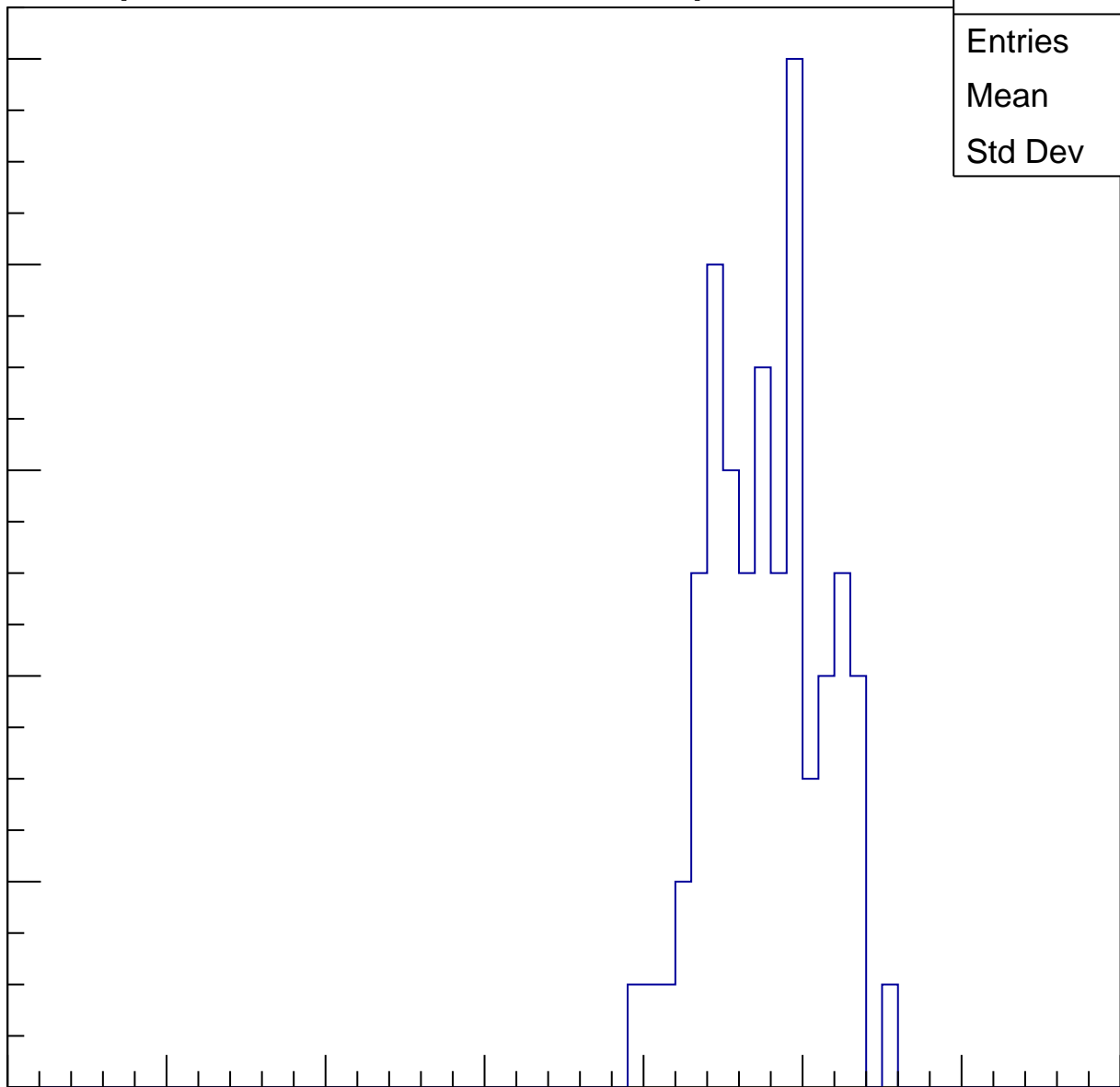
40

50

60

70

ampl

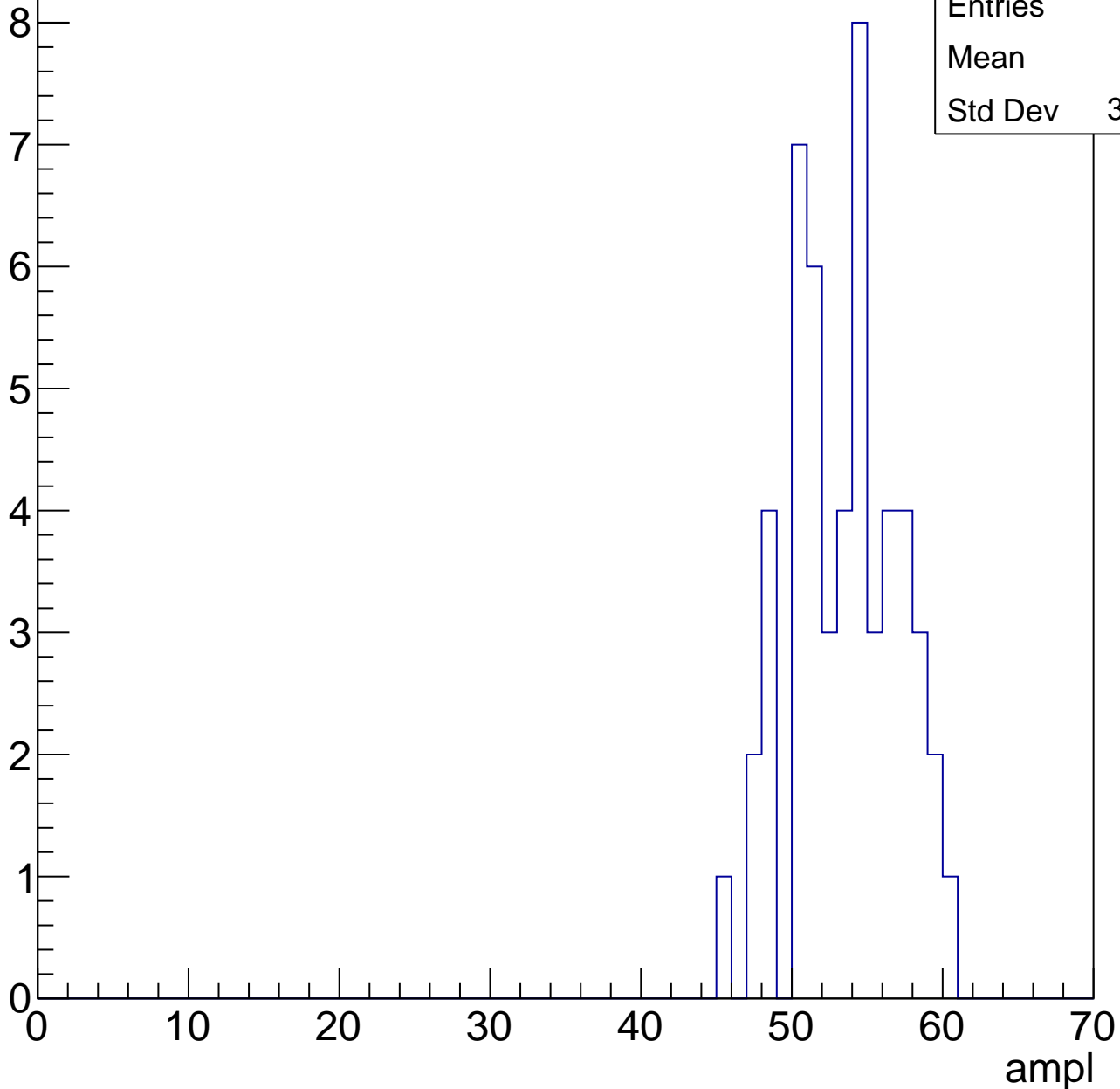


B1L102S, U19-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53
Std Dev	3.514

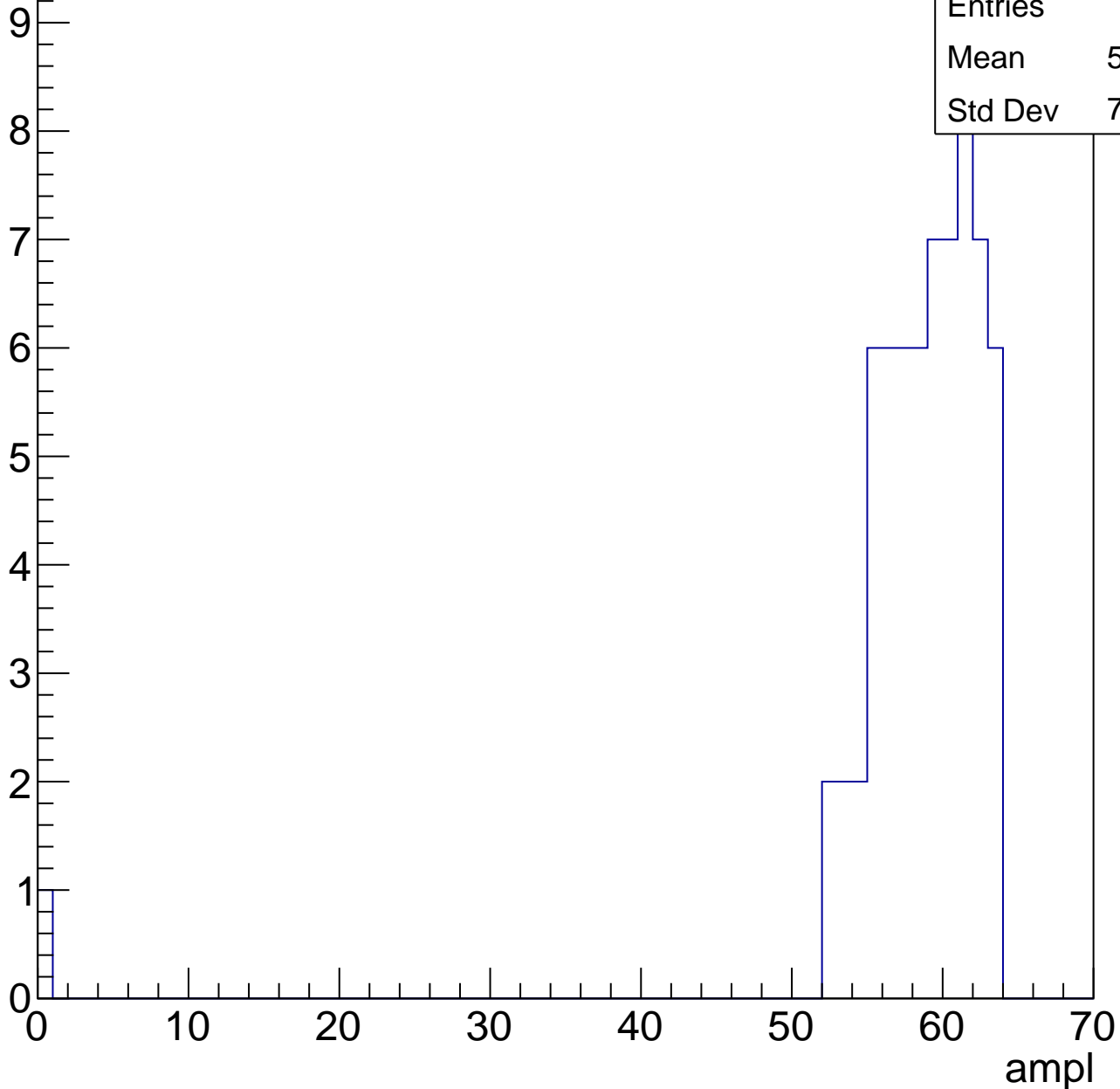


B1L102S, U19-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	57.73
Std Dev	7.703



B1L102S, U19-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

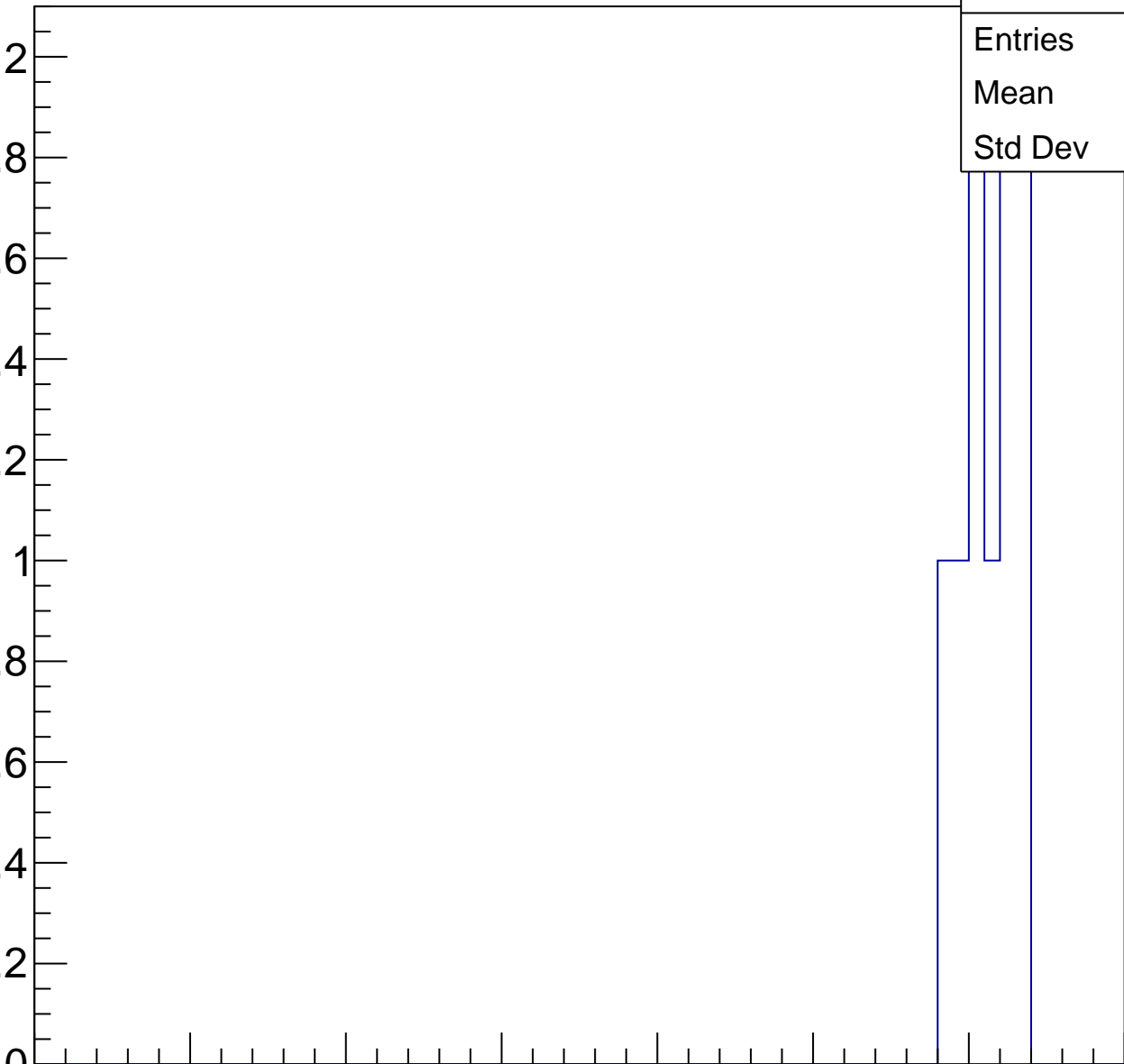
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.89
Std Dev	1.663

0 10 20 30 40 50 60 70

ampl



B1L102S, U19-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch59, adc0

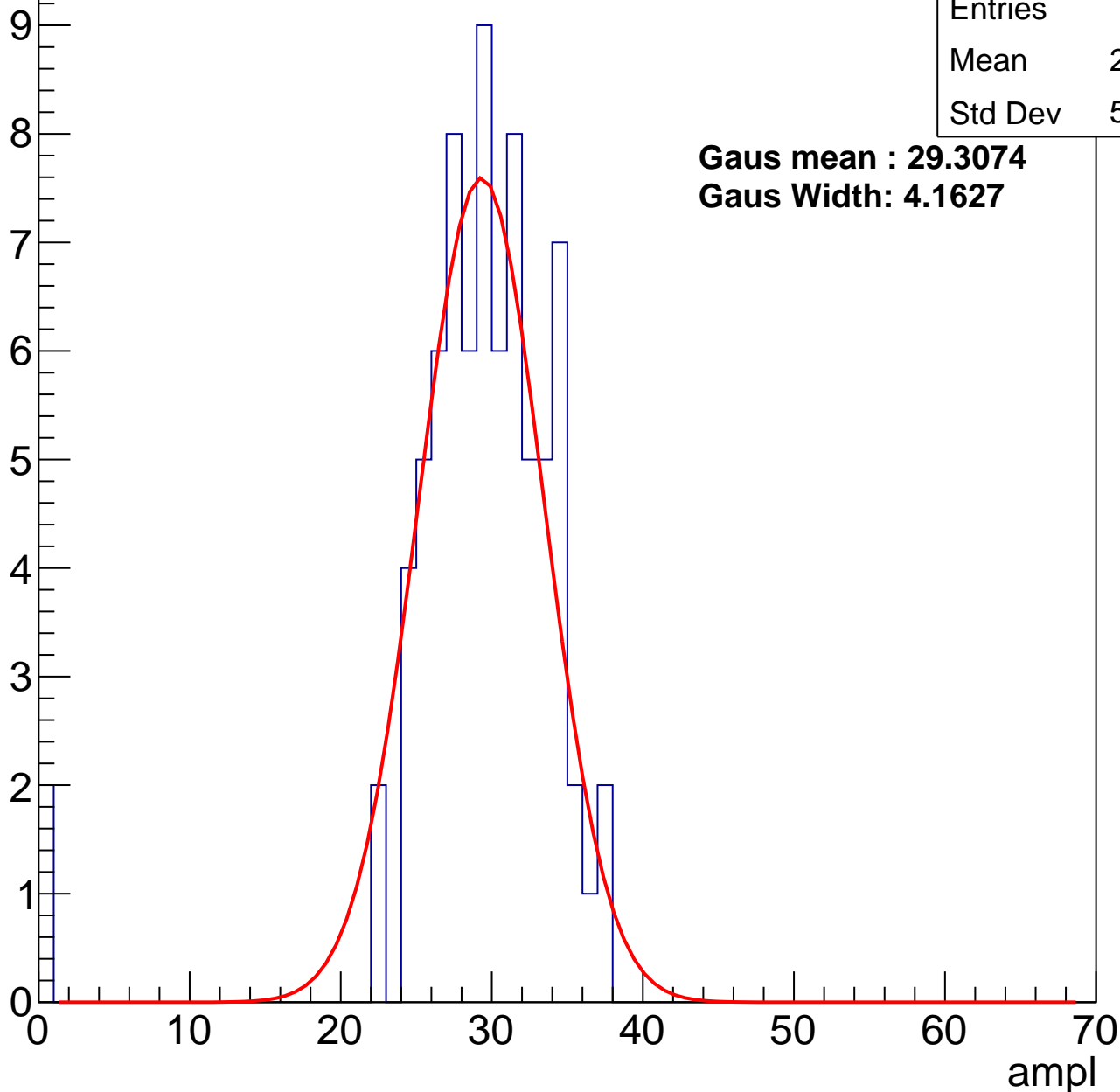
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.68
Std Dev	5.808

Gaus mean : 29.3074

Gaus Width: 4.1627



B1L102S, U19-ch59, adc1

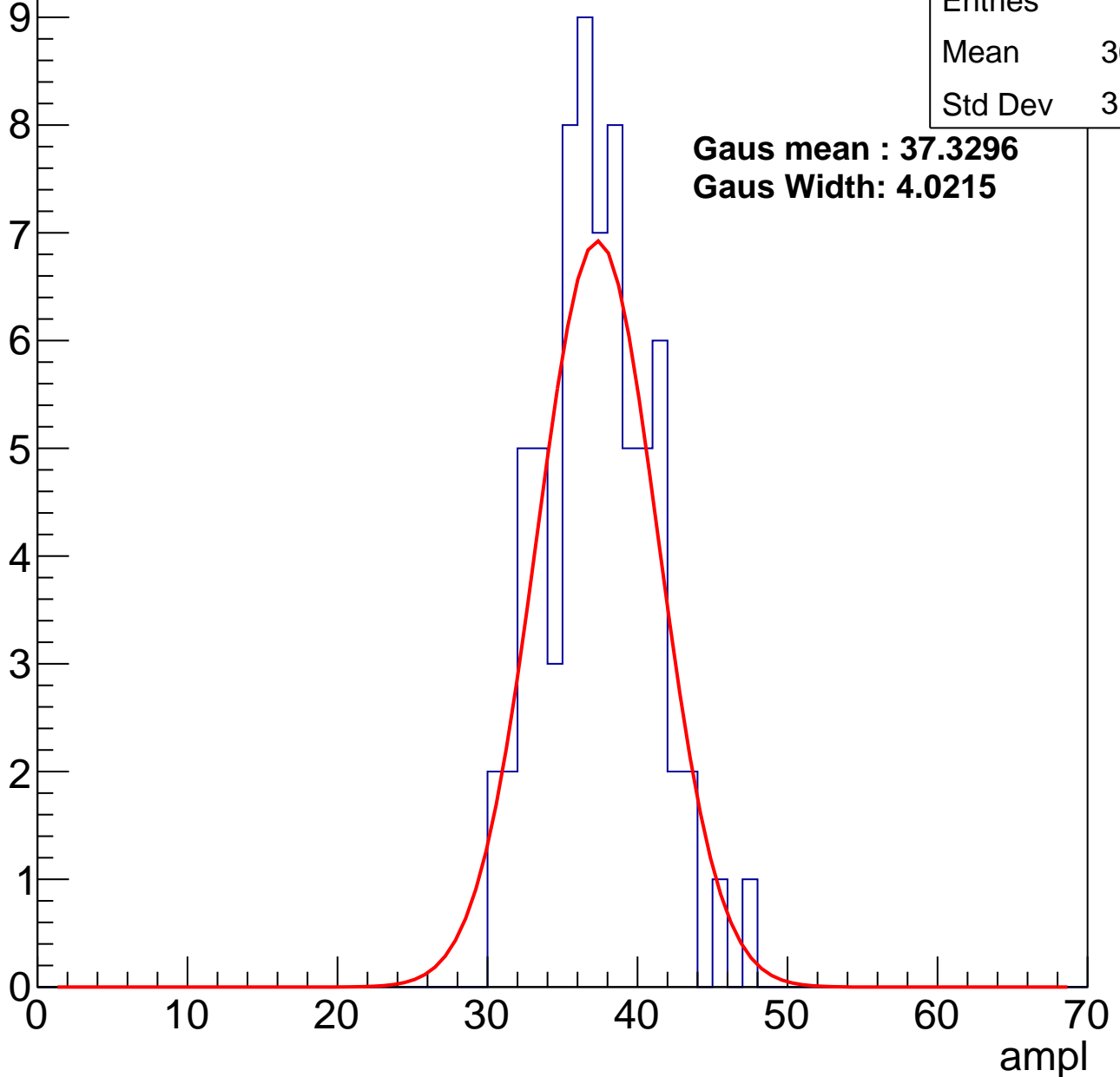
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.89
Std Dev	3.555

Gaus mean : 37.3296

Gaus Width: 4.0215



B1L102S, U19-ch59, adc2

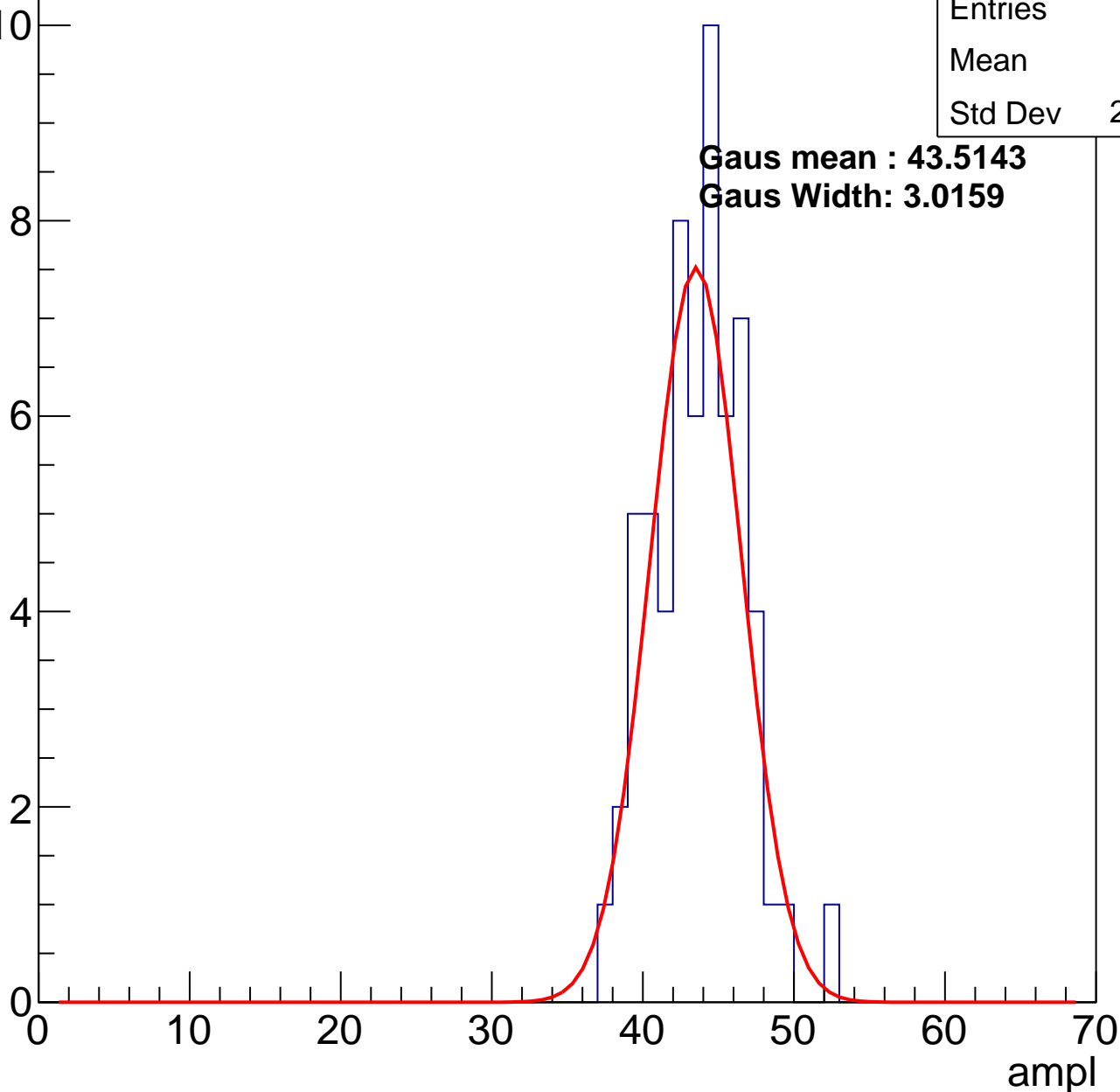
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.2
Std Dev	2.963

Gaus mean : 43.5143

Gaus Width: 3.0159



B1L102S, U19-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	50.67
Std Dev	3.444

Entry

10

8

6

4

2

0

0

10

20

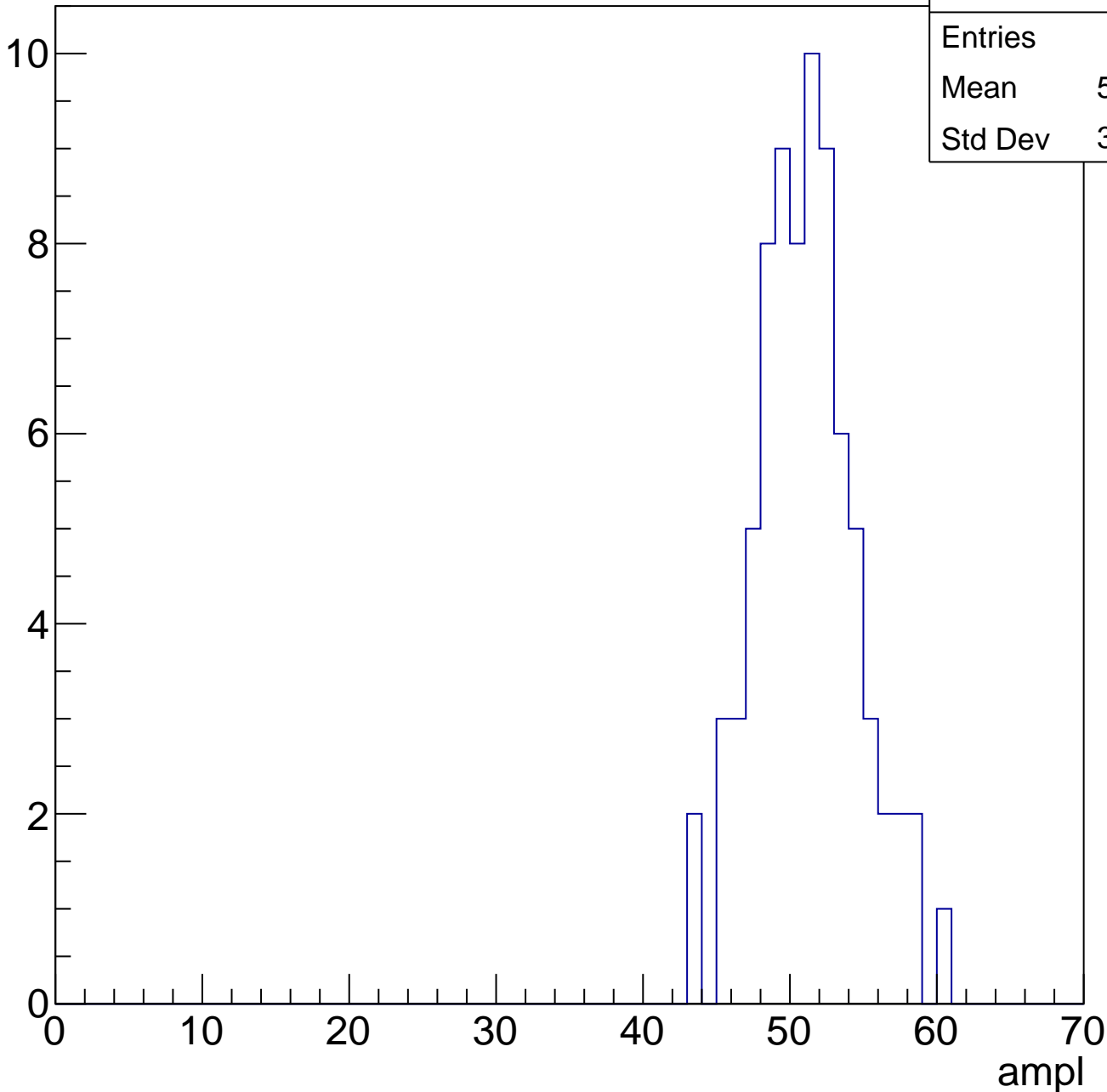
30

40

50

60

ampl

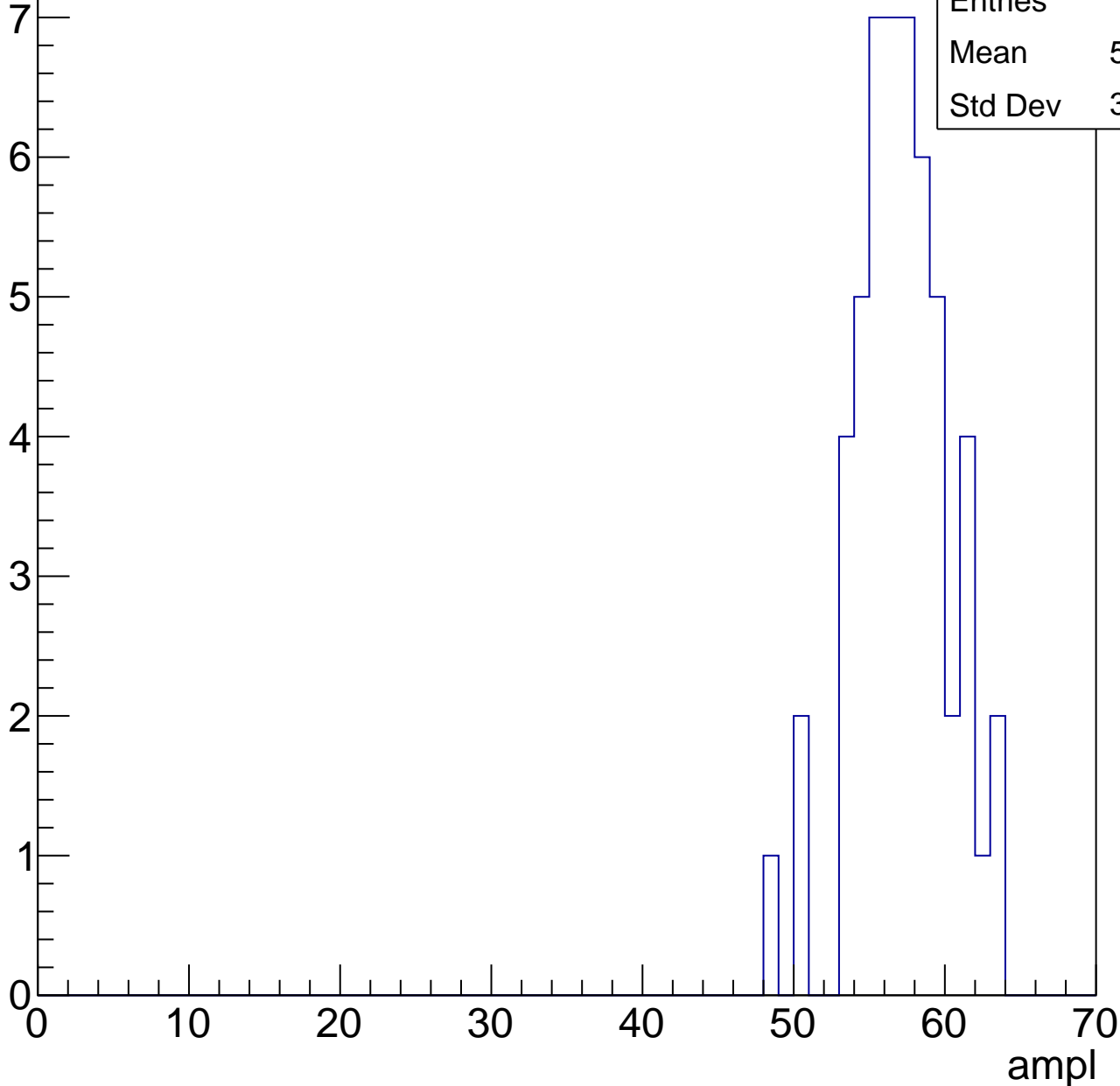


B1L102S, U19-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.62
Std Dev	3.134

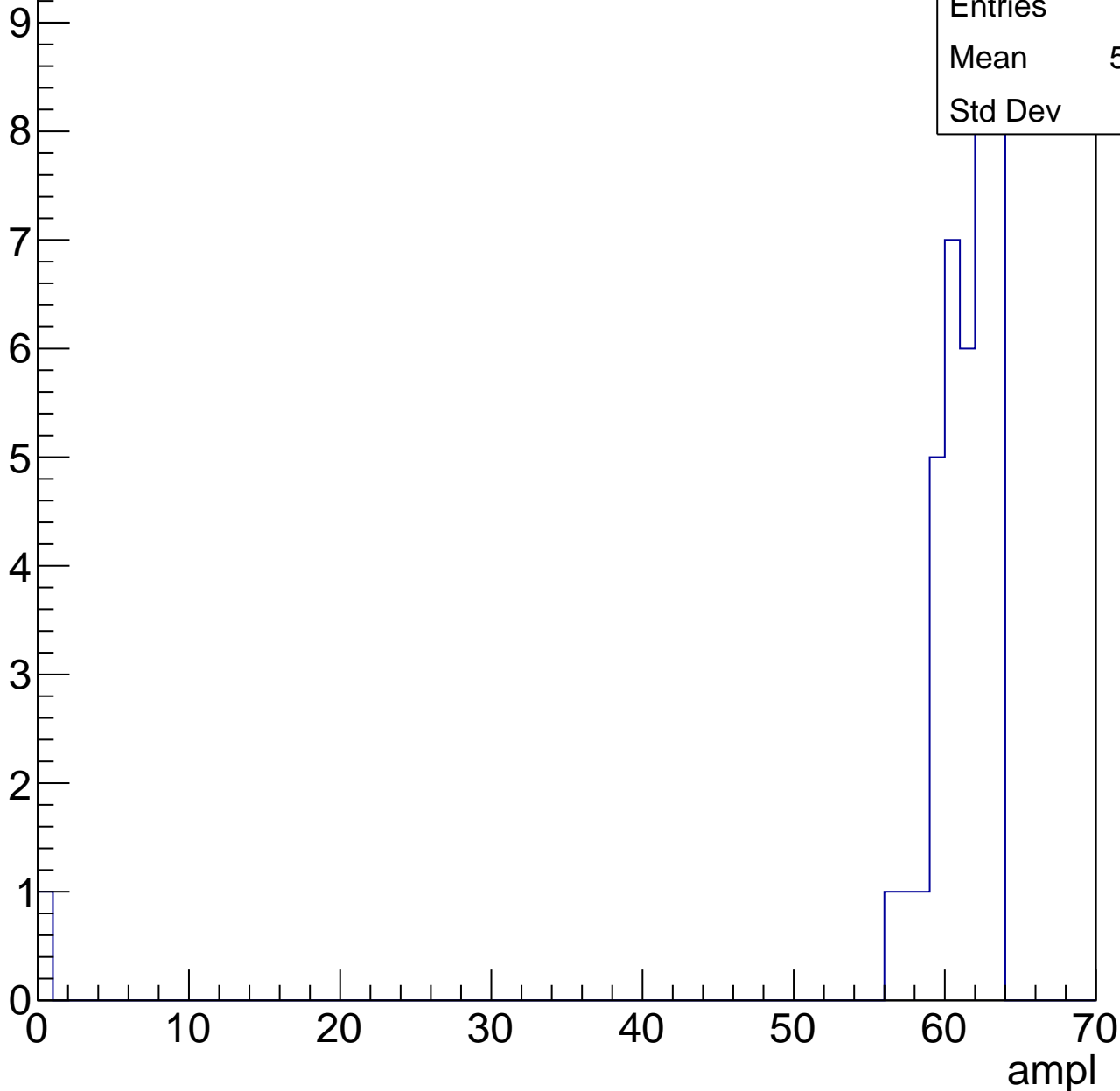


B1L102S, U19-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

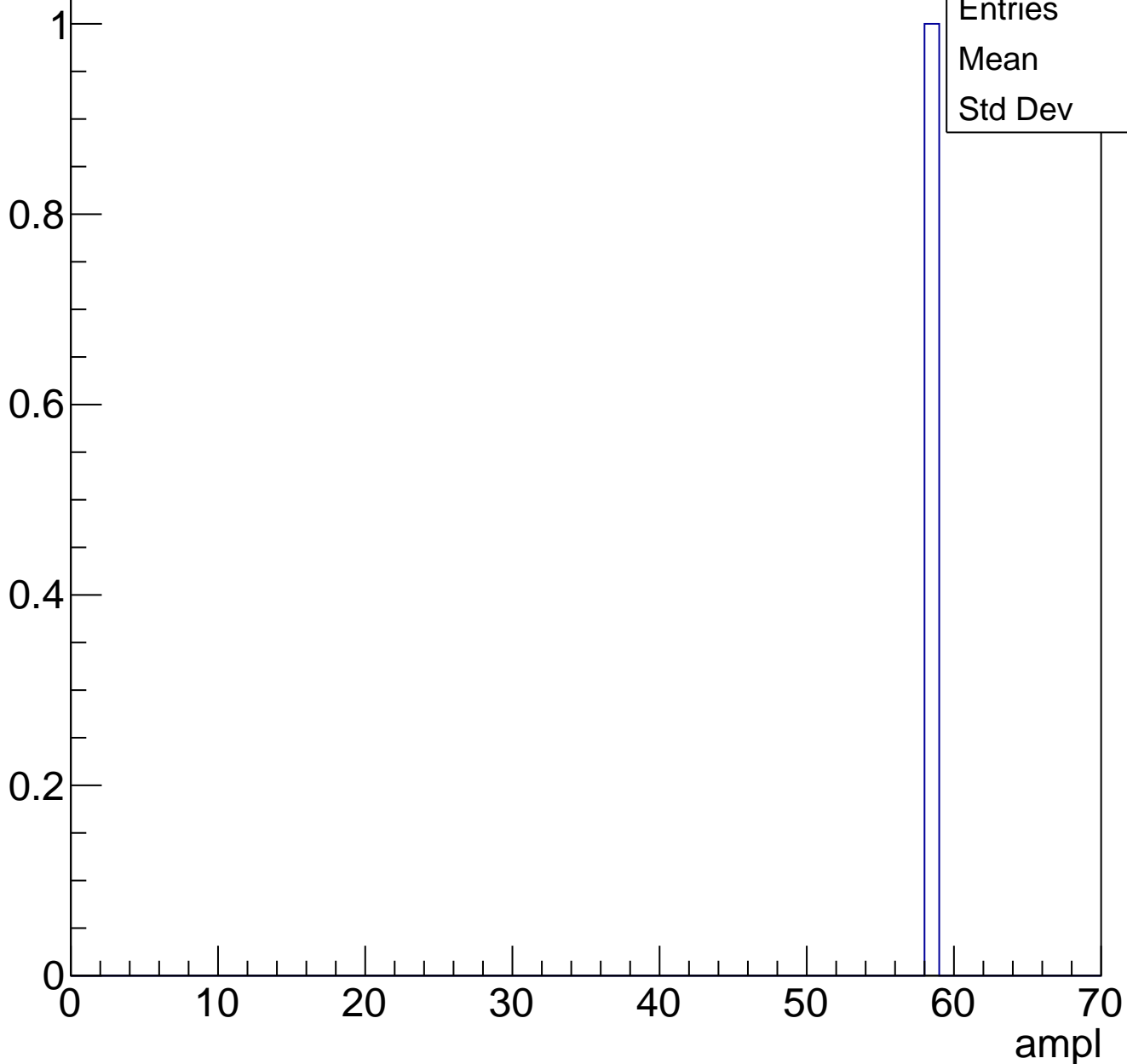
Entries	39
Mean	59.33
Std Dev	9.78



B1L102S, U19-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L102S, U19-ch60, adc0

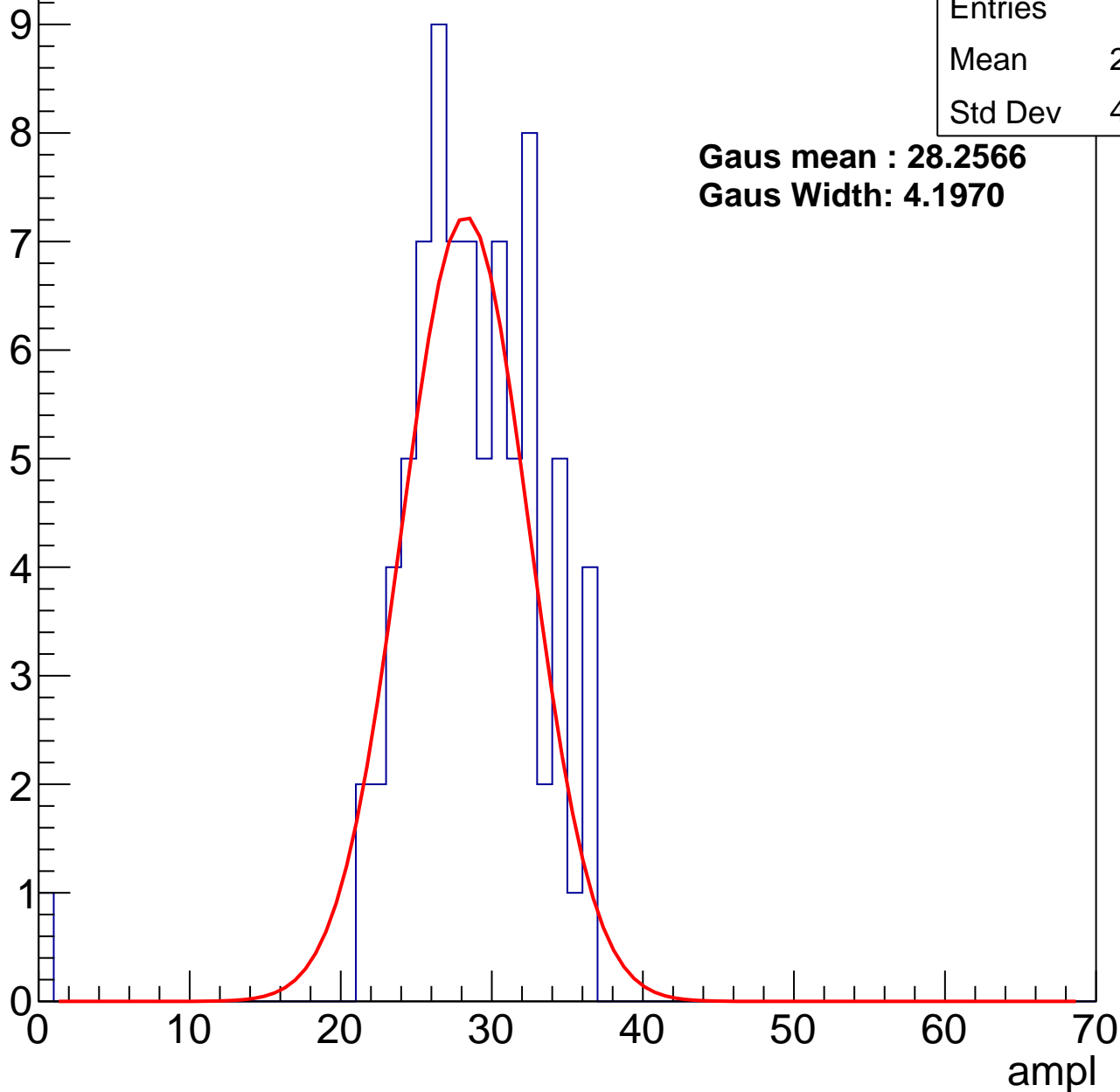
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.06
Std Dev	4.945

Gaus mean : 28.2566

Gaus Width: 4.1970



B1L102S, U19-ch60, adc1

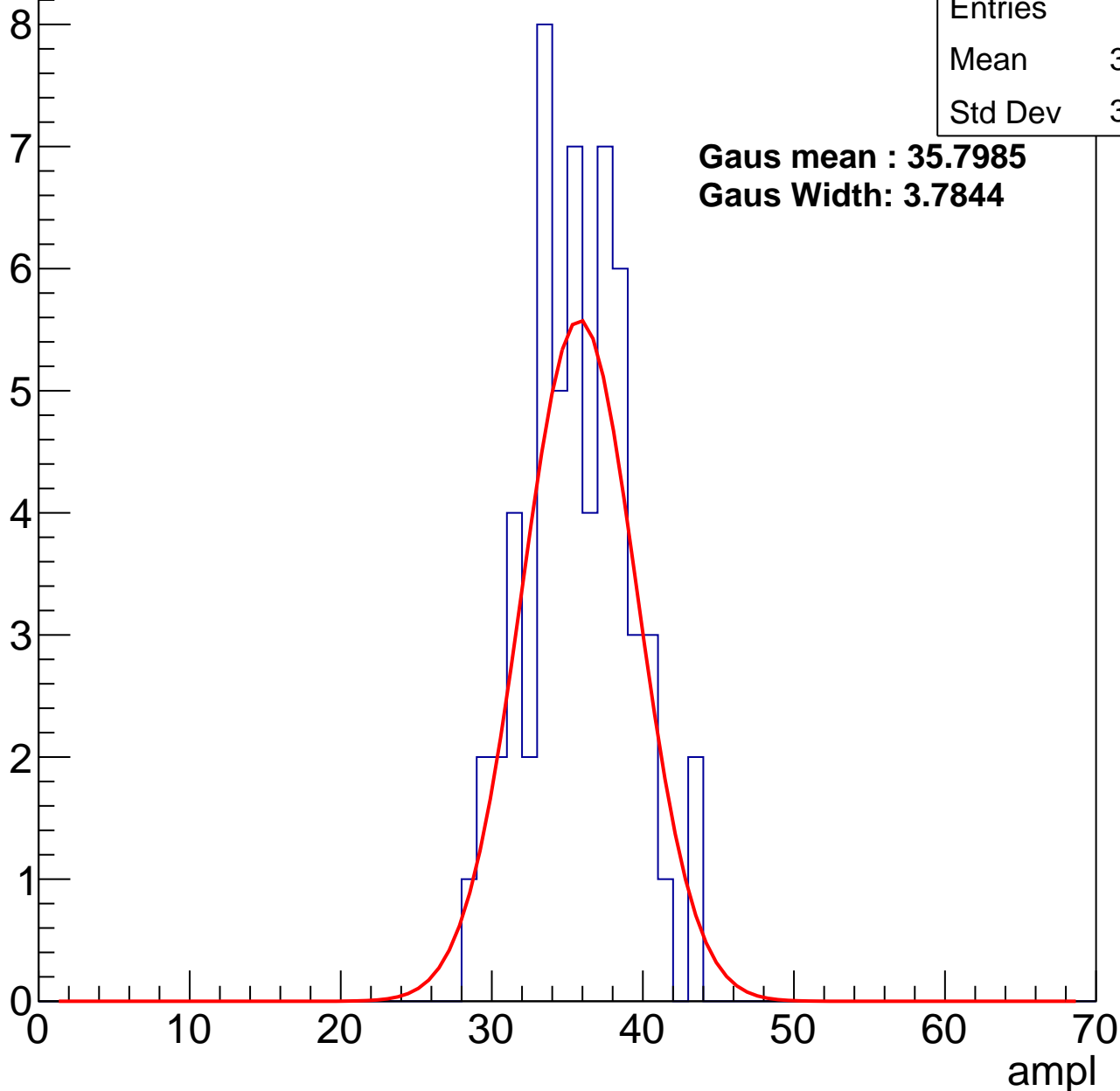
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.23
Std Dev	3.403

Gaus mean : 35.7985

Gaus Width: 3.7844



B1L102S, U19-ch60, adc2

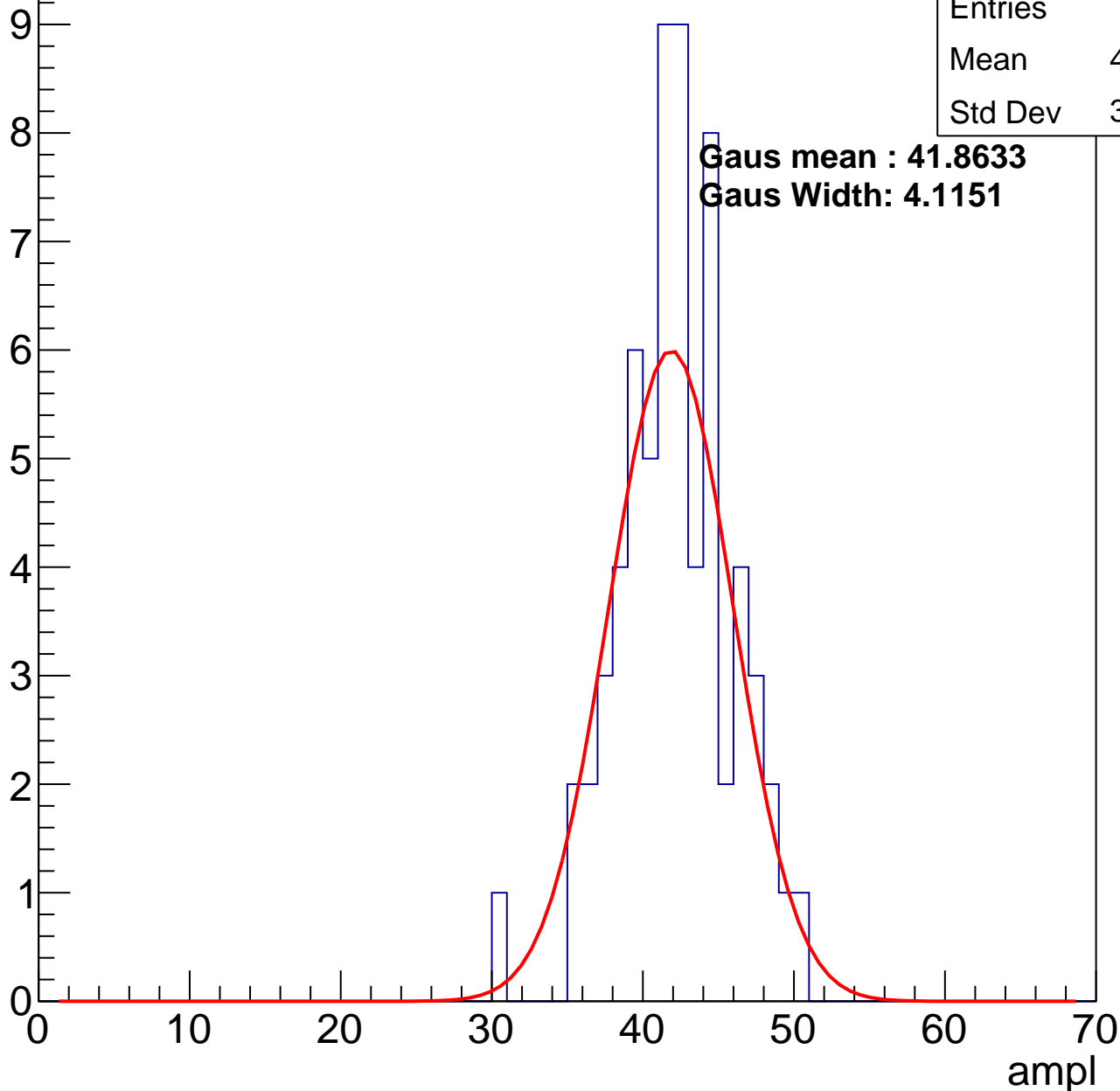
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.67
Std Dev	3.706

Gaus mean : 41.8633

Gaus Width: 4.1151

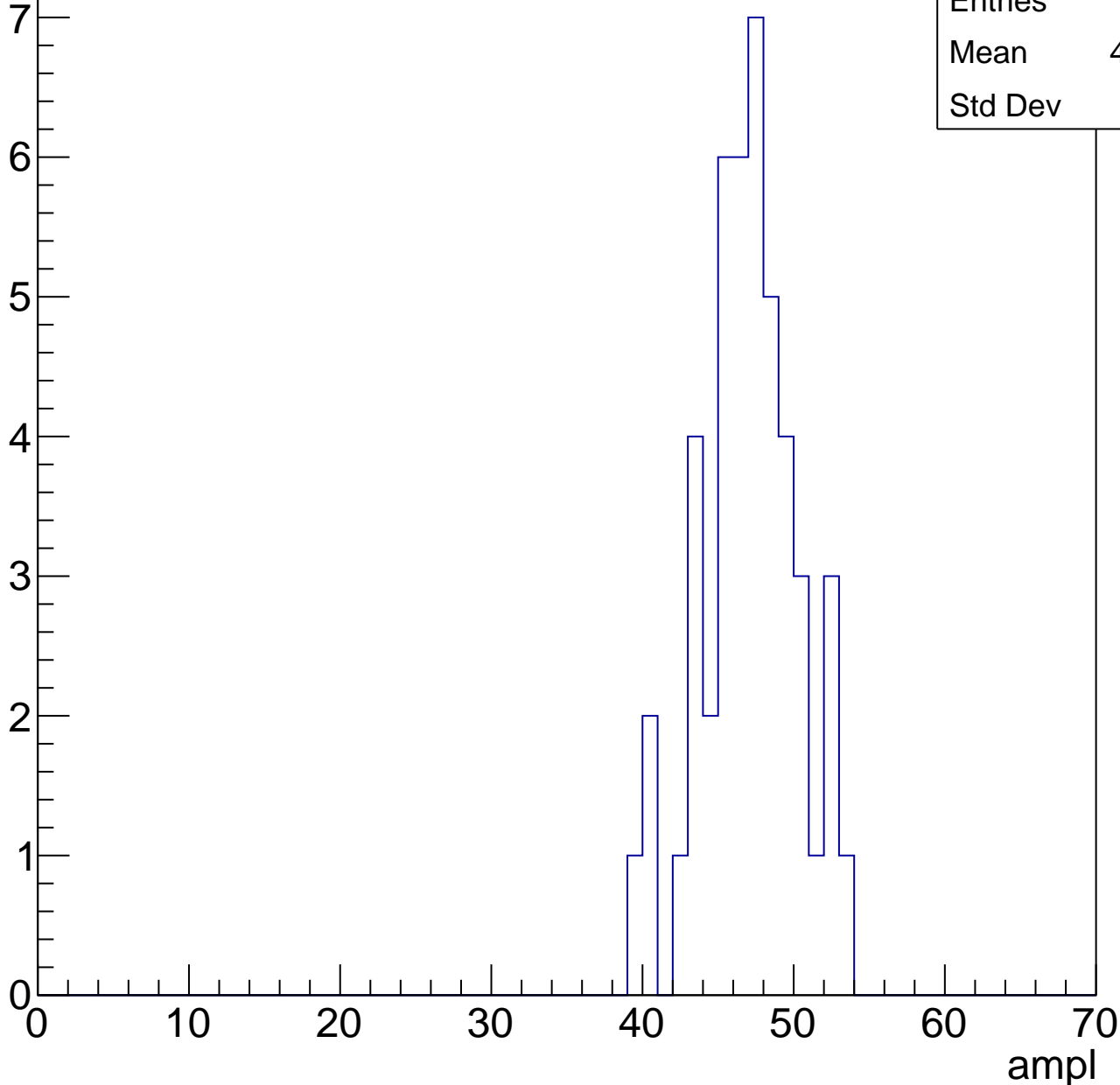


B1L102S, U19-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	46.57
Std Dev	3.18

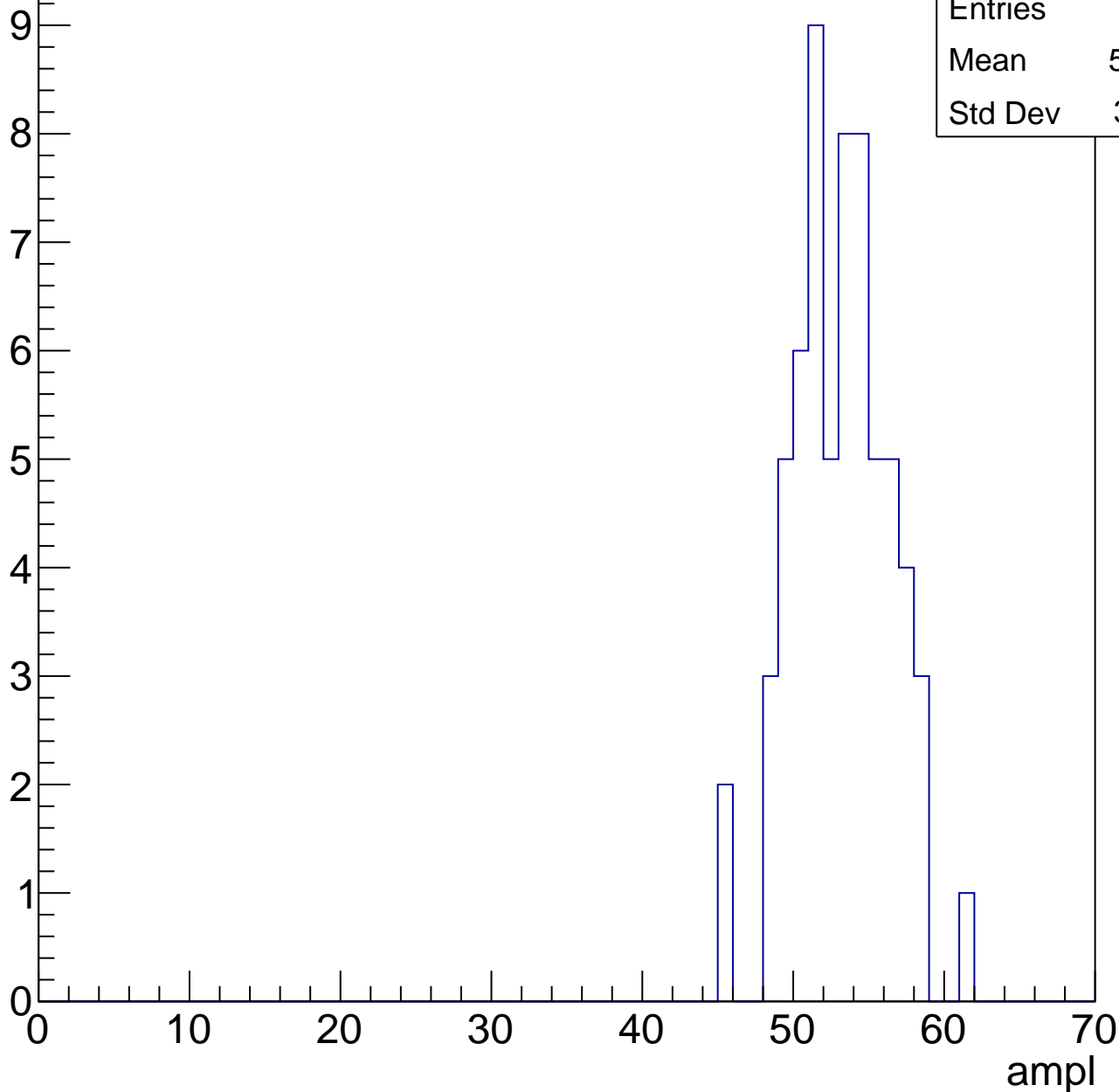


B1L102S, U19-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

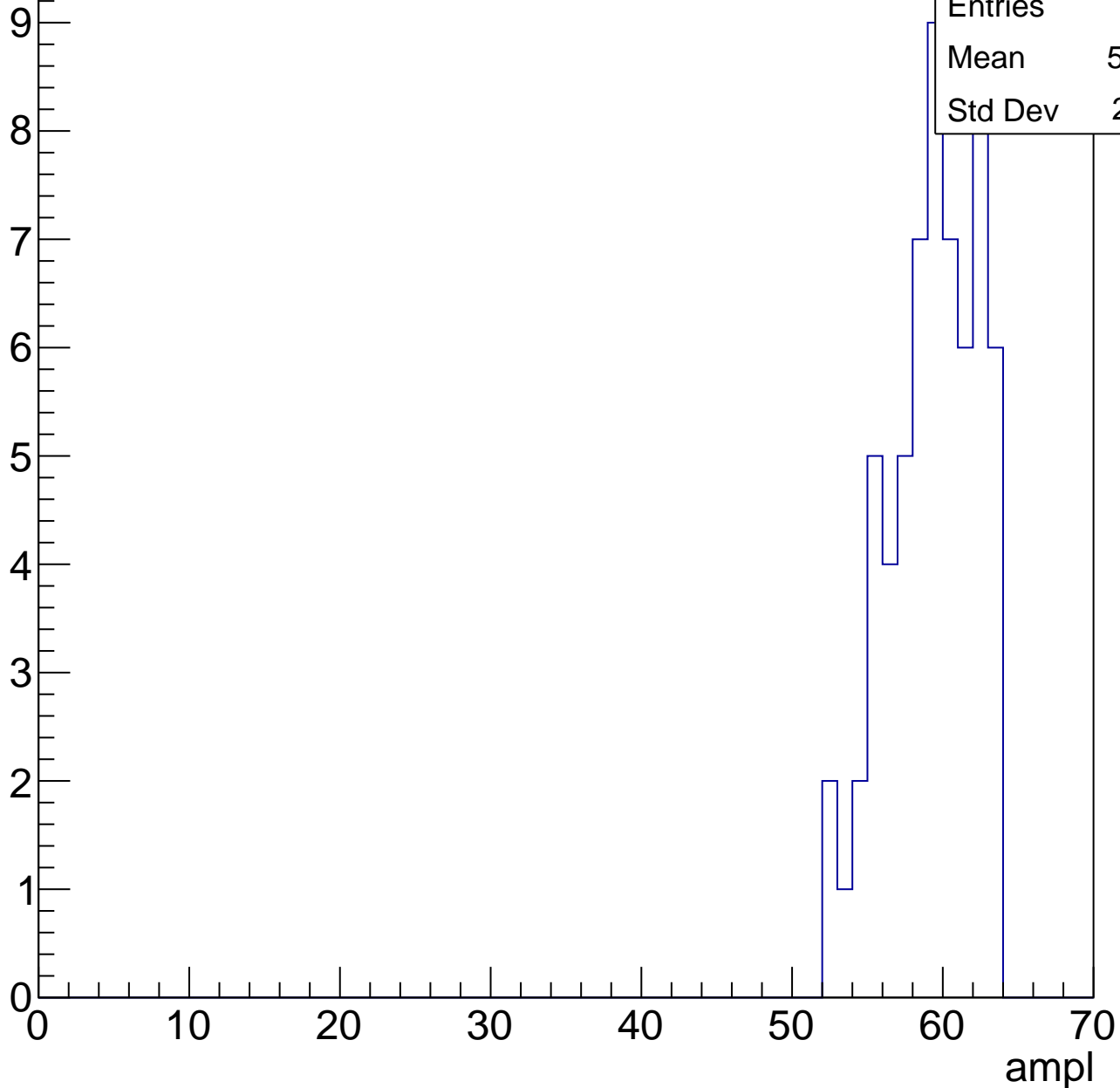
Entries	64
Mean	52.69
Std Dev	3.181



B1L102S, U19-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

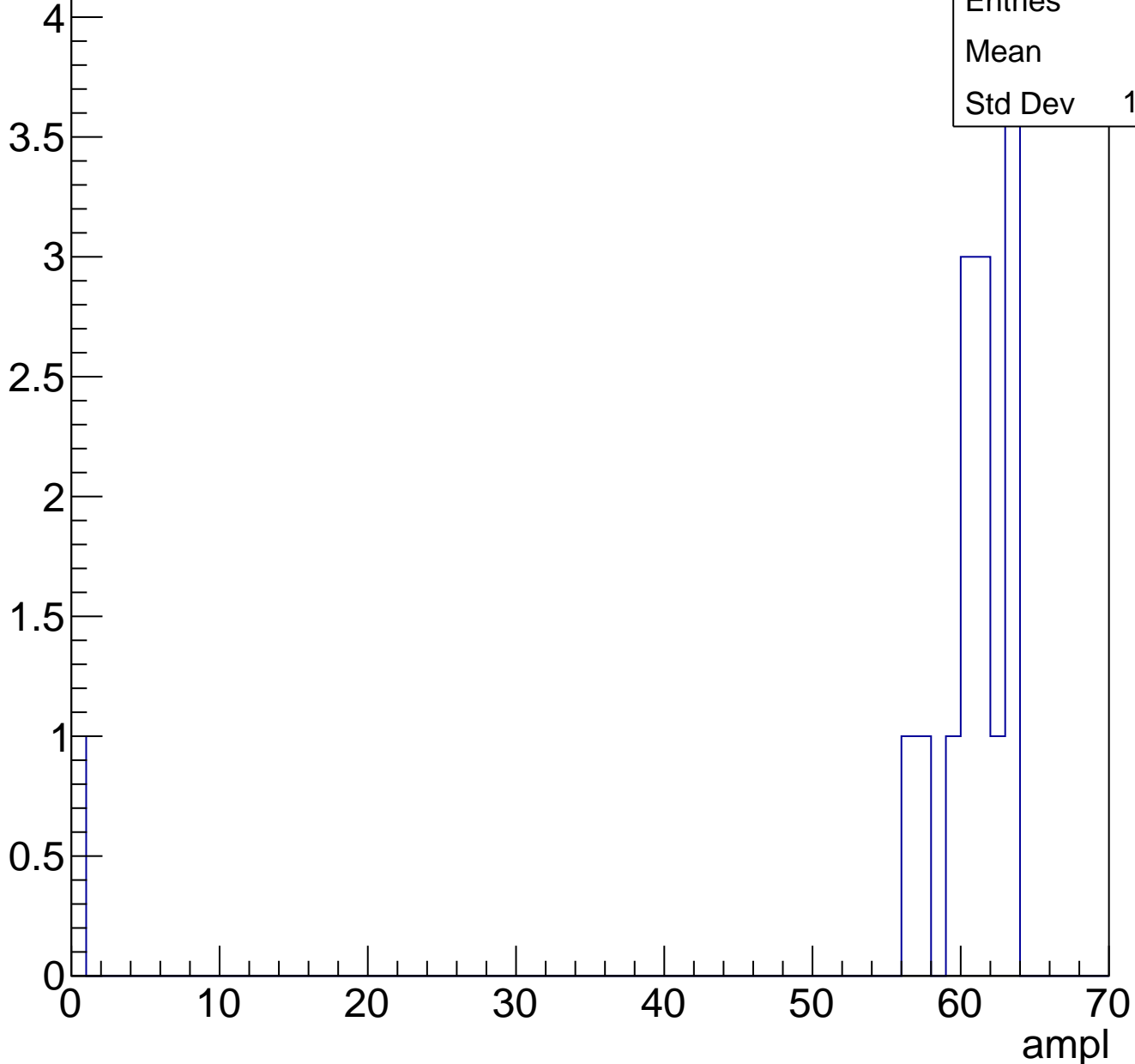
Entry



B1L102S, U19-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch61, adc0

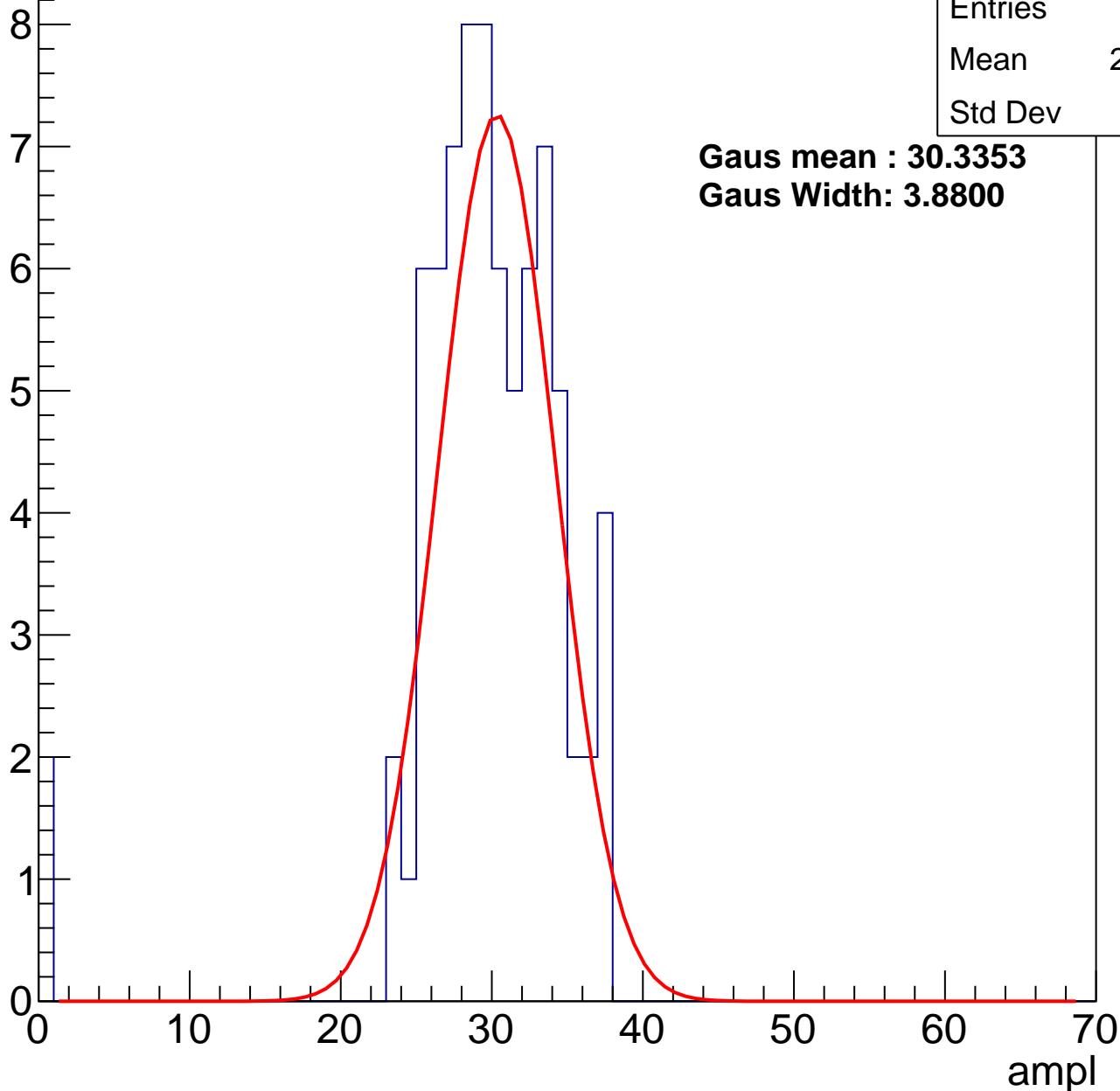
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.08
Std Dev	5.93

Gaus mean : 30.3353

Gaus Width: 3.8800



B1L102S, U19-ch61, adc1

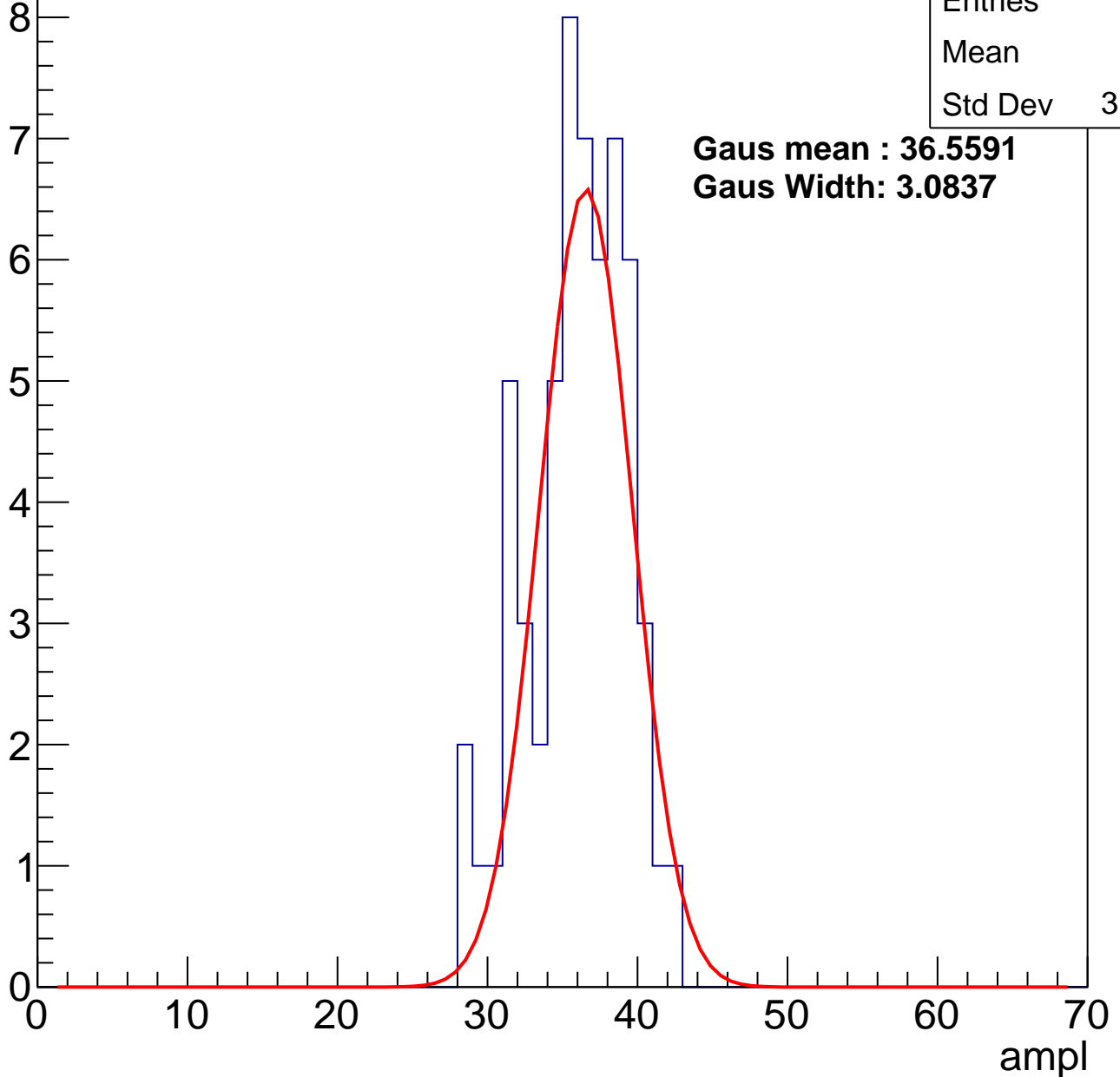
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.5
Std Dev	3.255

Gaus mean : 36.5591

Gaus Width: 3.0837



B1L102S, U19-ch61, adc2

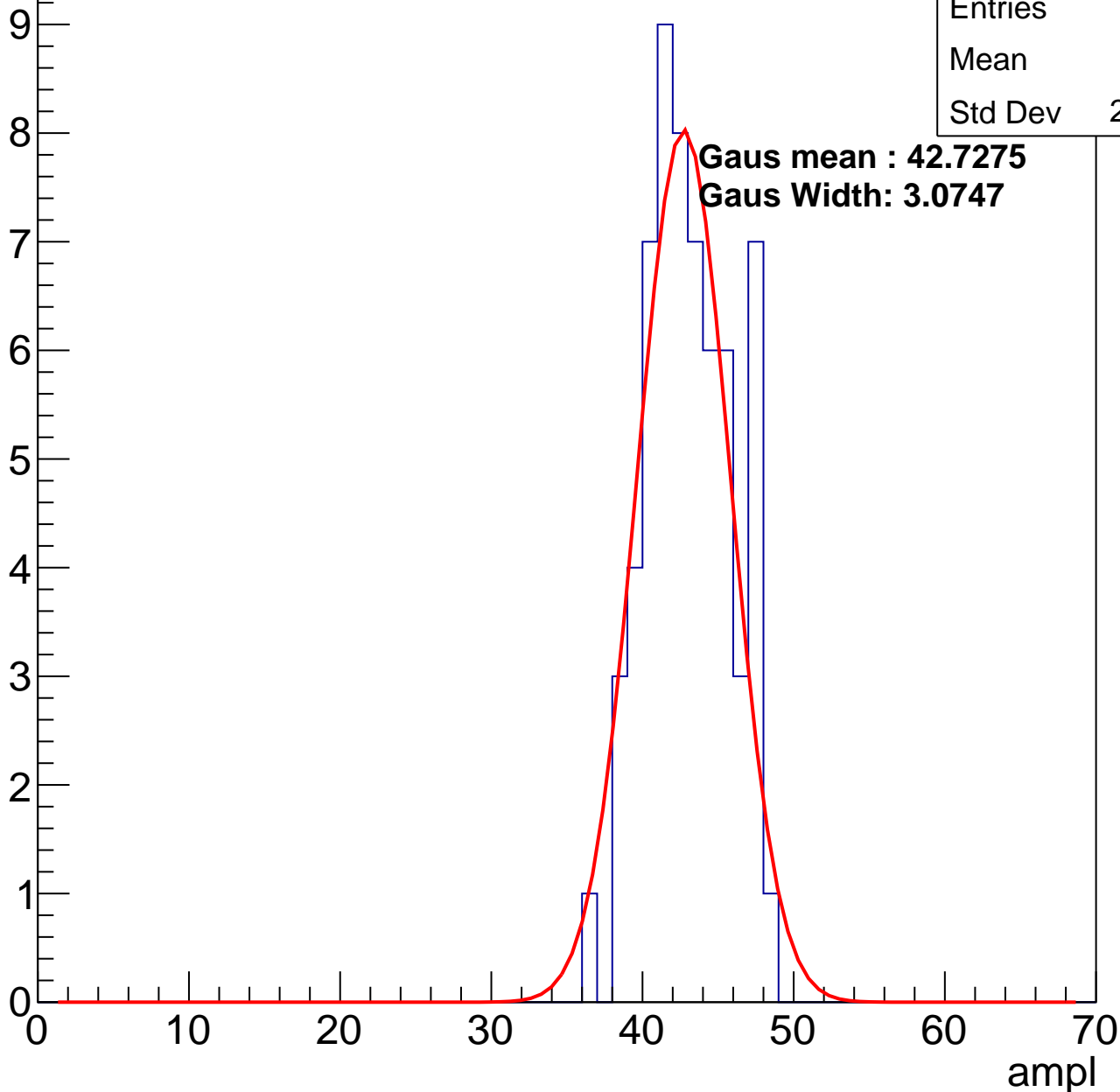
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.6
Std Dev	2.779

Gaus mean : 42.7275

Gaus Width: 3.0747

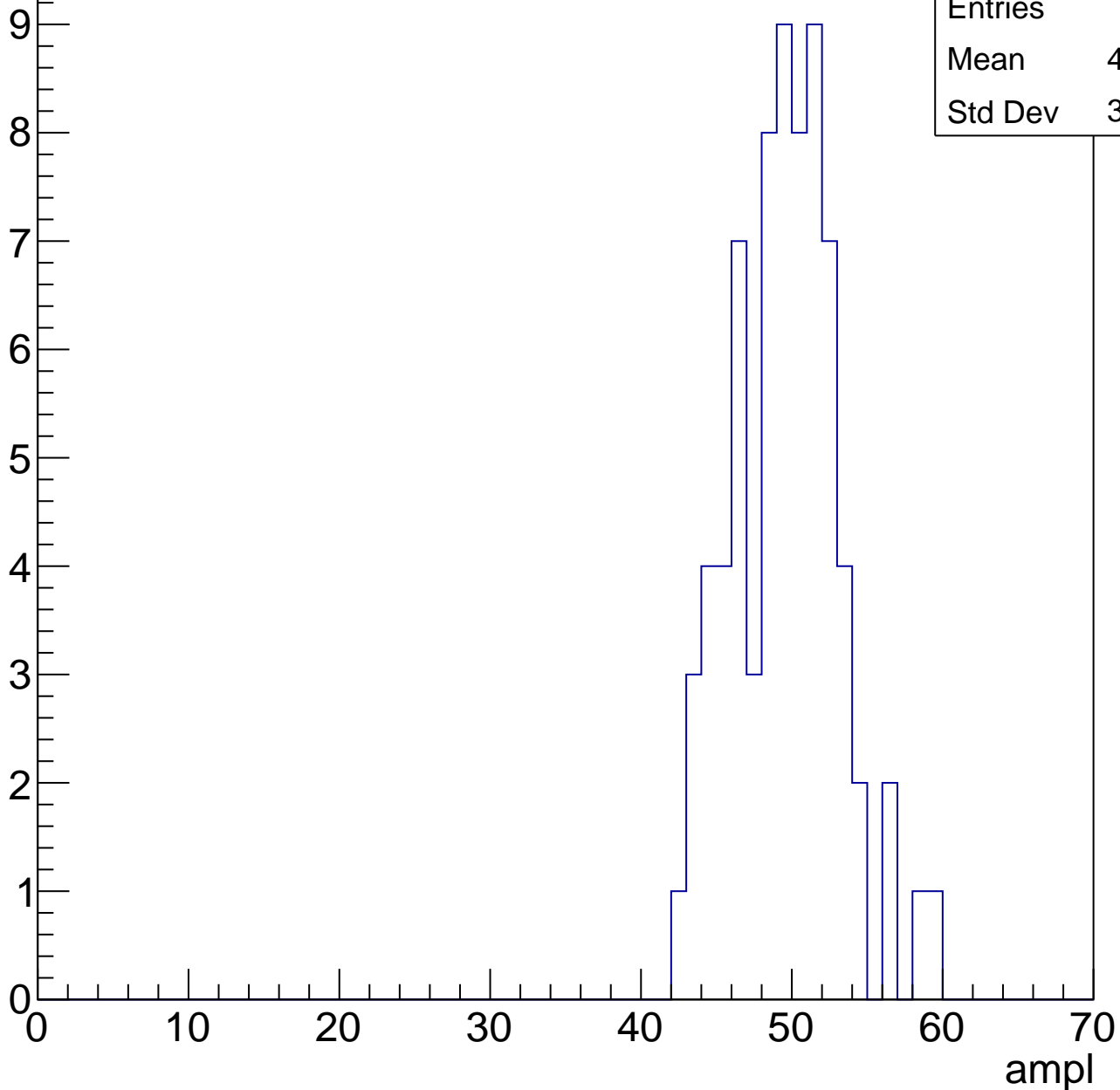


B1L102S, U19-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.14
Std Dev	3.516

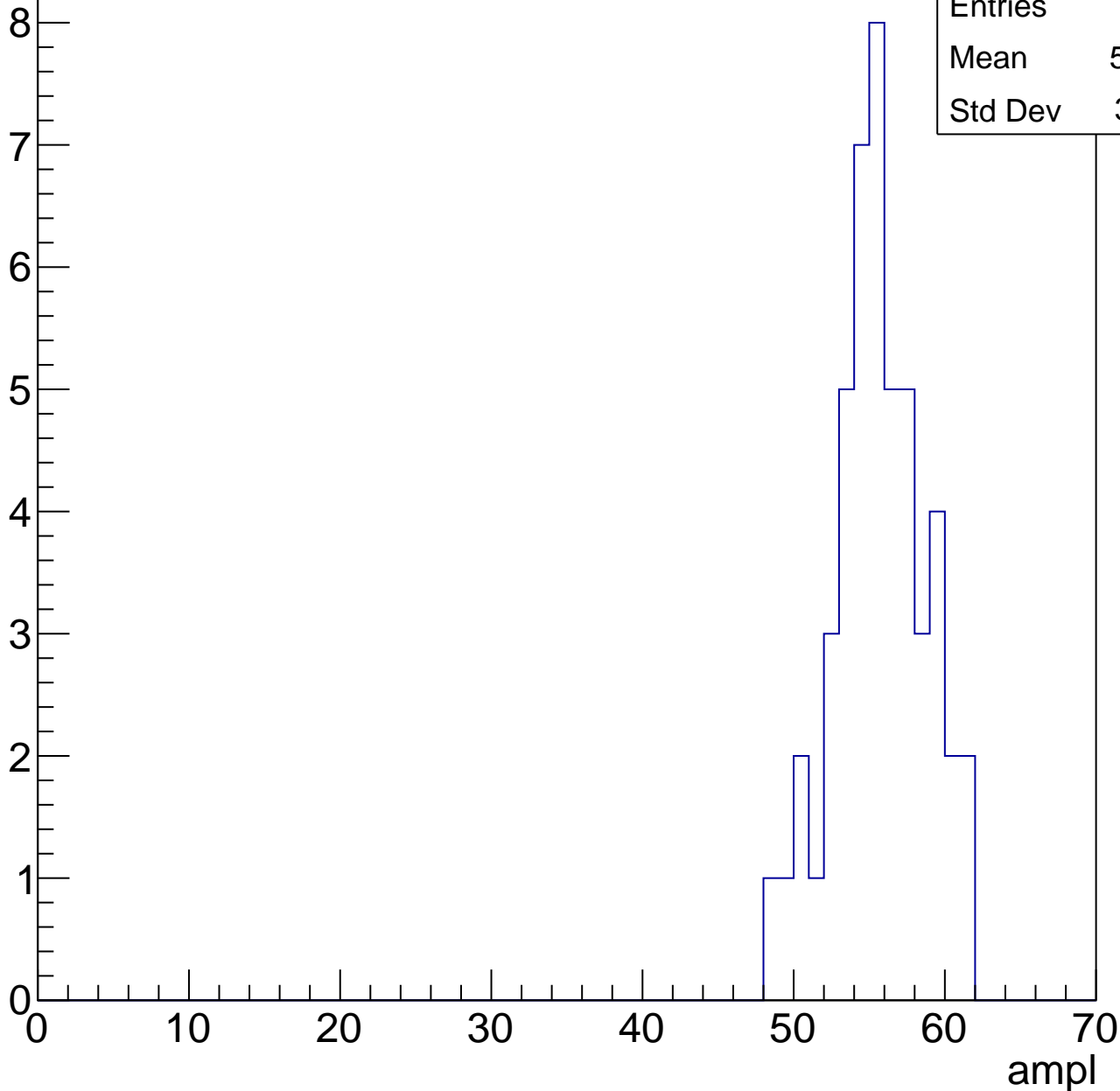


B1L102S, U19-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.18
Std Dev	3.001



B1L102S, U19-ch61, adc5

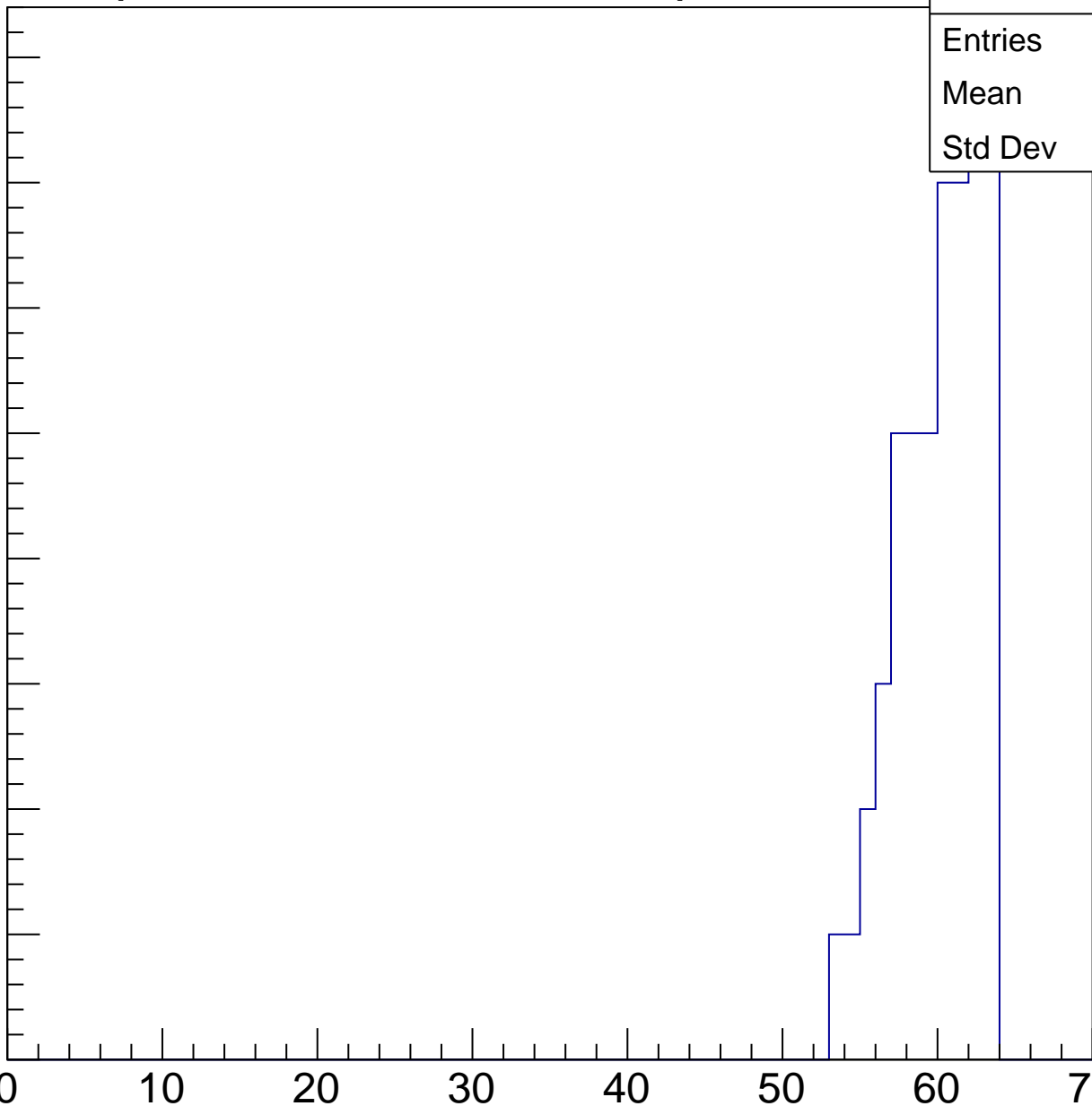
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.65
Std Dev	2.623

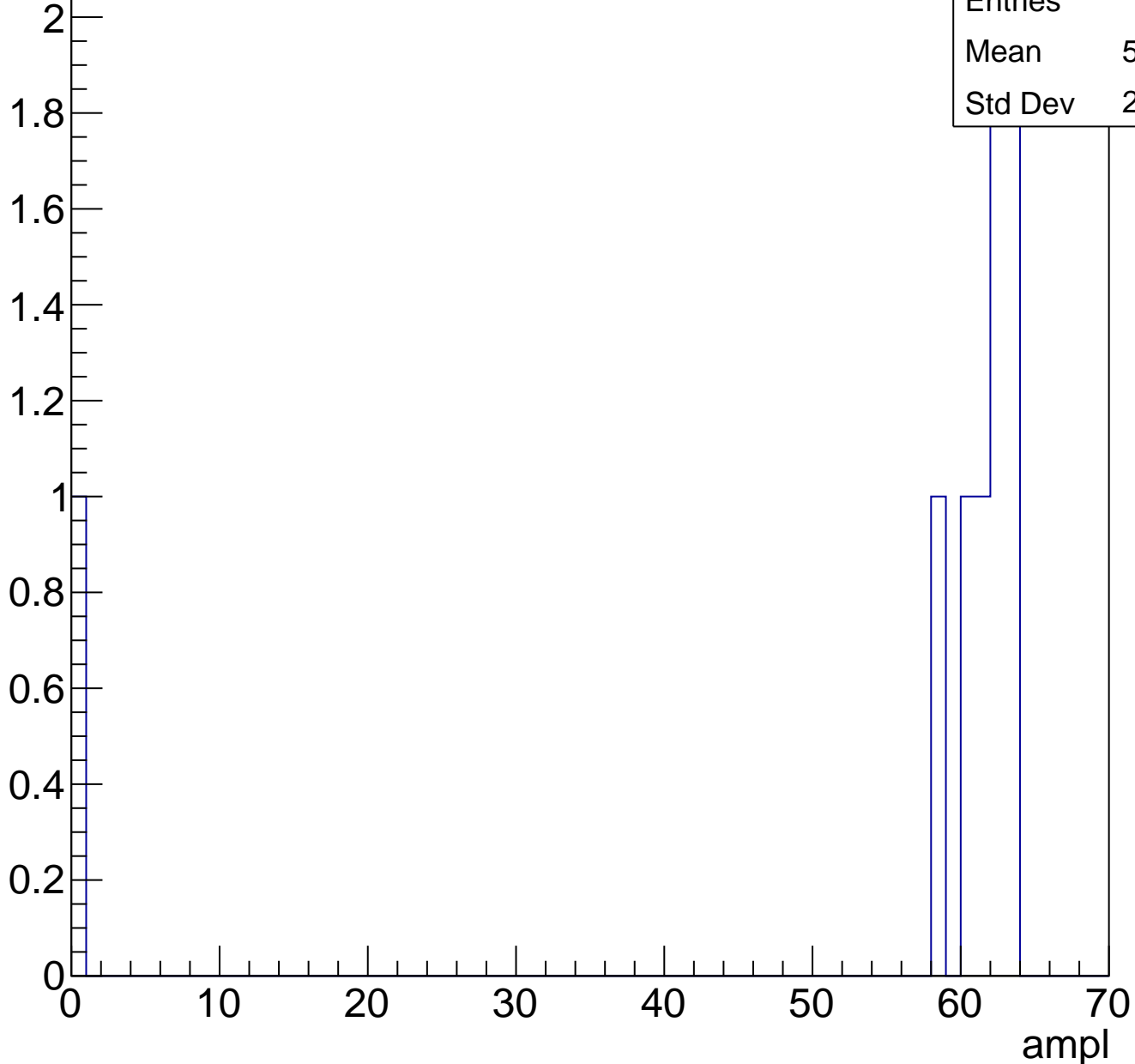
ampl



B1L102S, U19-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	8
Mean	53.62
Std Dev	20.33

B1L102S, U19-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch62, adc0

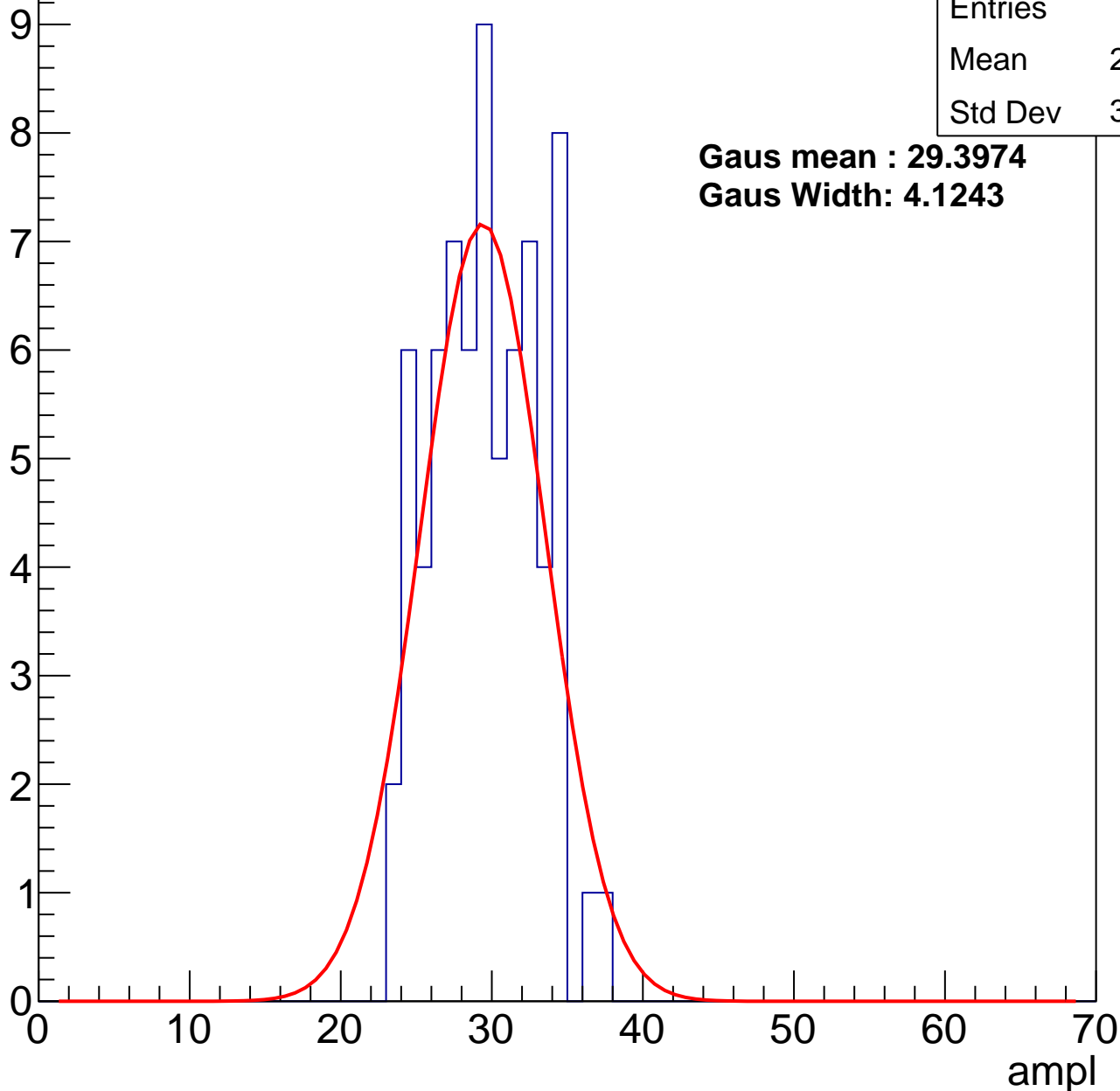
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.18
Std Dev	3.417

Gaus mean : 29.3974

Gaus Width: 4.1243



B1L102S, U19-ch62, adc1

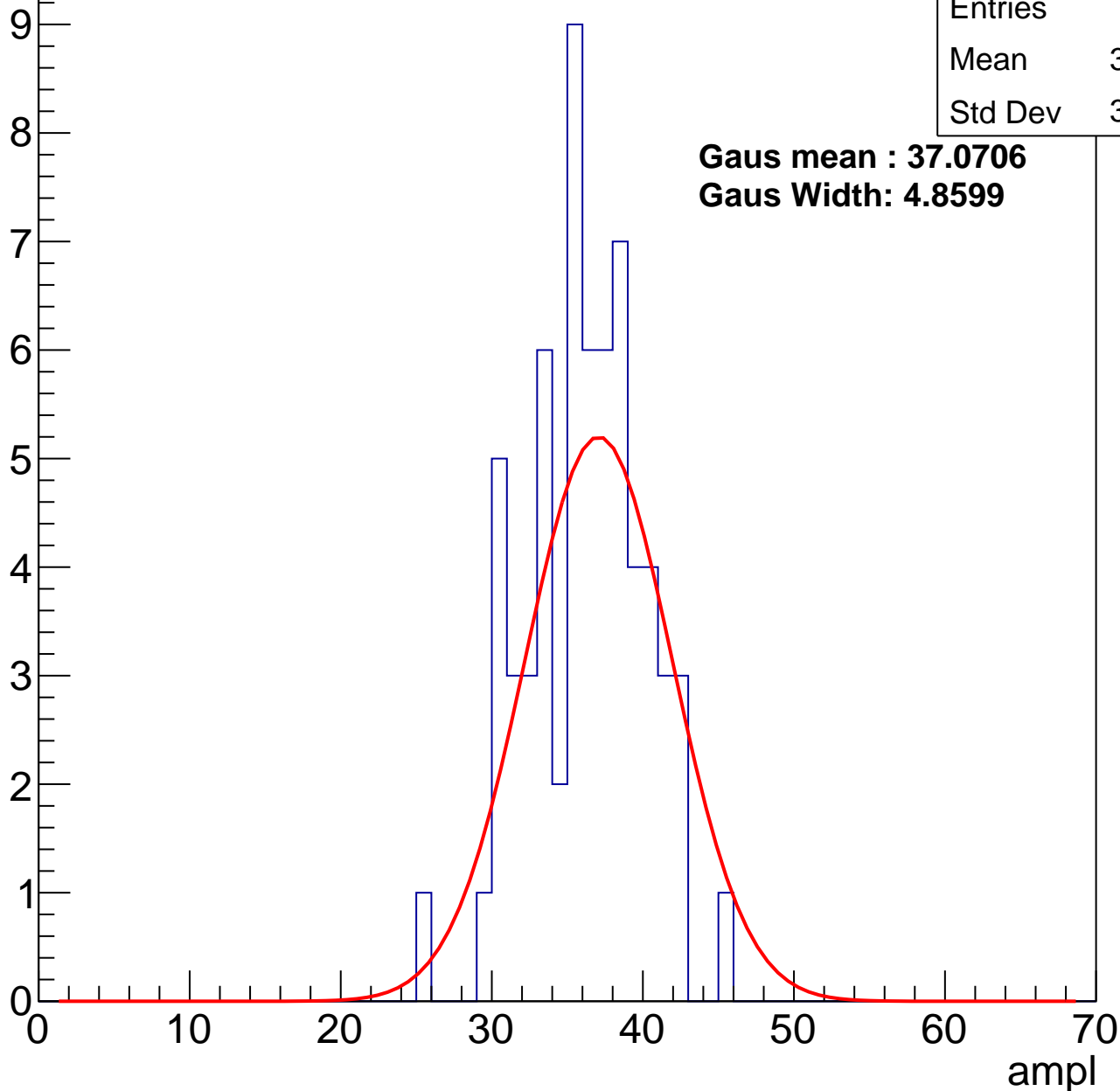
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.75
Std Dev	3.828

Gaus mean : 37.0706

Gaus Width: 4.8599



B1L102S, U19-ch62, adc2

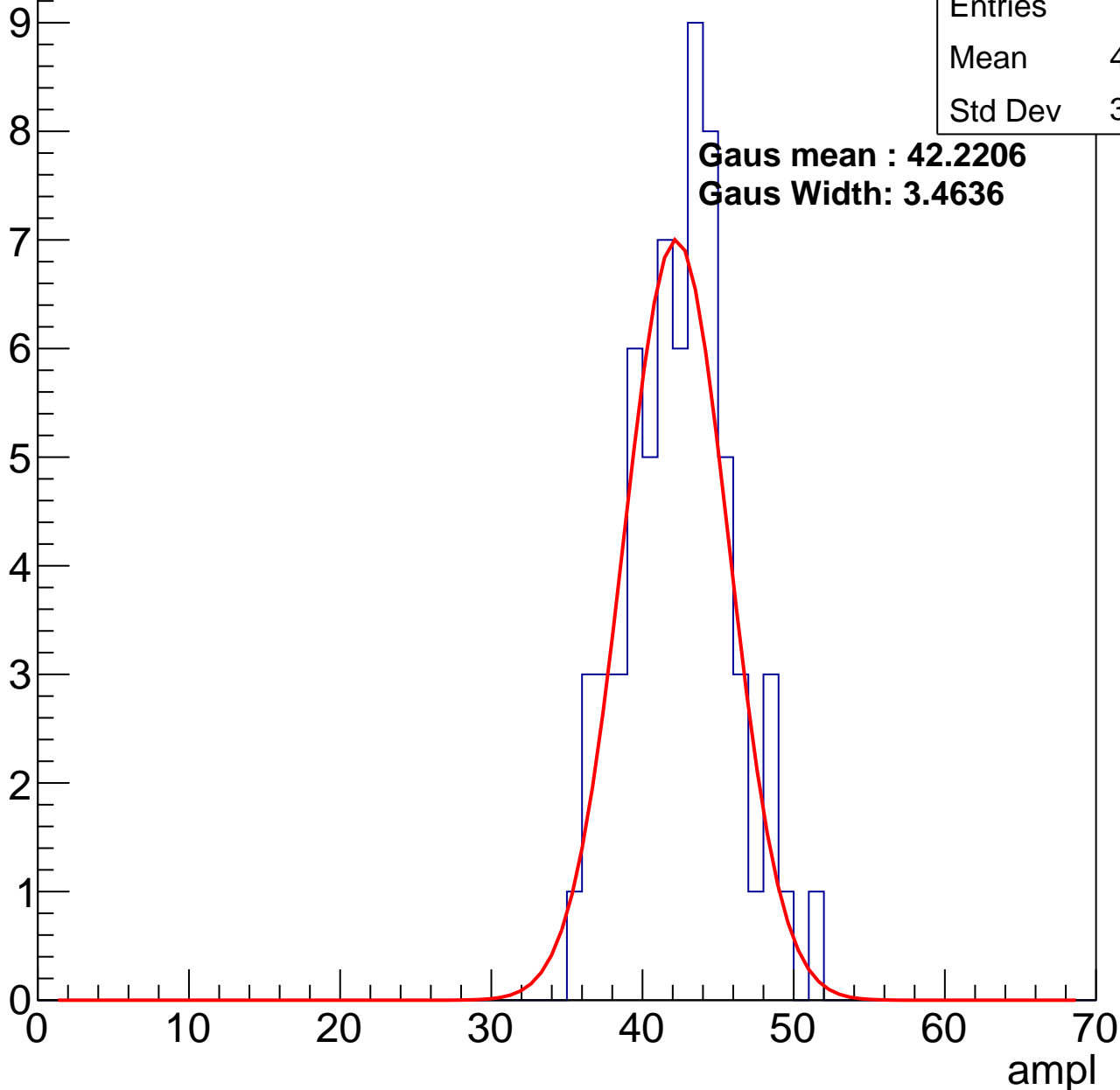
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.06
Std Dev	3.414

Gaus mean : 42.2206

Gaus Width: 3.4636

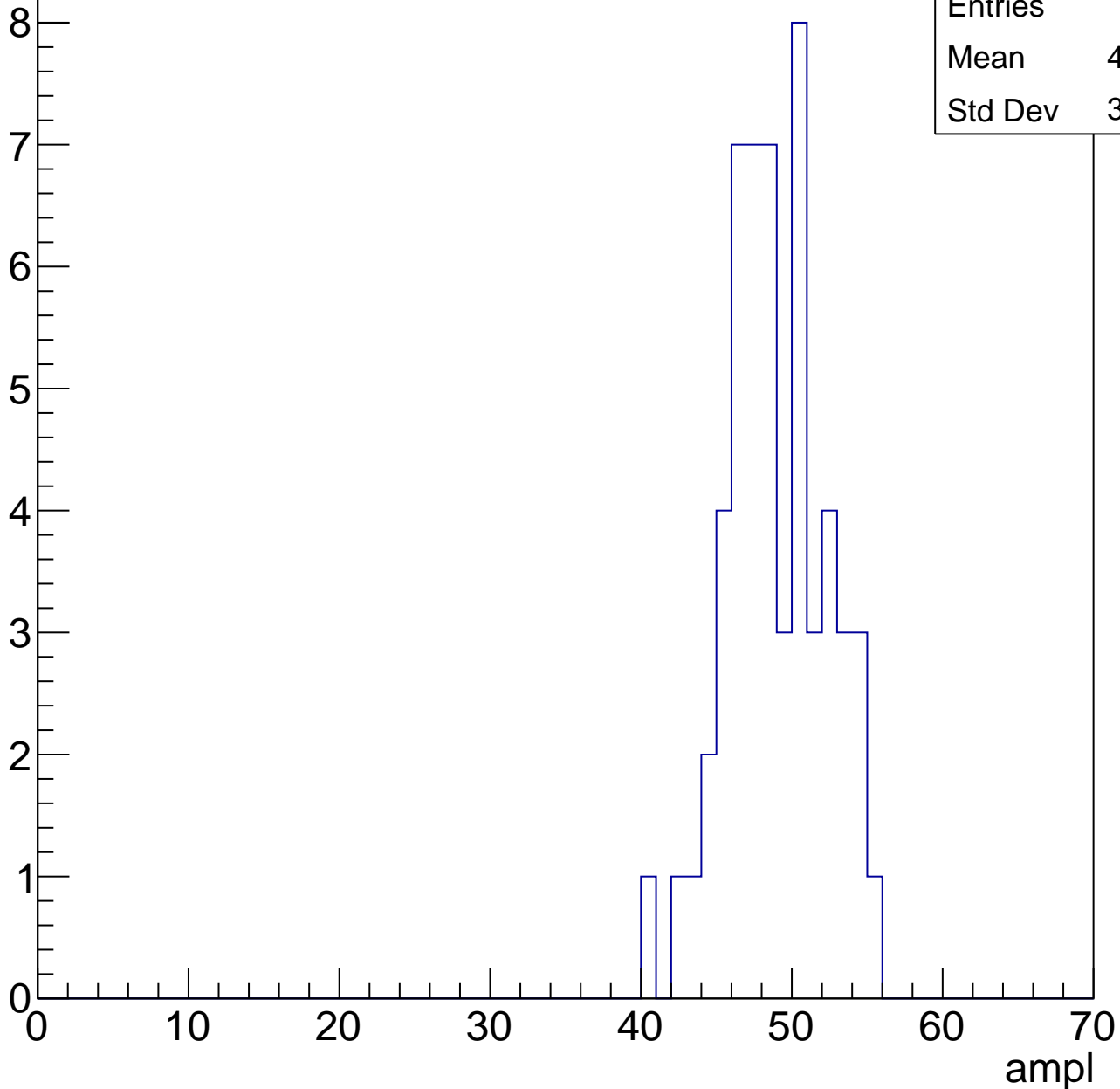


B1L102S, U19-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.44
Std Dev	3.235

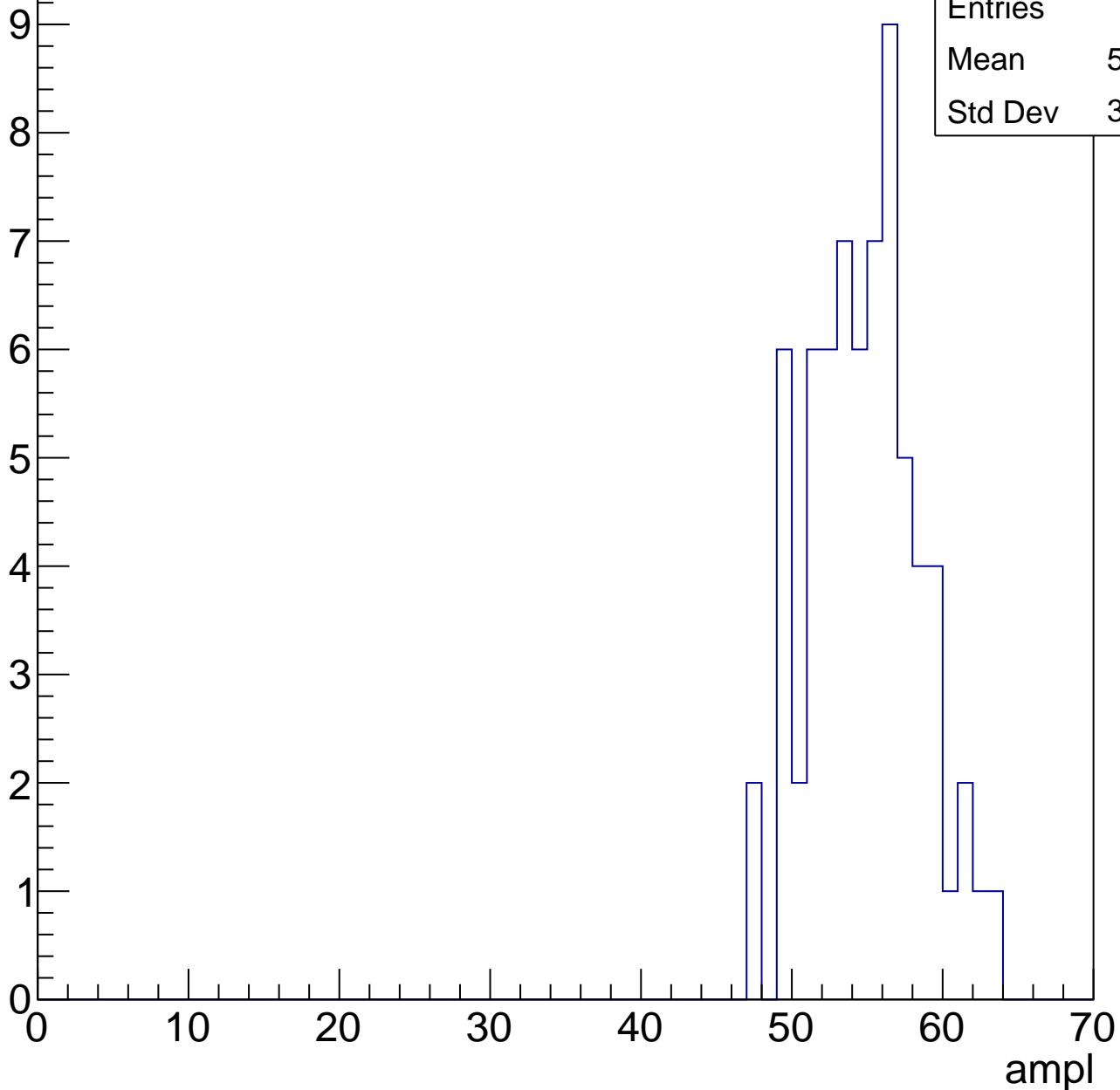


B1L102S, U19-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

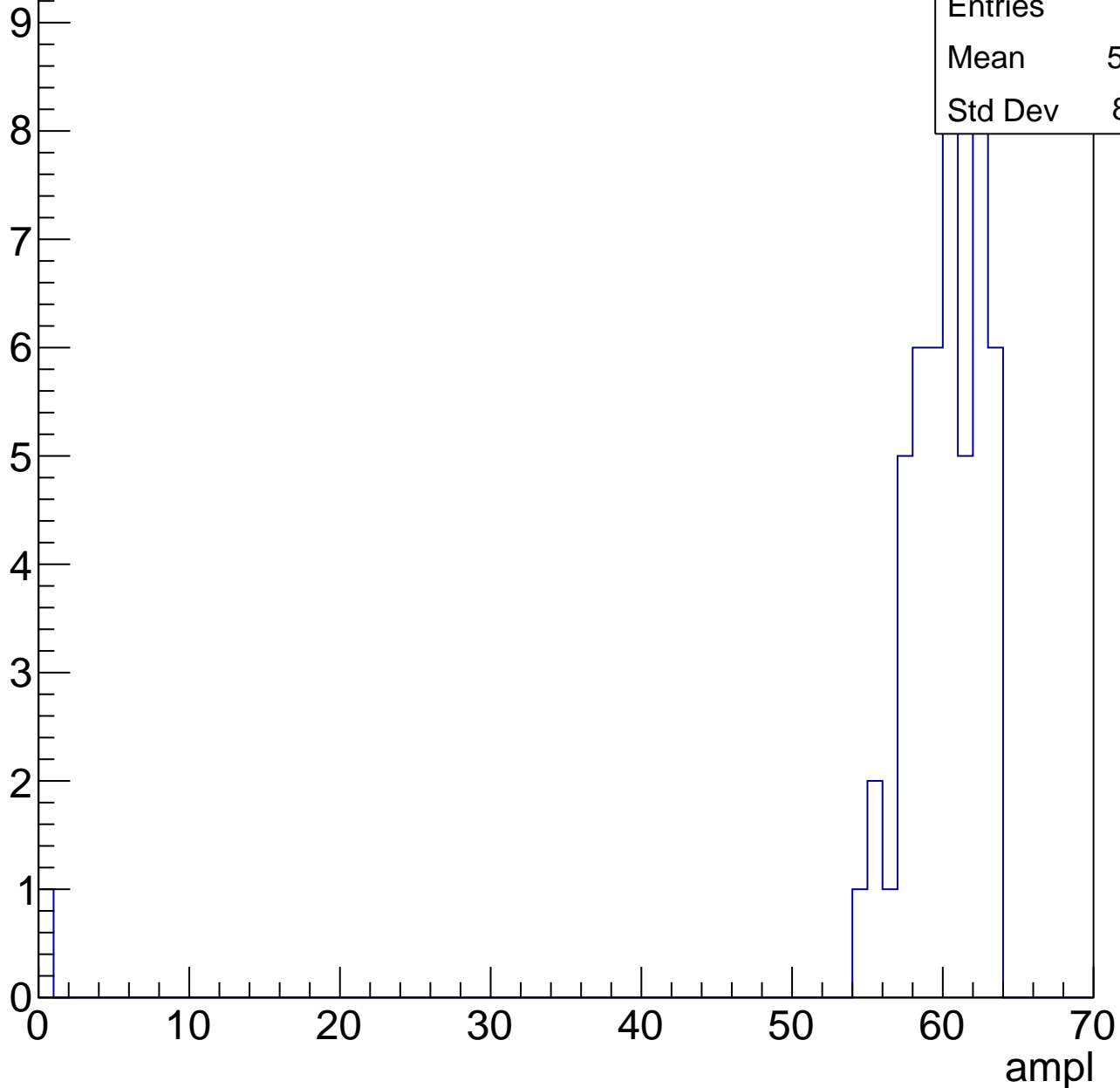
Entries	69
Mean	54.35
Std Dev	3.587



B1L102S, U19-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

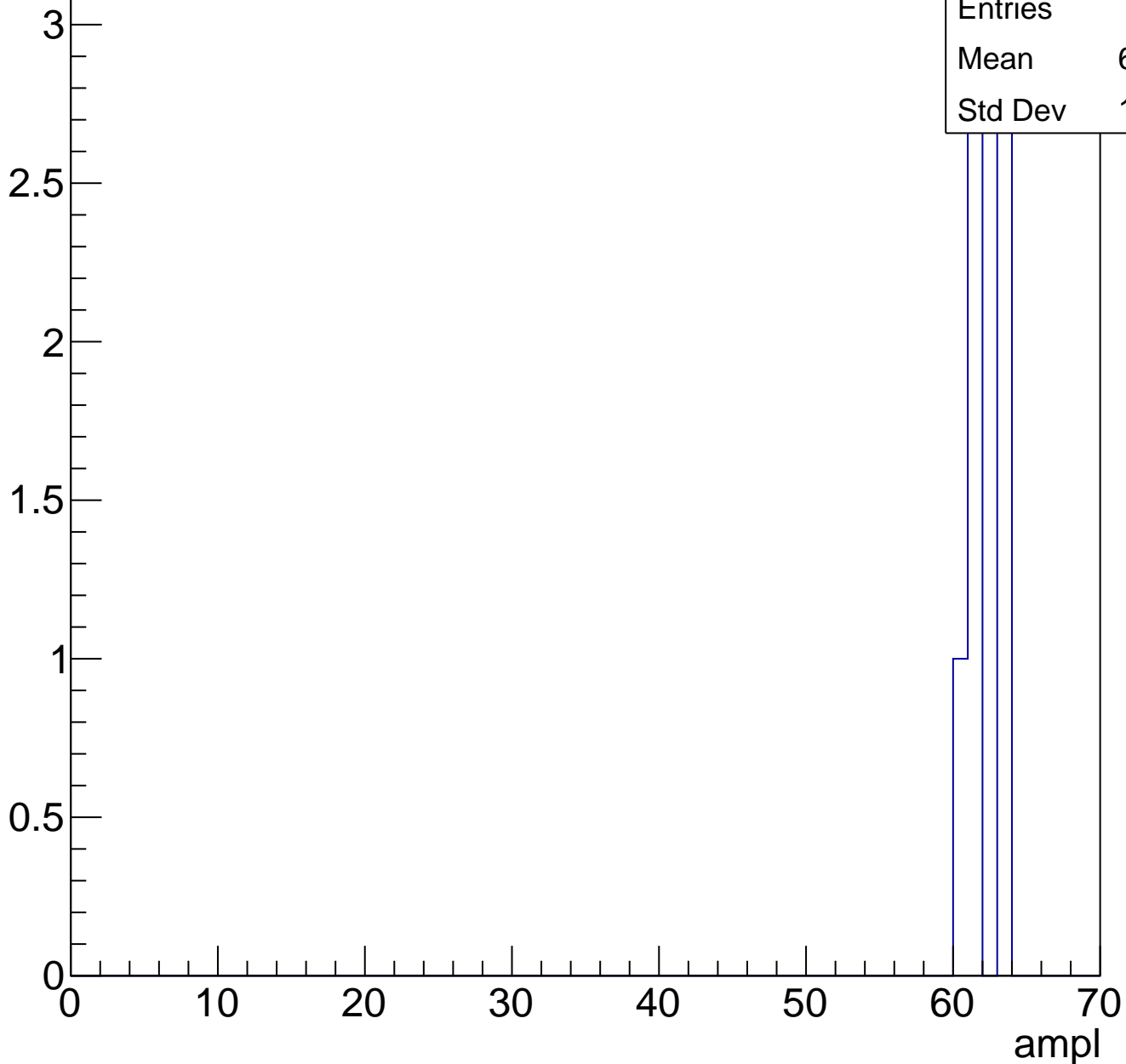
Entry



B1L102S, U19-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch63, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	28.77
Std Dev	4.754

Gaus mean : 29.8650

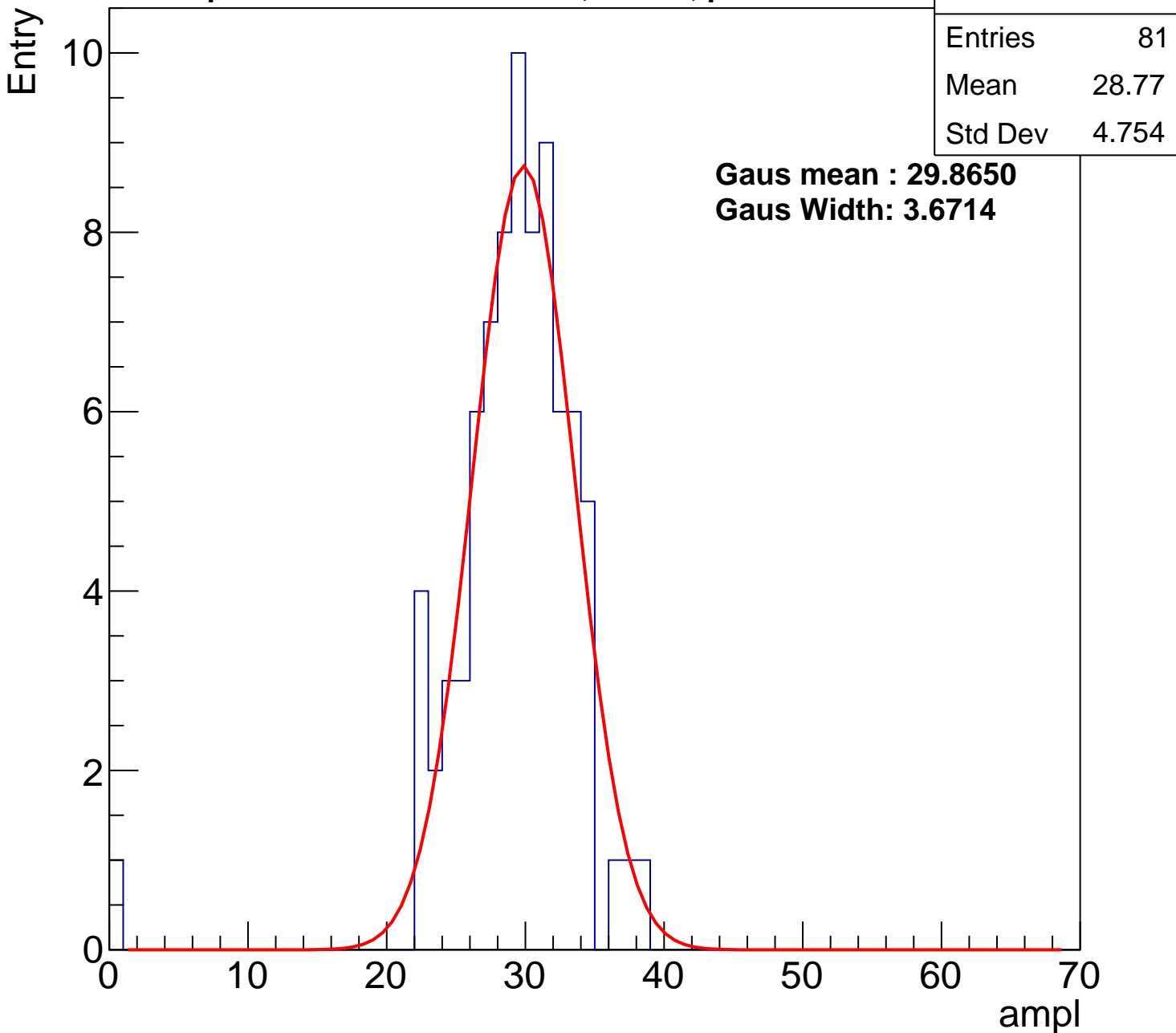
Gaus Width: 3.6714

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch63, adc1

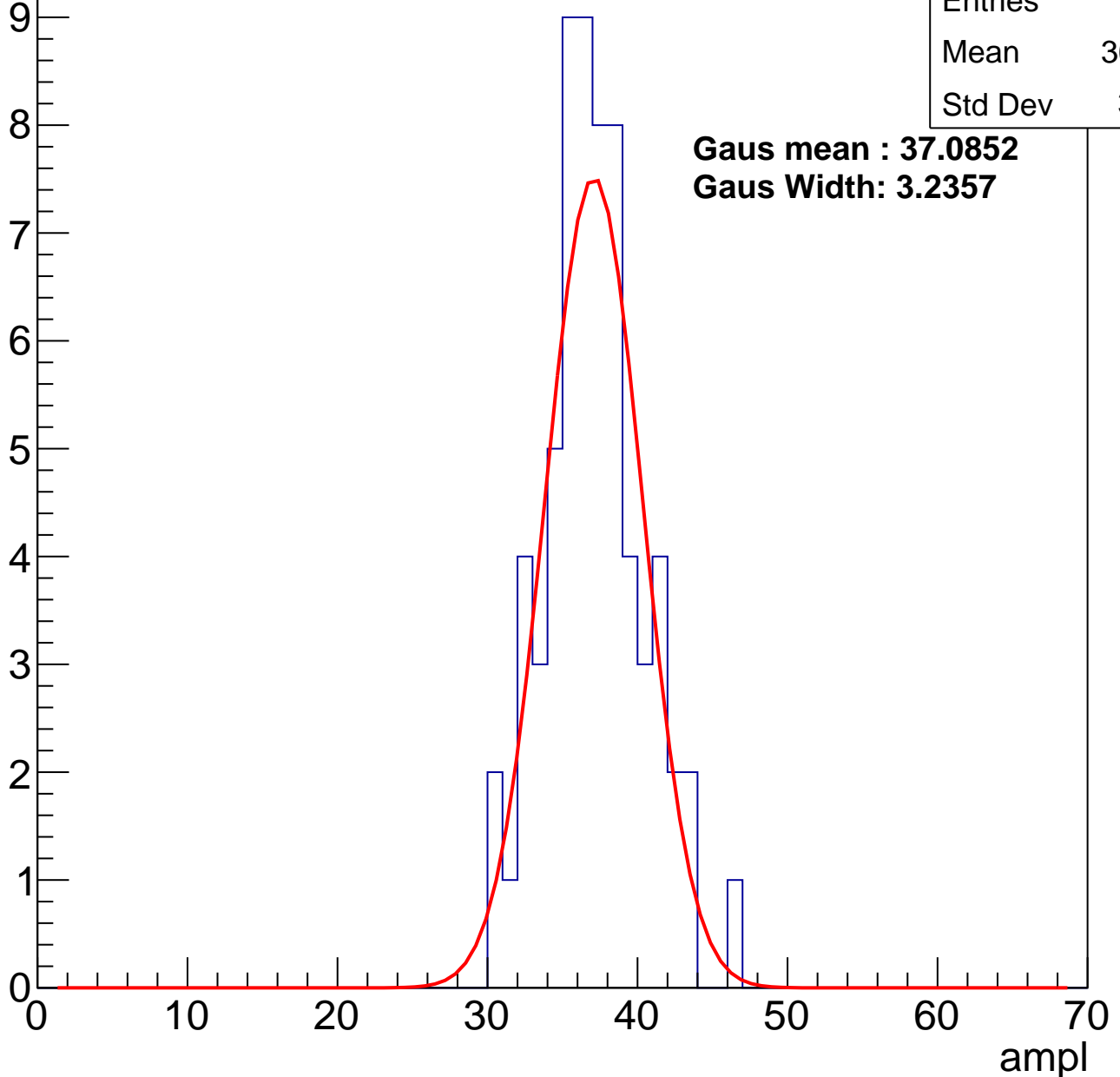
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.66
Std Dev	3.24

Gaus mean : 37.0852

Gaus Width: 3.2357



B1L102S, U19-ch63, adc2

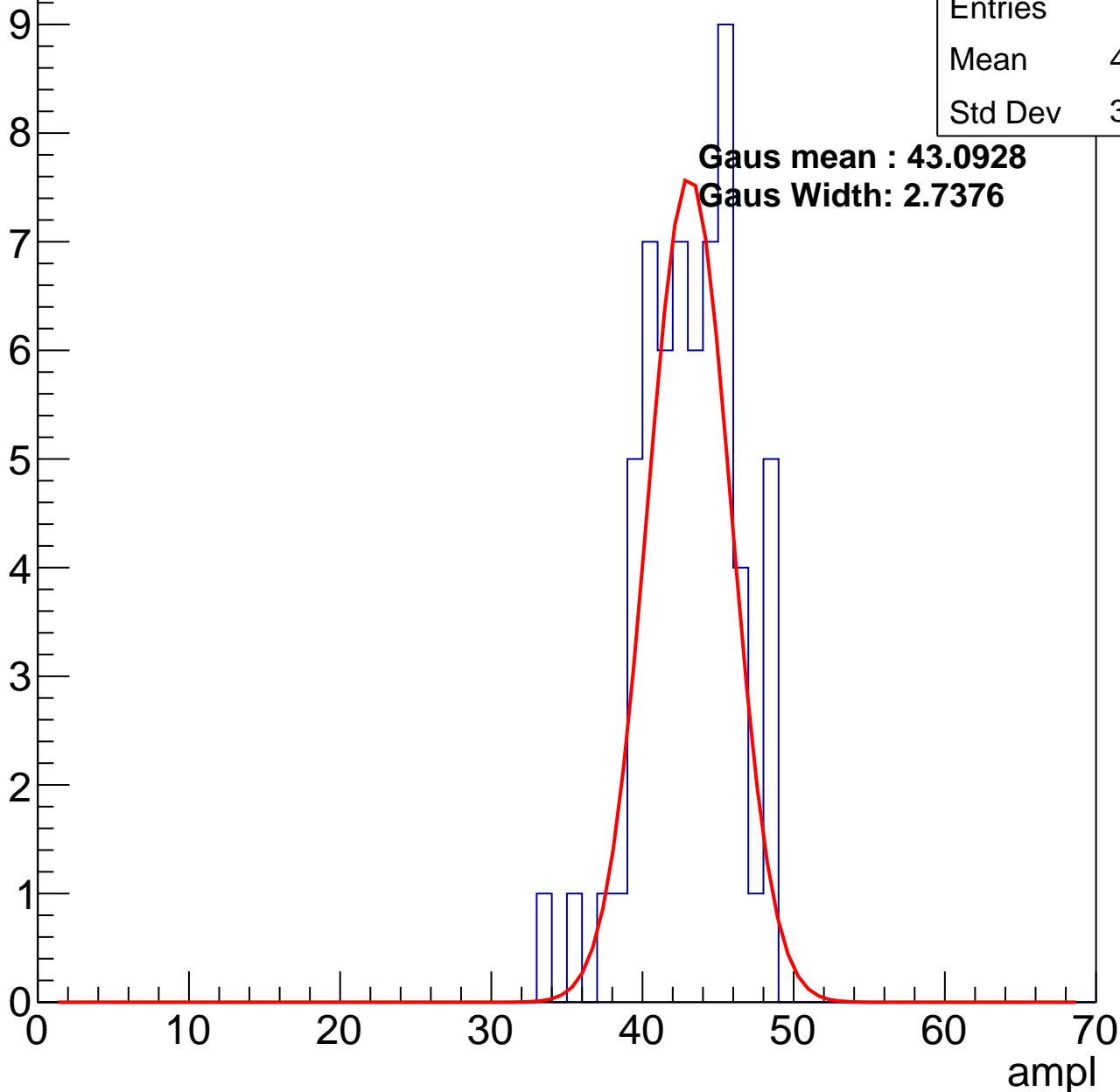
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.62
Std Dev	3.163

Gaus mean : 43.0928

Gaus Width: 2.7376

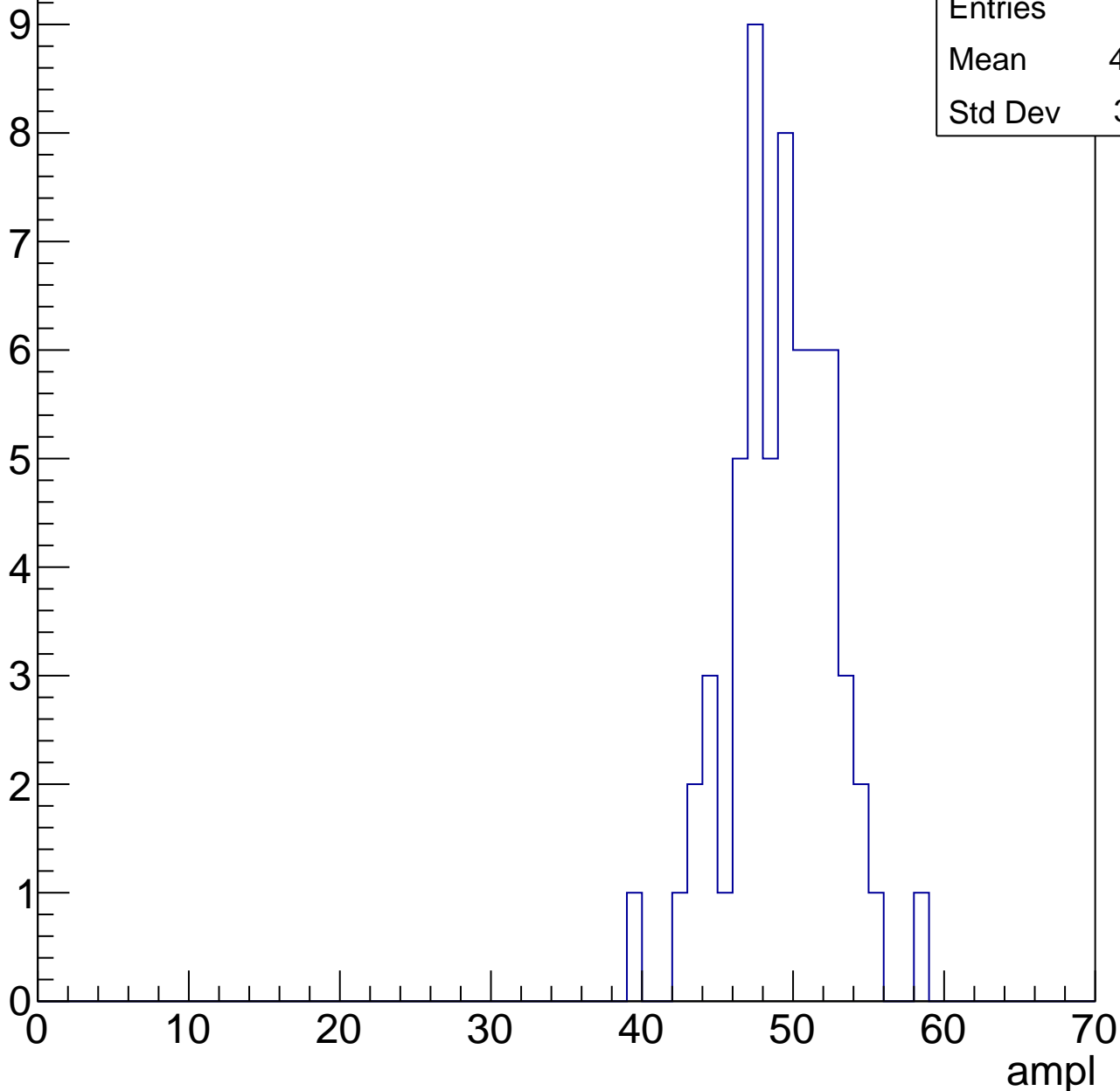


B1L102S, U19-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

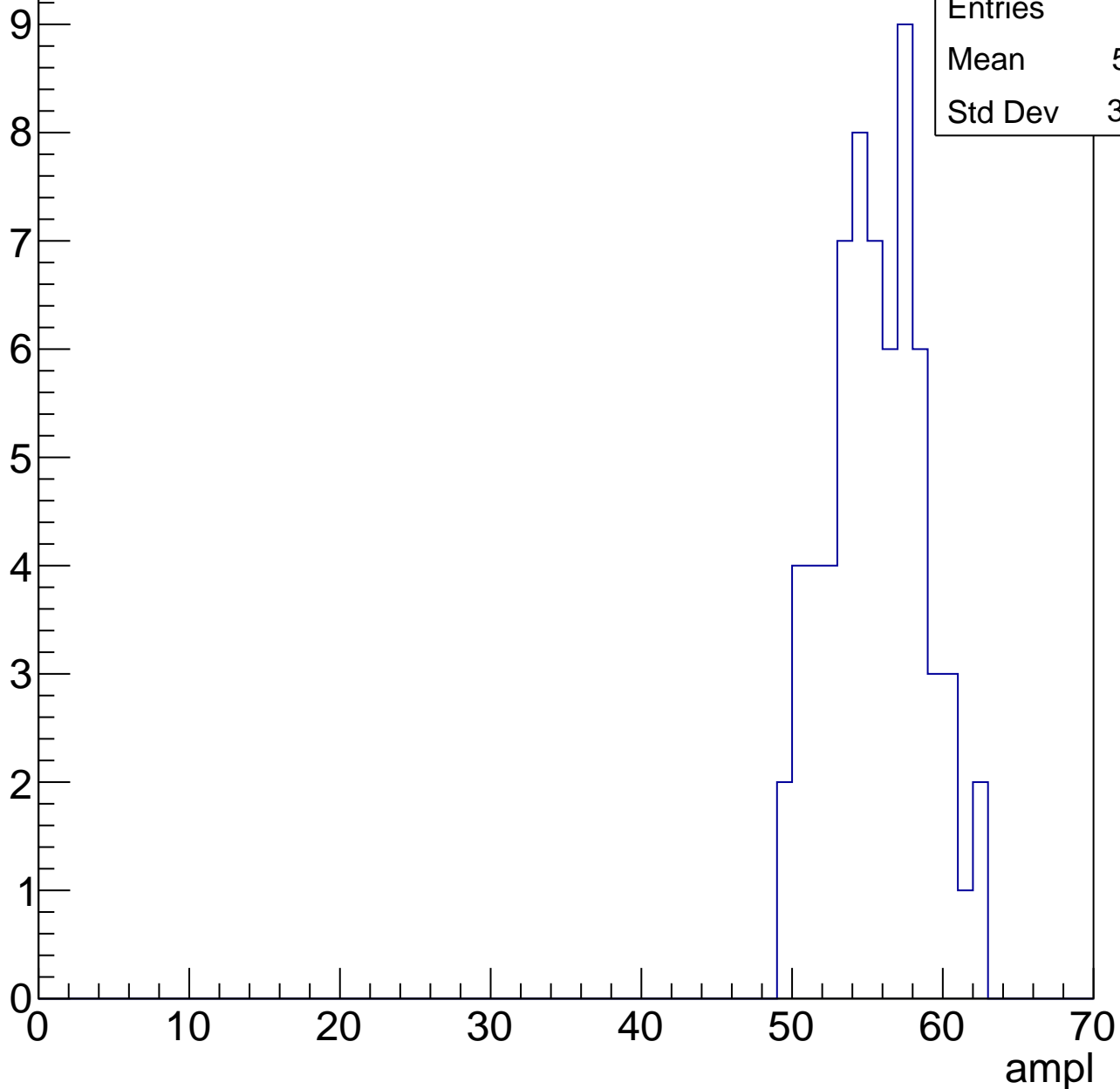
Entries	60
Mean	48.78
Std Dev	3.401



B1L102S, U19-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	66
Mean	55.11
Std Dev	3.163

B1L102S, U19-ch63, adc5

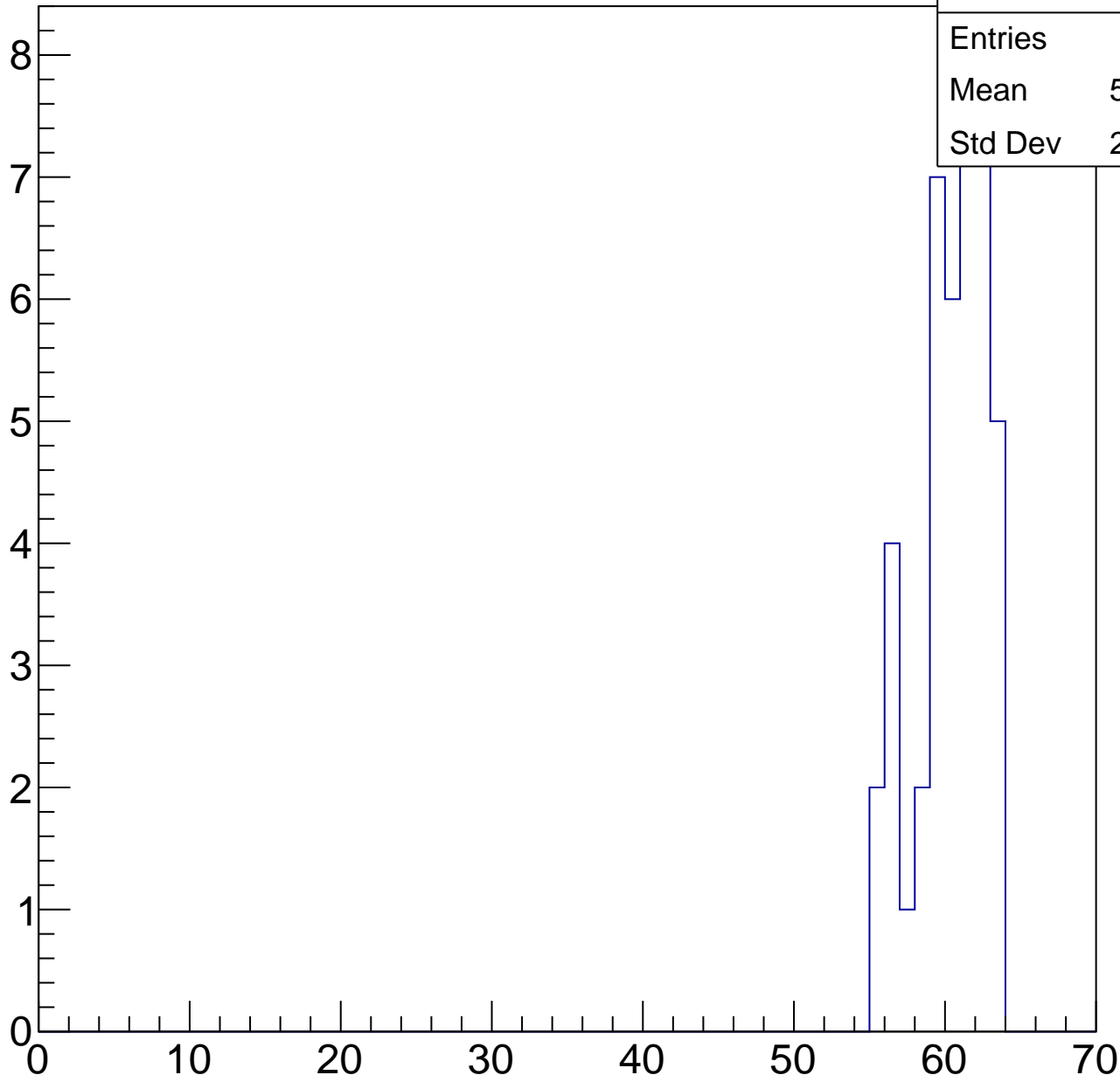
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.98
Std Dev	2.277

ampl

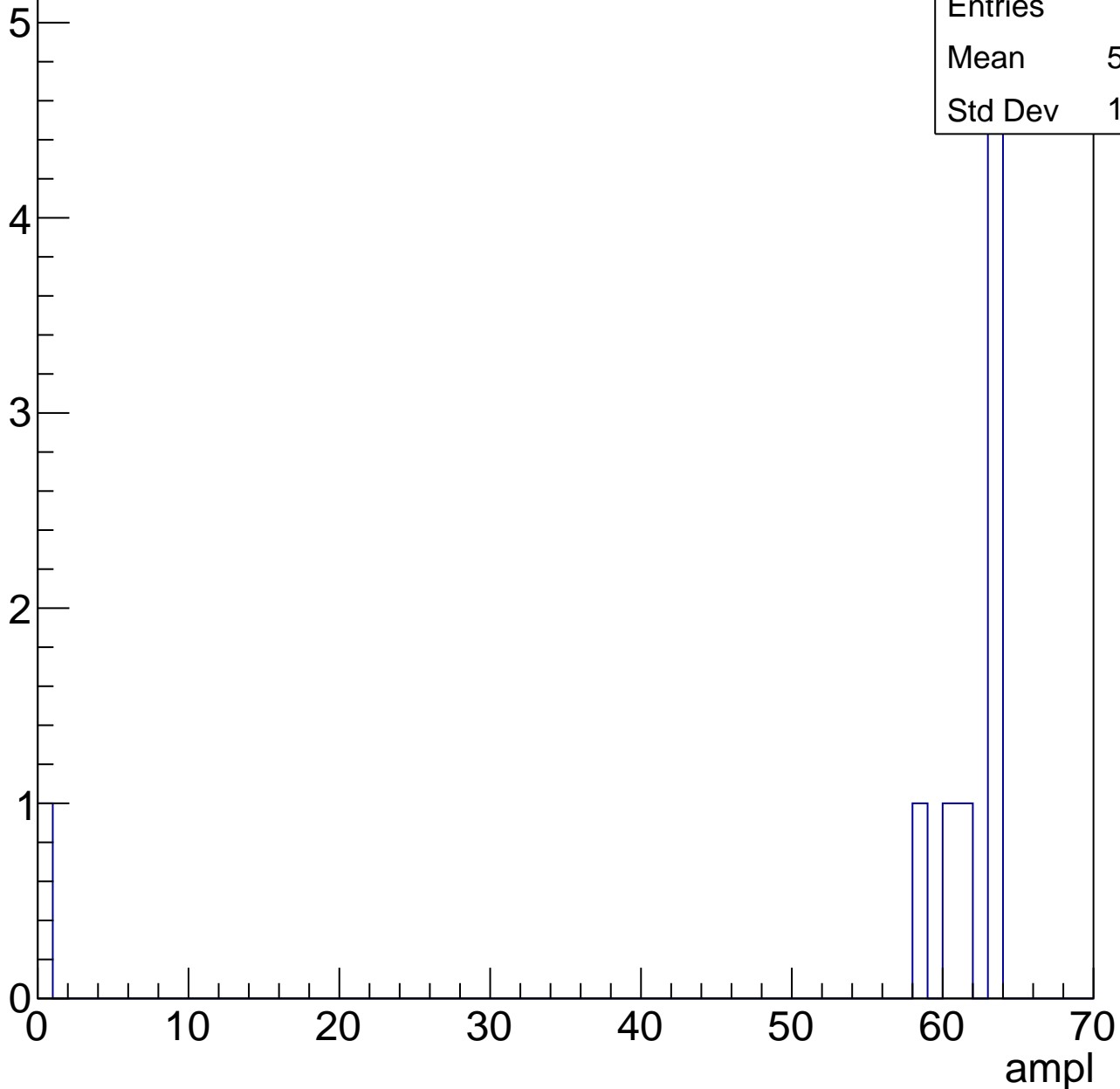


B1L102S, U19-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	54.89
Std Dev	19.48



B1L102S, U19-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch64, adc0

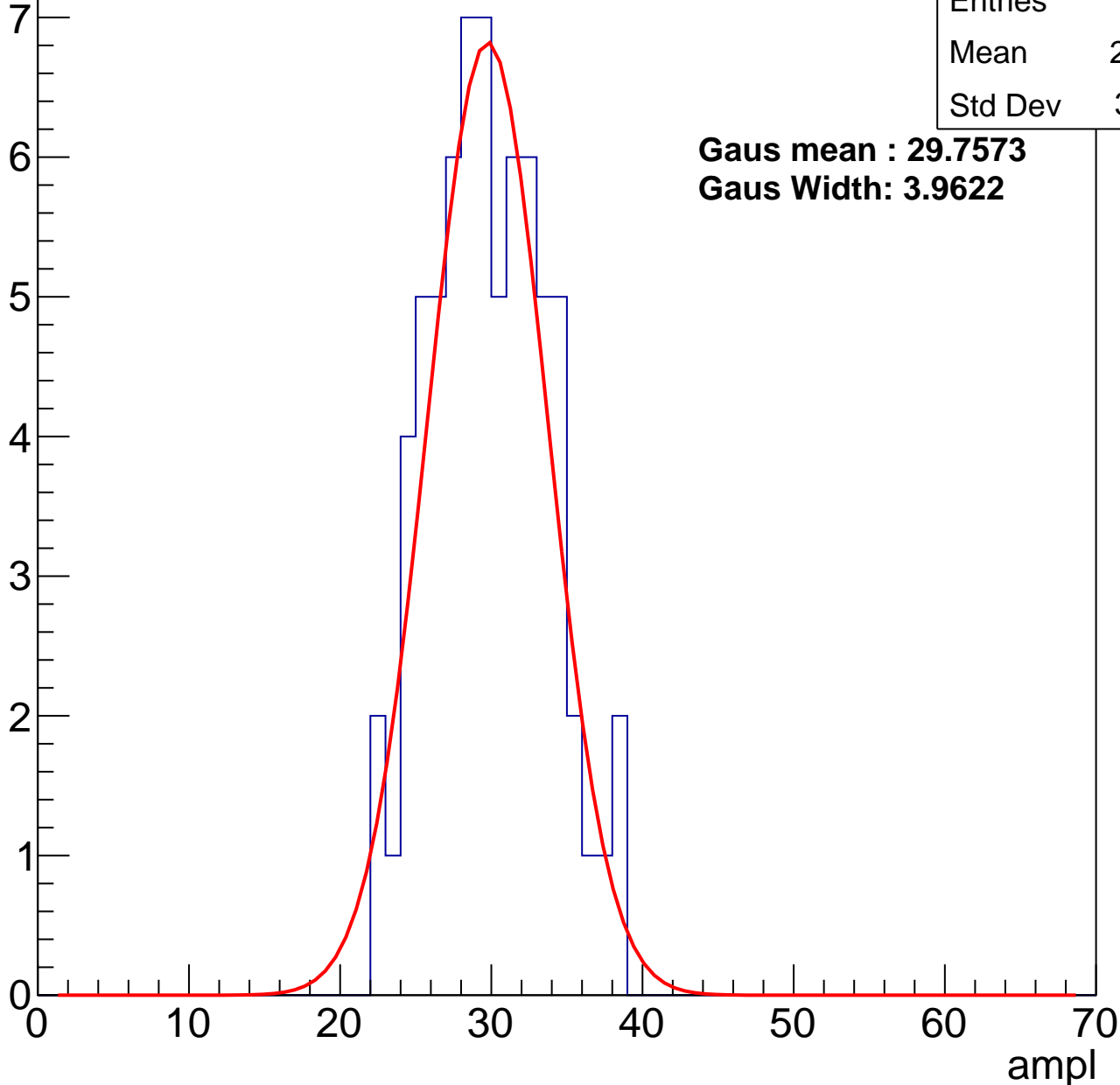
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.44
Std Dev	3.801

Gaus mean : 29.7573

Gaus Width: 3.9622



B1L102S, U19-ch64, adc1

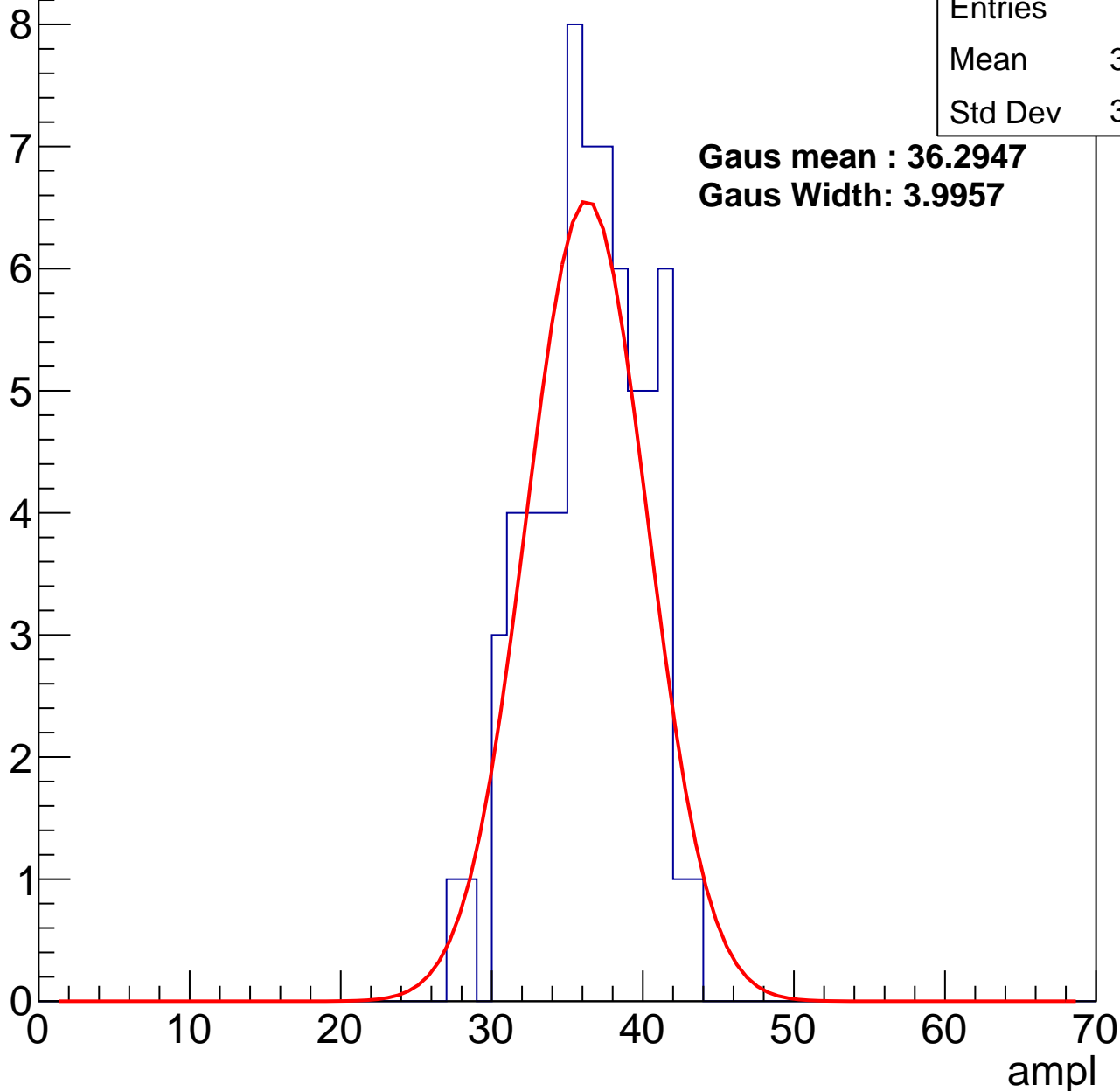
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.97
Std Dev	3.599

Gaus mean : 36.2947

Gaus Width: 3.9957



B1L102S, U19-ch64, adc2

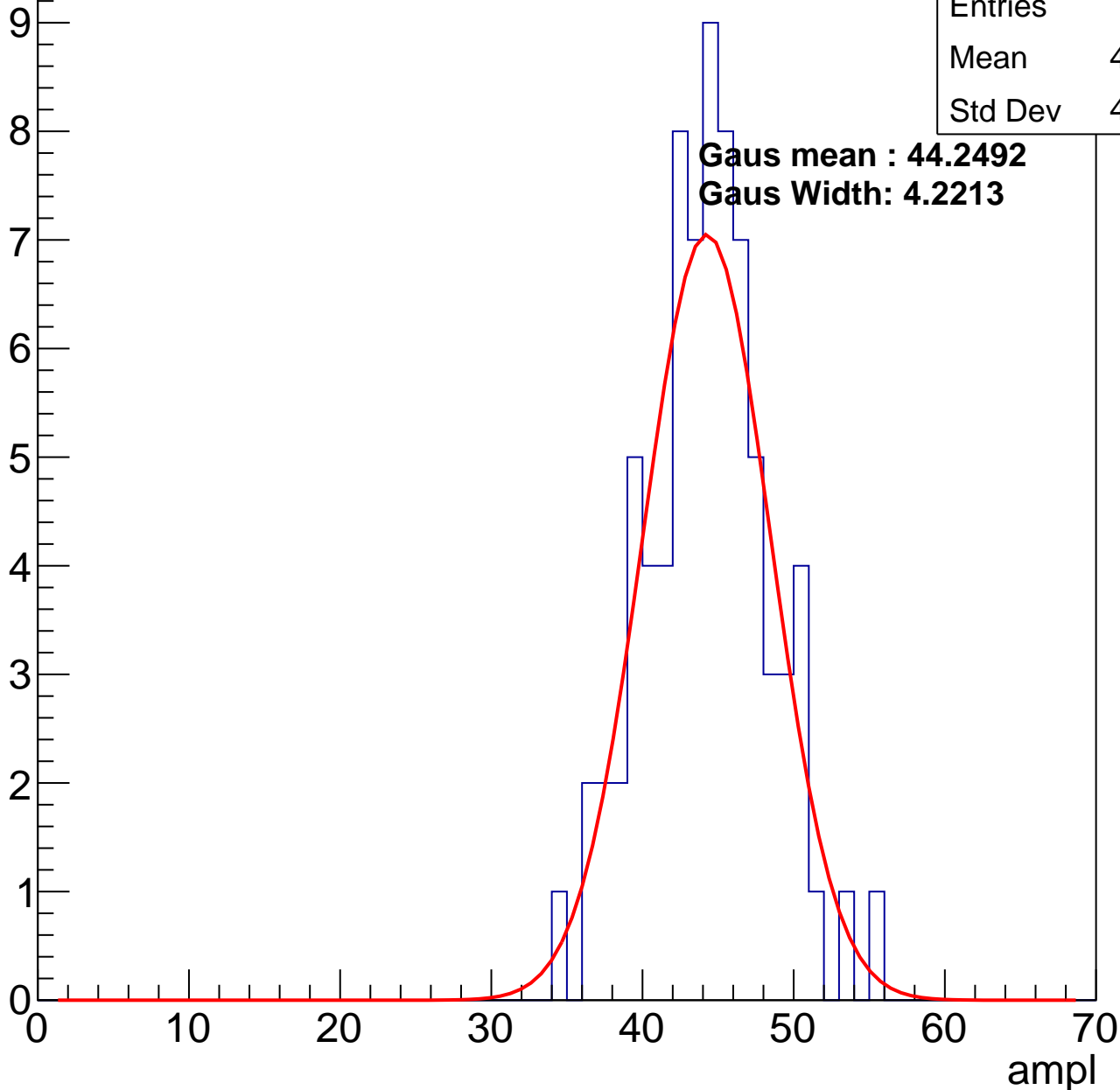
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	43.83
Std Dev	4.043

Gaus mean : 44.2492

Gaus Width: 4.2213

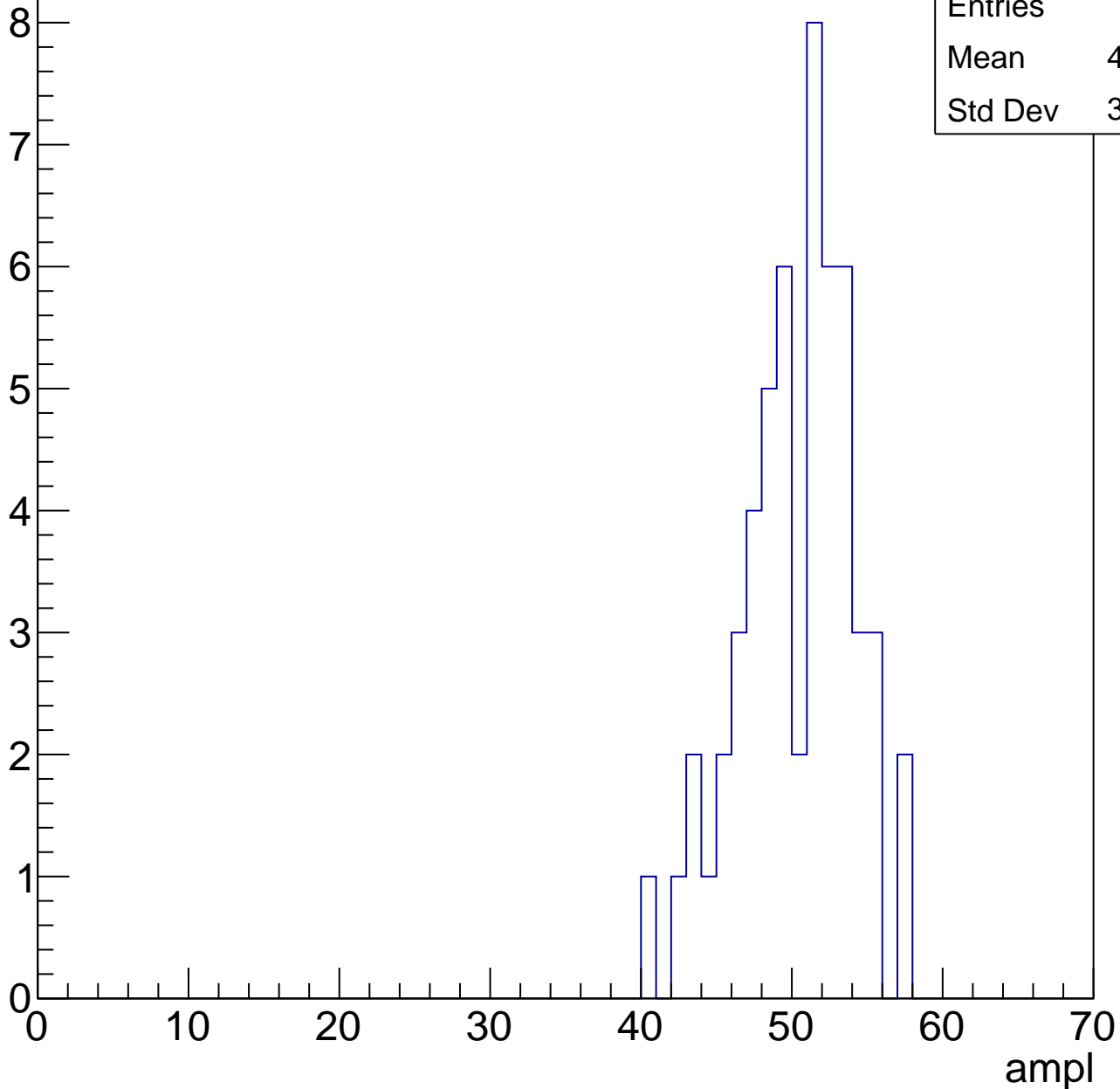


B1L102S, U19-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

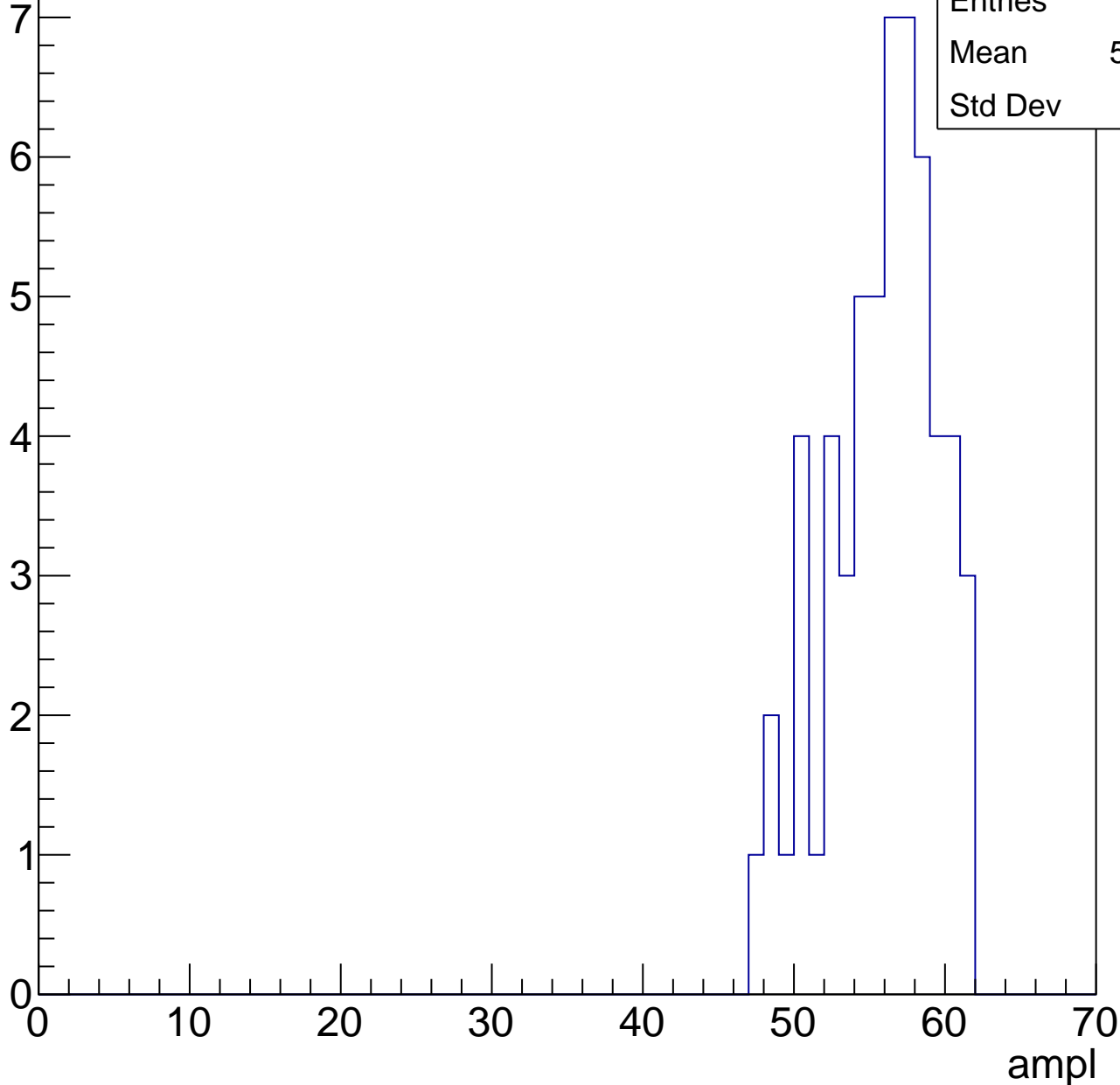
Entries	55
Mean	49.84
Std Dev	3.726



B1L102S, U19-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



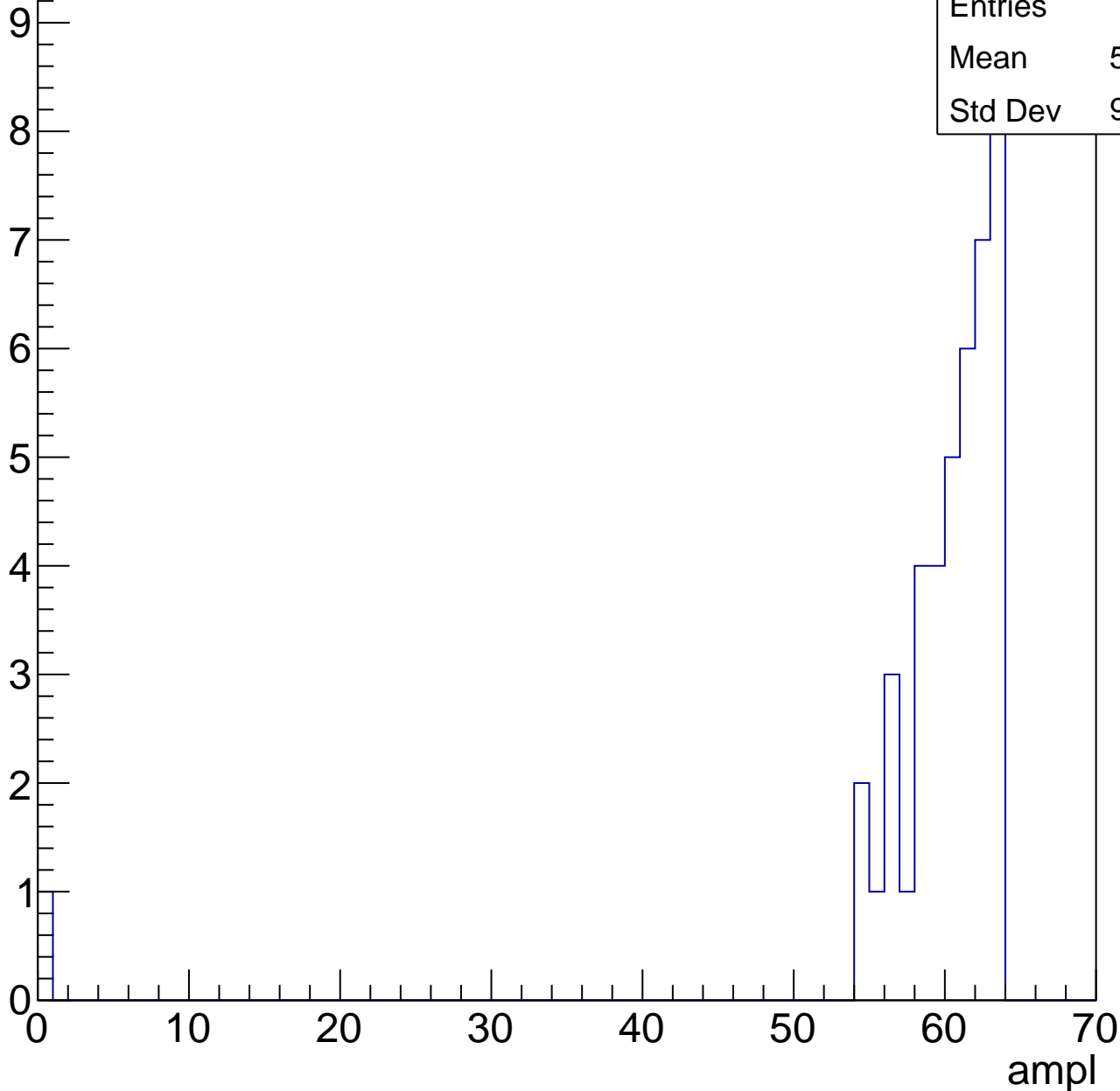
Entries	57
Mean	55.32
Std Dev	3.56

B1L102S, U19-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

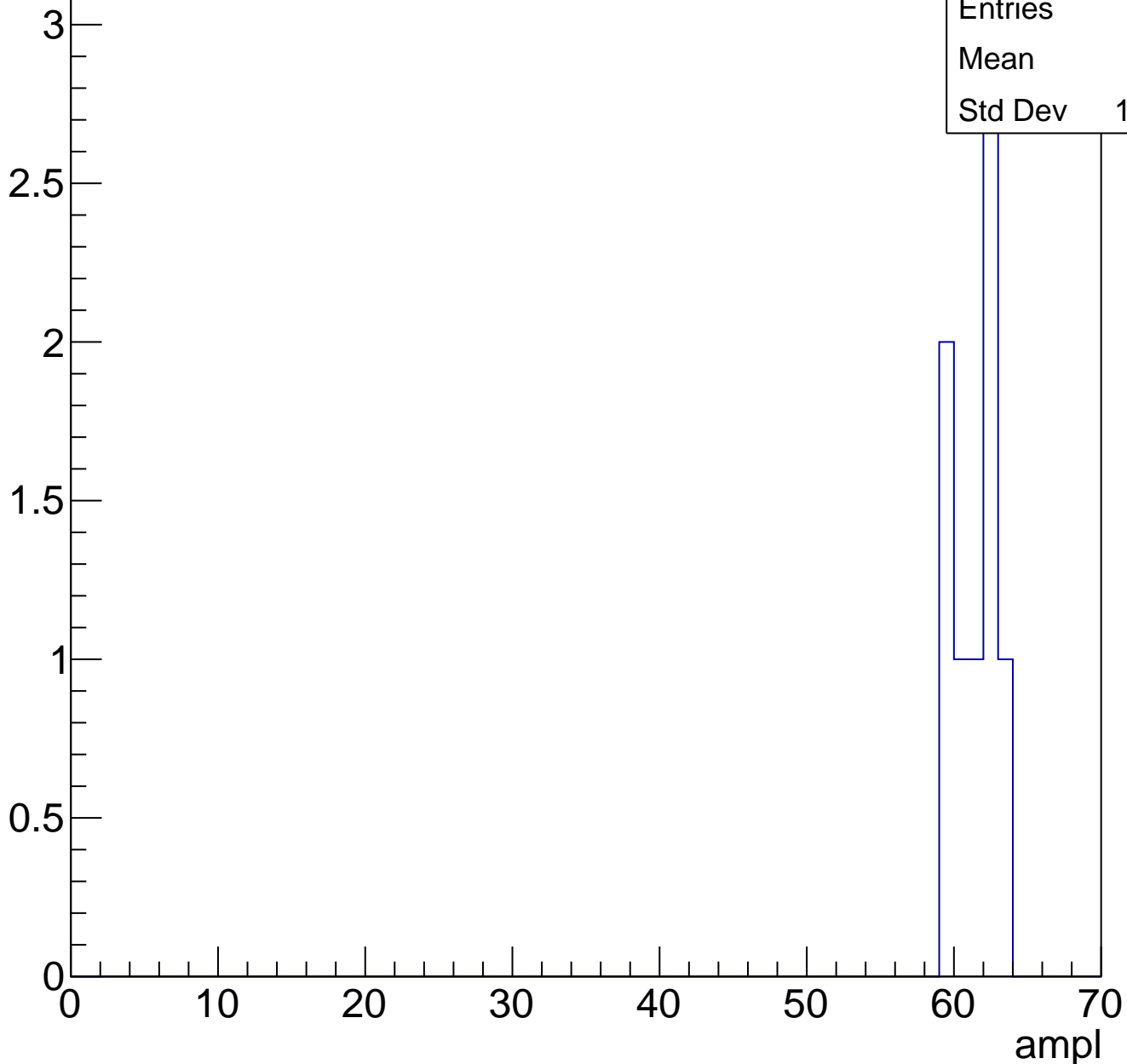
Entries	43
Mean	58.67
Std Dev	9.417



B1L102S, U19-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch65, adc0

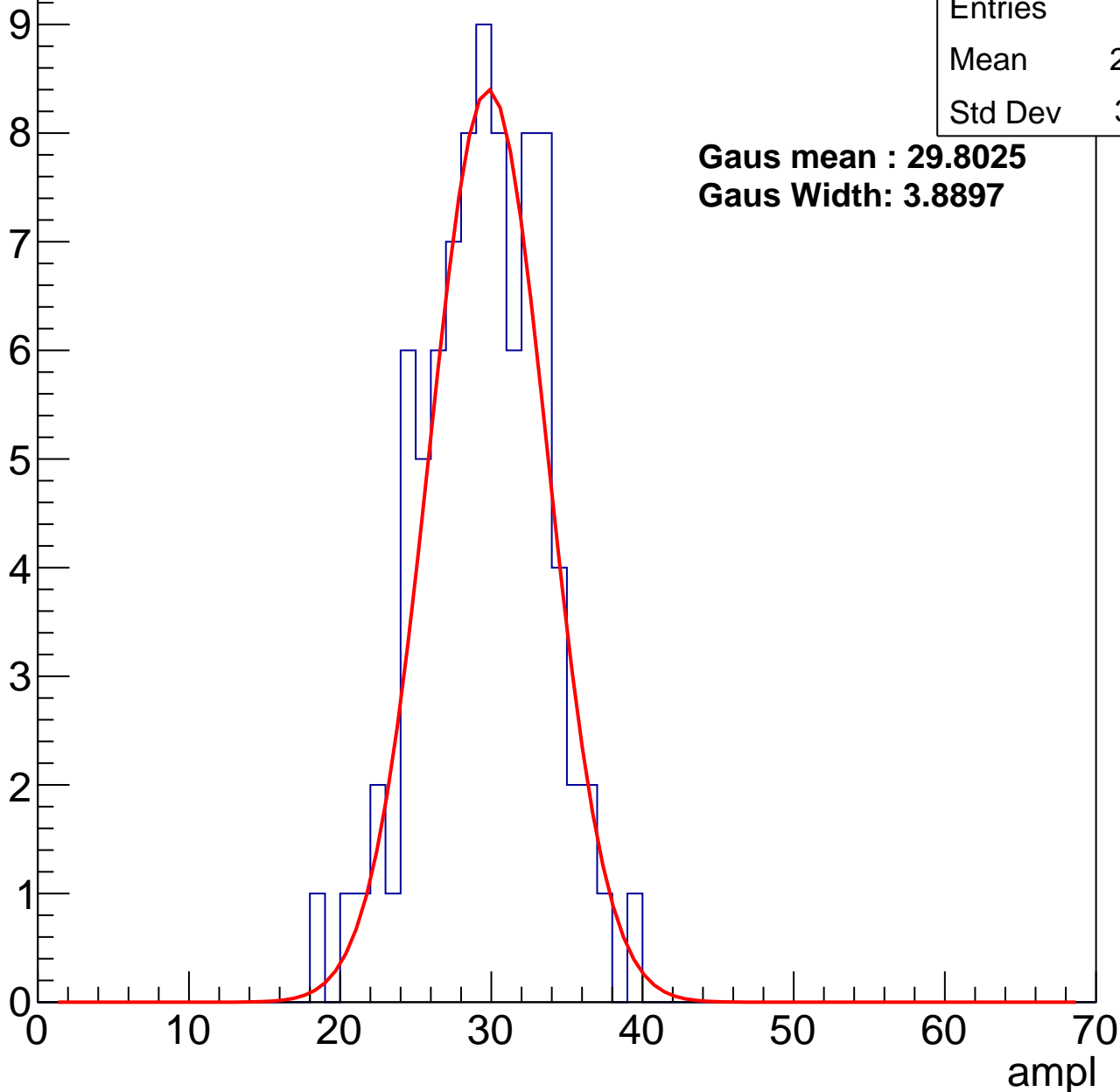
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	29.02
Std Dev	3.991

Gaus mean : 29.8025

Gaus Width: 3.8897



B1L102S, U19-ch65, adc1

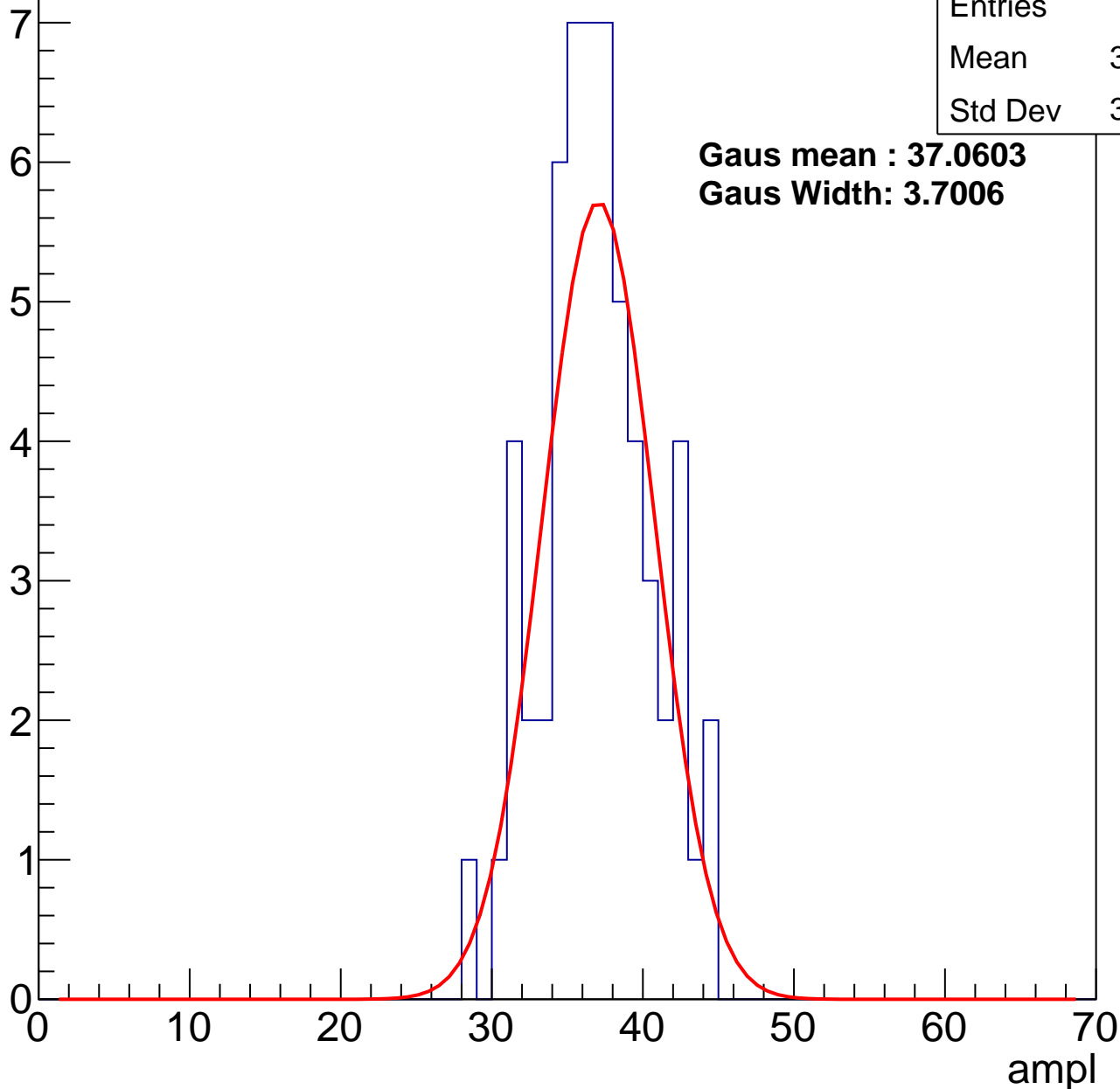
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.53
Std Dev	3.587

Gaus mean : 37.0603

Gaus Width: 3.7006



B1L102S, U19-ch65, adc2

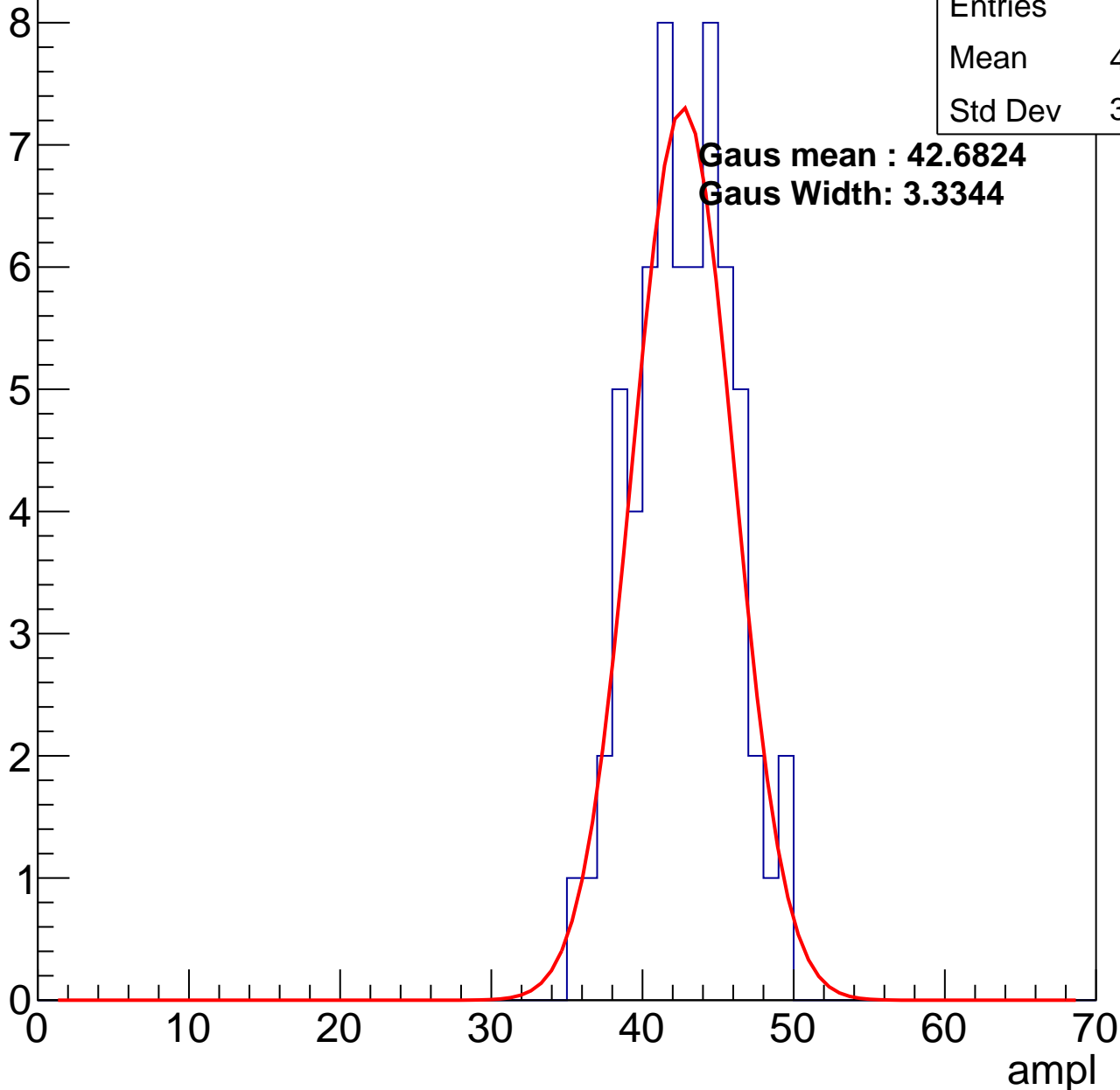
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.24
Std Dev	3.176

Gaus mean : 42.6824

Gaus Width: 3.3344

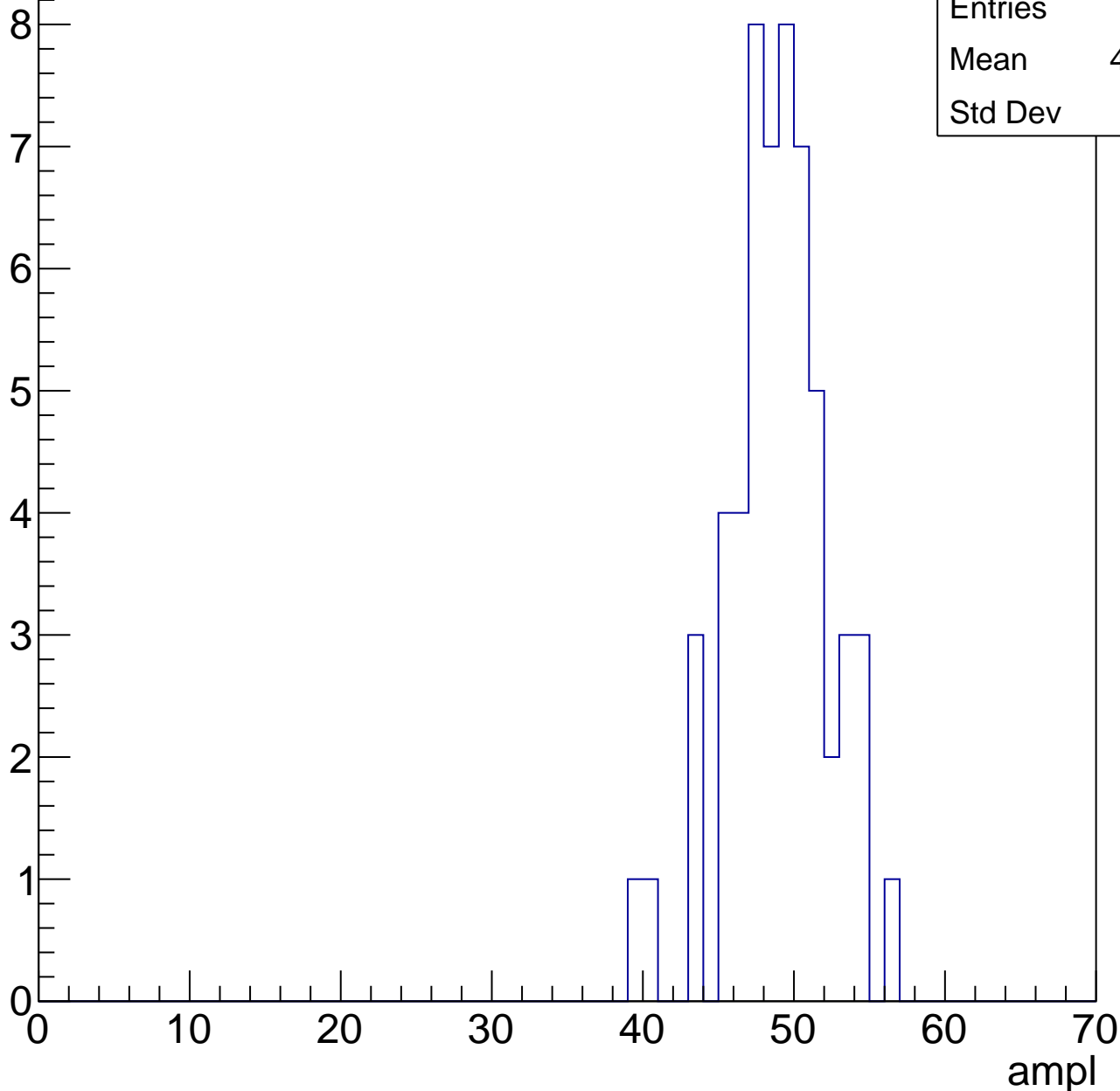


B1L102S, U19-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.46
Std Dev	3.32

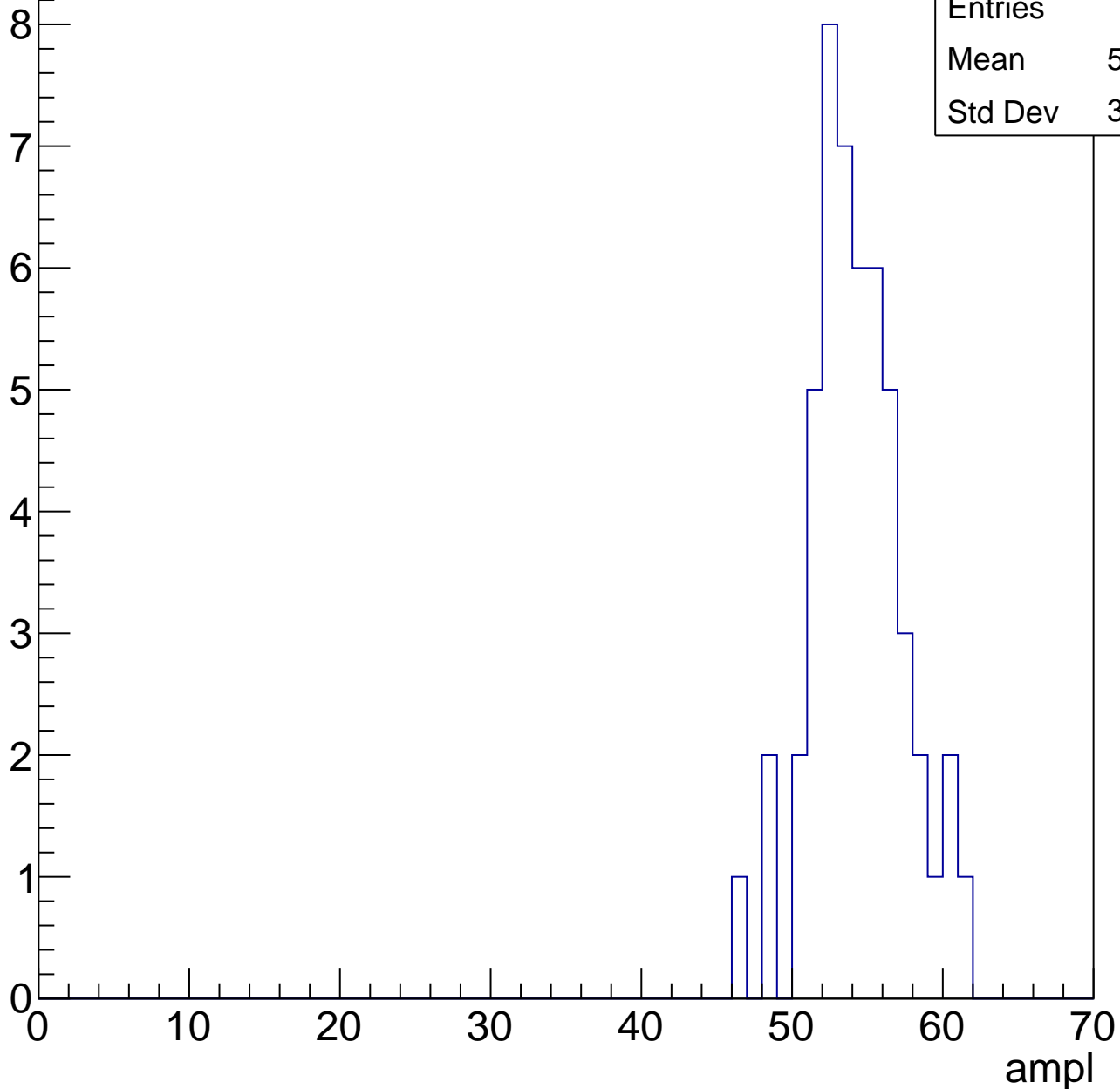


B1L102S, U19-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	53.82
Std Dev	3.072

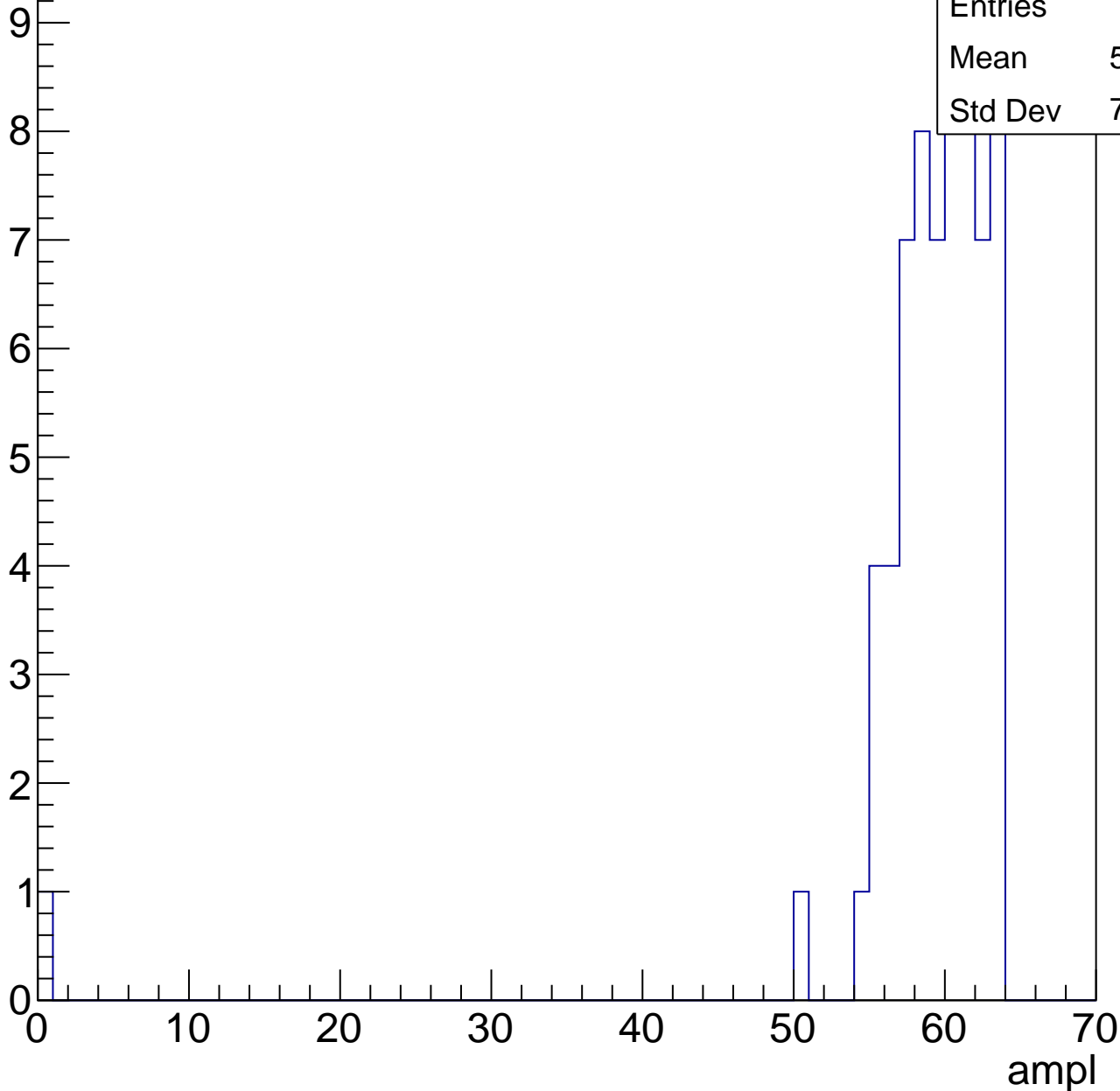


B1L102S, U19-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

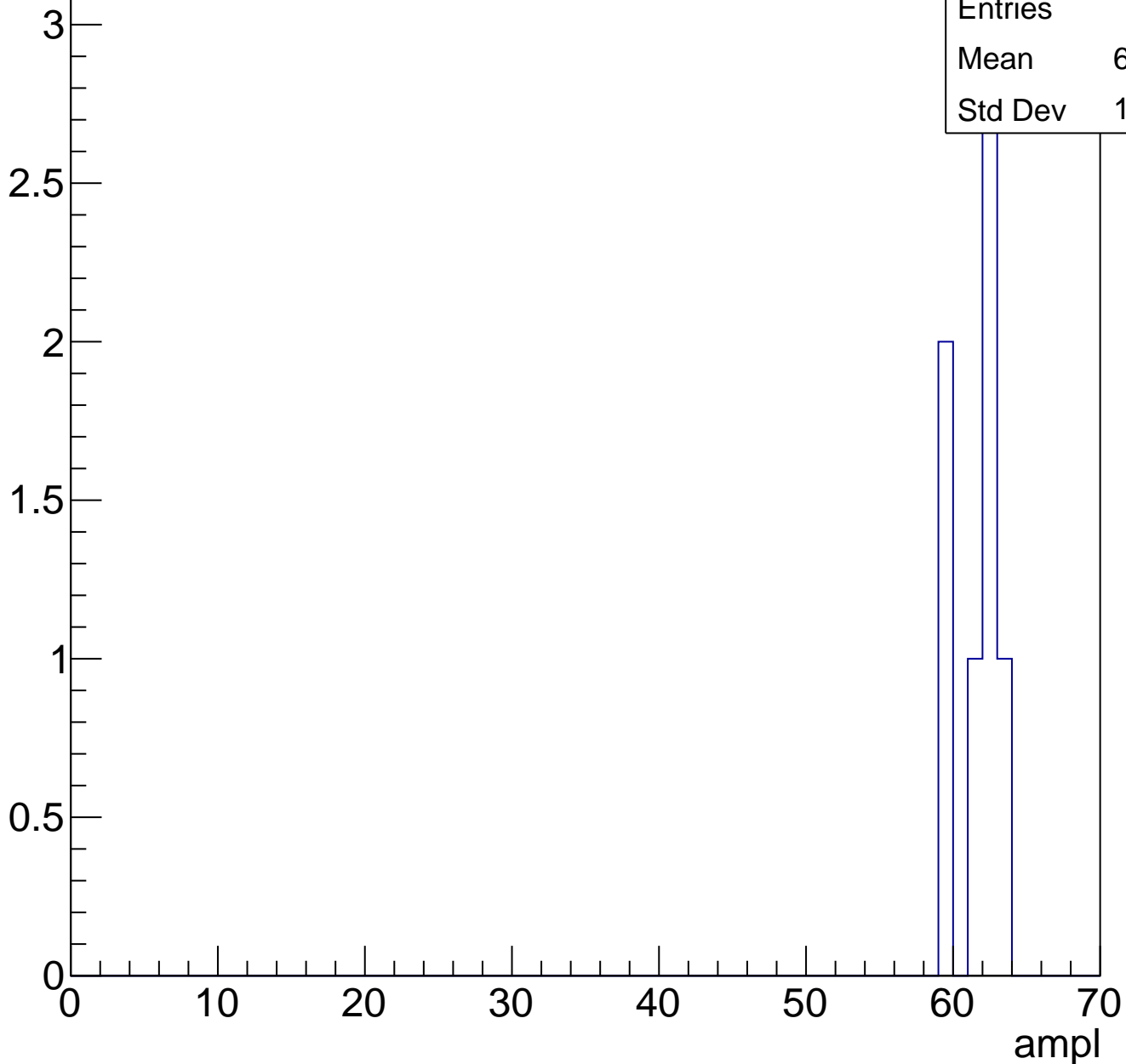
Entries	65
Mean	58.35
Std Dev	7.788



B1L102S, U19-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch66, adc0

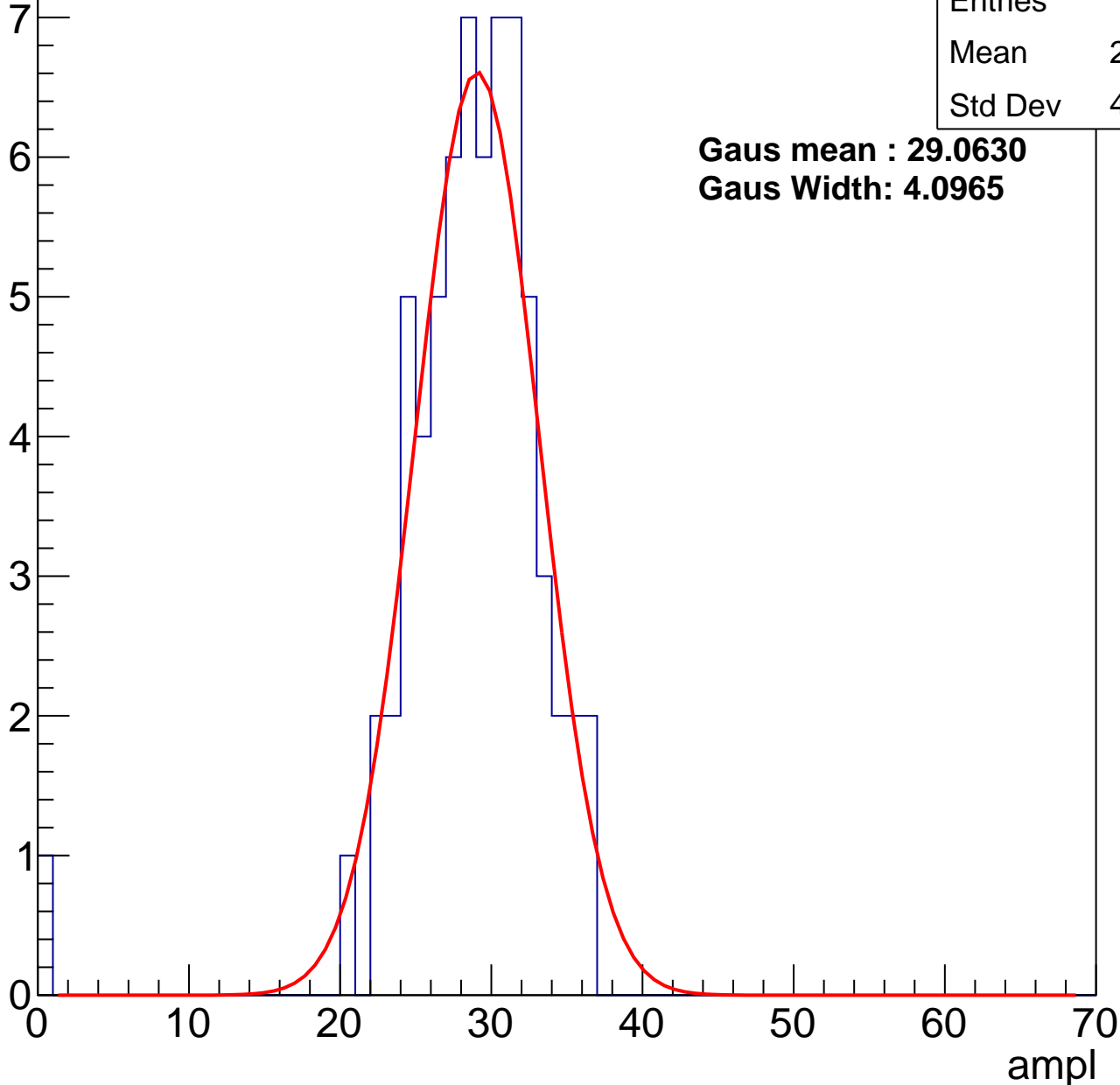
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.18
Std Dev	4.977

Gaus mean : 29.0630

Gaus Width: 4.0965



B1L102S, U19-ch66, adc1

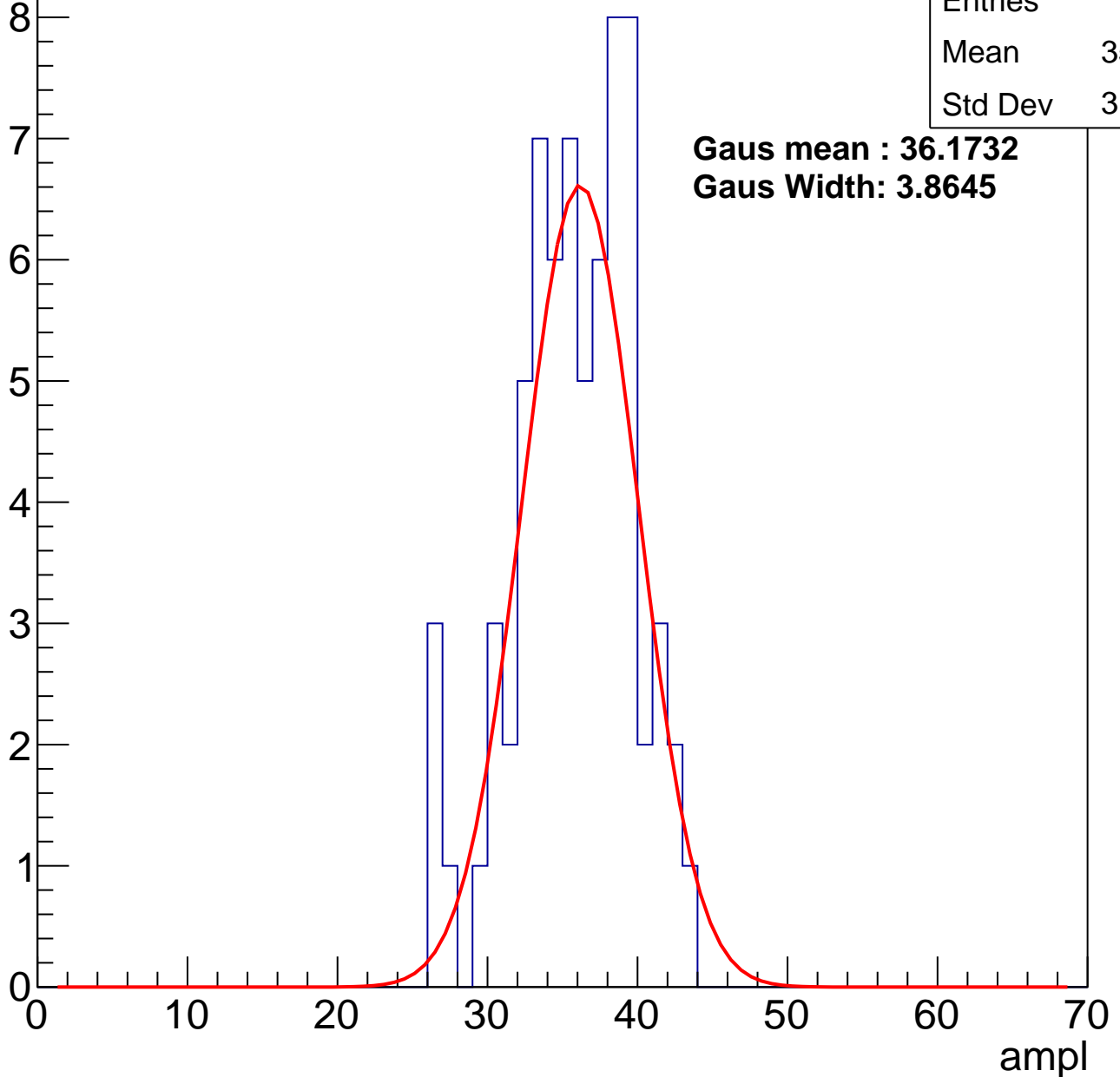
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.34
Std Dev	3.909

Gaus mean : 36.1732

Gaus Width: 3.8645



B1L102S, U19-ch66, adc2

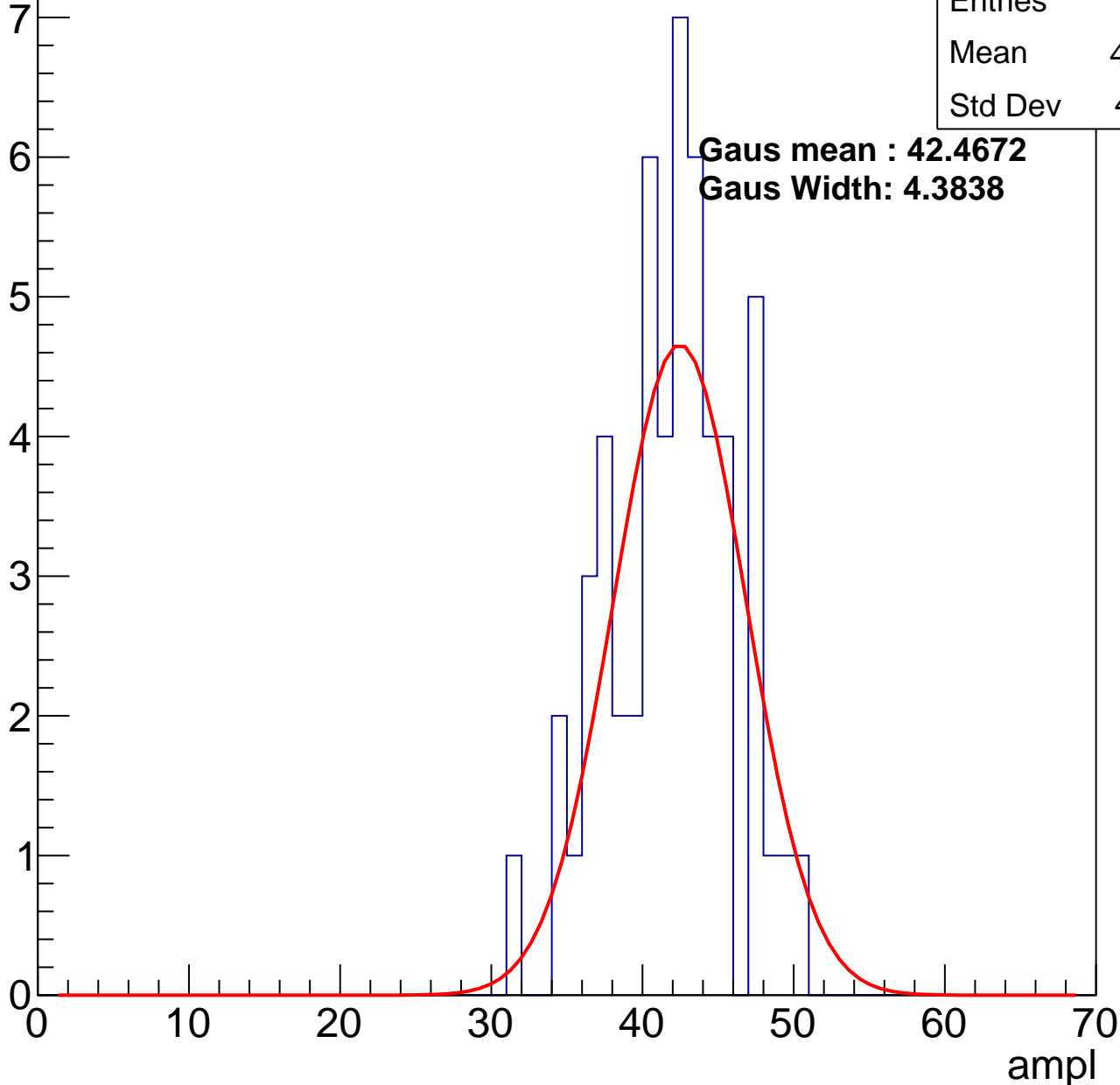
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.44
Std Dev	4.081

Gaus mean : 42.4672

Gaus Width: 4.3838

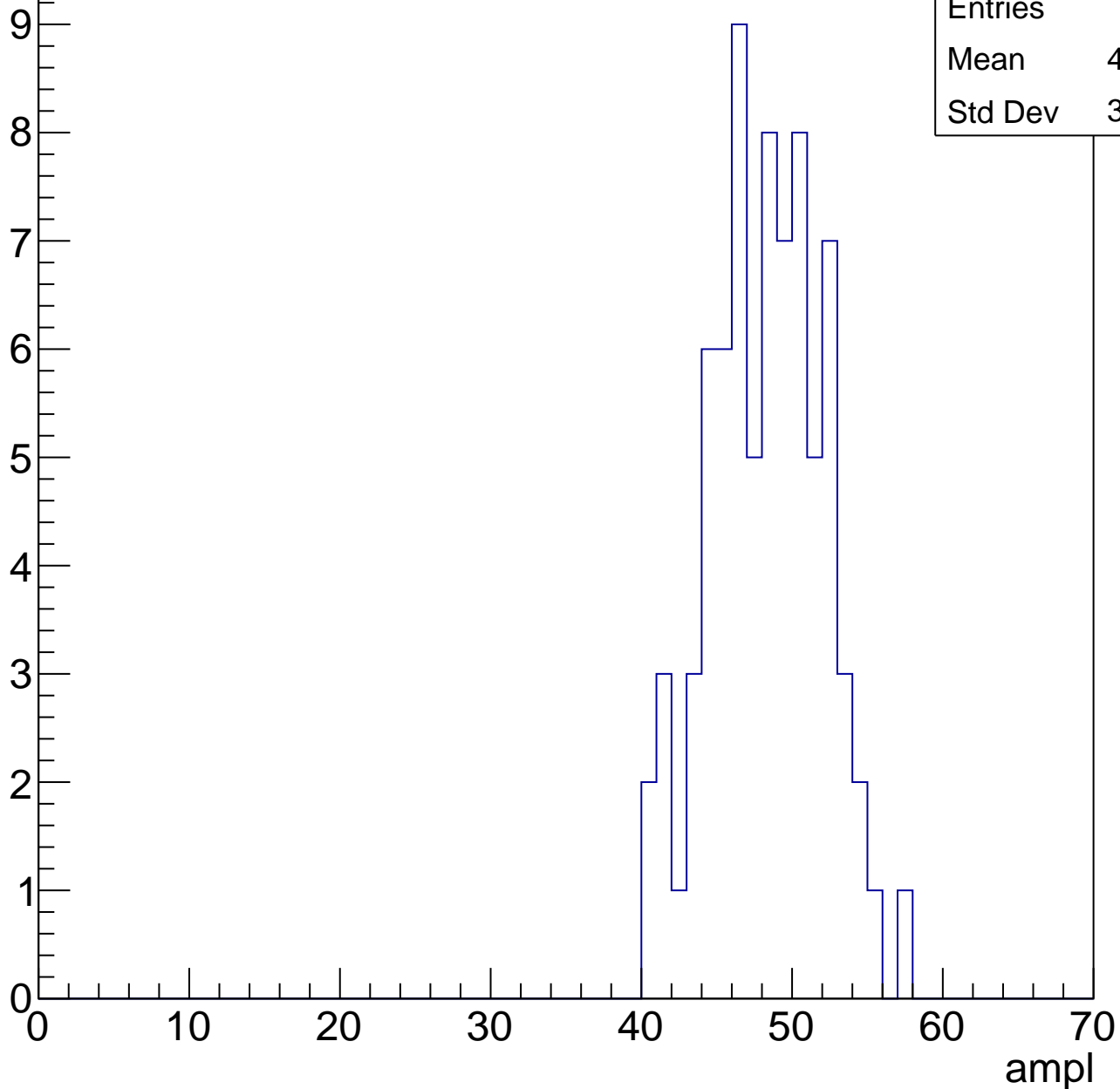


B1L102S, U19-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

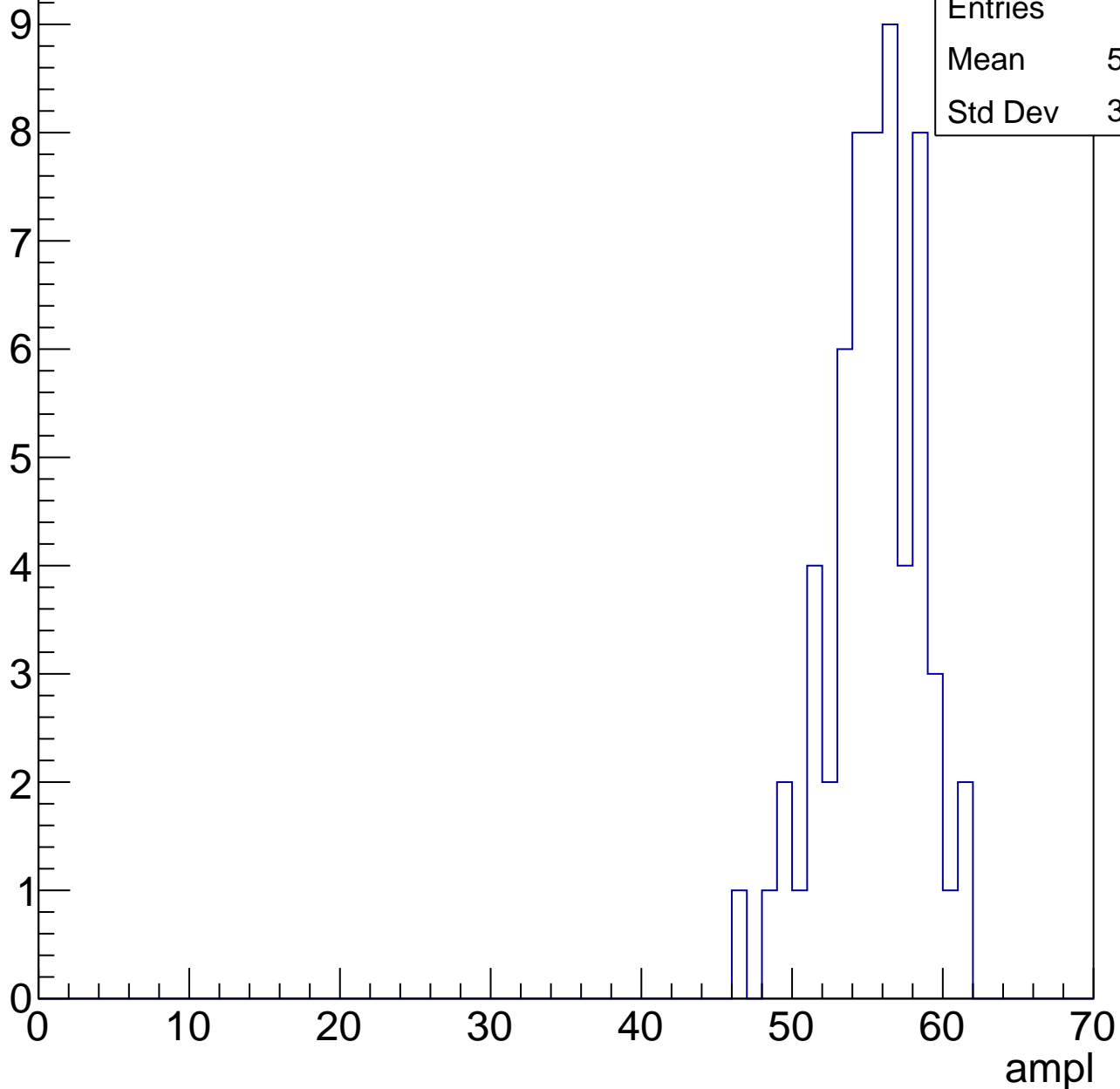
Entries	77
Mean	47.82
Std Dev	3.688



B1L102S, U19-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch66, adc5

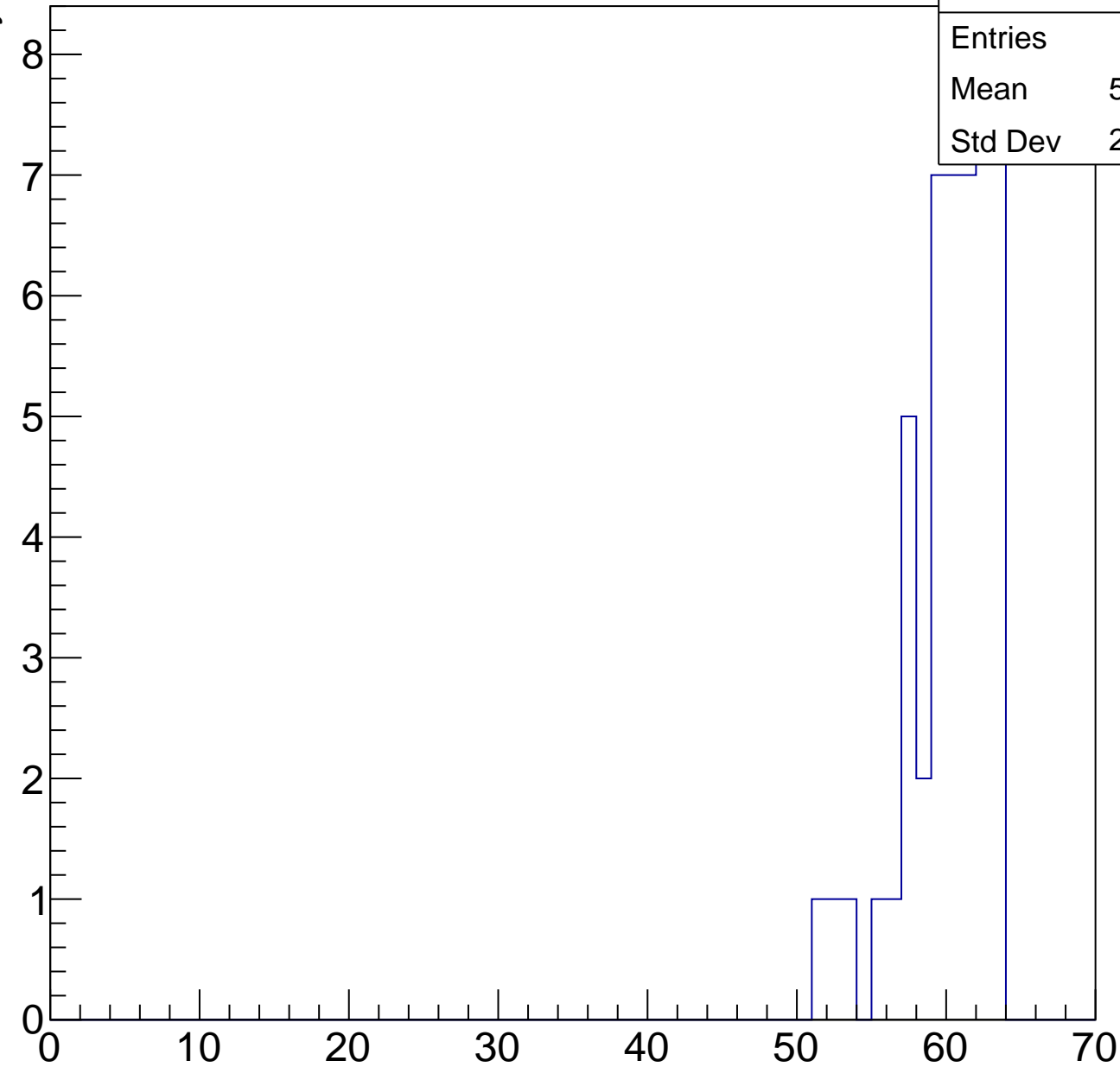
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.76
Std Dev	2.868

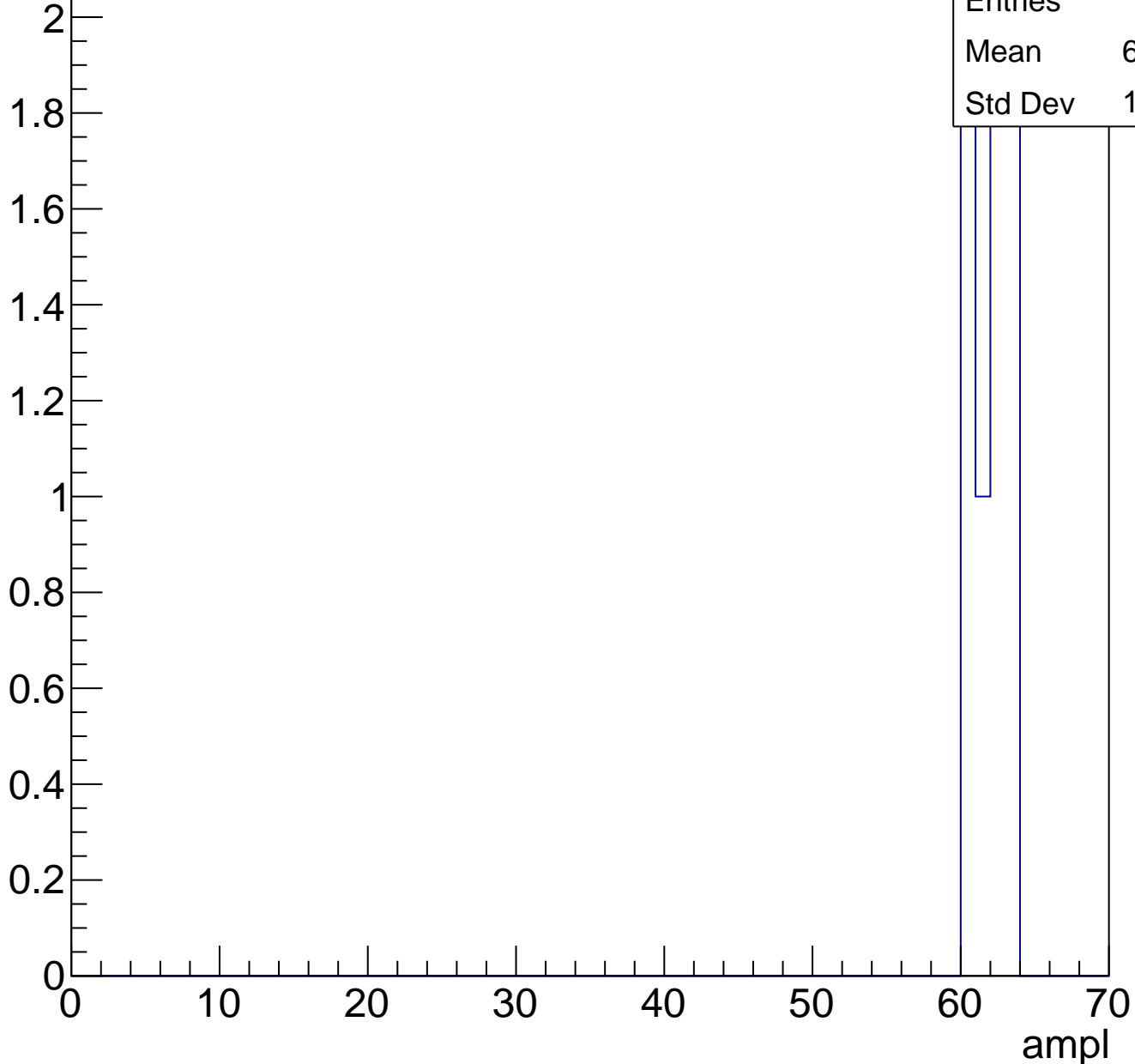
ampl



B1L102S, U19-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

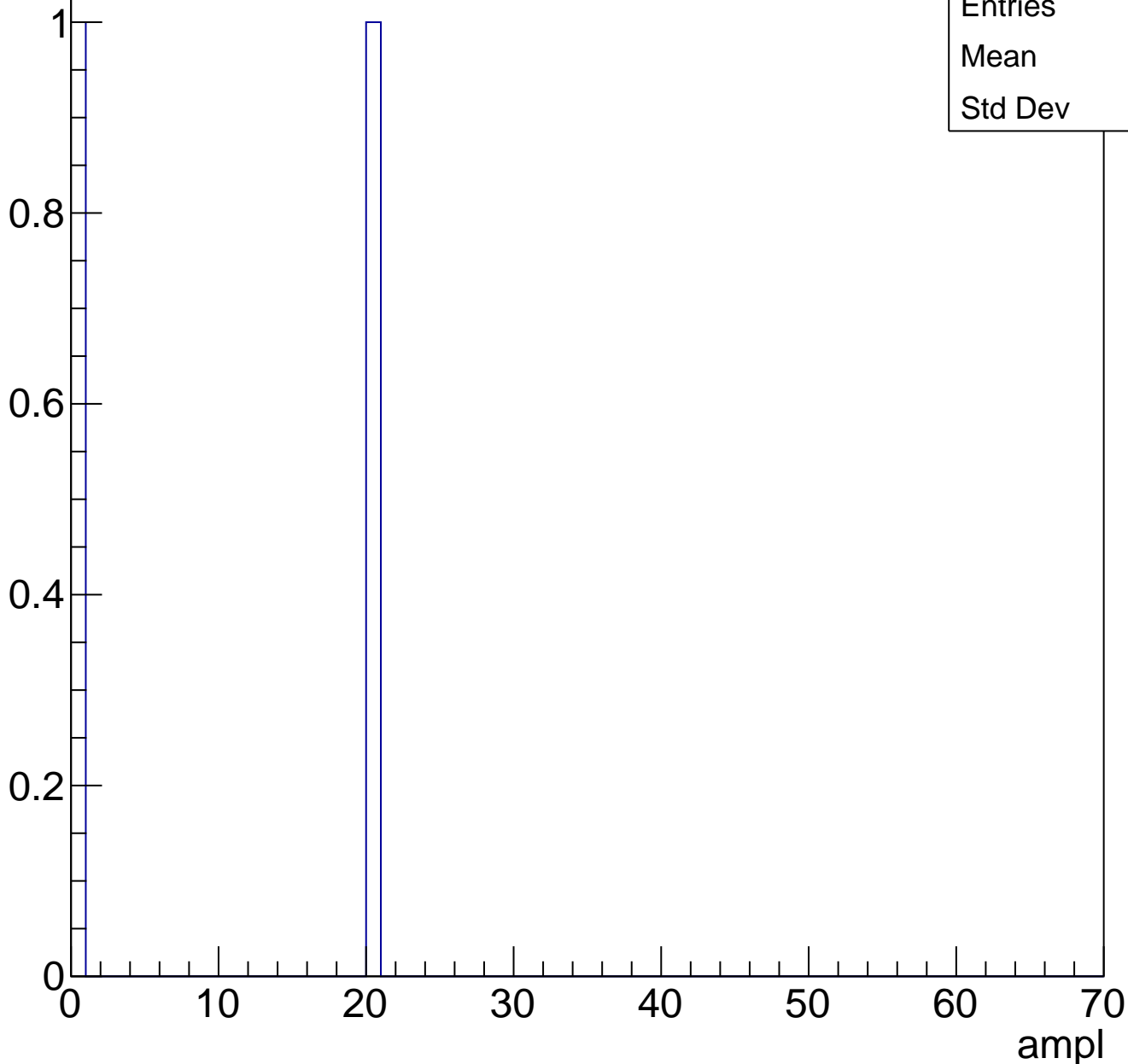
Entry



B1L102S, U19-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch67, adc0

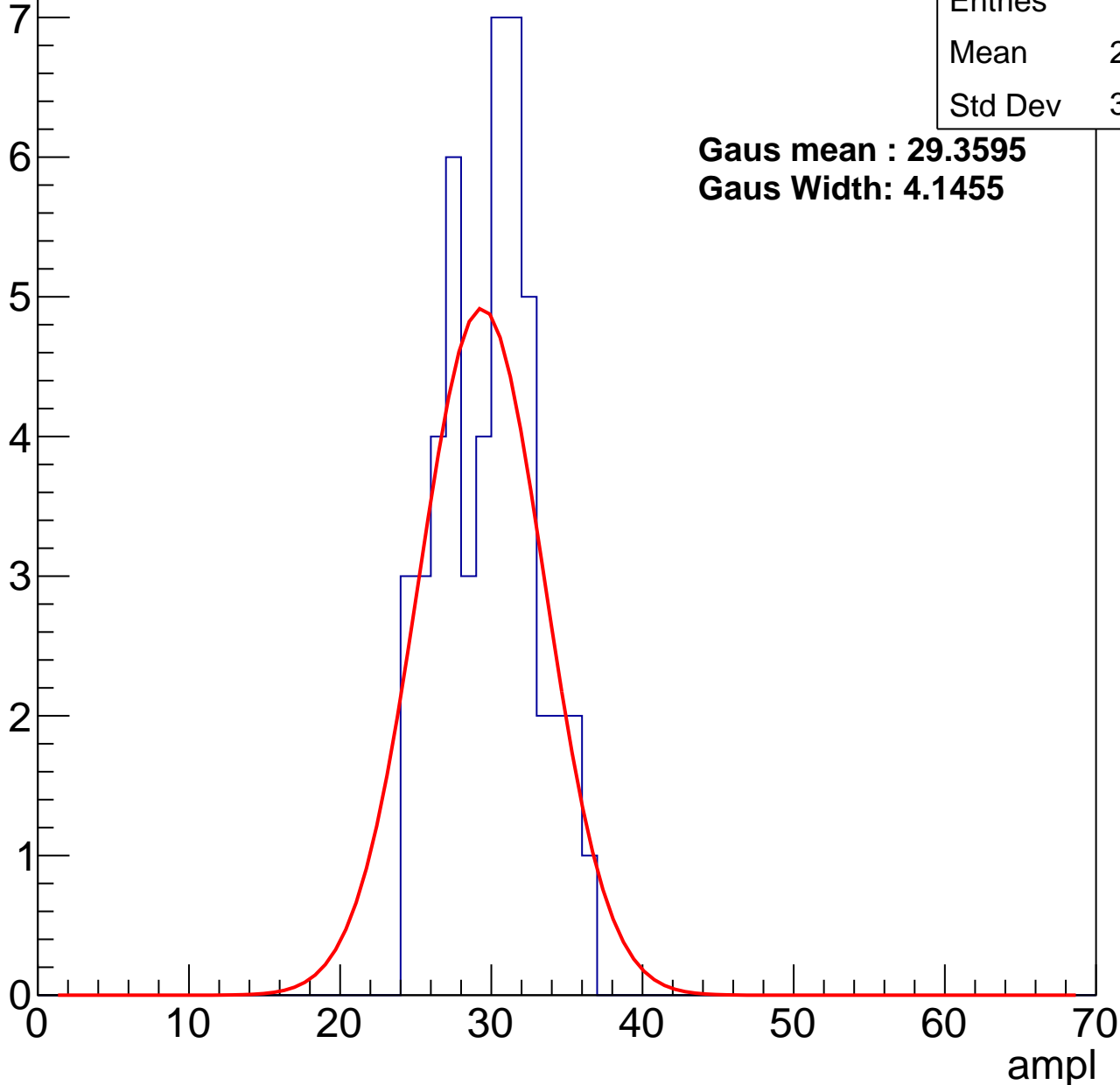
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	29.39
Std Dev	3.069

Gaus mean : 29.3595

Gaus Width: 4.1455



B1L102S, U19-ch67, adc1

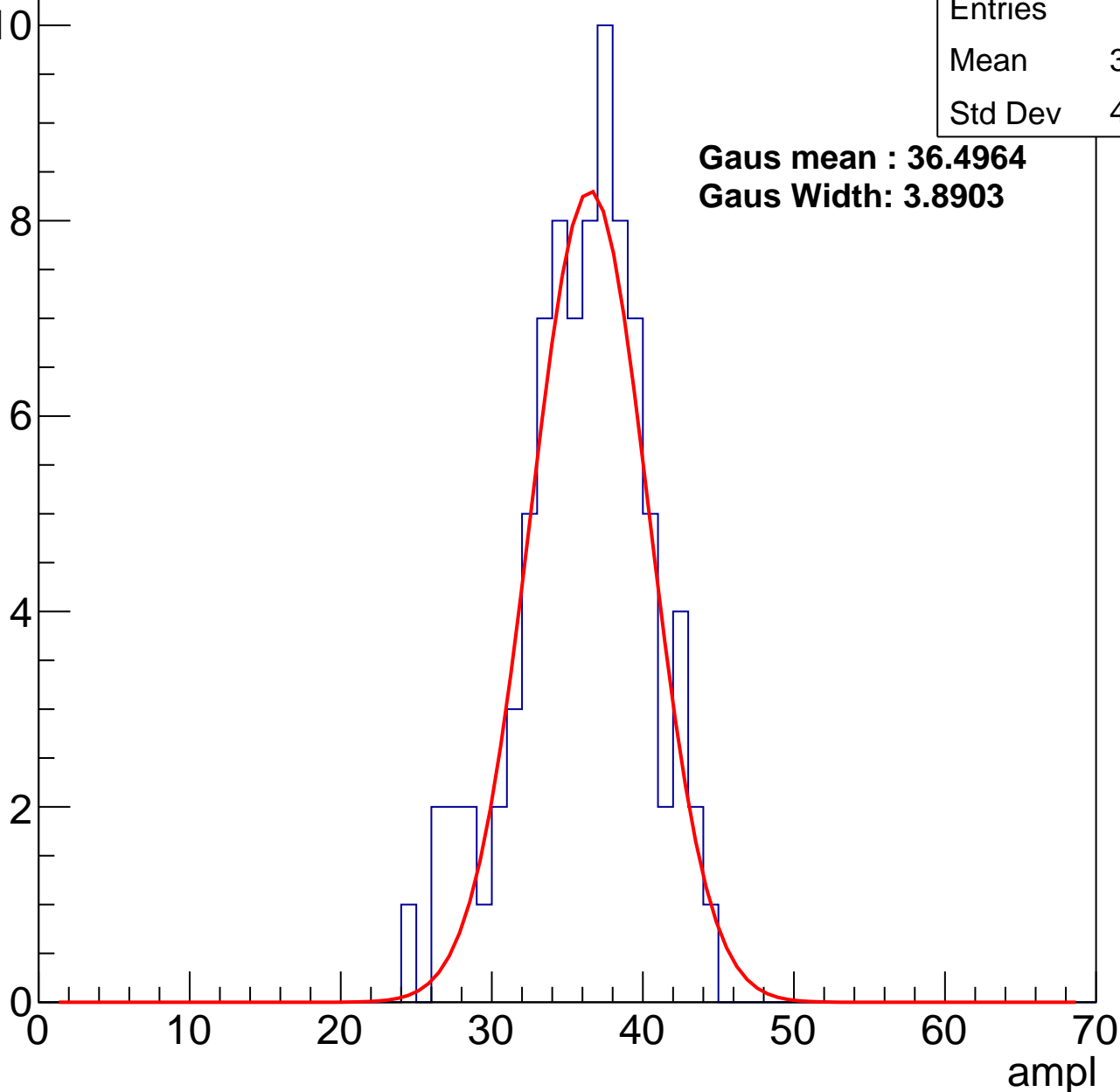
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.53
Std Dev	4.199

Gaus mean : 36.4964

Gaus Width: 3.8903



B1L102S, U19-ch67, adc2

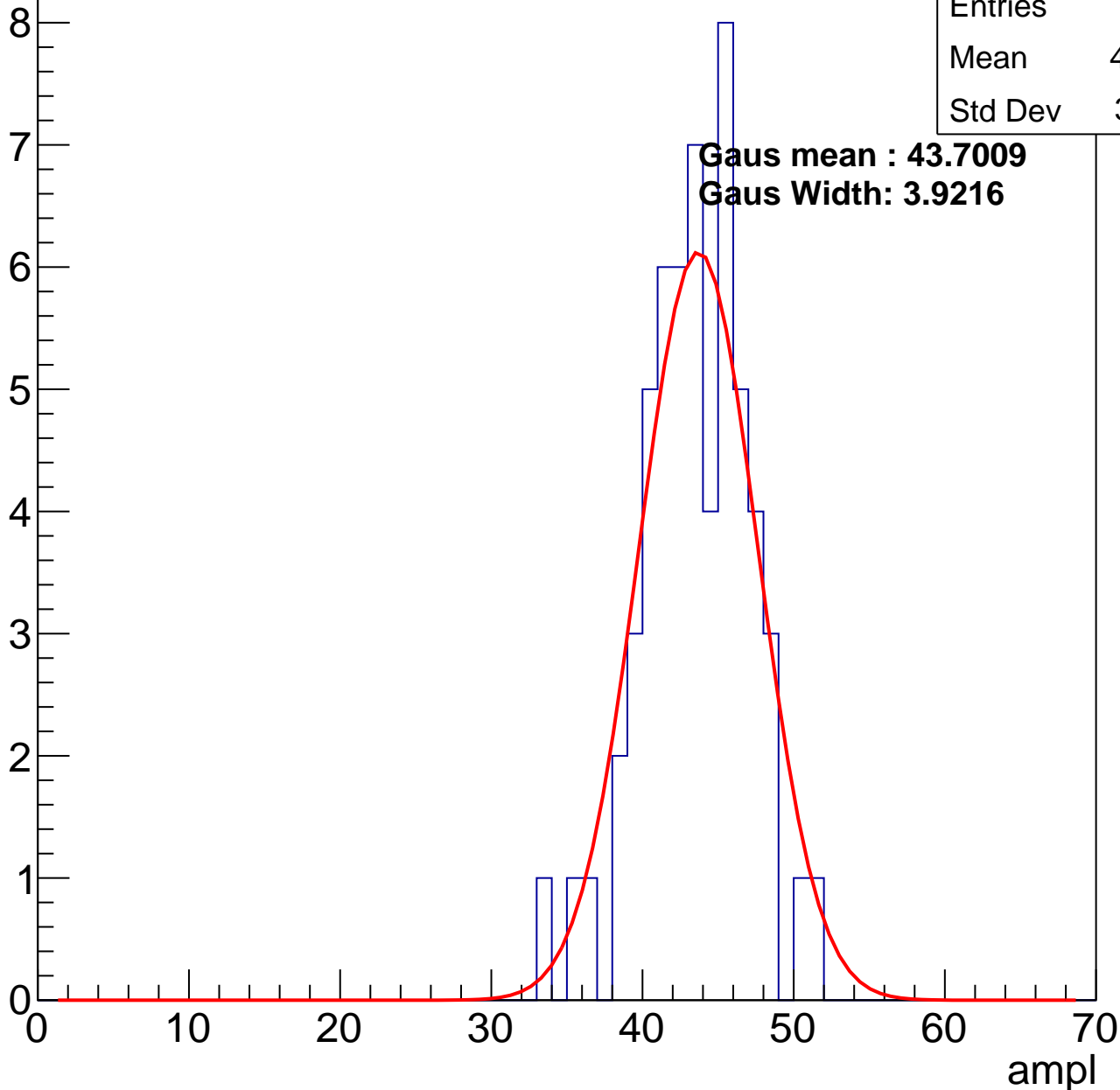
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.02
Std Dev	3.521

Gaus mean : 43.7009

Gaus Width: 3.9216

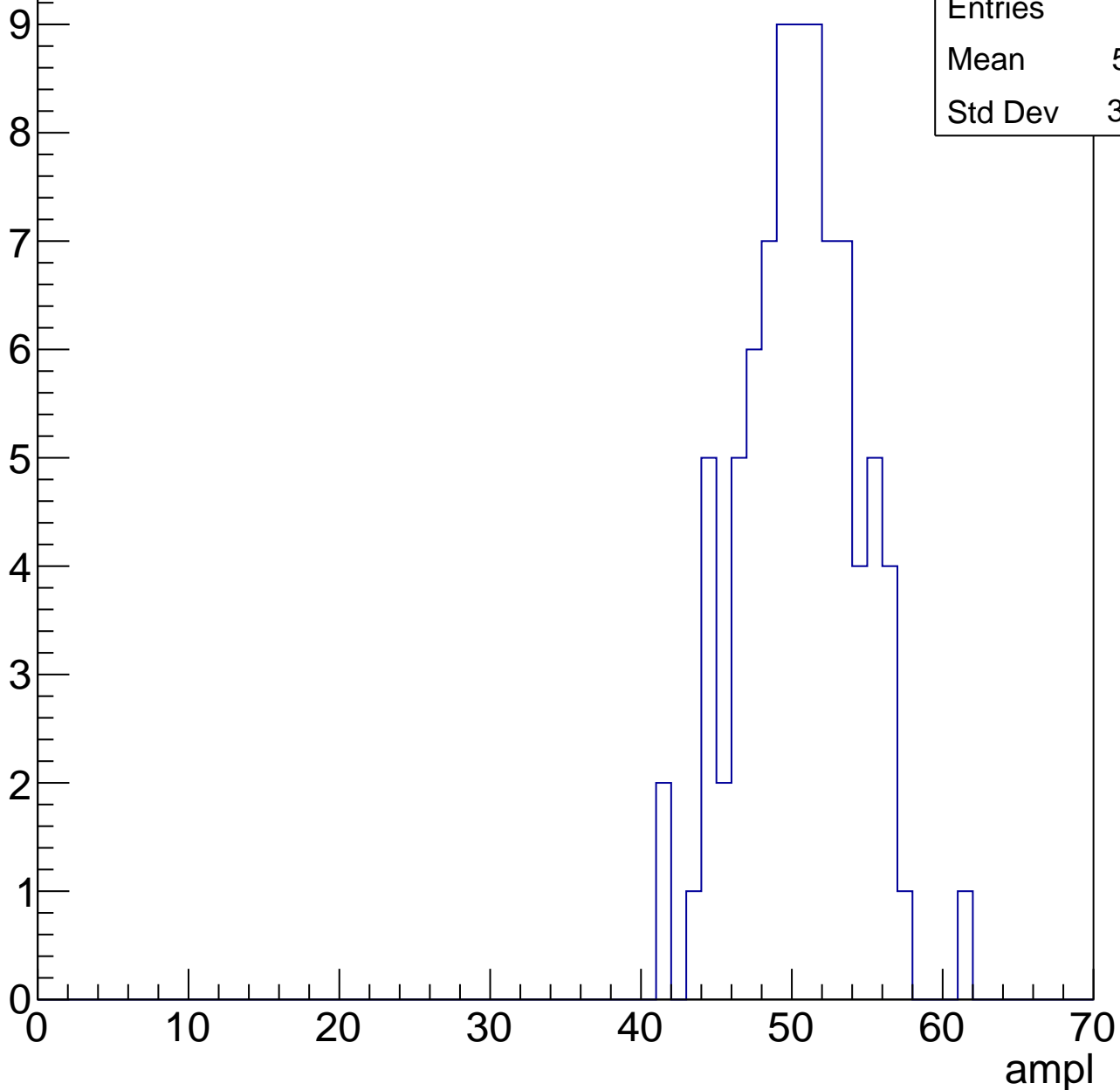


B1L102S, U19-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

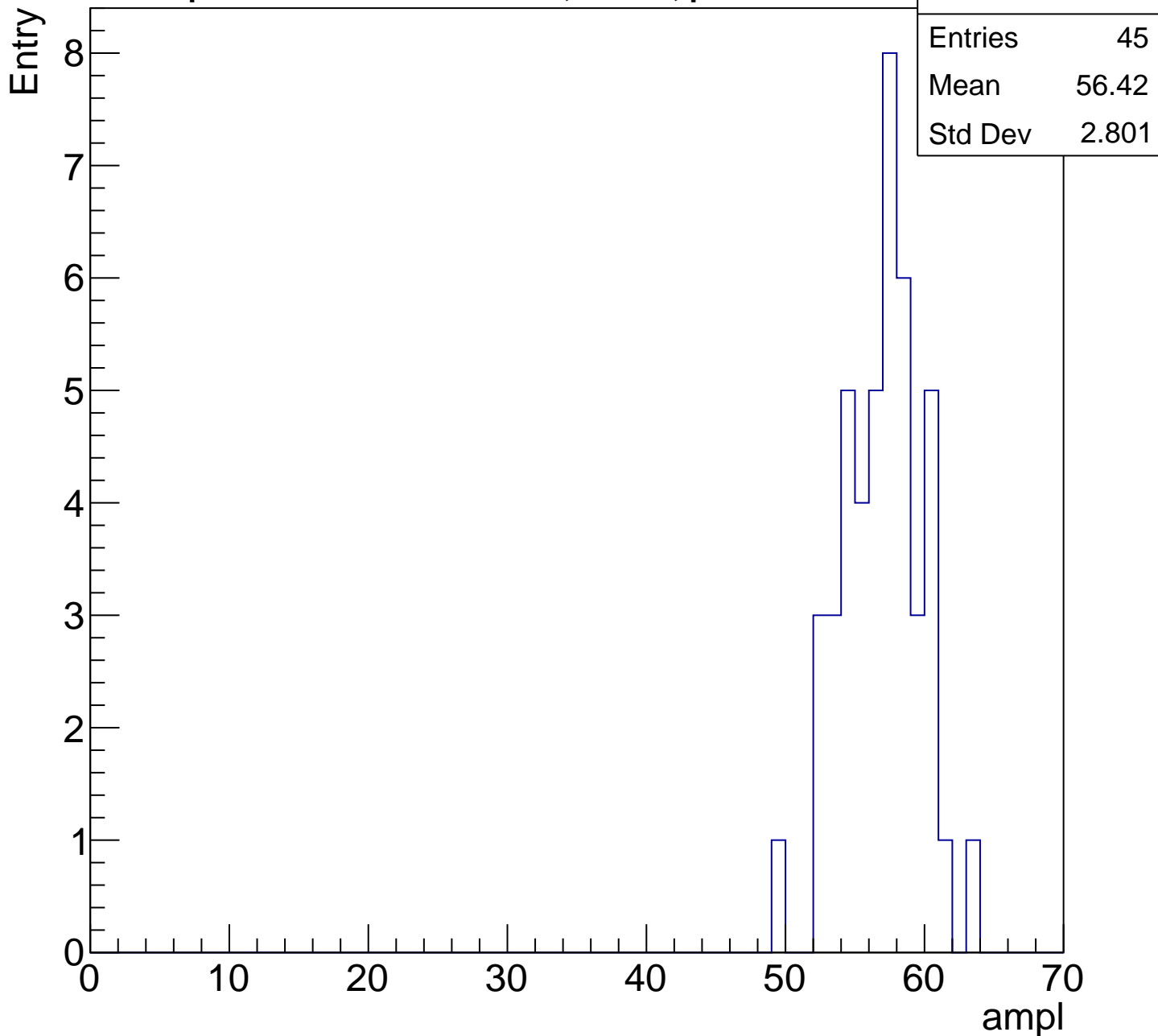
Entry

Entries	84
Mean	50.01
Std Dev	3.803



B1L102S, U19-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

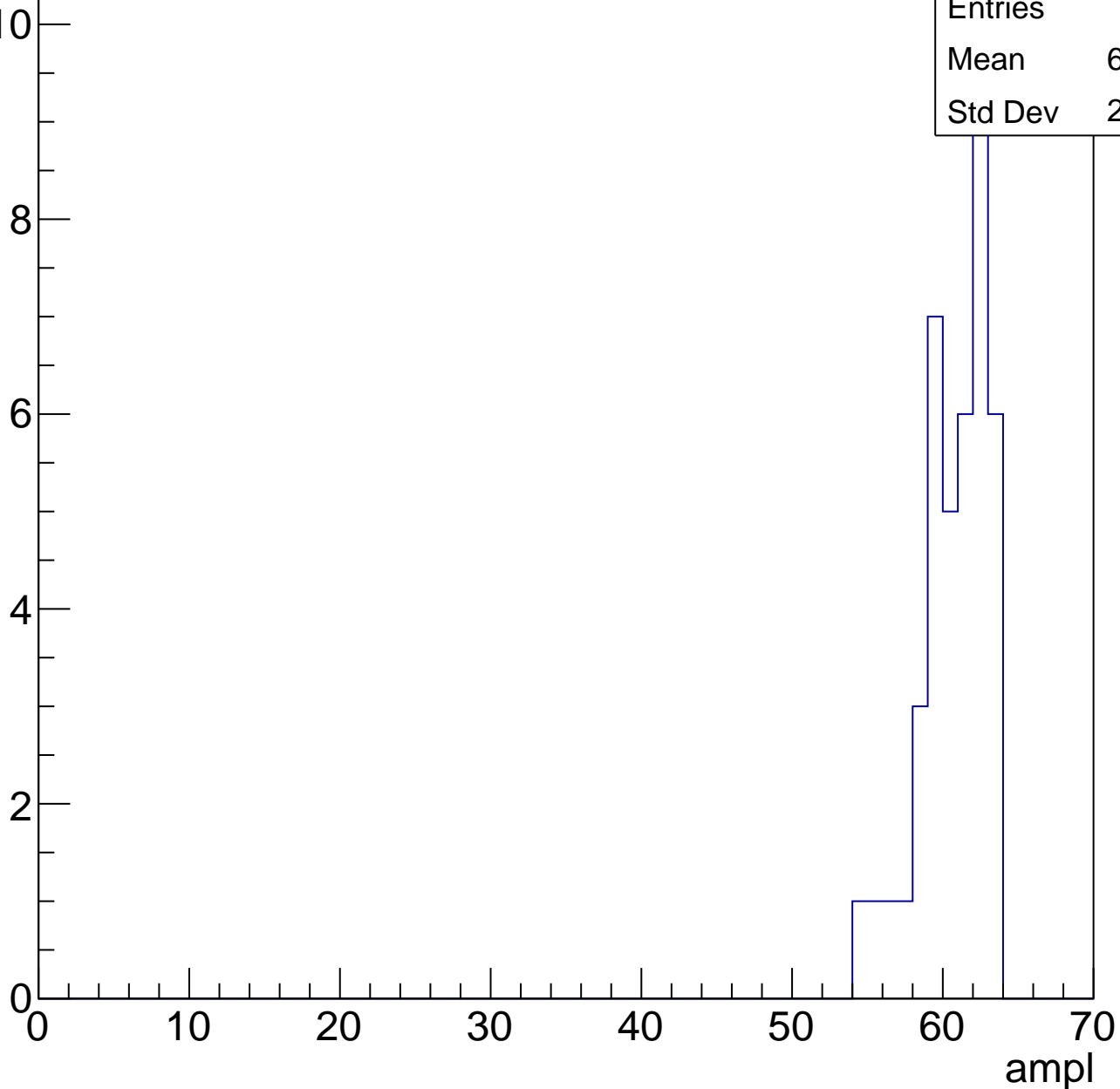


B1L102S, U19-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

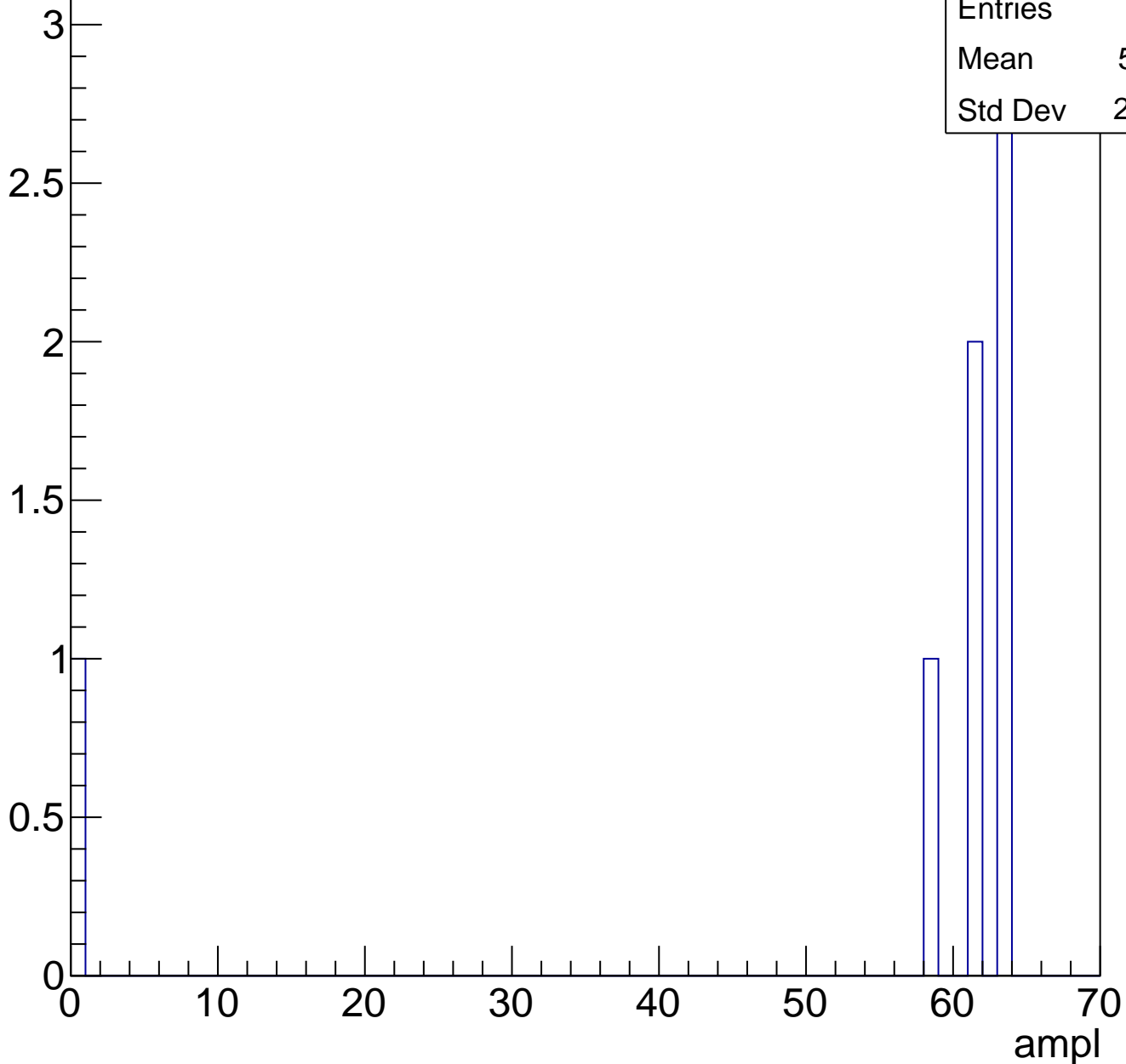
Entries	41
Mean	60.32
Std Dev	2.213



B1L102S, U19-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch68, adc0

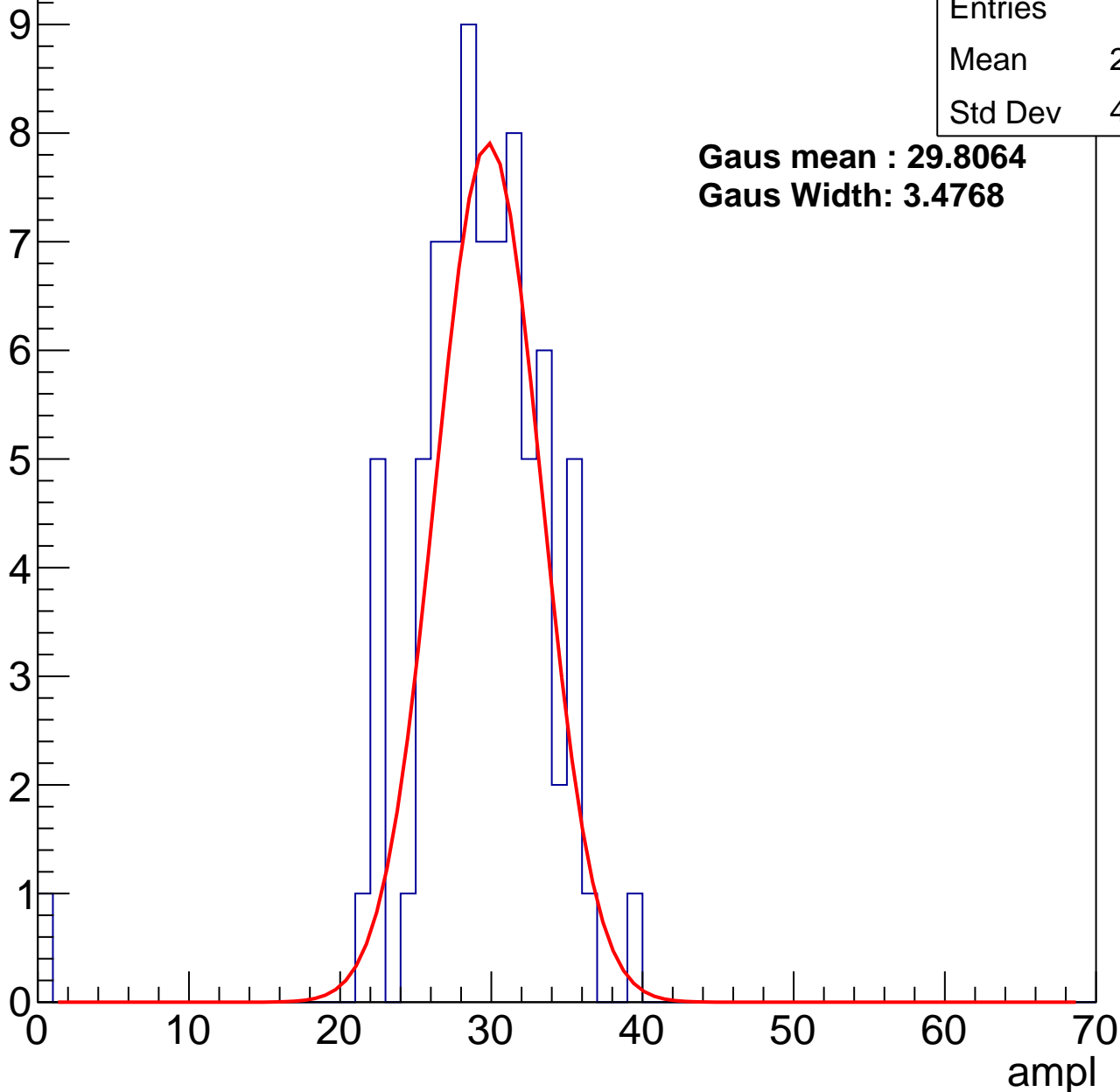
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.72
Std Dev	4.946

Gaus mean : 29.8064

Gaus Width: 3.4768



B1L102S, U19-ch68, adc1

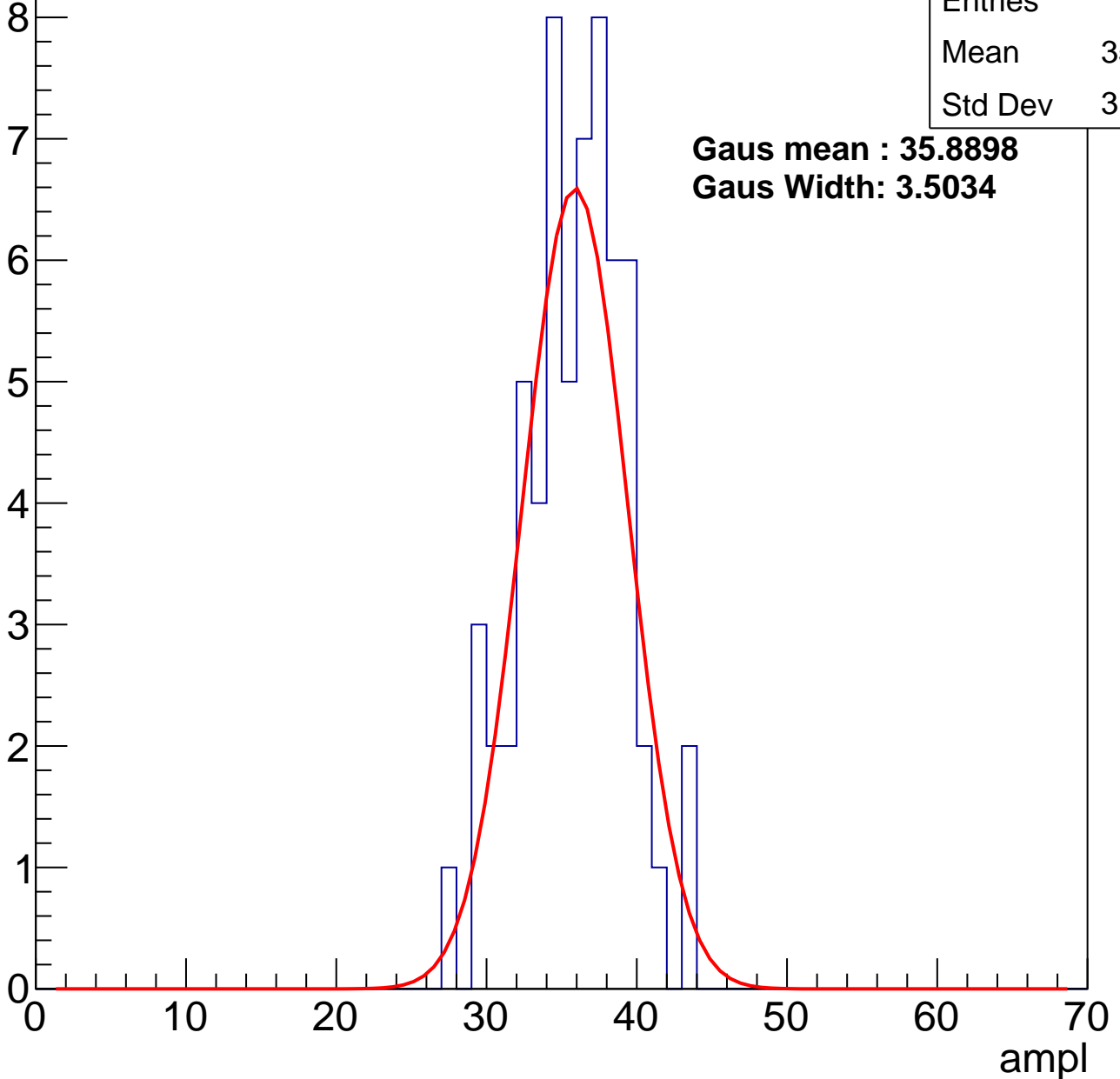
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.35
Std Dev	3.403

Gaus mean : 35.8898

Gaus Width: 3.5034



B1L102S, U19-ch68, adc2

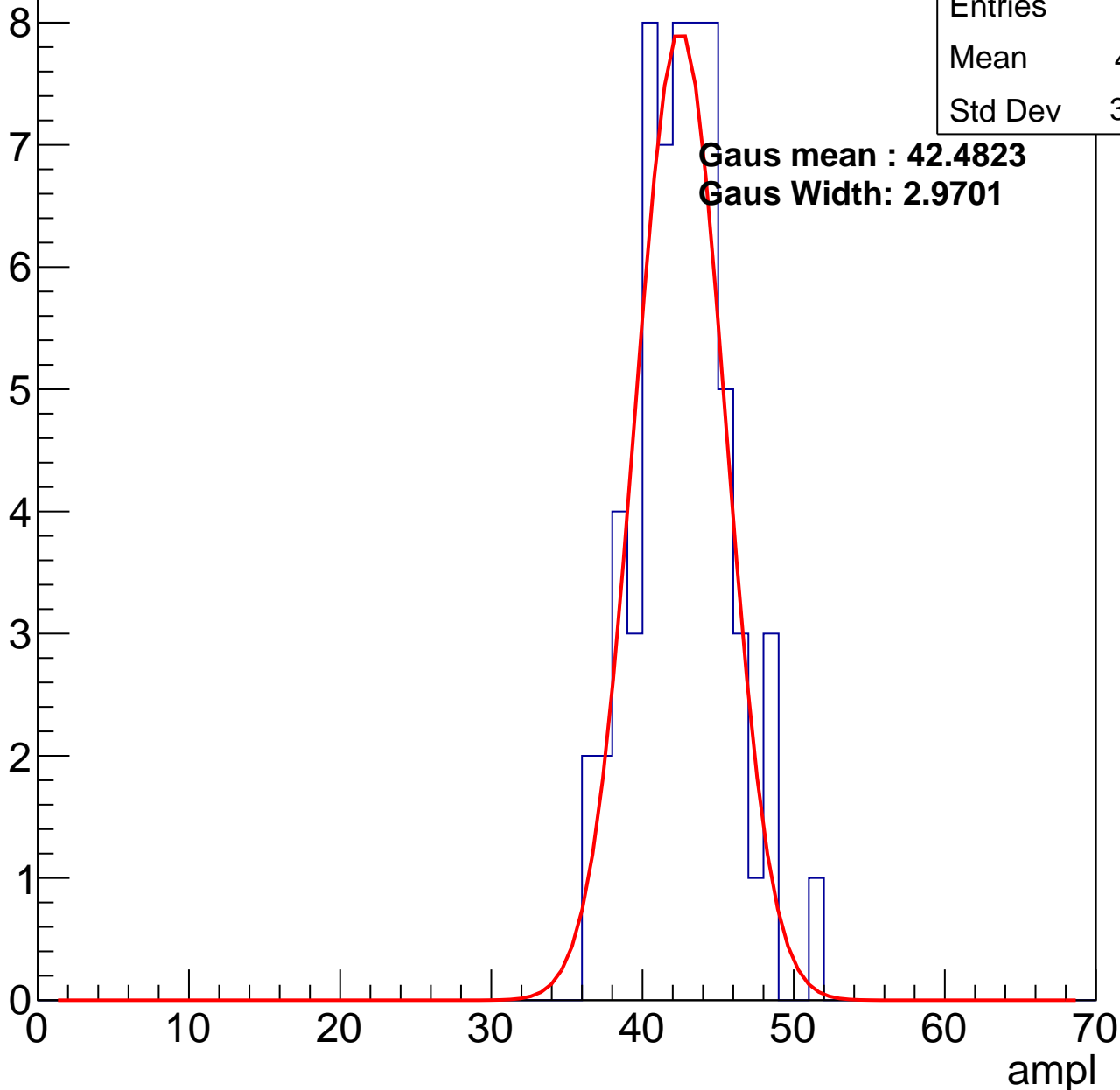
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.21
Std Dev	3.077

Gaus mean : 42.4823

Gaus Width: 2.9701

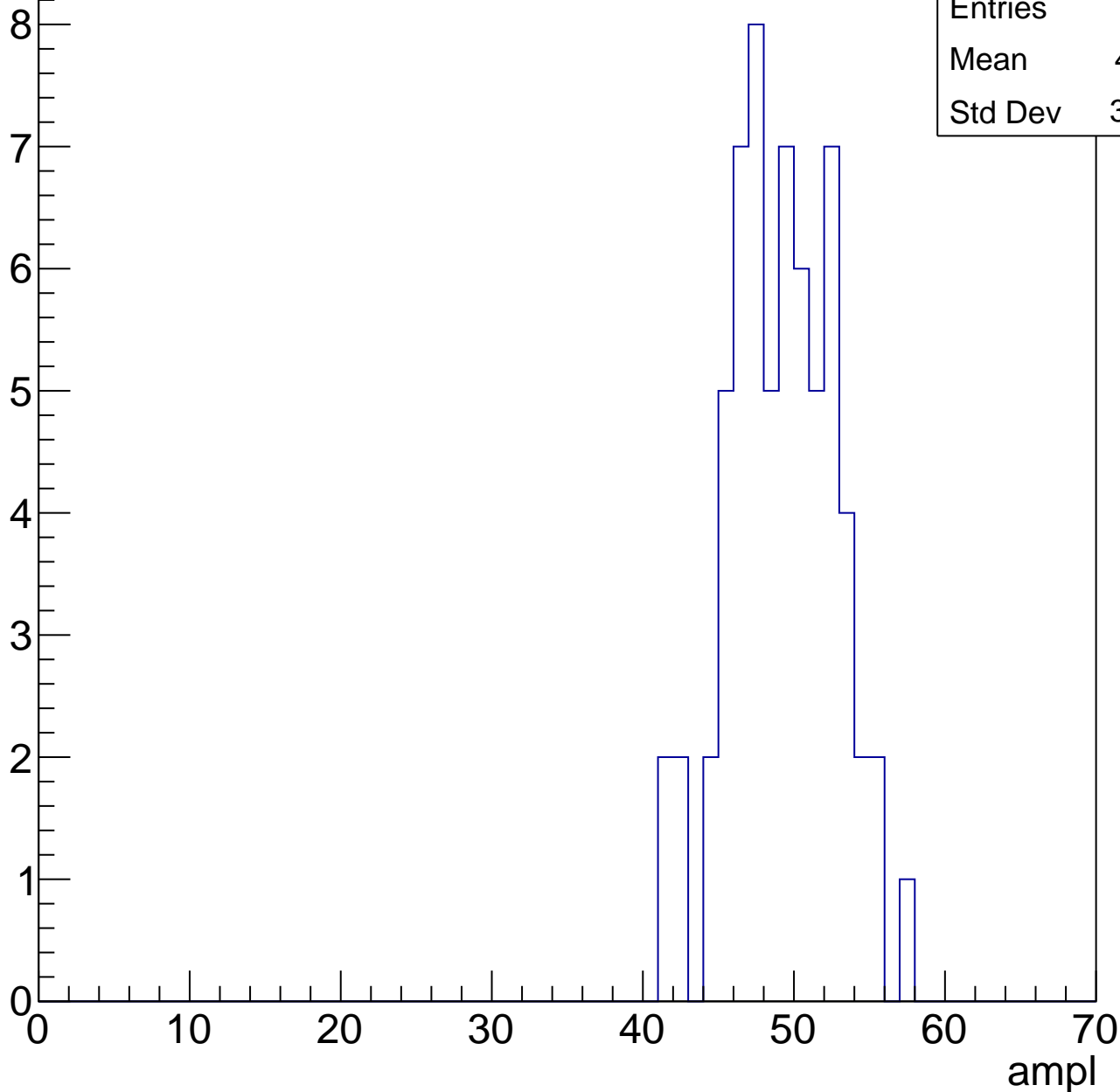


B1L102S, U19-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.71
Std Dev	3.485

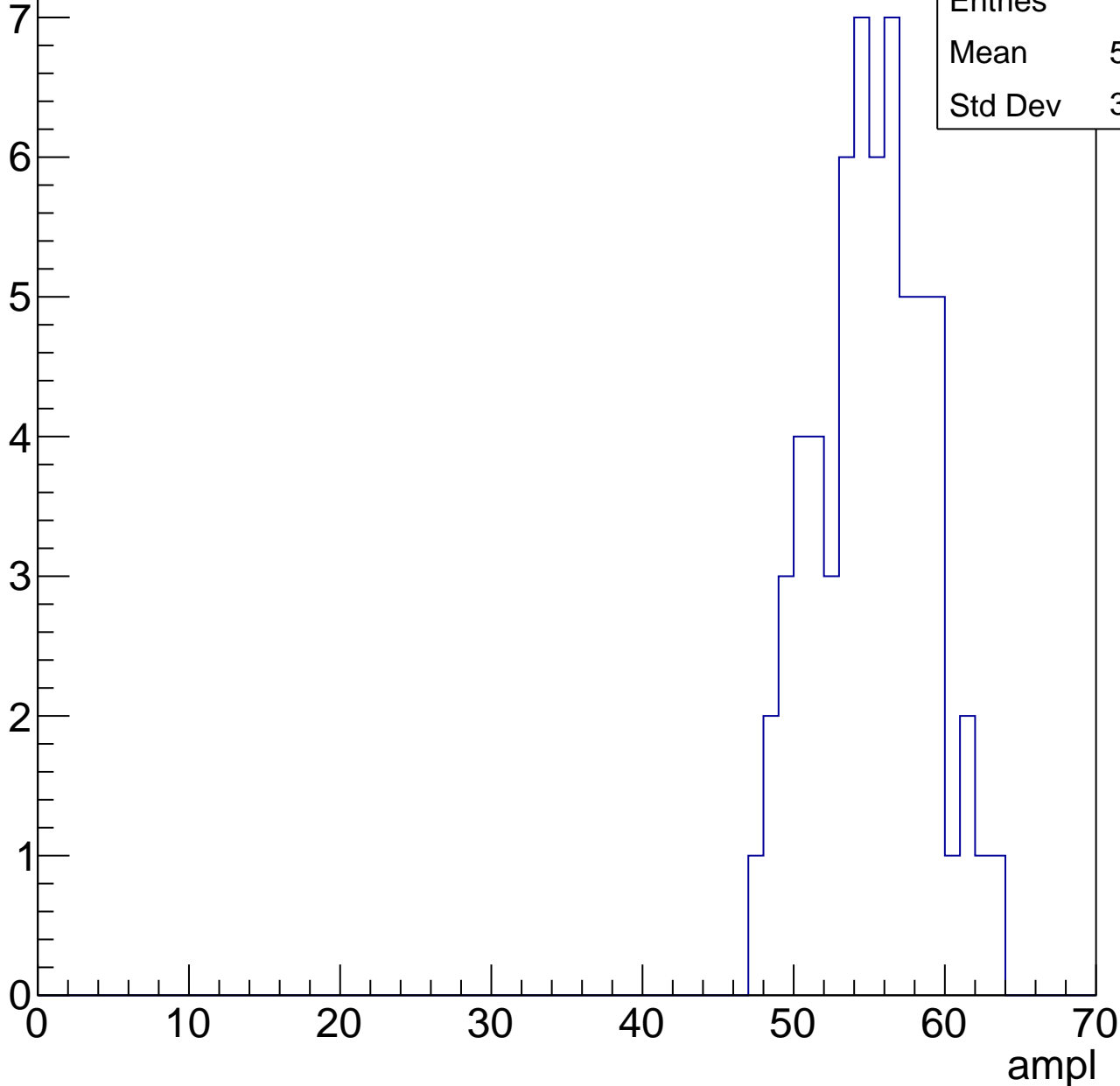


B1L102S, U19-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.68
Std Dev	3.668

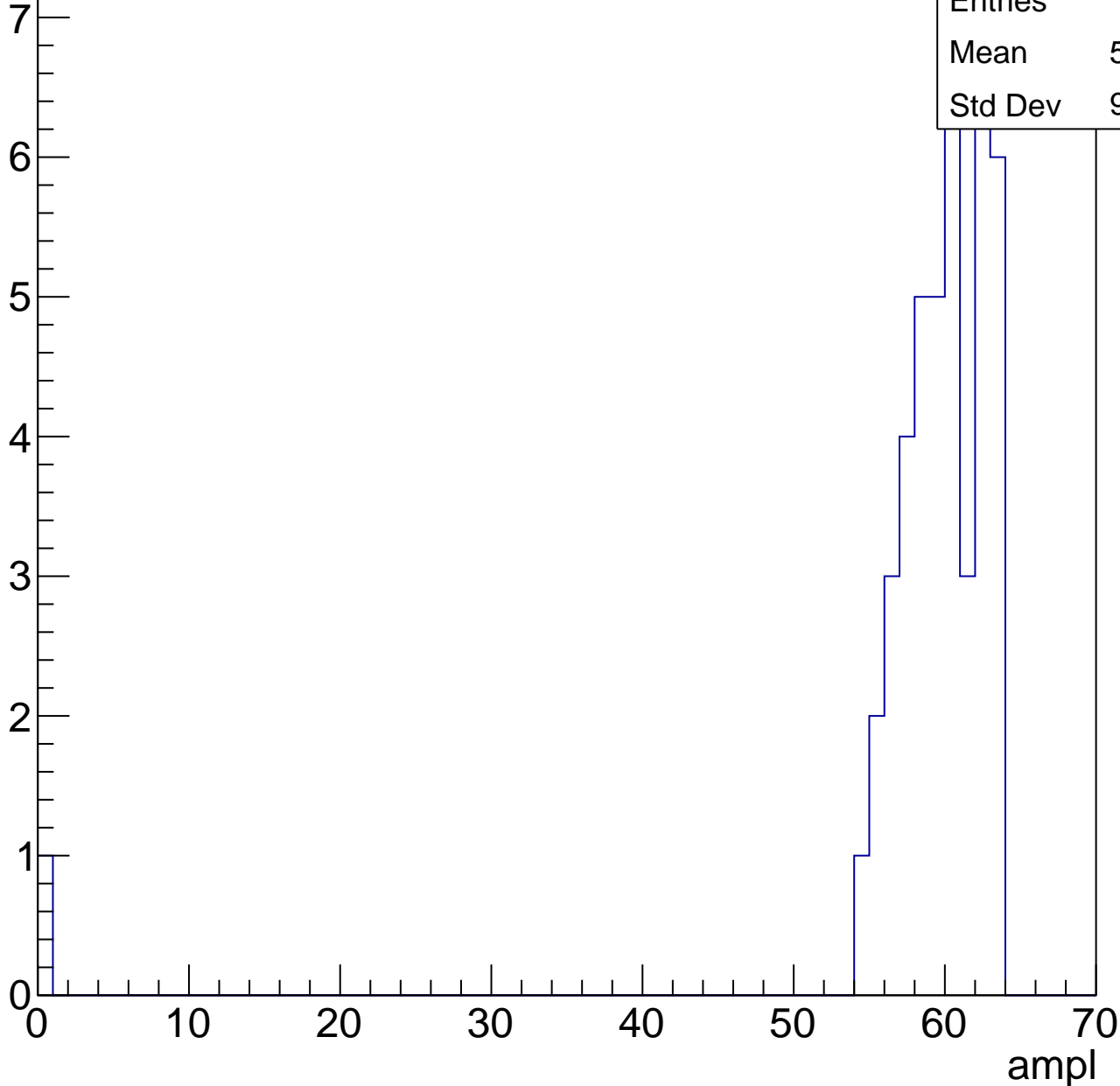


B1L102S, U19-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.18
Std Dev	9.213

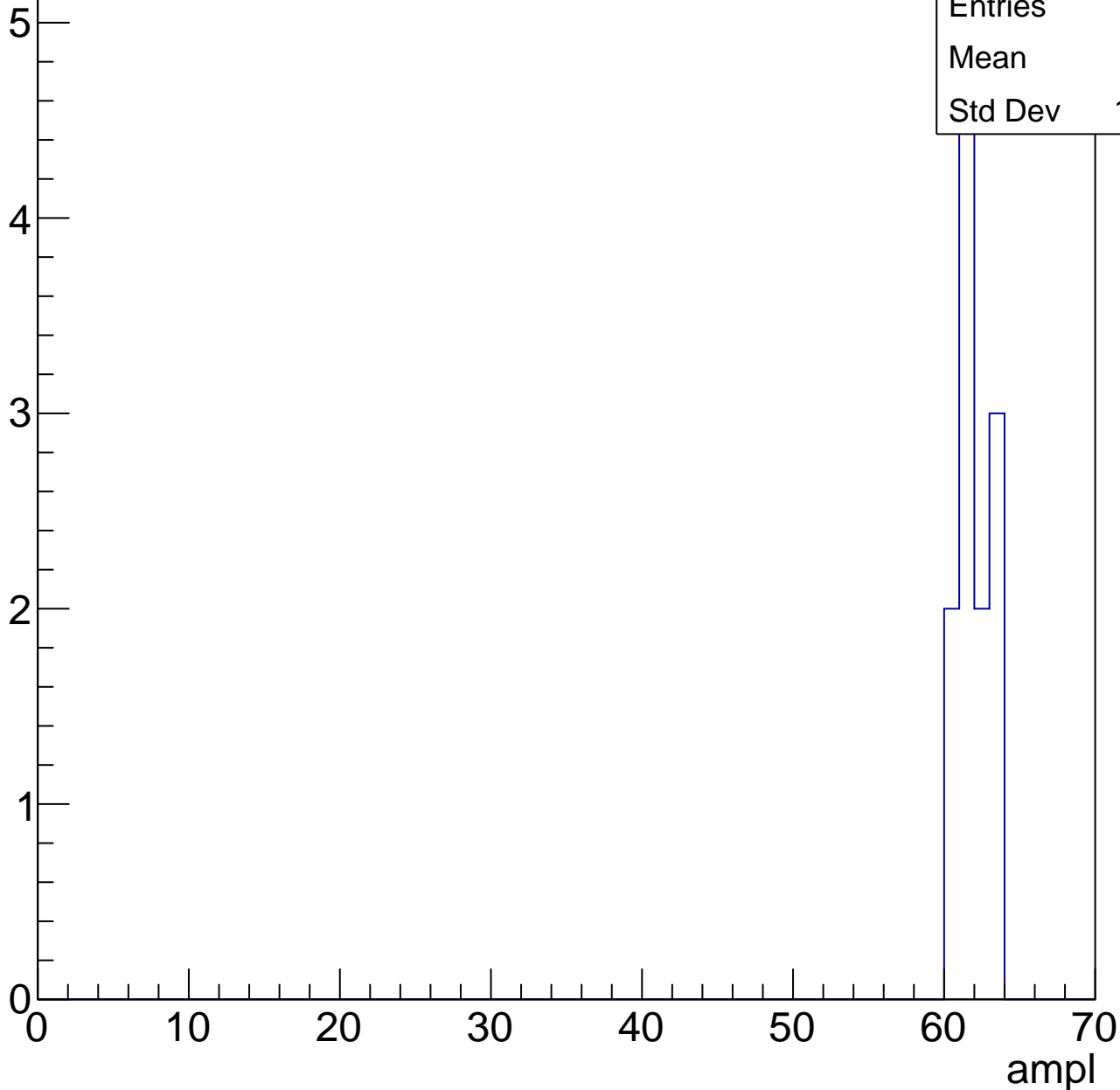


B1L102S, U19-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.5
Std Dev	1.041



B1L102S, U19-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch69, adc0

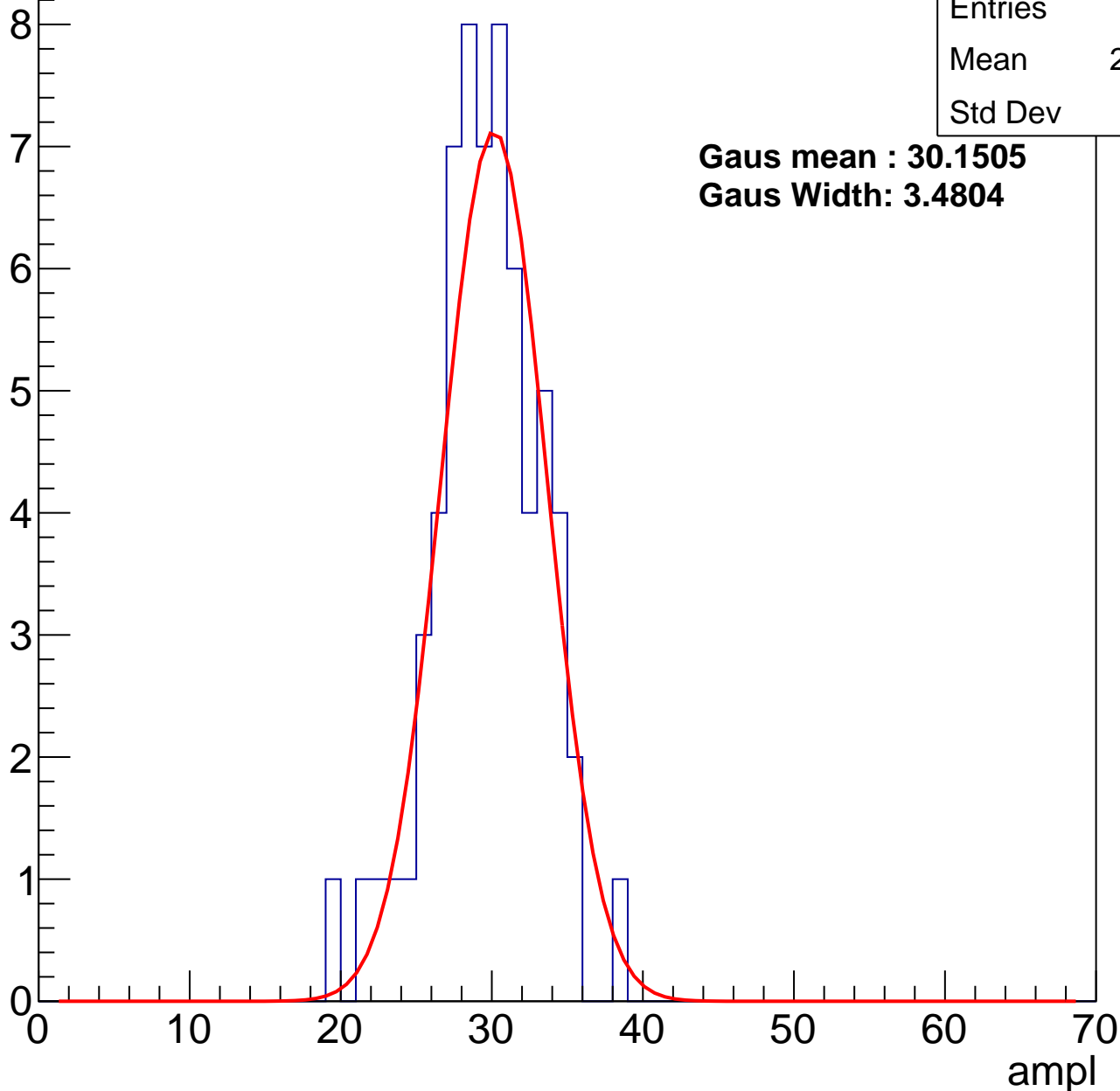
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.17
Std Dev	3.52

Gaus mean : 30.1505

Gaus Width: 3.4804



B1L102S, U19-ch69, adc1

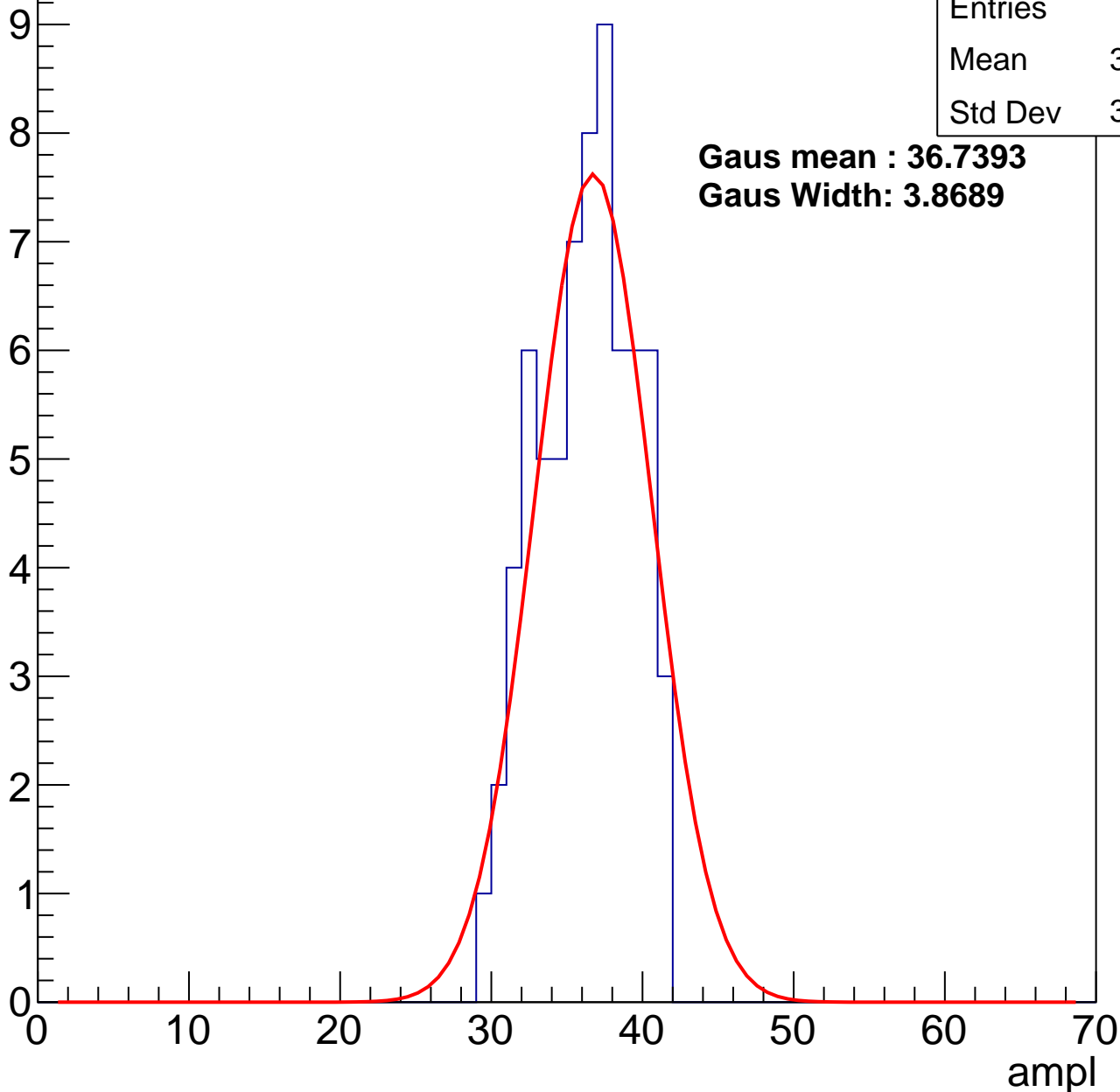
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.75
Std Dev	3.074

Gaus mean : 36.7393

Gaus Width: 3.8689



B1L102S, U19-ch69, adc2

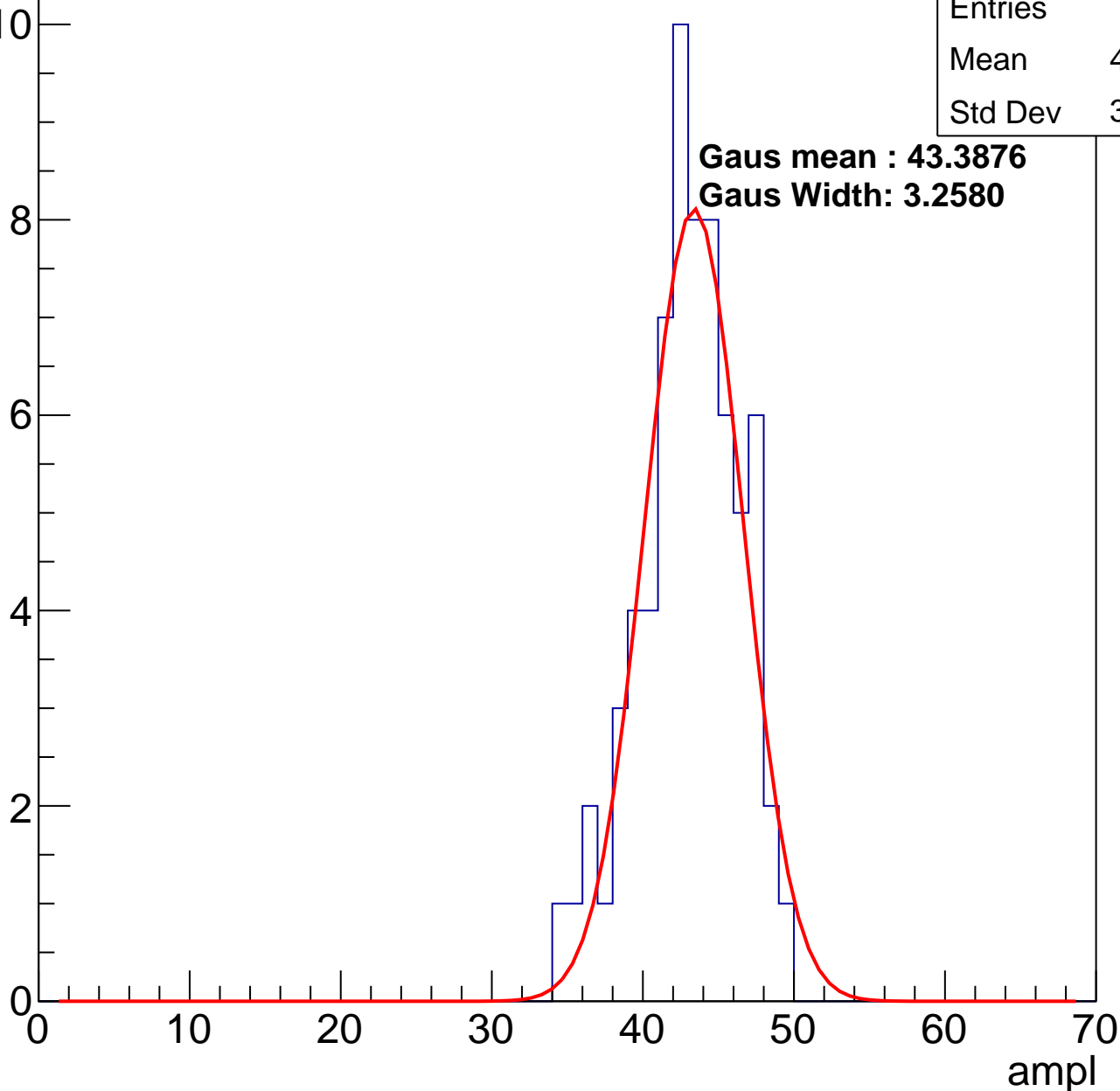
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.58
Std Dev	3.272

Gaus mean : 43.3876

Gaus Width: 3.2580

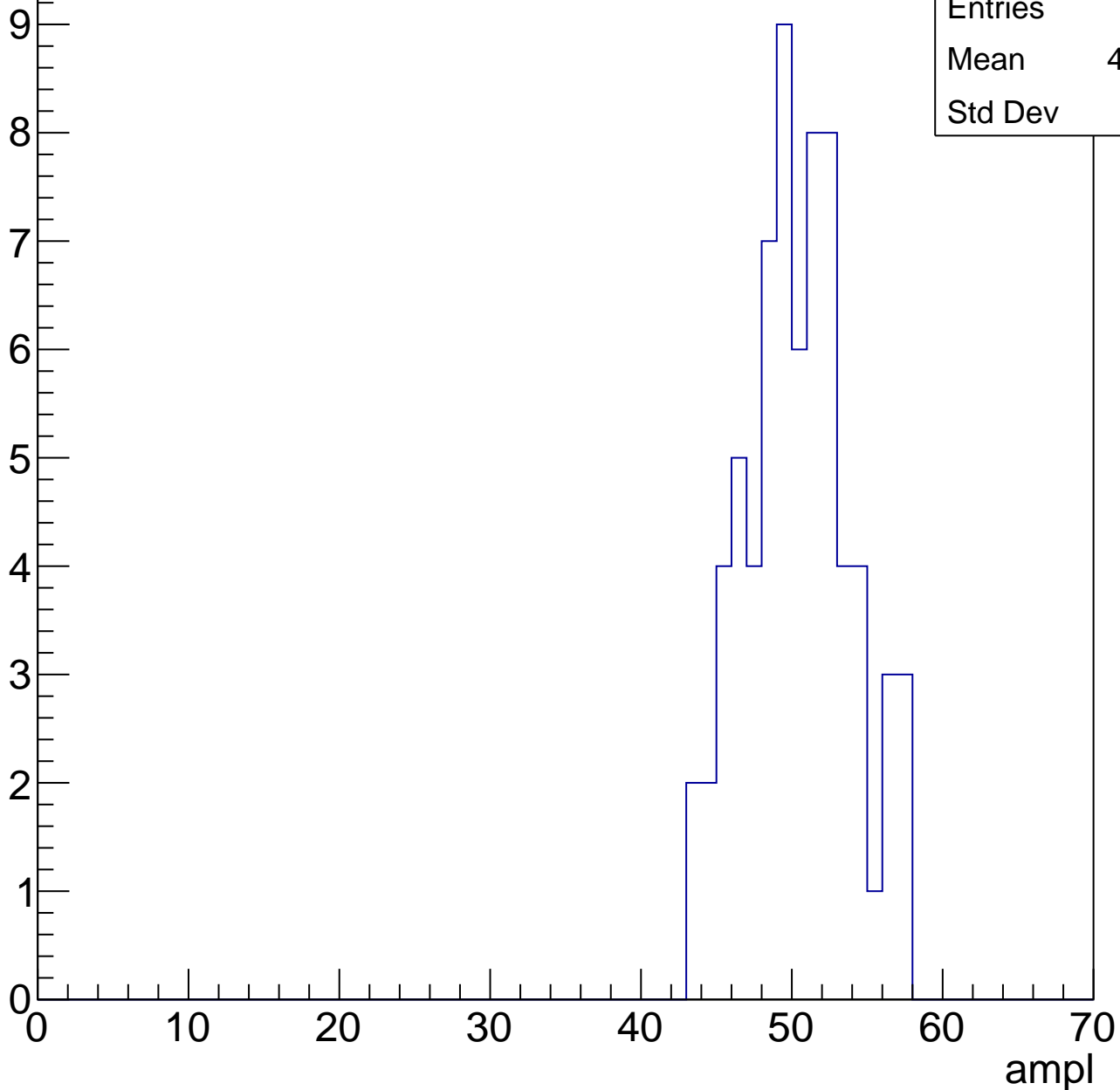


B1L102S, U19-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

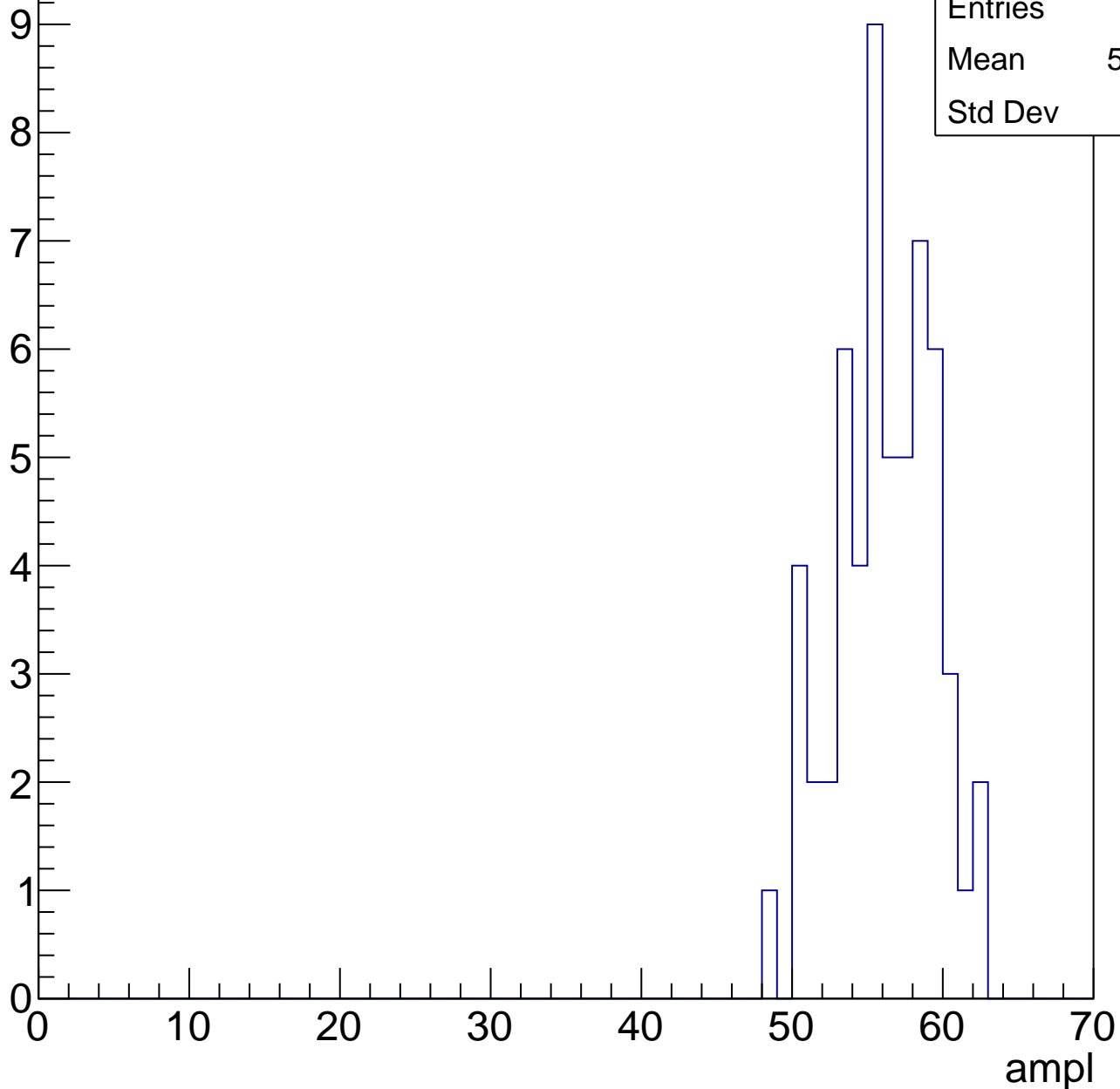
Entries	70
Mean	49.93
Std Dev	3.47



B1L102S, U19-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	57
Mean	55.67
Std Dev	3.23

B1L102S, U19-ch69, adc5

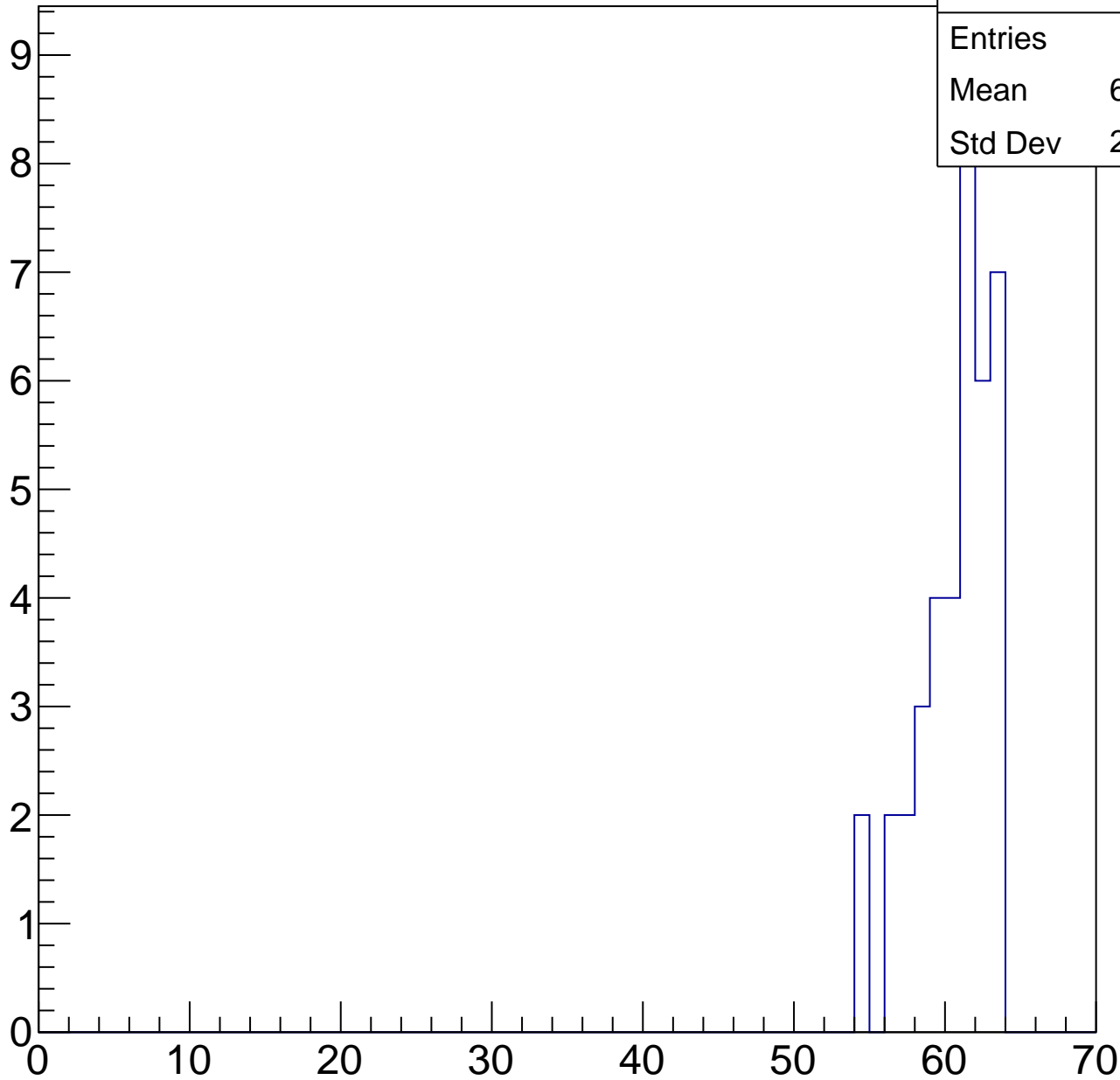
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.15
Std Dev	2.445

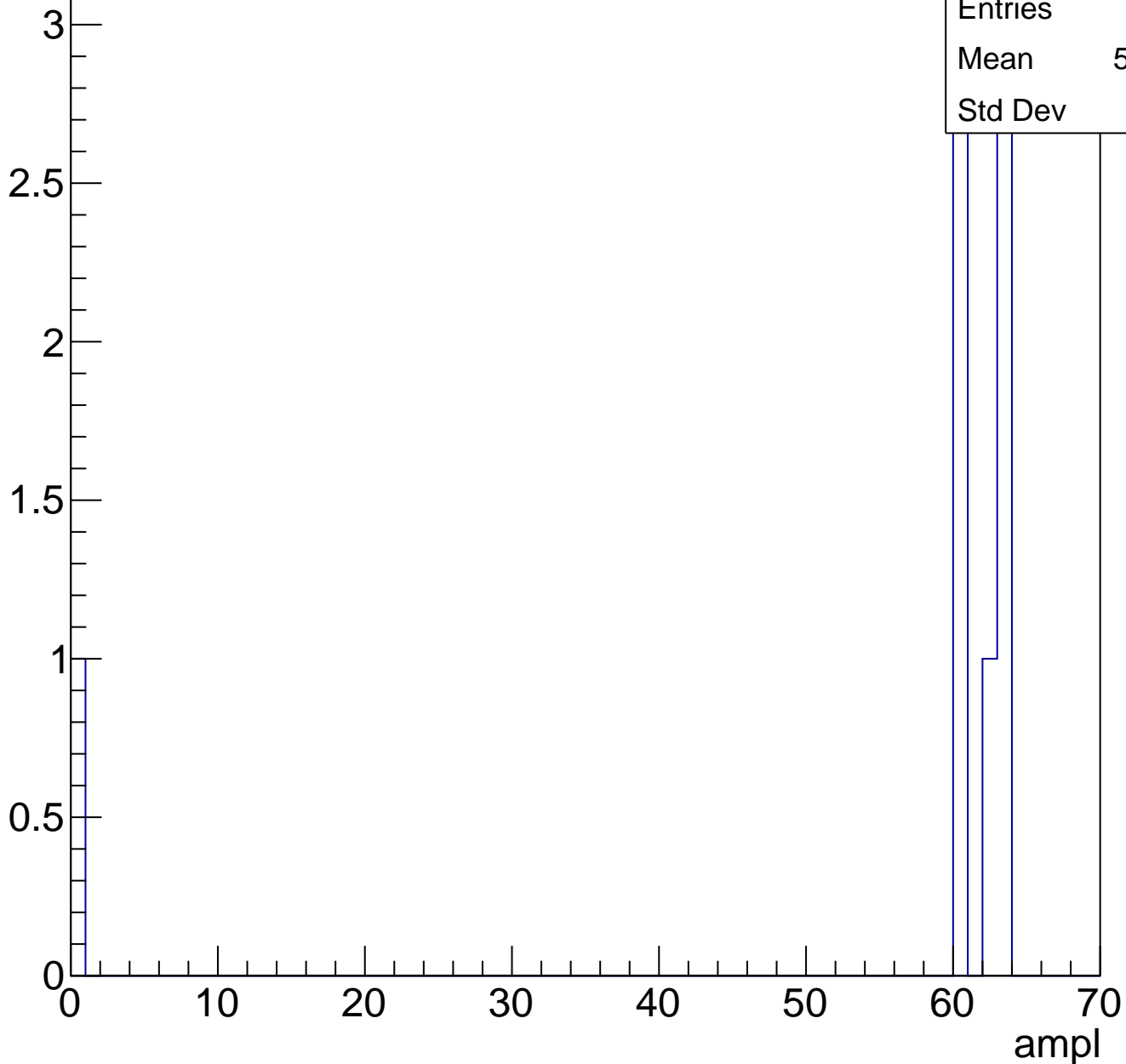
ampl



B1L102S, U19-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

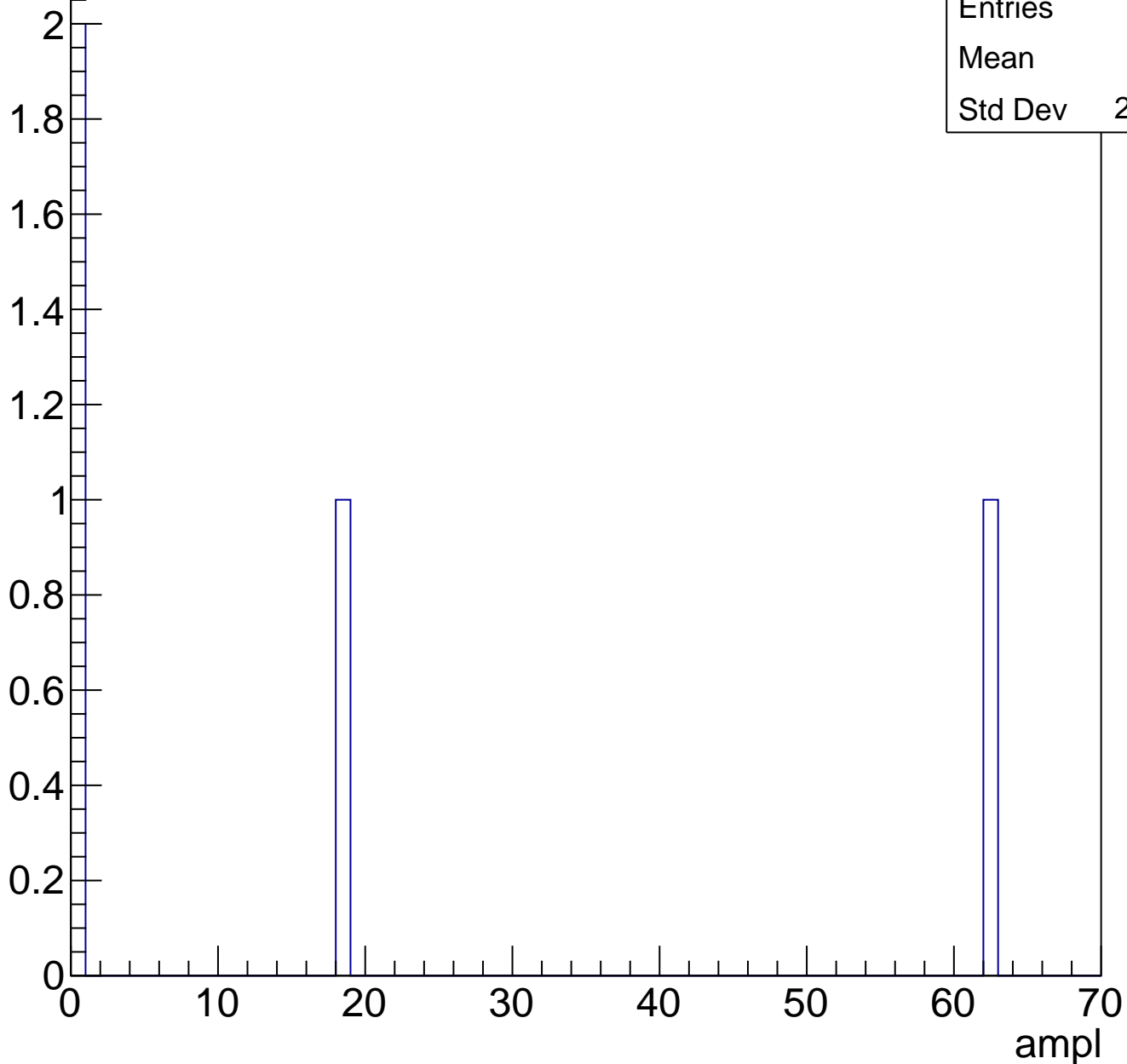


Entries	8
Mean	53.88
Std Dev	20.4

B1L102S, U19-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20
Std Dev	25.34

B1L102S, U19-ch70, adc0

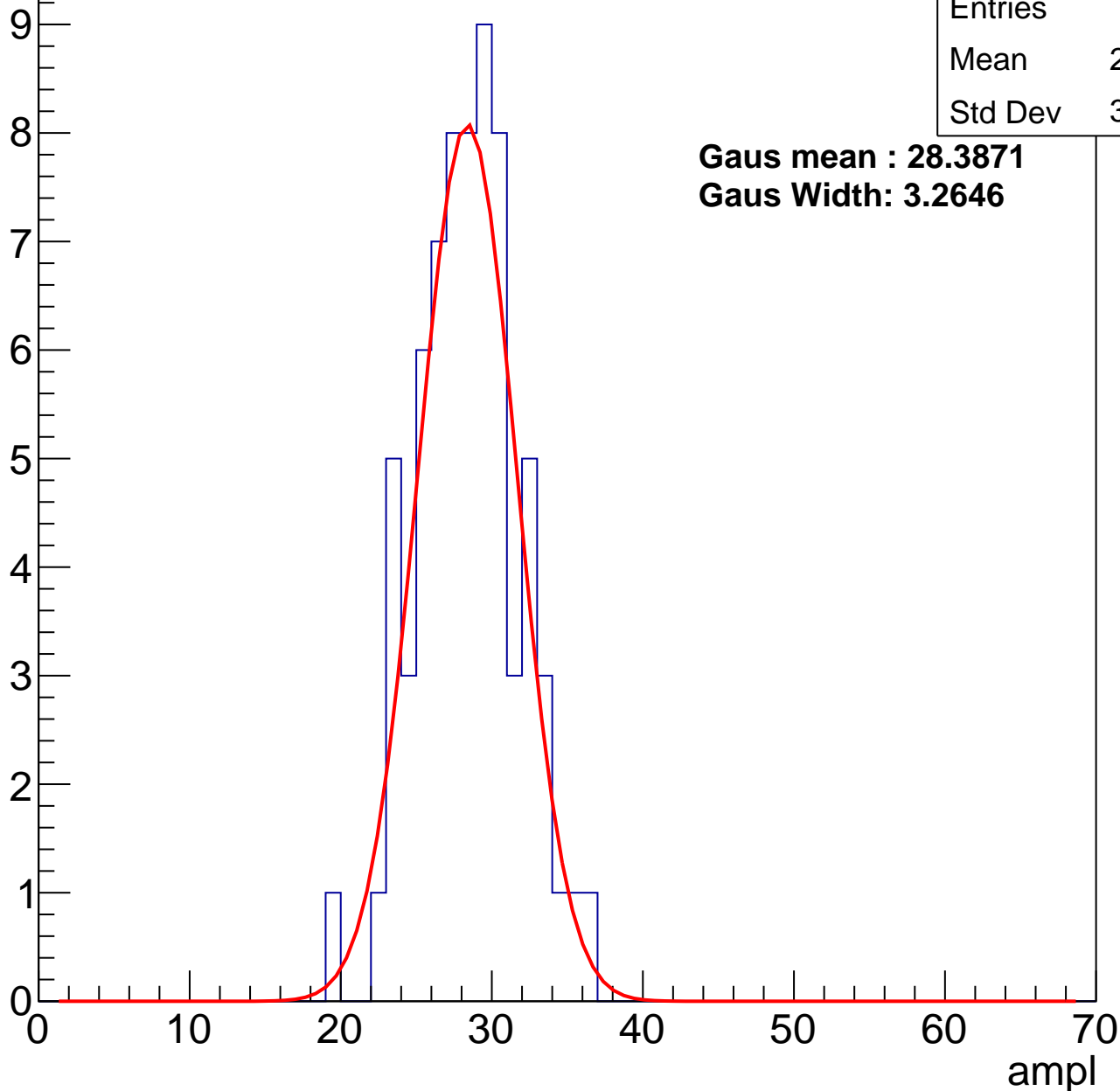
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.97
Std Dev	3.278

Gaus mean : 28.3871

Gaus Width: 3.2646



B1L102S, U19-ch70, adc1

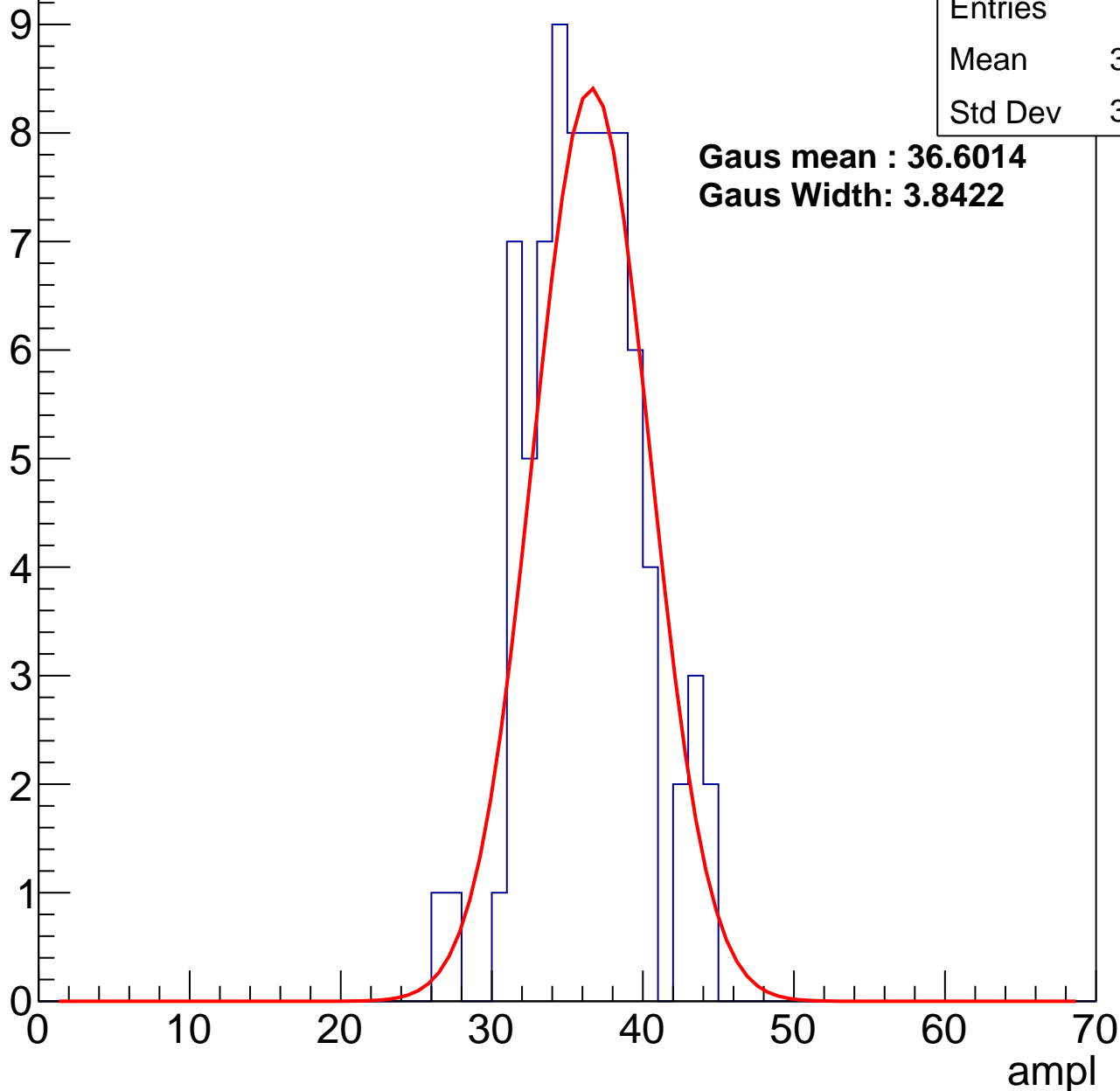
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.75
Std Dev	3.673

Gaus mean : 36.6014

Gaus Width: 3.8422



B1L102S, U19-ch70, adc2

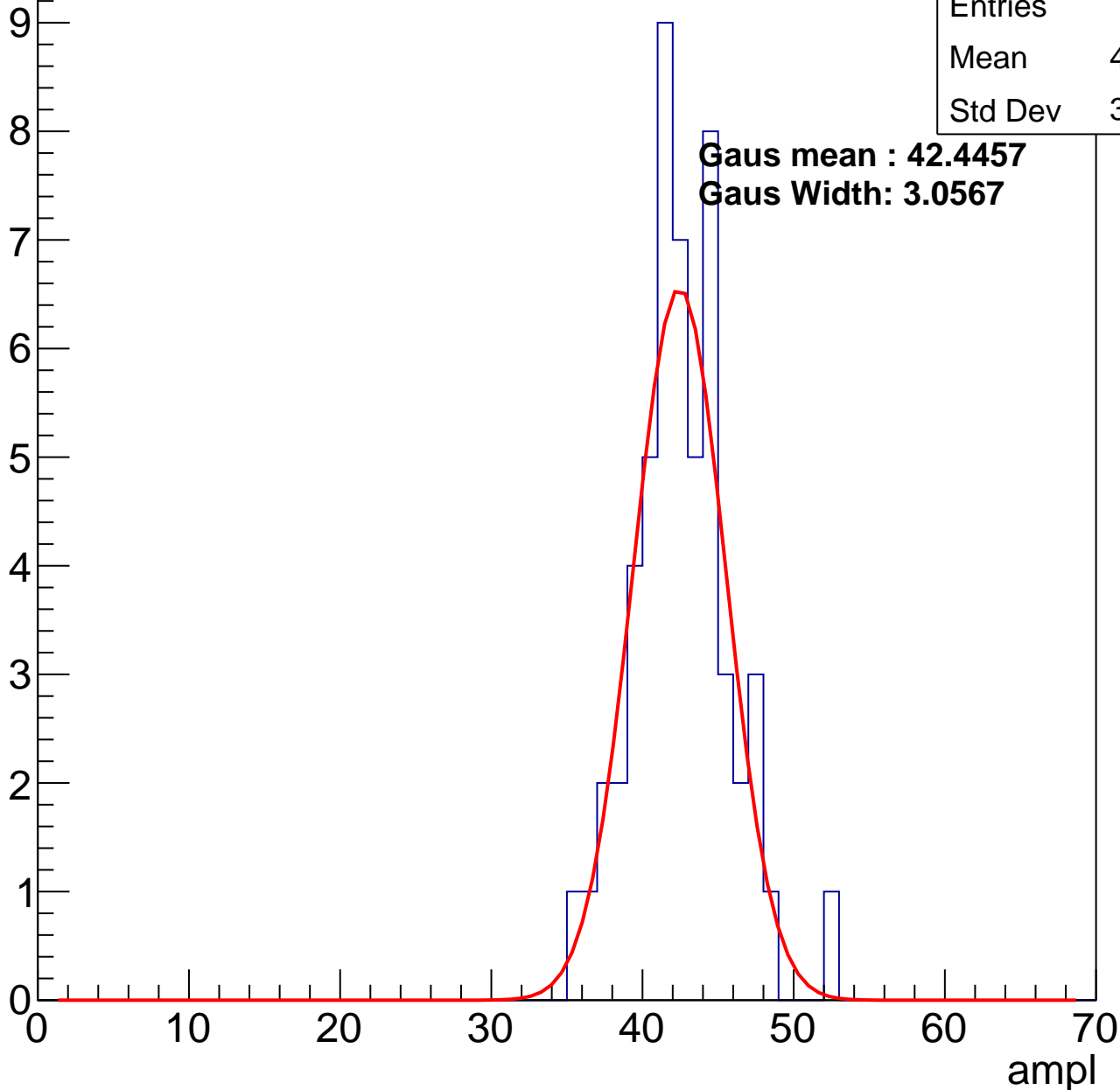
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.13
Std Dev	3.157

Gaus mean : 42.4457

Gaus Width: 3.0567

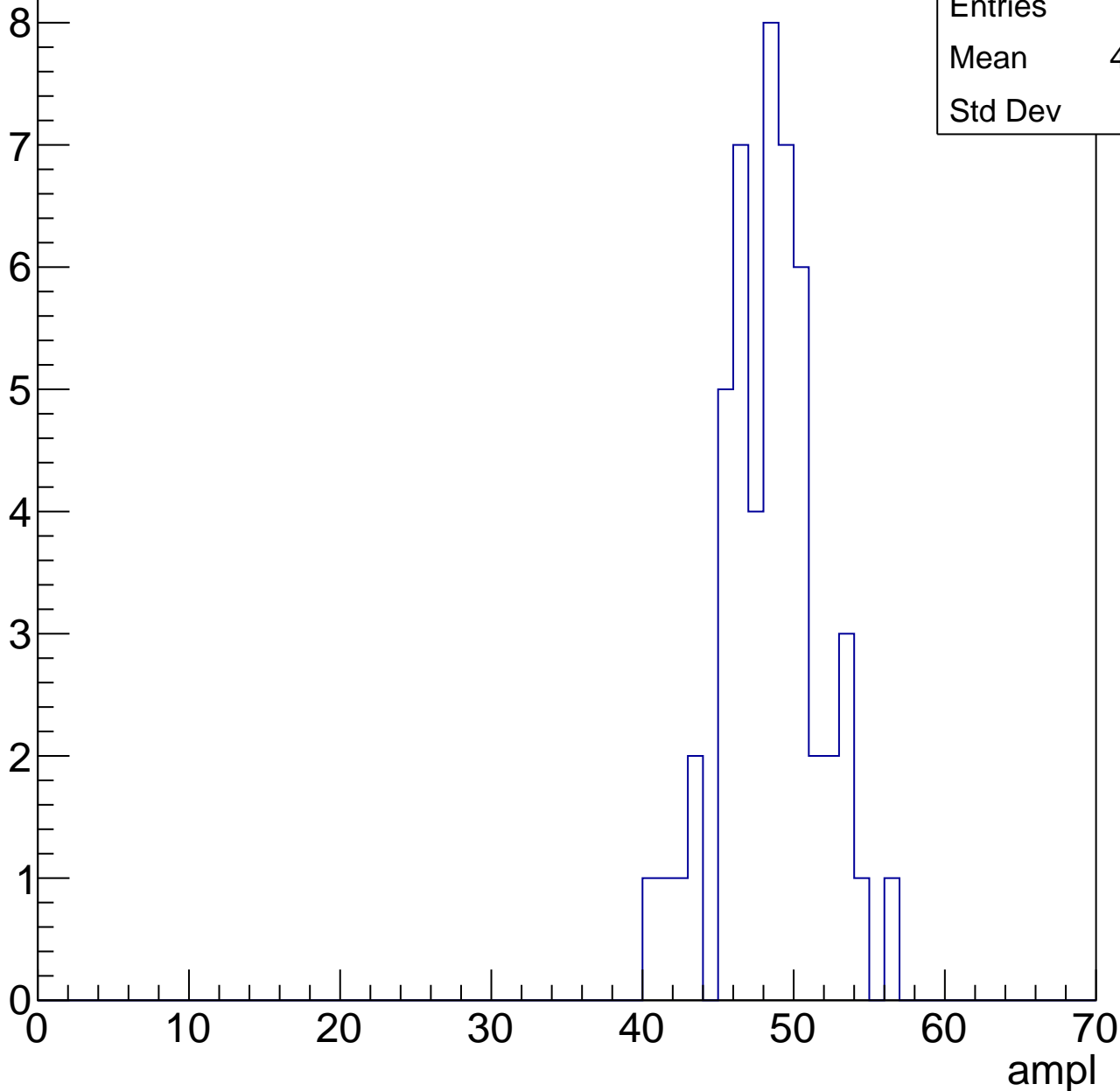


B1L102S, U19-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	47.96
Std Dev	3.23

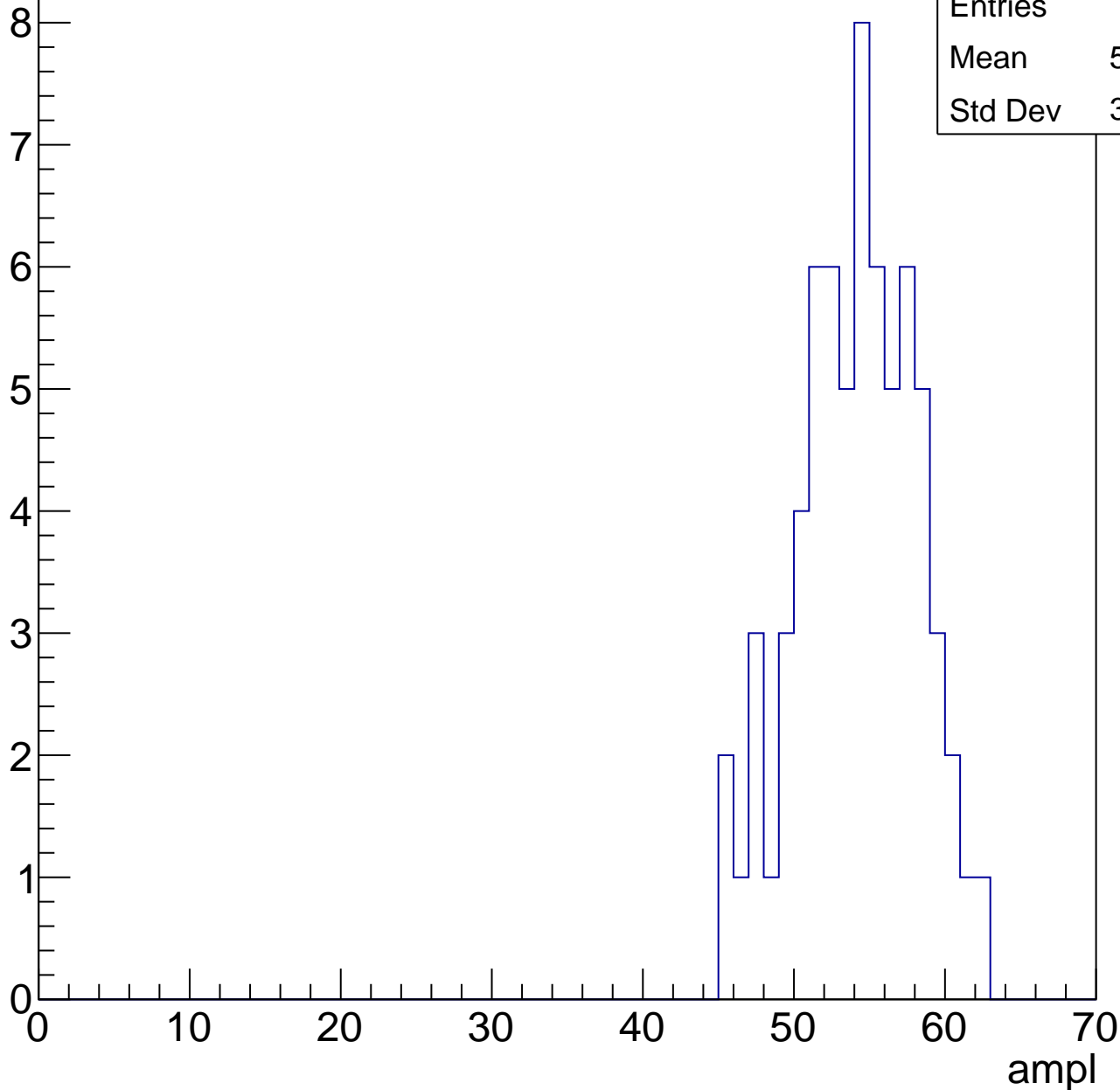


B1L102S, U19-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	53.66
Std Dev	3.917

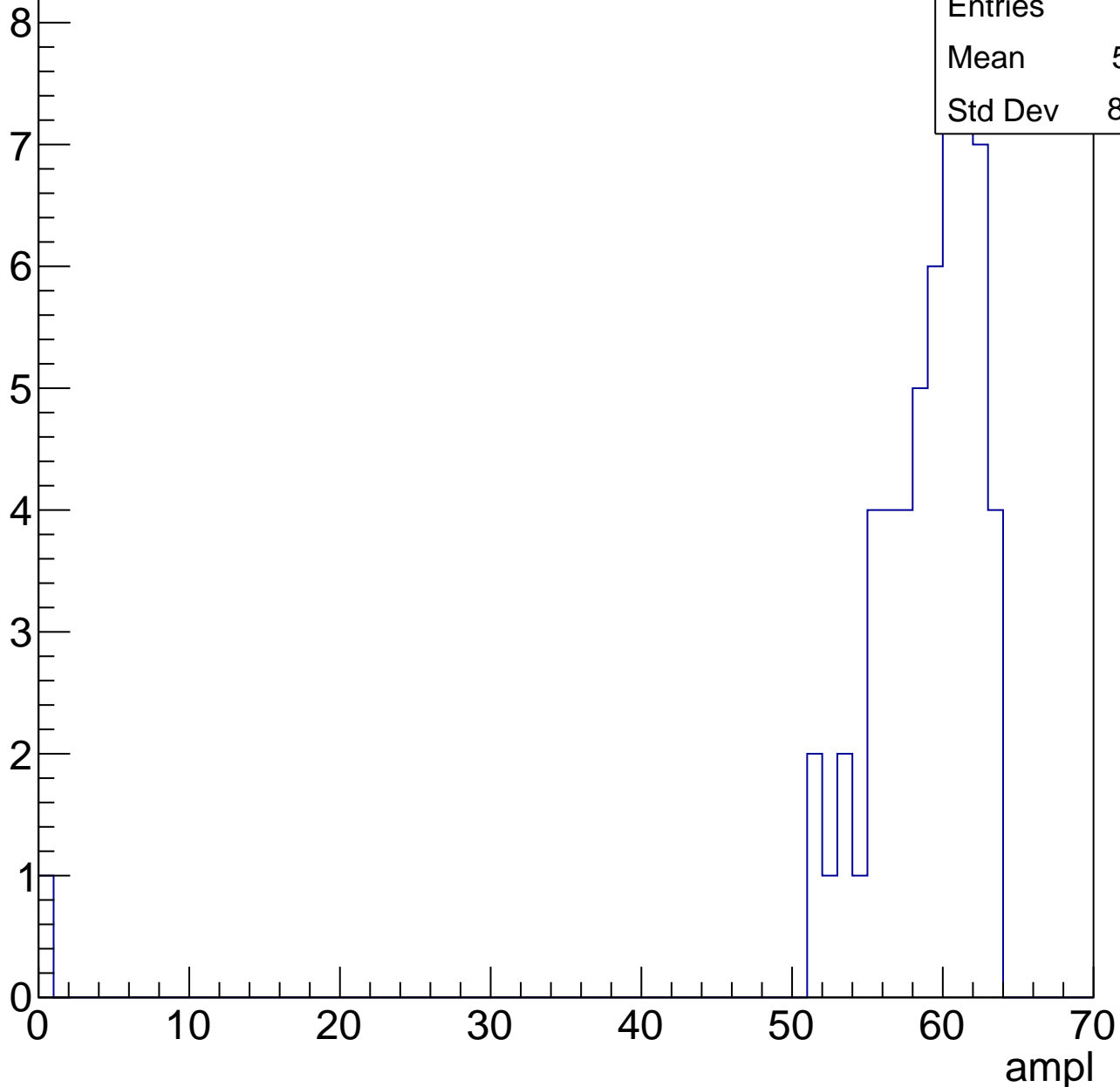


B1L102S, U19-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.61
Std Dev	8.307

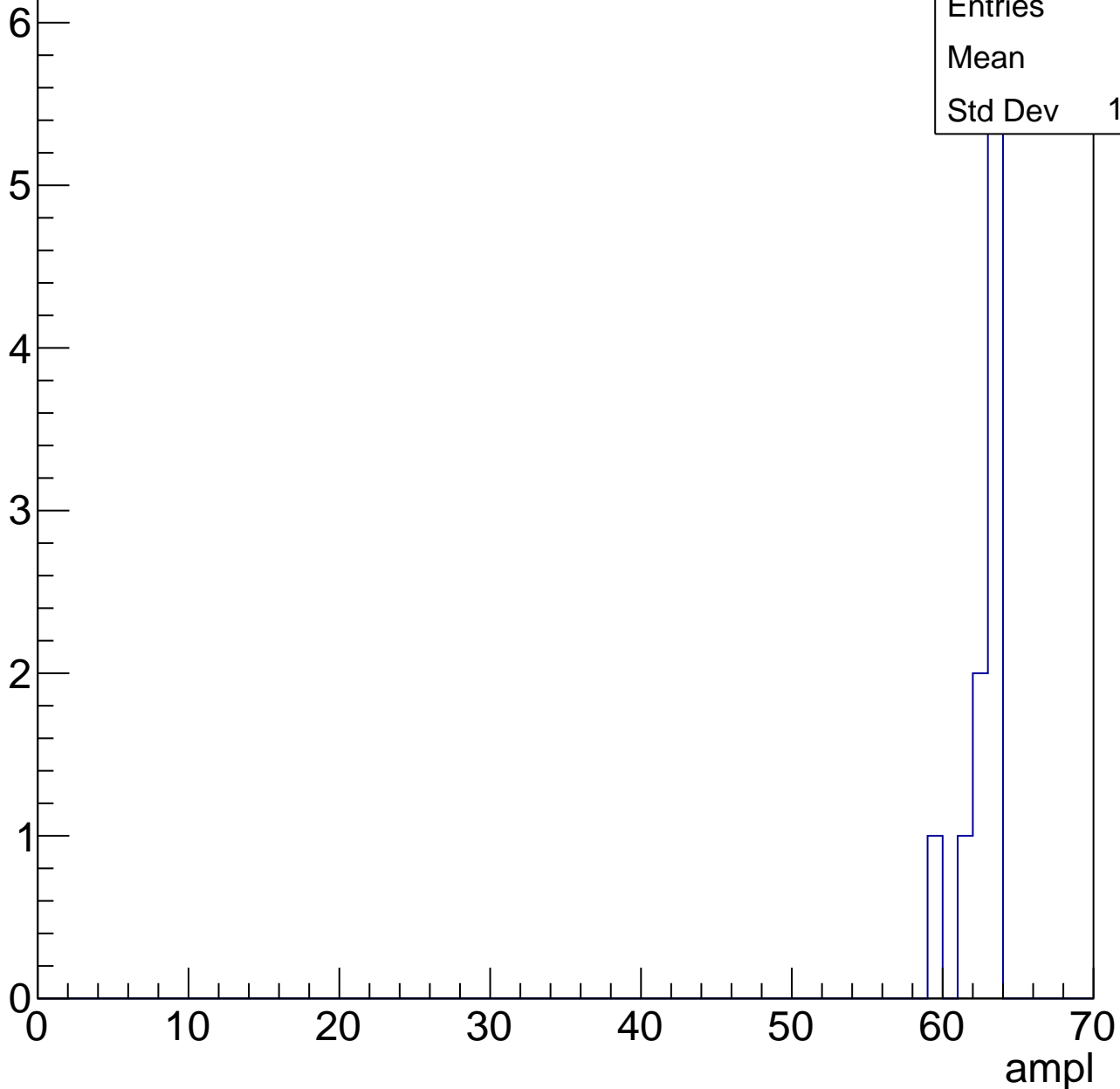


B1L102S, U19-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.2
Std Dev	1.249



B1L102S, U19-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U19-ch71, adc0

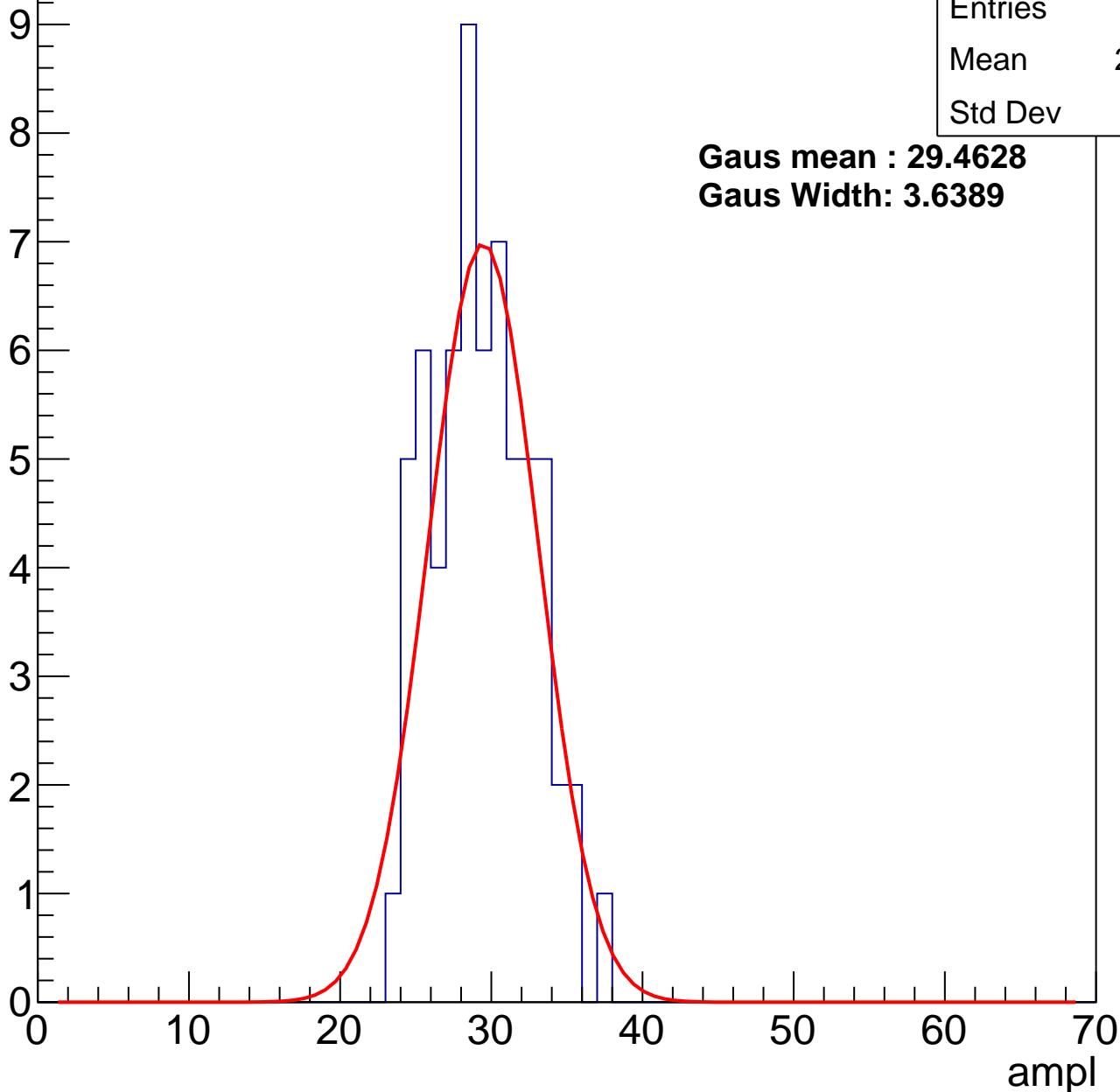
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.91
Std Dev	3.22

Gaus mean : 29.4628

Gaus Width: 3.6389



B1L102S, U19-ch71, adc1

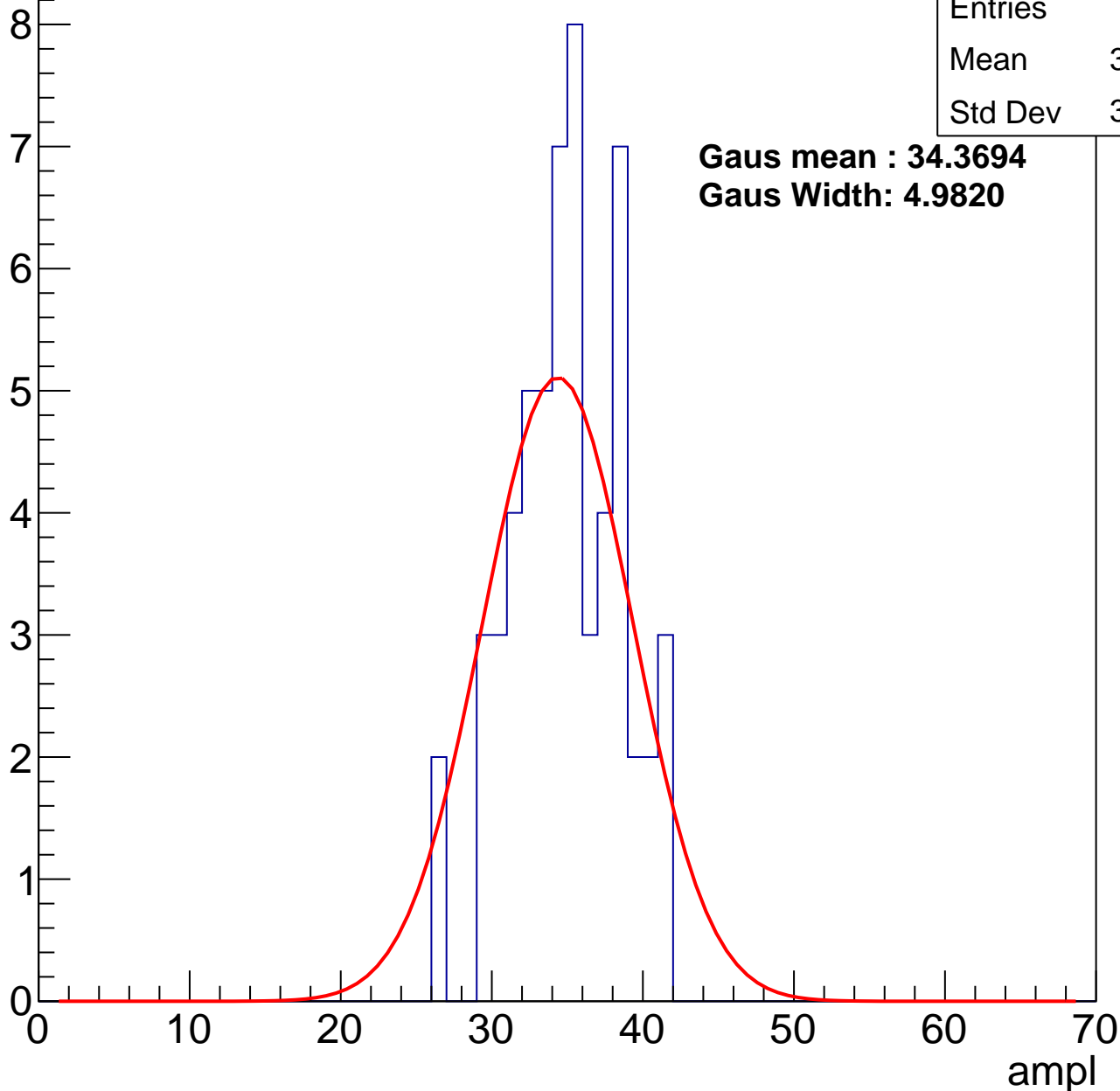
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.47
Std Dev	3.563

Gaus mean : 34.3694

Gaus Width: 4.9820



B1L102S, U19-ch71, adc2

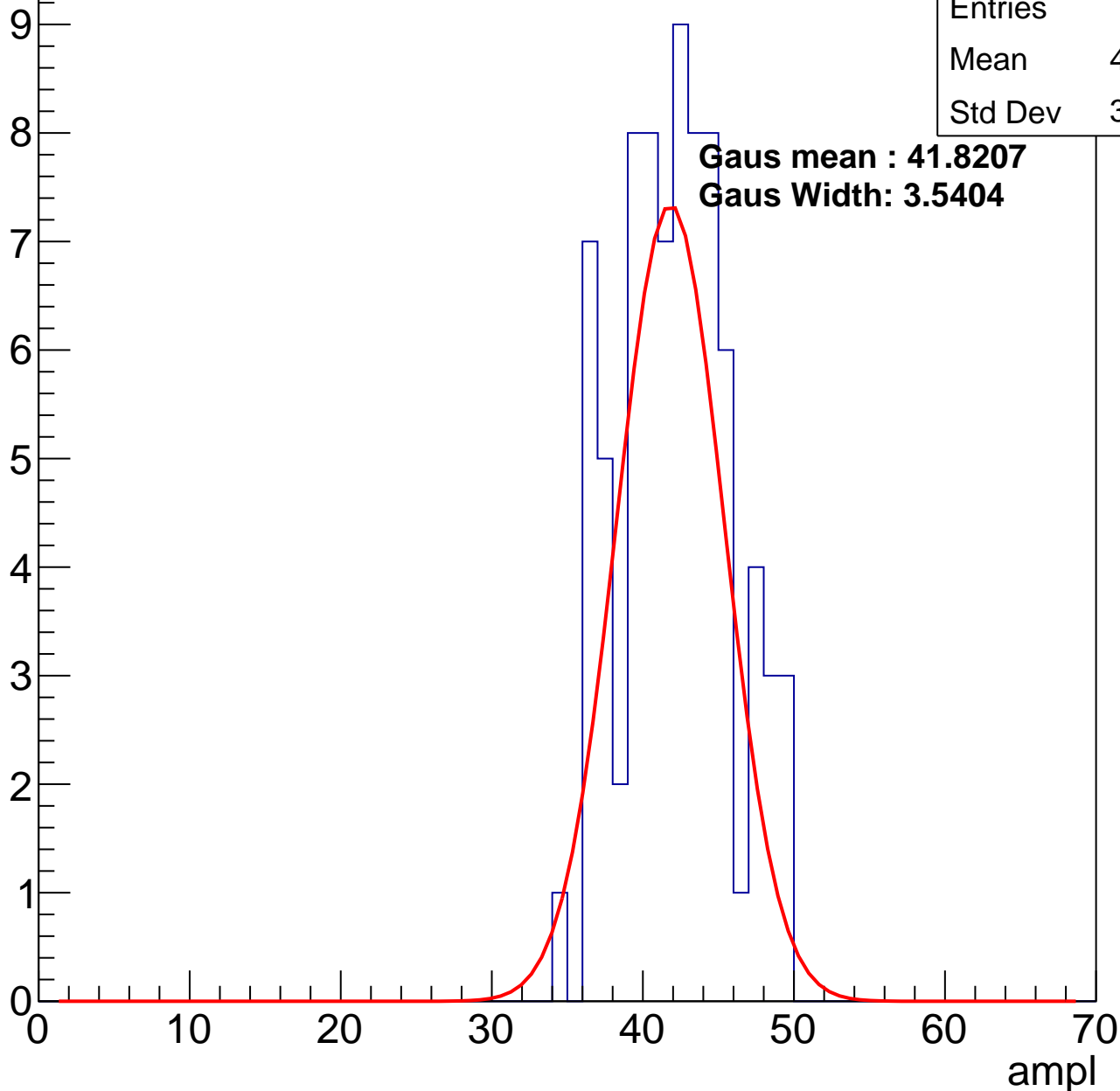
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.69
Std Dev	3.608

Gaus mean : 41.8207

Gaus Width: 3.5404



B1L102S, U19-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	49.41
Std Dev	4.027

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

2

4

6

8

10

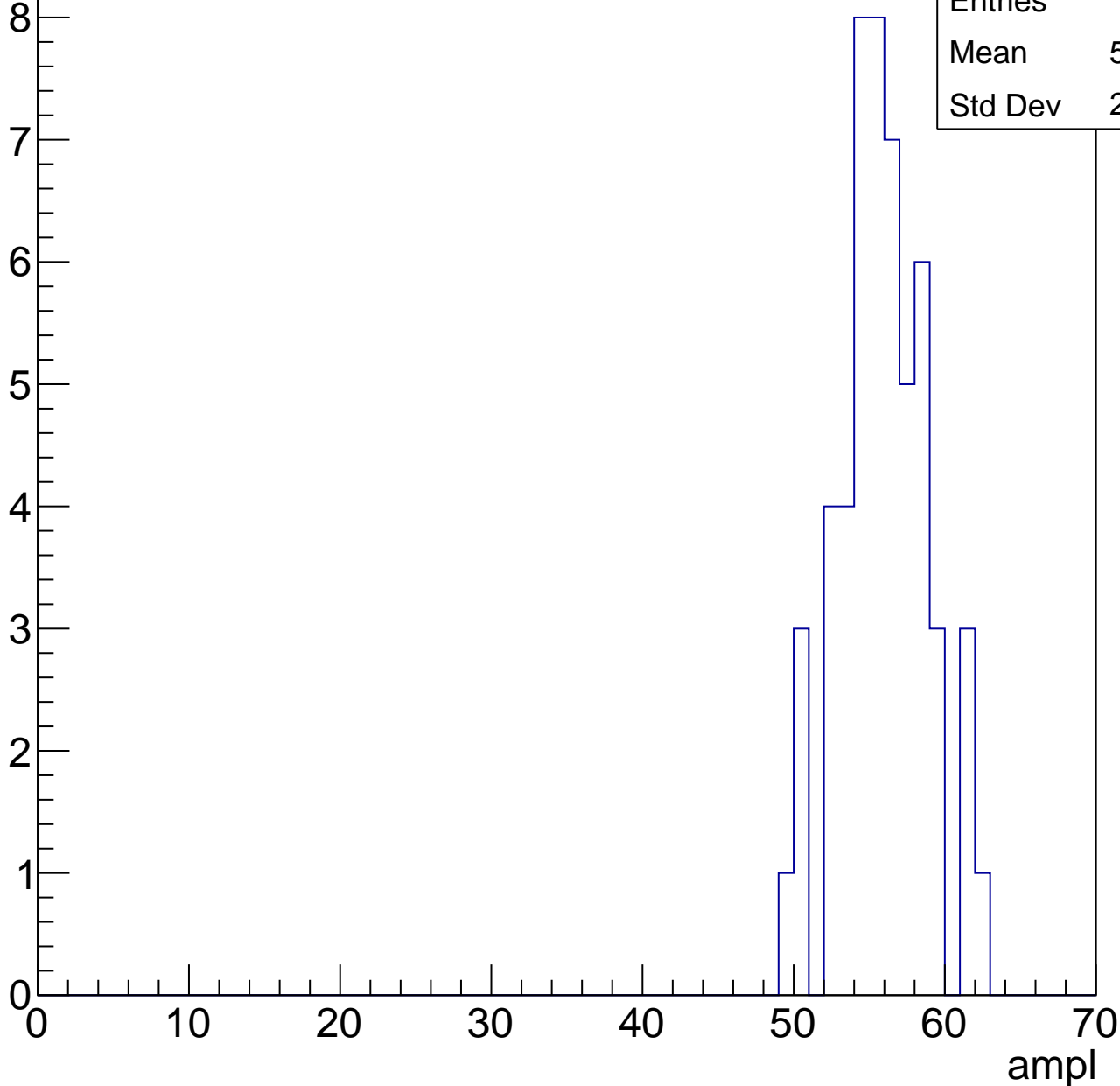
10

B1L102S, U19-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.43
Std Dev	2.904



B1L102S, U19-ch71, adc5

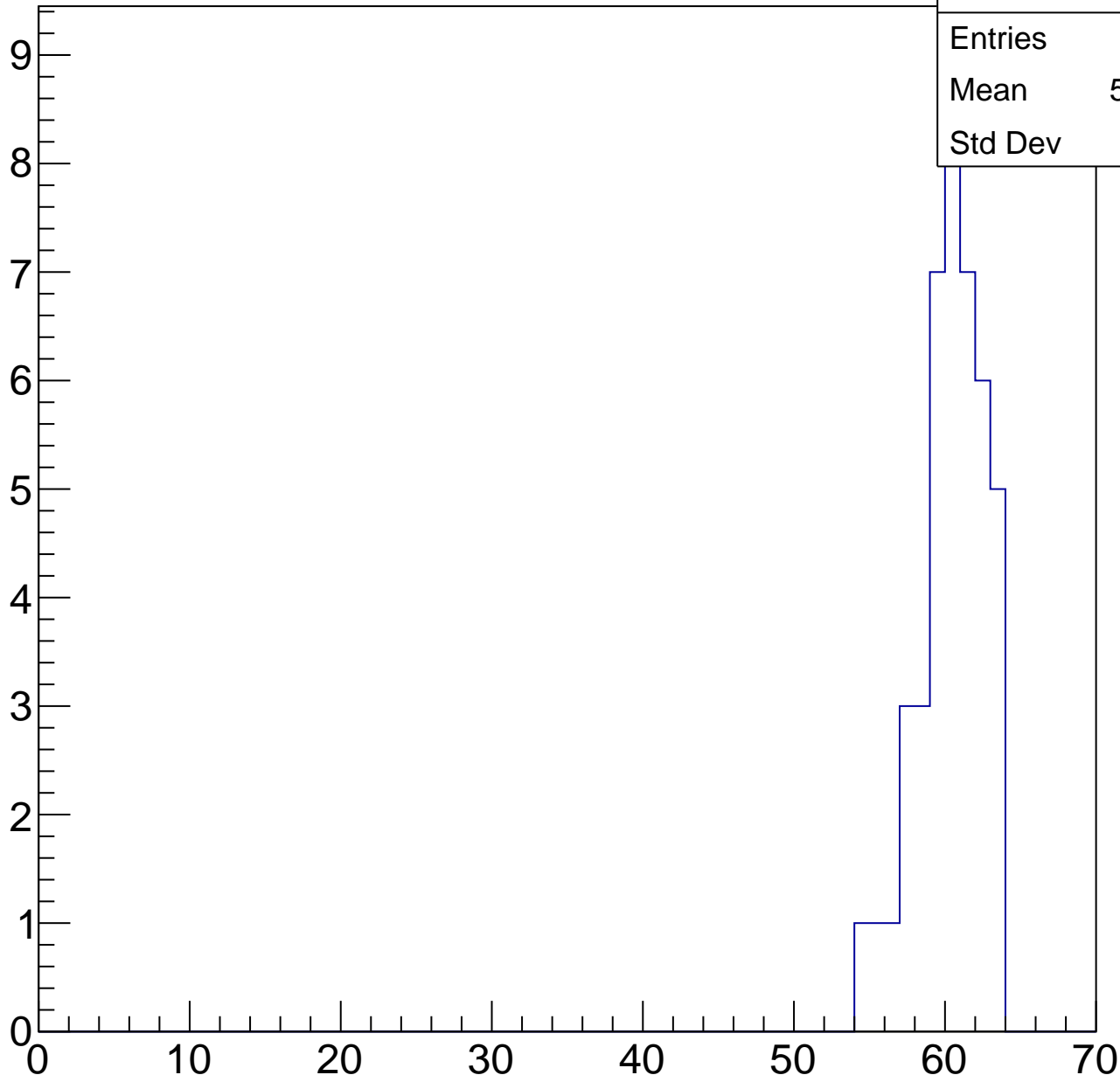
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.93
Std Dev	2.15

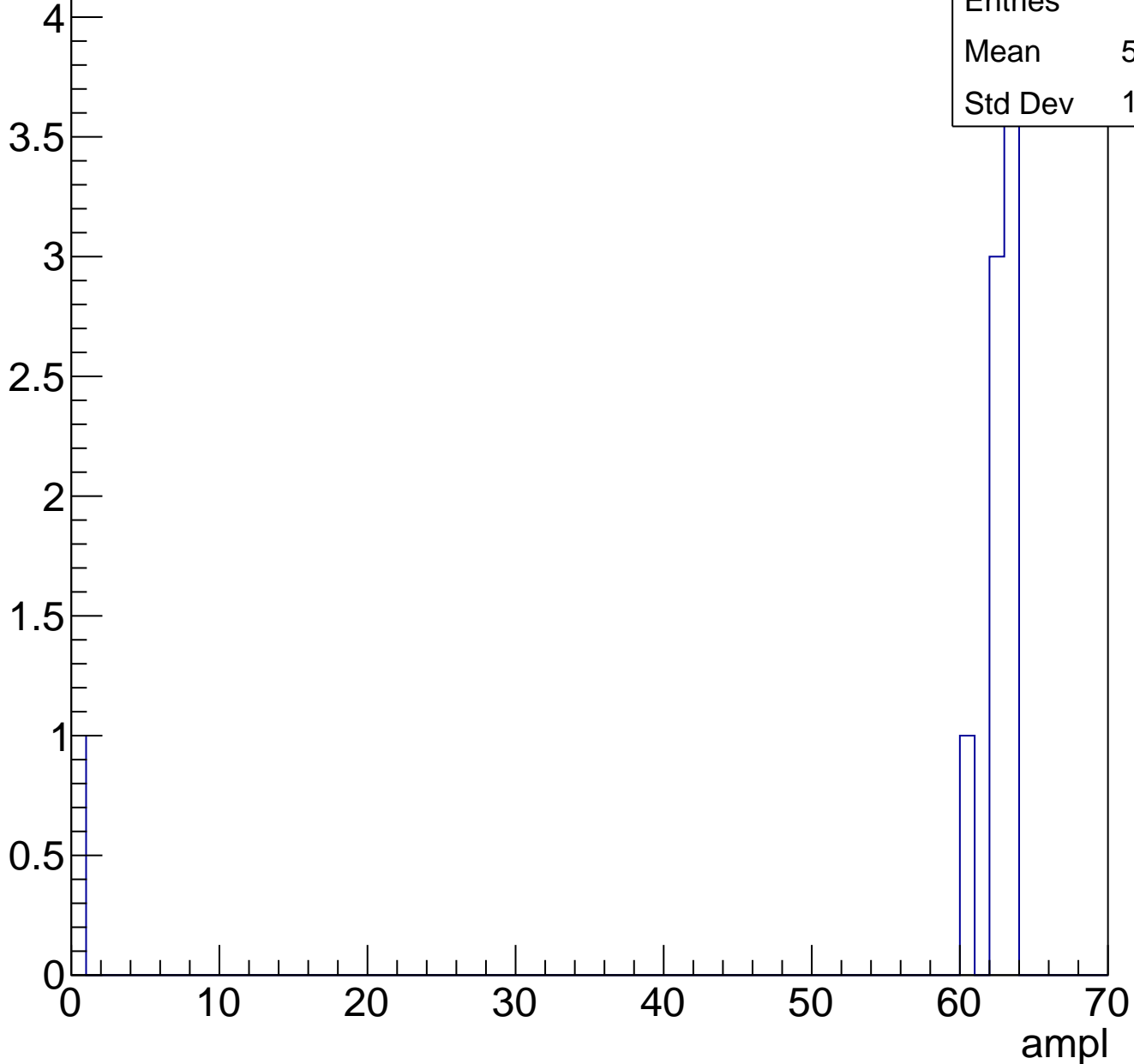
ampl



B1L102S, U19-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L102S, U19-ch72, adc0

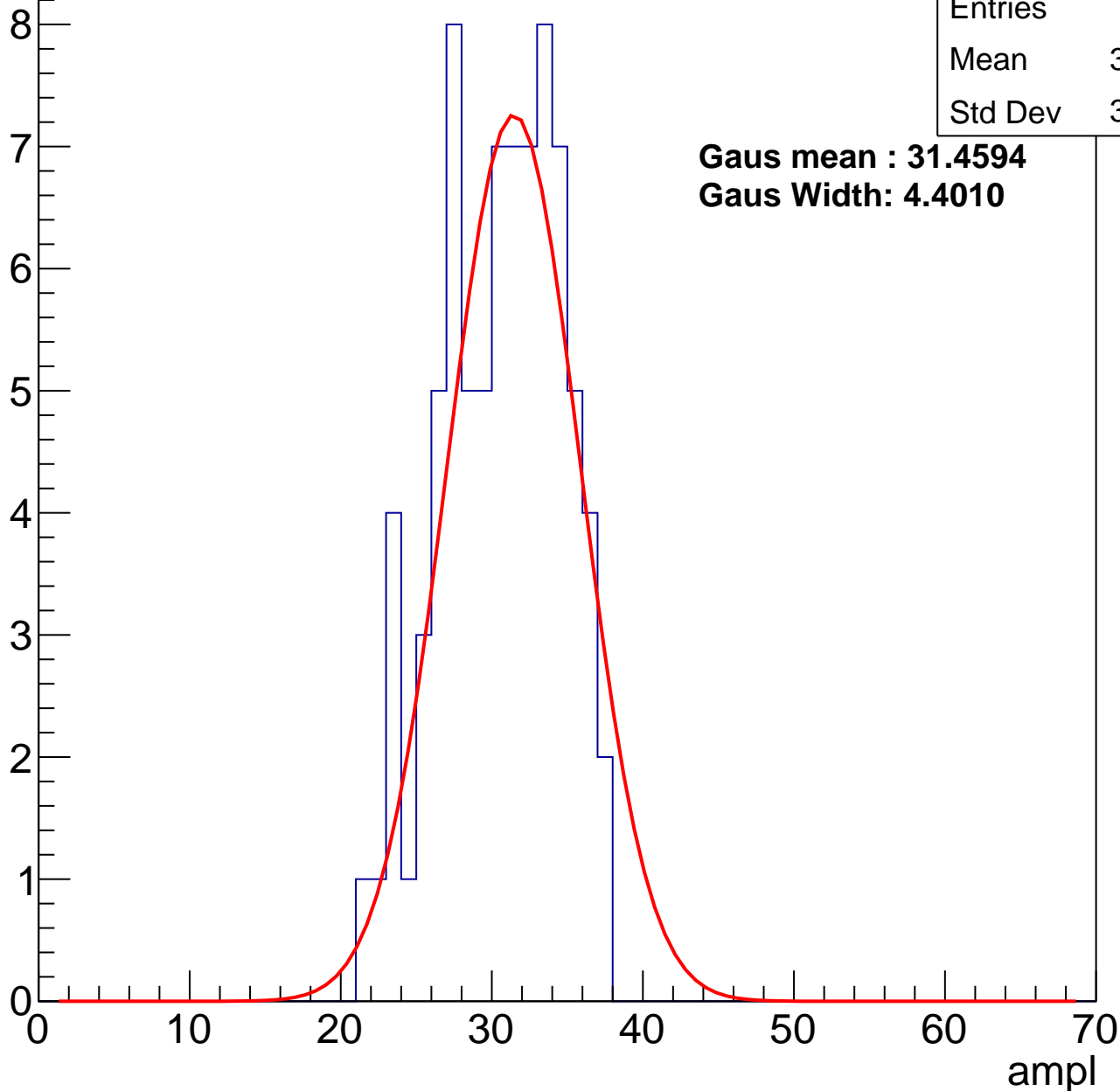
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	30.14
Std Dev	3.895

Gaus mean : 31.4594

Gaus Width: 4.4010



B1L102S, U19-ch72, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	37.48
Std Dev	3.65

Gaus mean : 38.5788

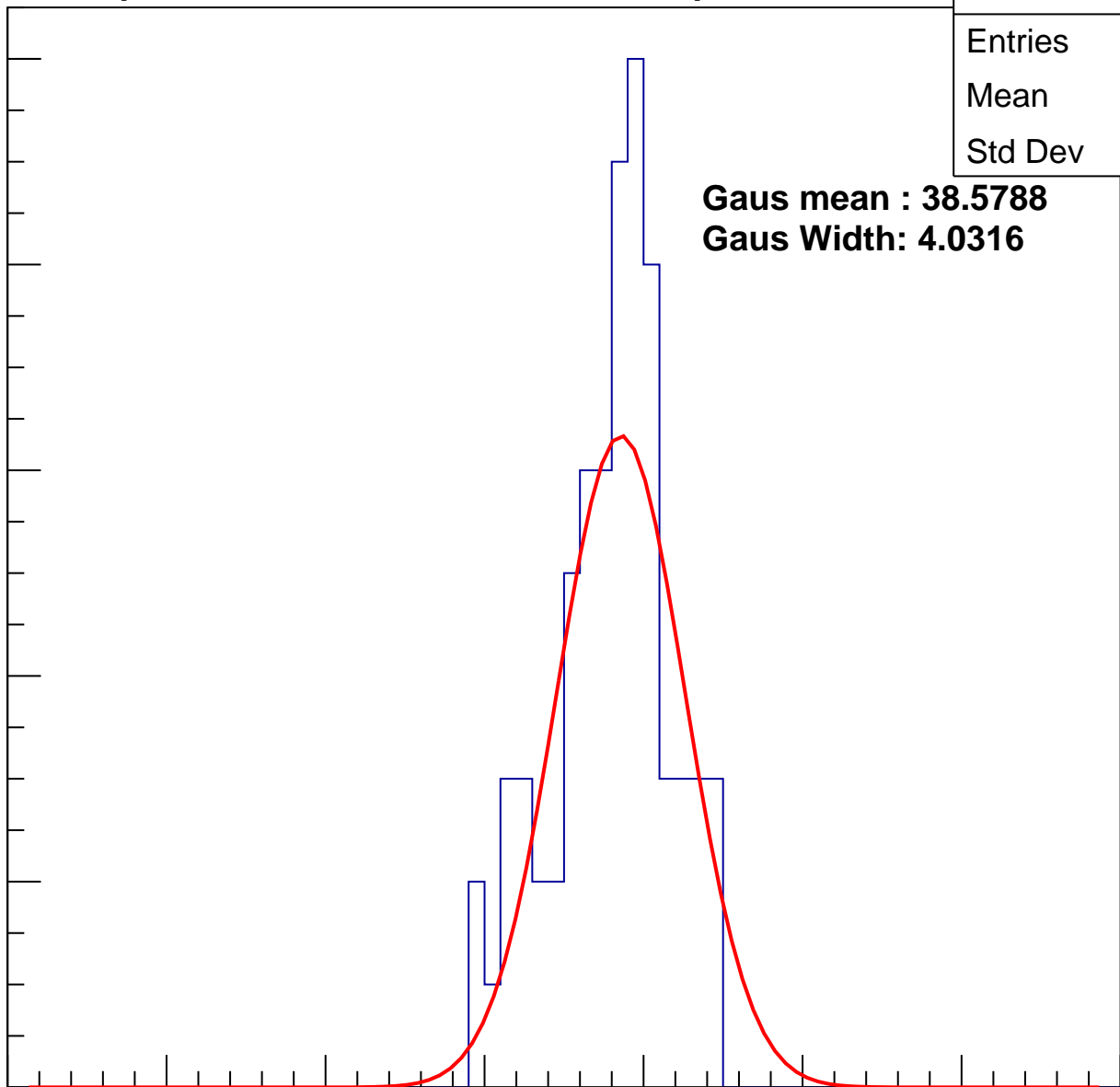
Gaus Width: 4.0316

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

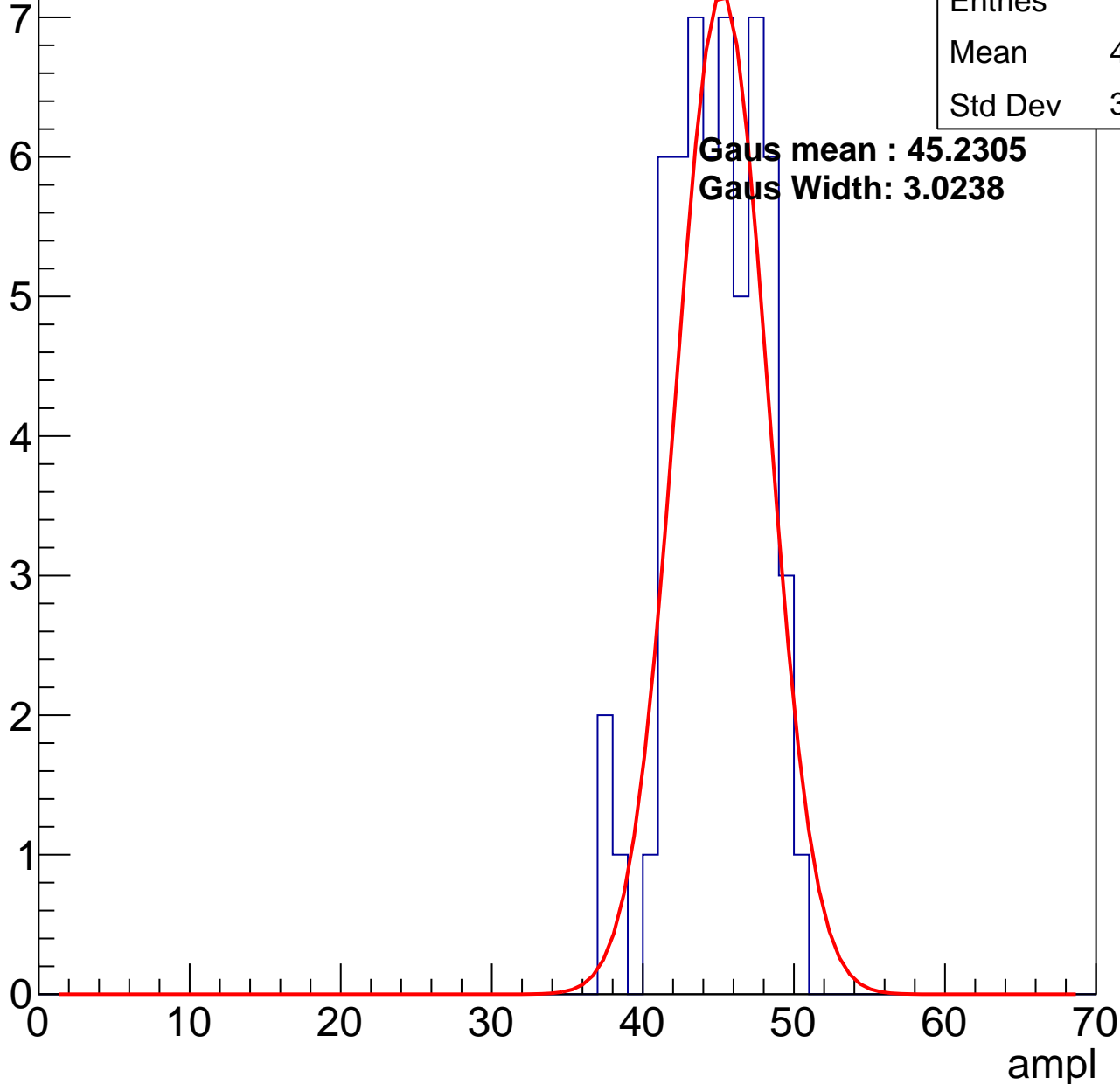


B1L102S, U19-ch72, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	44.38
Std Dev	3.005



B1L102S, U19-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	50.47
Std Dev	3.804

Entry

10

8

6

4

2

0

0

10

20

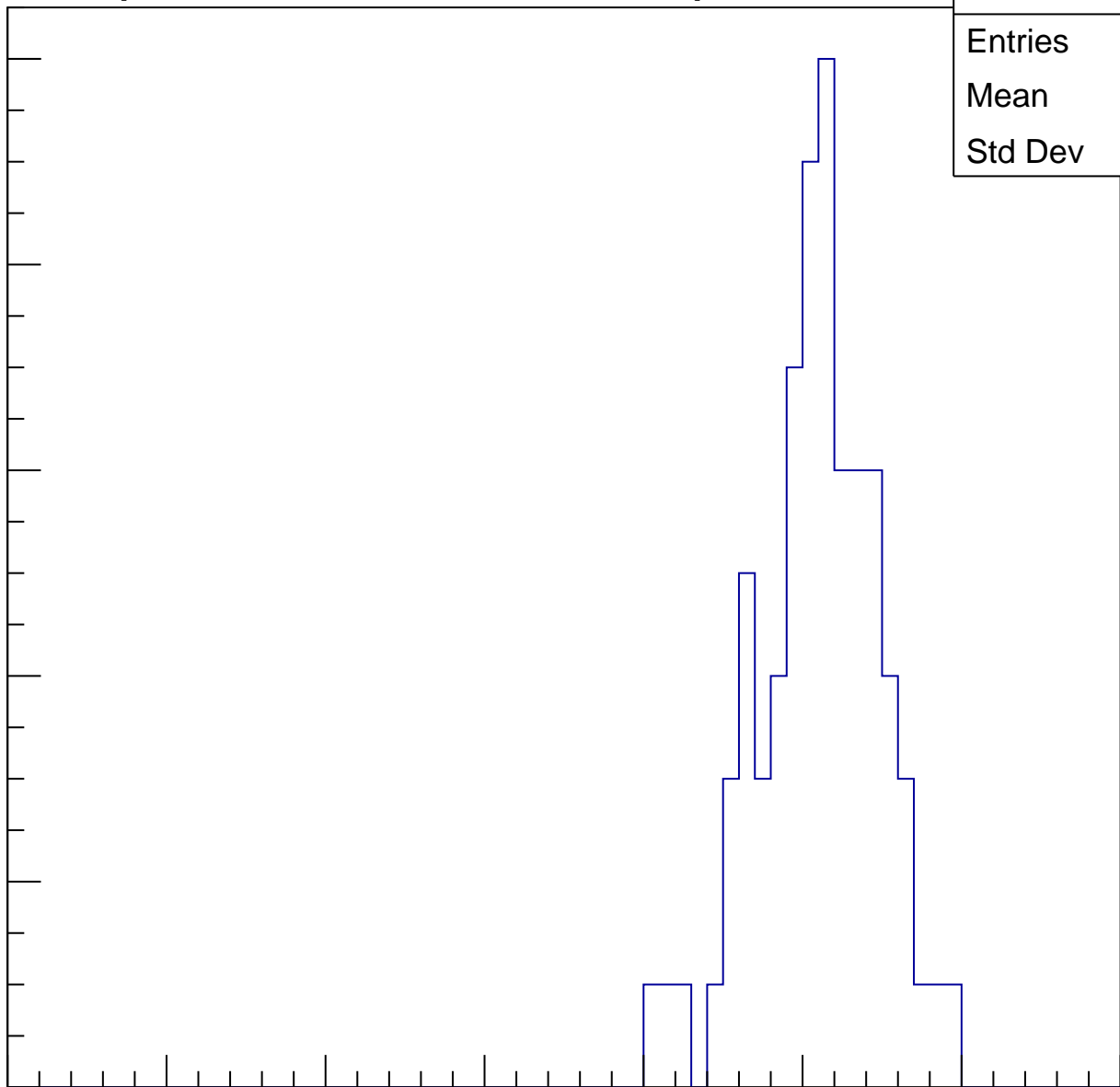
30

40

50

60

ampl

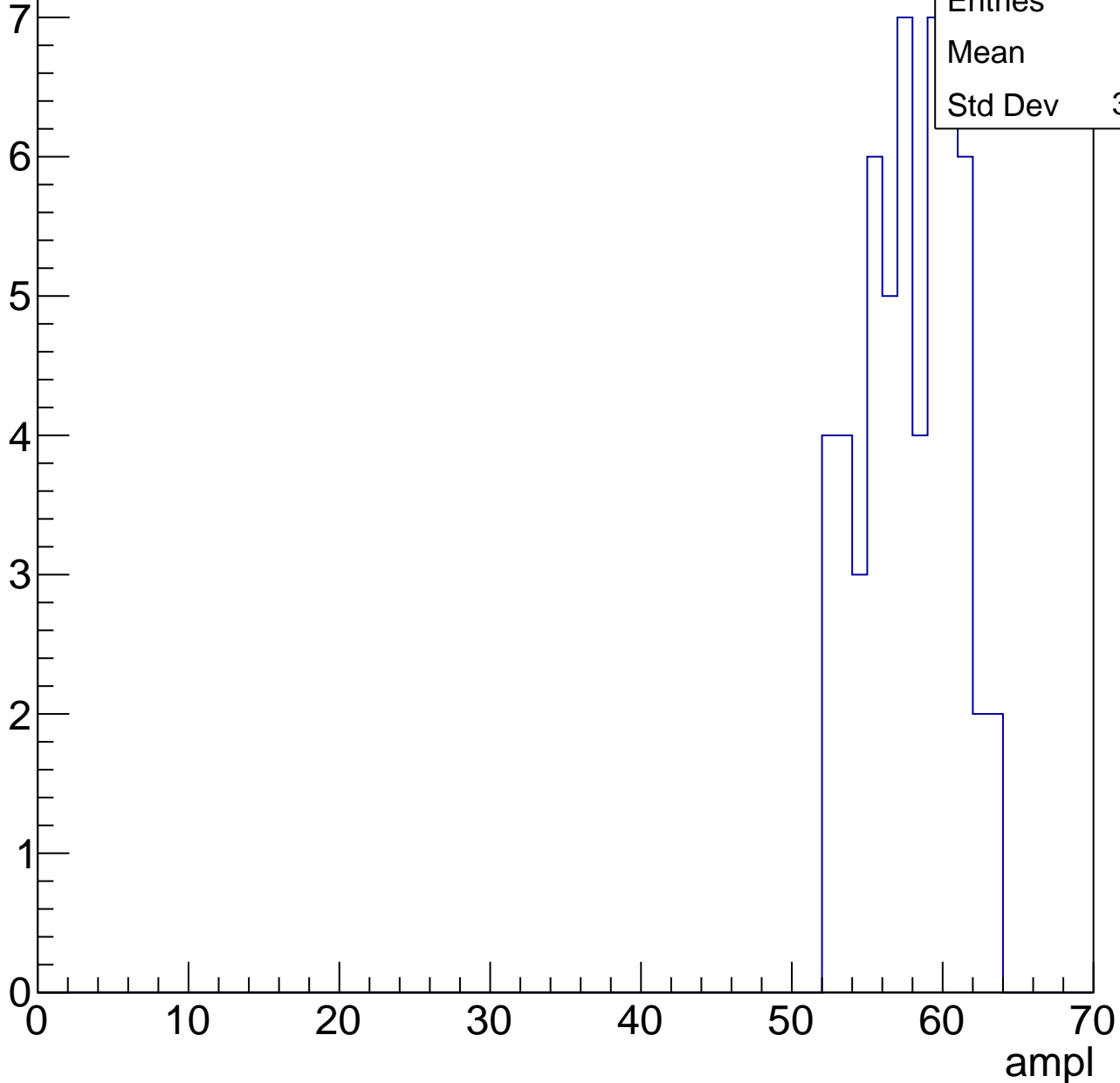


B1L102S, U19-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.4
Std Dev	3.031

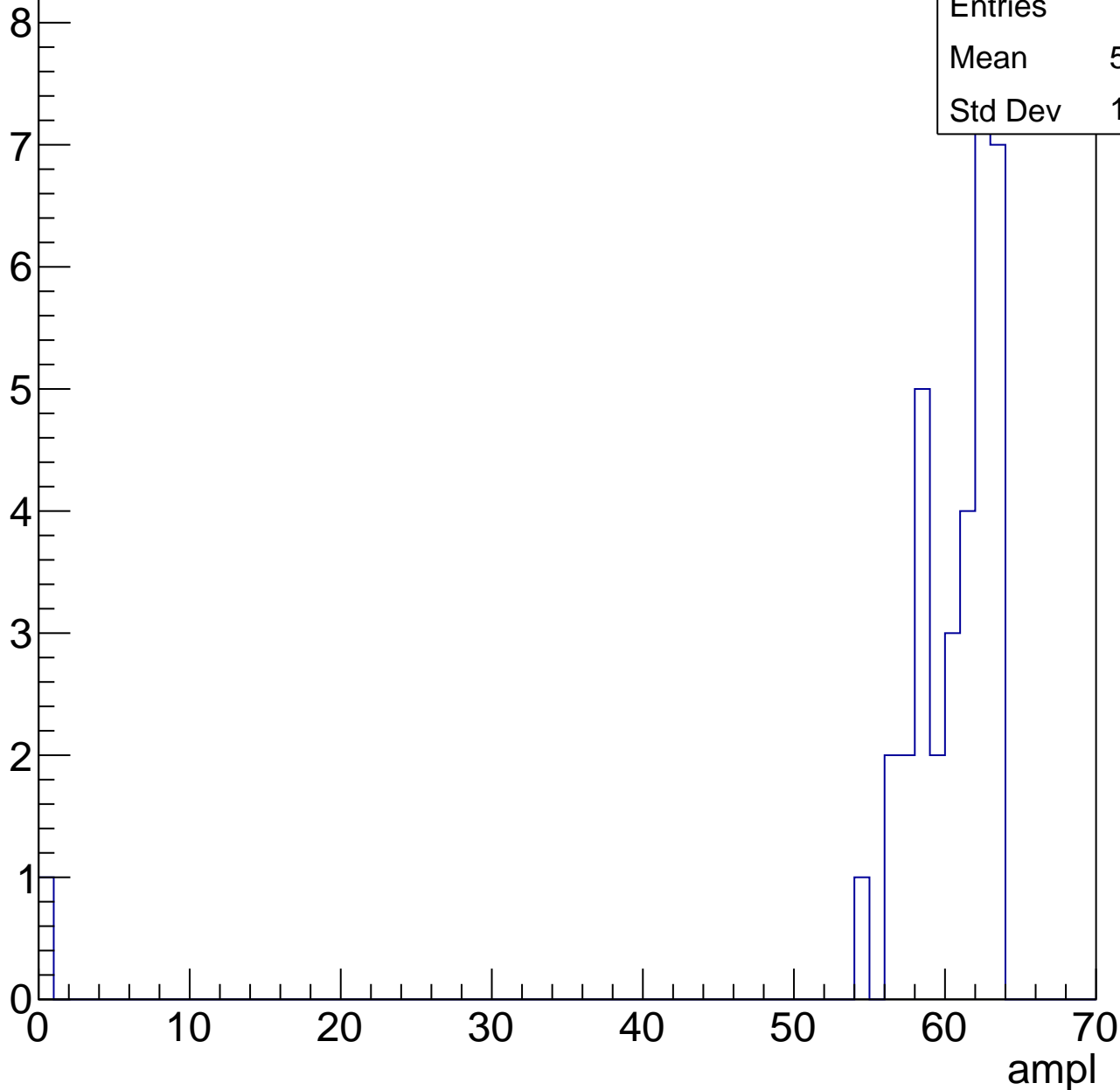


B1L102S, U19-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.54
Std Dev	10.33



B1L102S, U19-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch73, adc0

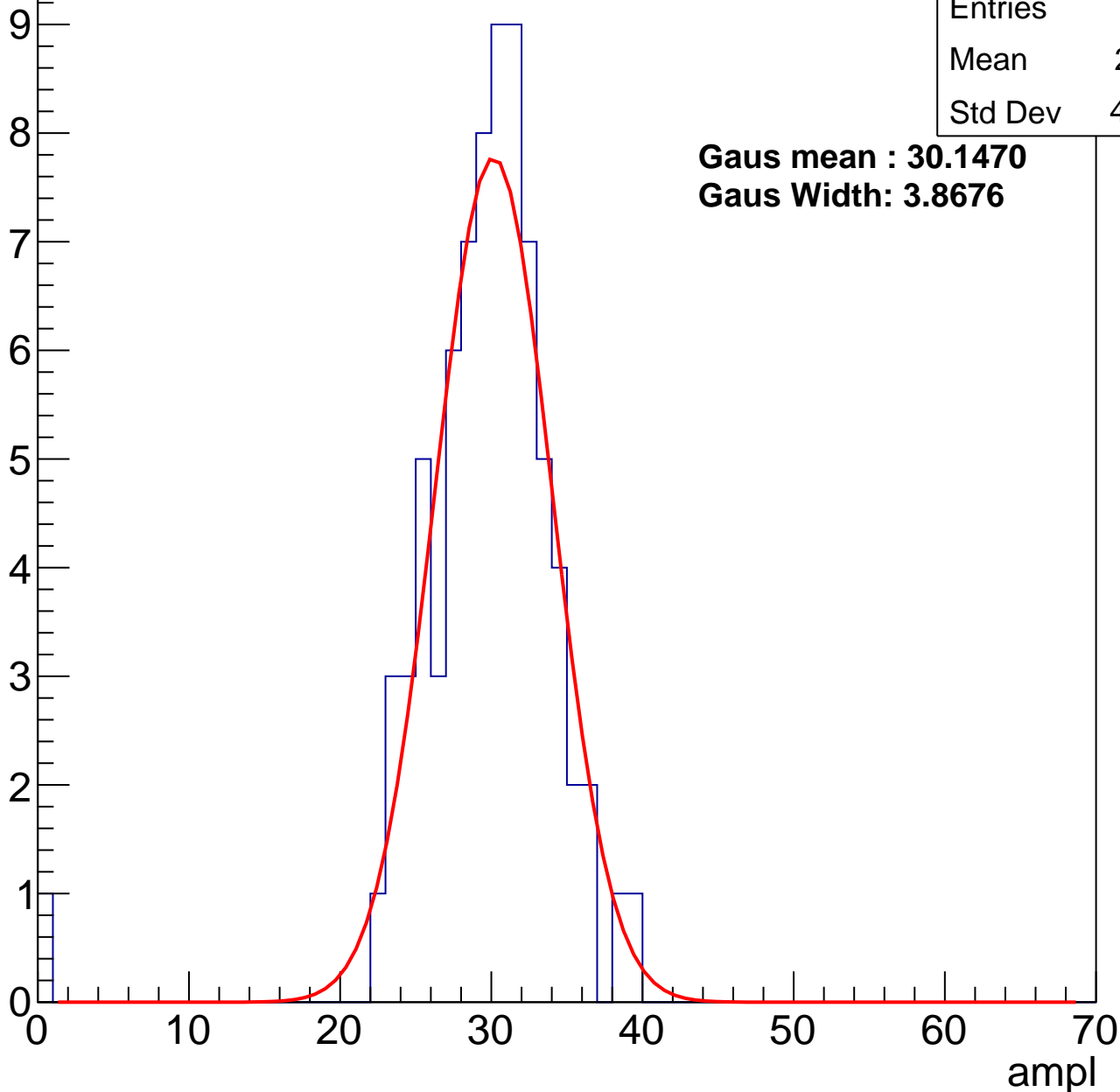
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.21
Std Dev	4.897

Gaus mean : 30.1470

Gaus Width: 3.8676



B1L102S, U19-ch73, adc1

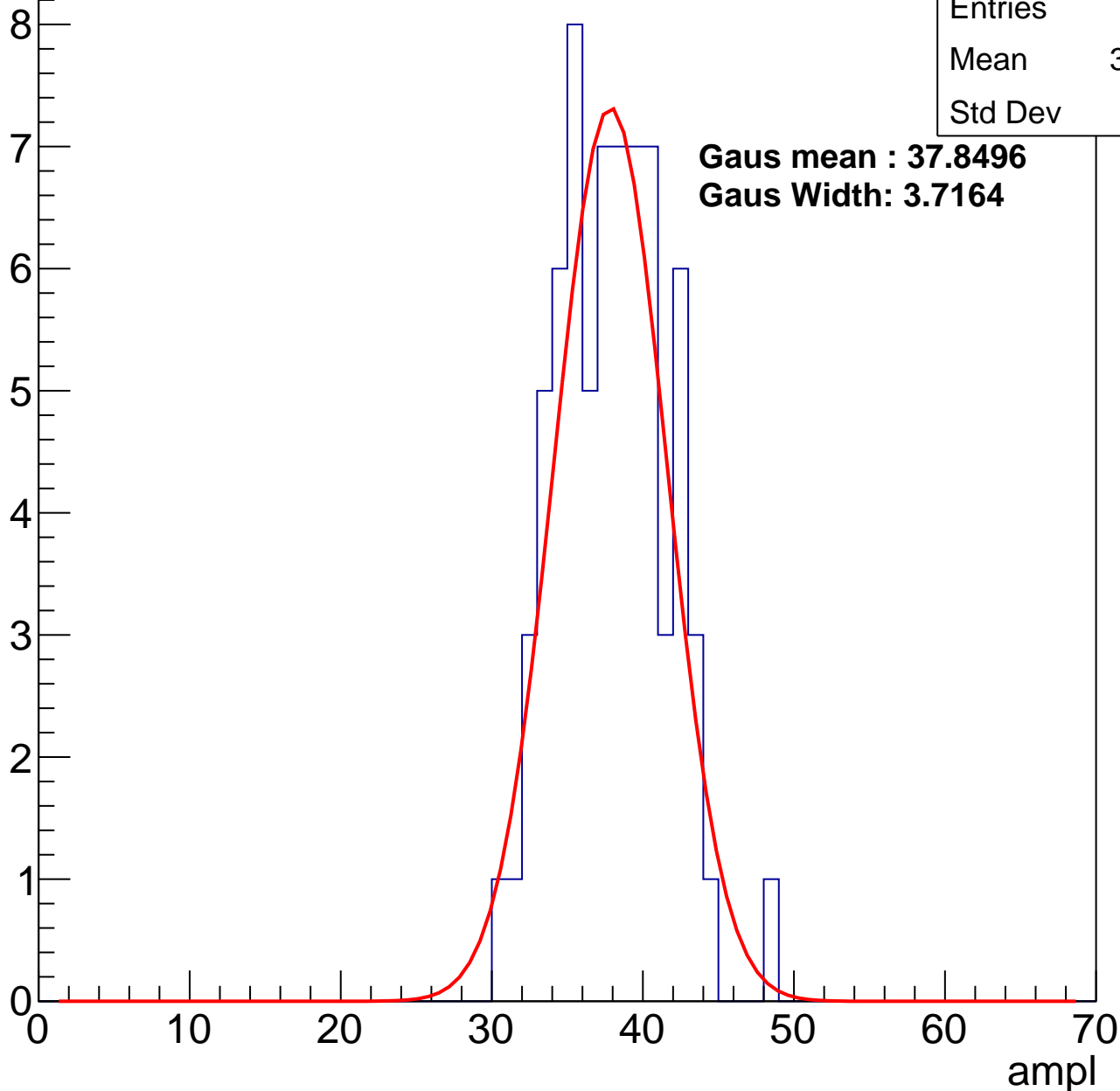
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	37.46
Std Dev	3.54

Gaus mean : 37.8496

Gaus Width: 3.7164



B1L102S, U19-ch73, adc2

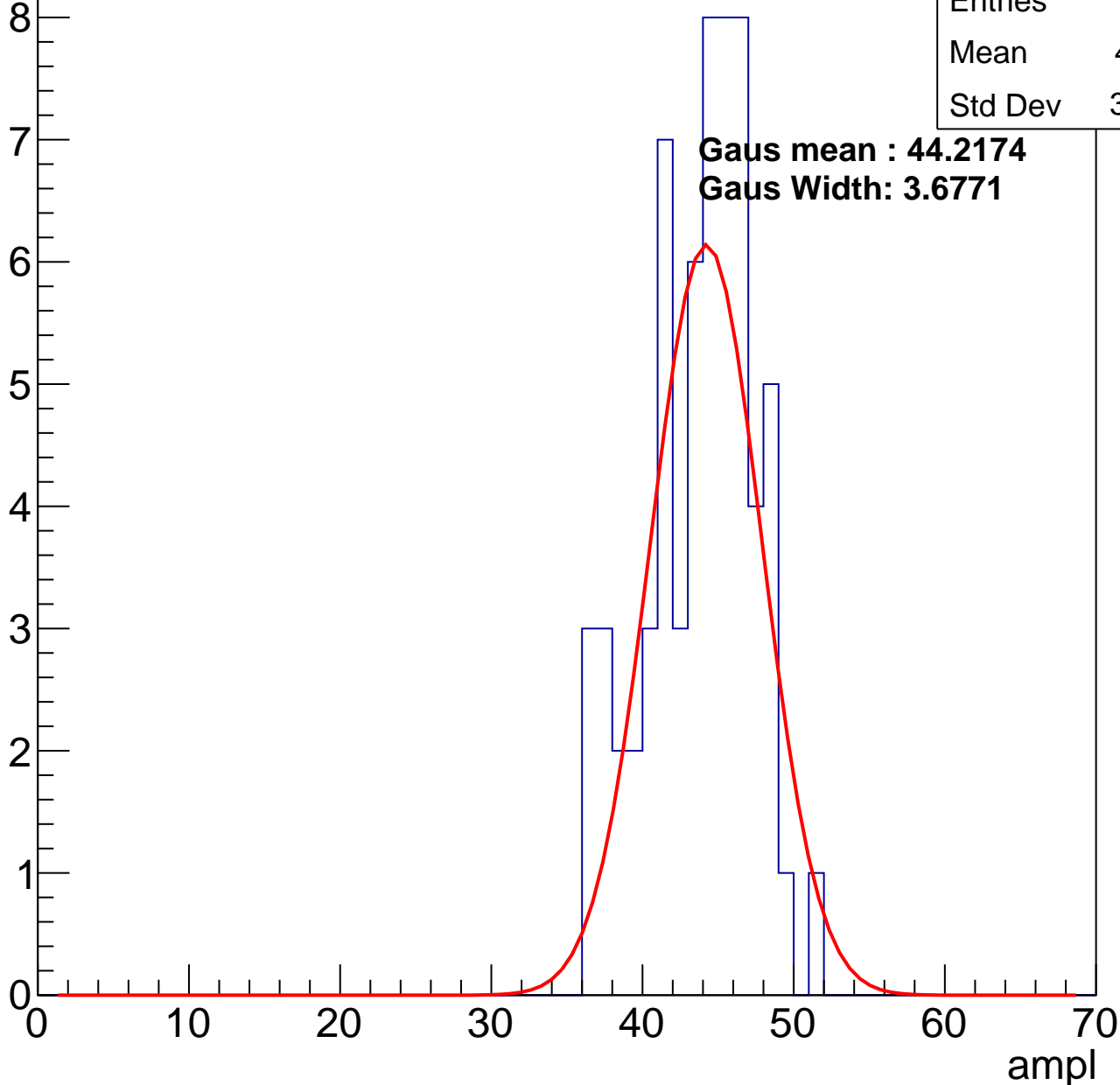
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.31
Std Dev	3.513

Gaus mean : 44.2174

Gaus Width: 3.6771

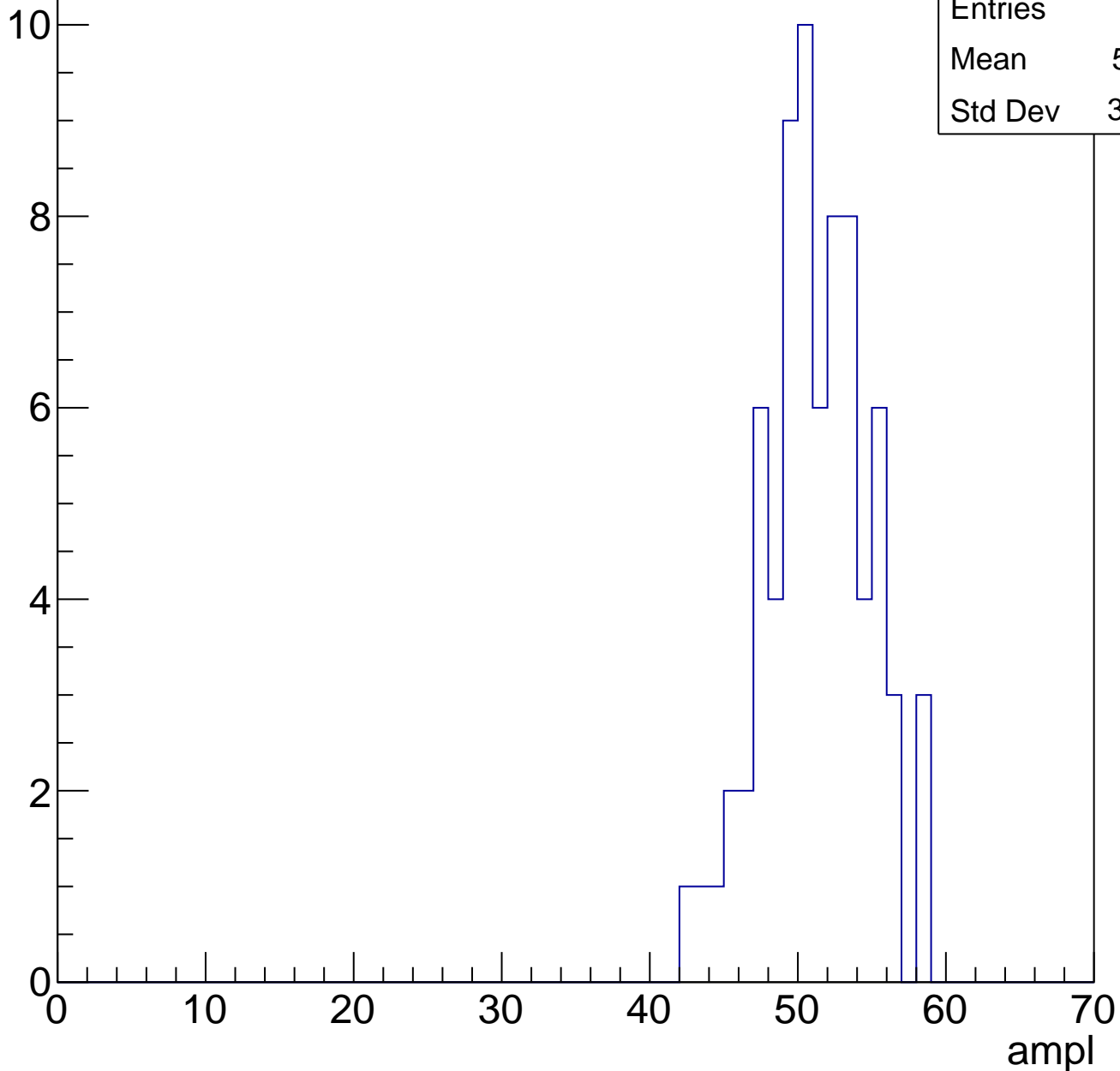


B1L102S, U19-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	50.81
Std Dev	3.459

Entry

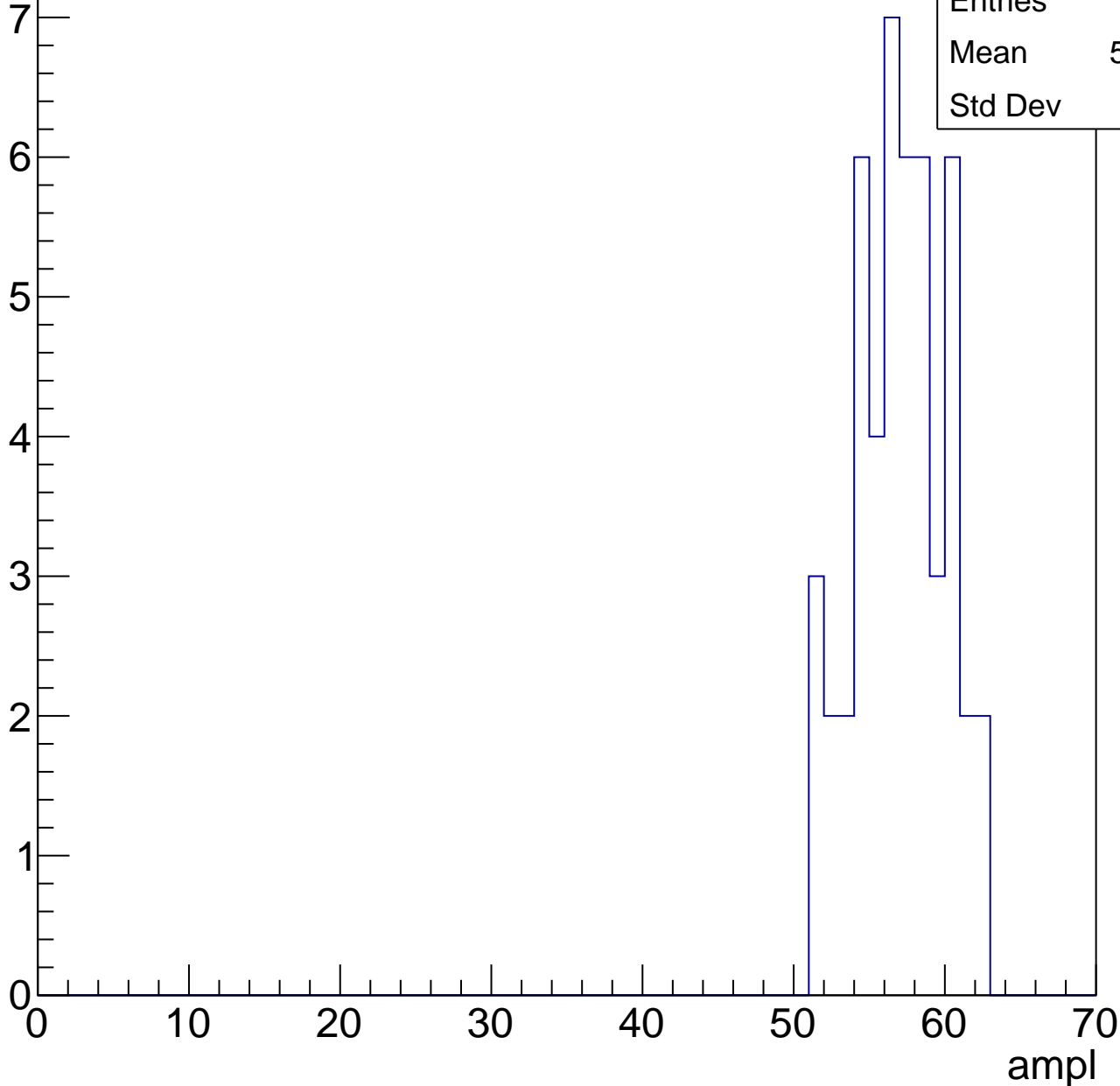


B1L102S, U19-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.57
Std Dev	2.9

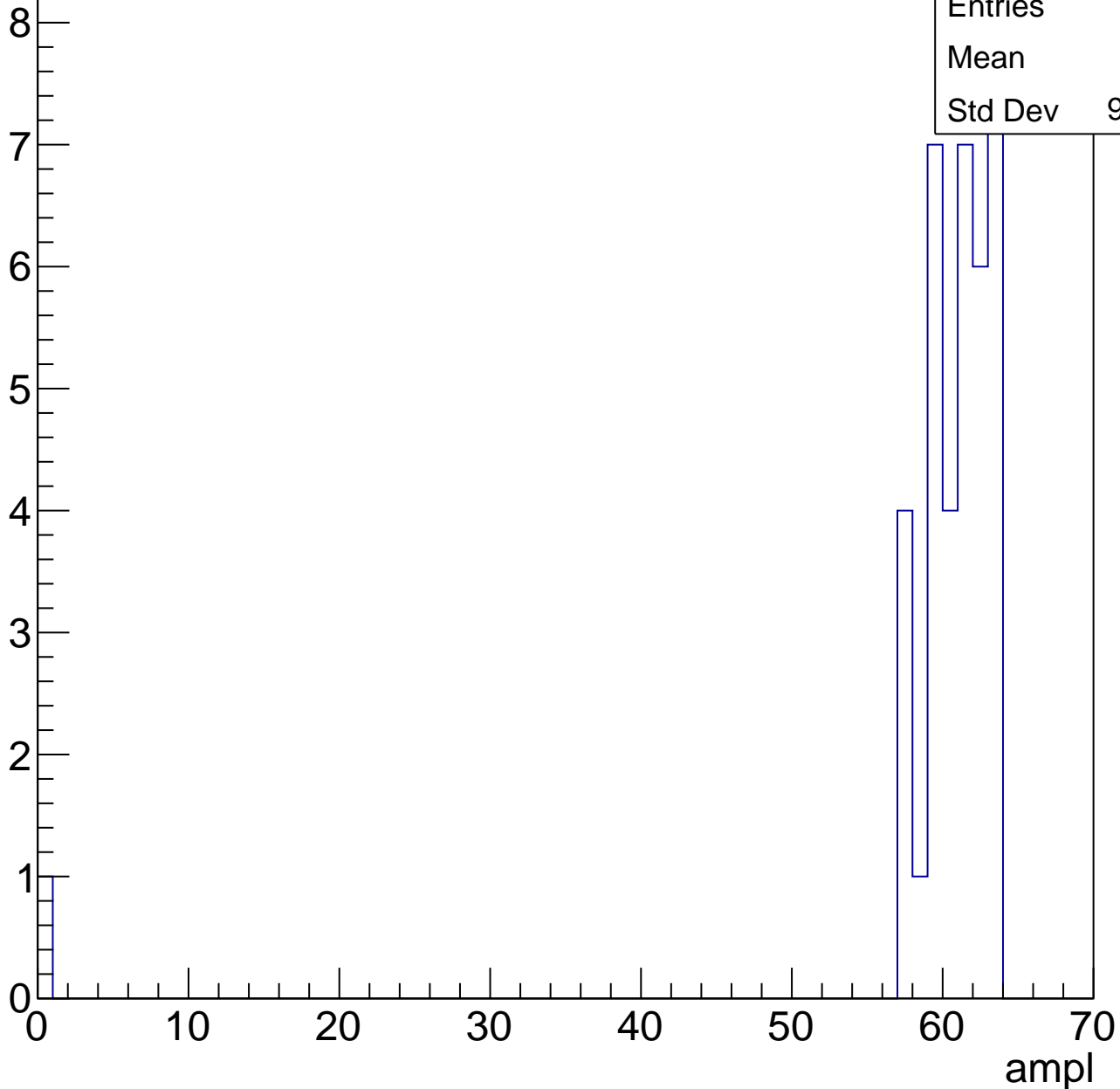


B1L102S, U19-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

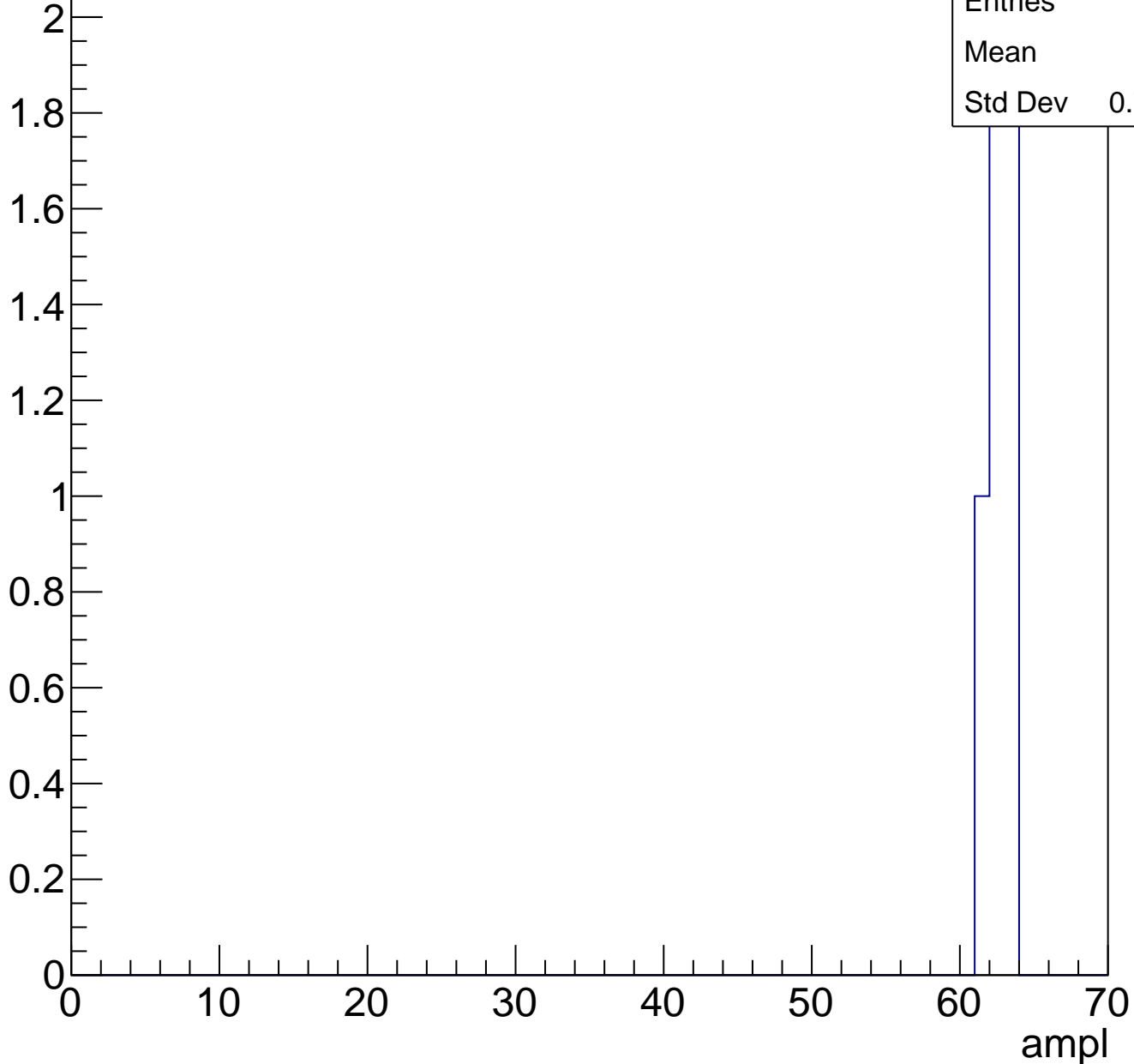
Entries	38
Mean	59
Std Dev	9.884



B1L102S, U19-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch74, adc0

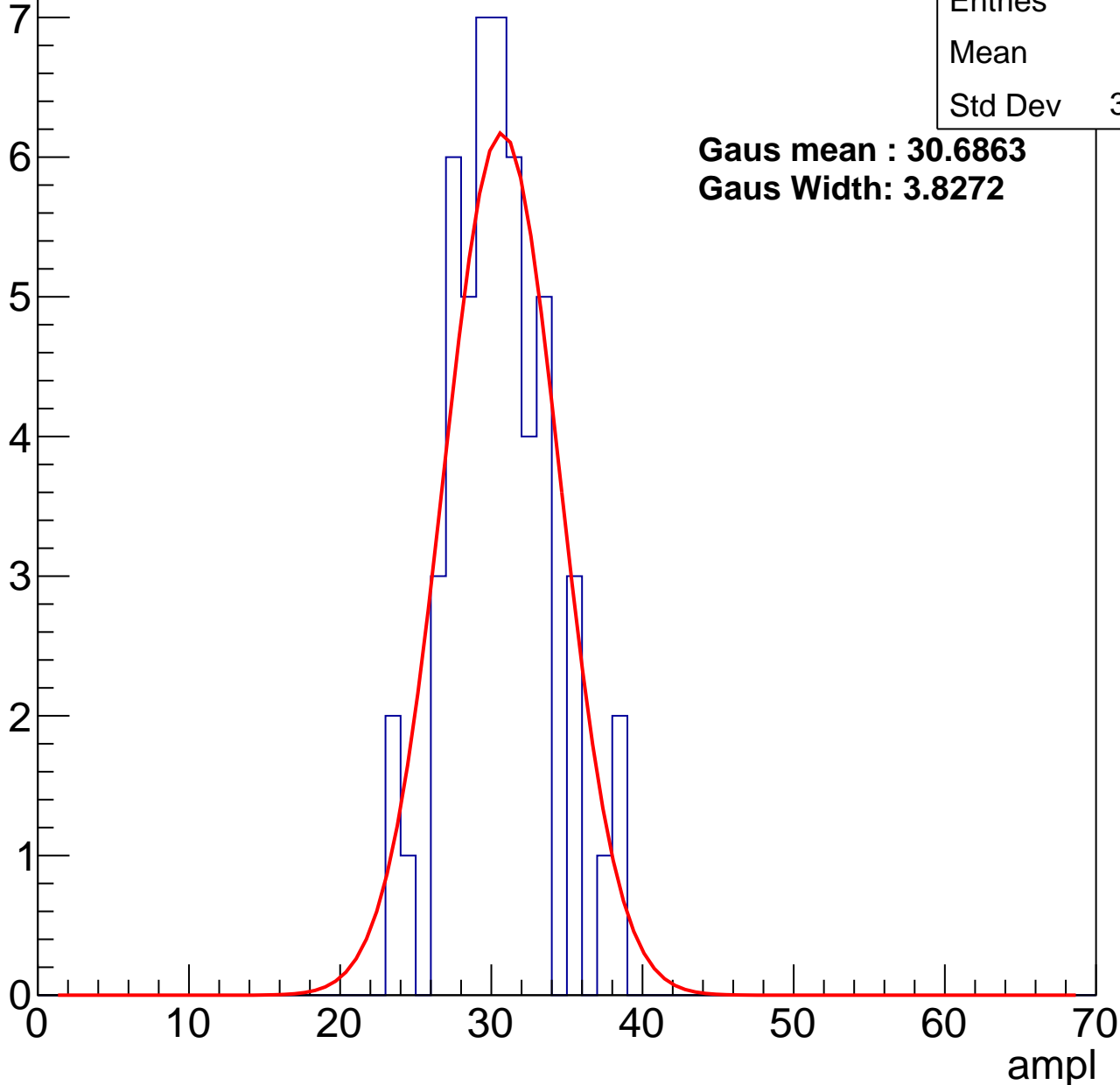
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	30
Std Dev	3.345

Gaus mean : 30.6863

Gaus Width: 3.8272



B1L102S, U19-ch74, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.96
Std Dev	3.49

Gaus mean : 36.3756

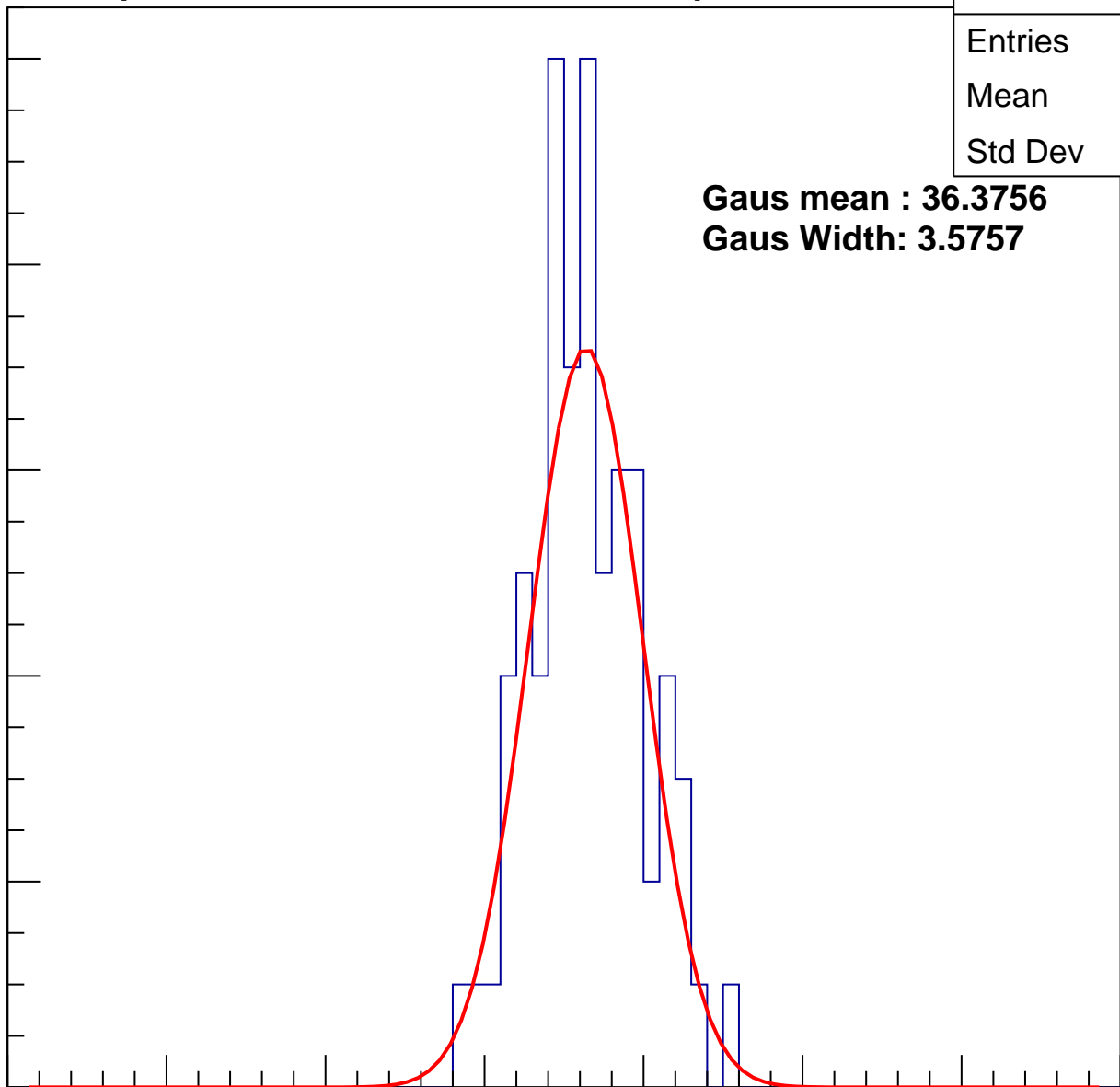
Gaus Width: 3.5757

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch74, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	43.02
Std Dev	3.554

Gaus mean : 42.7349

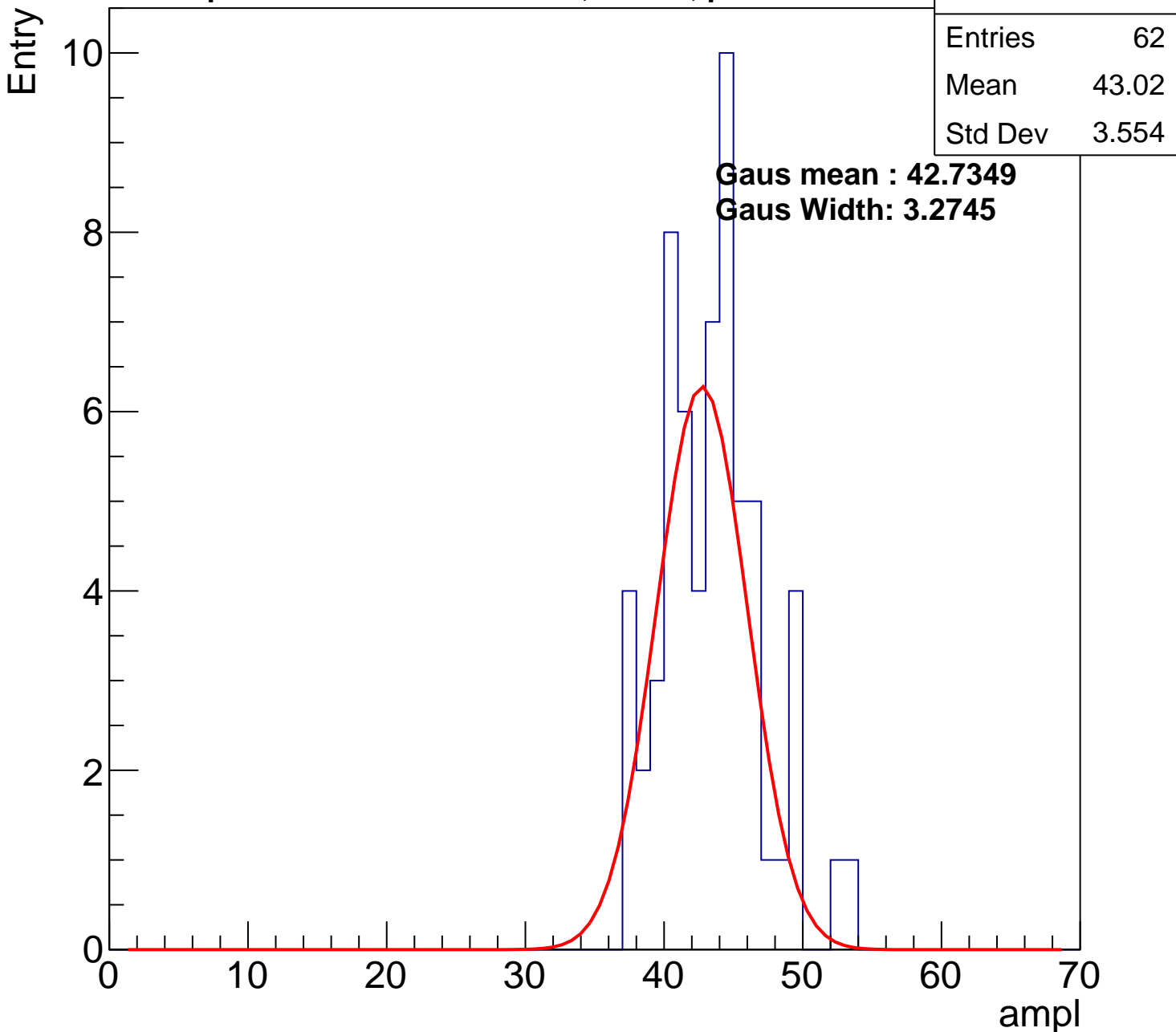
Gaus Width: 3.2745

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

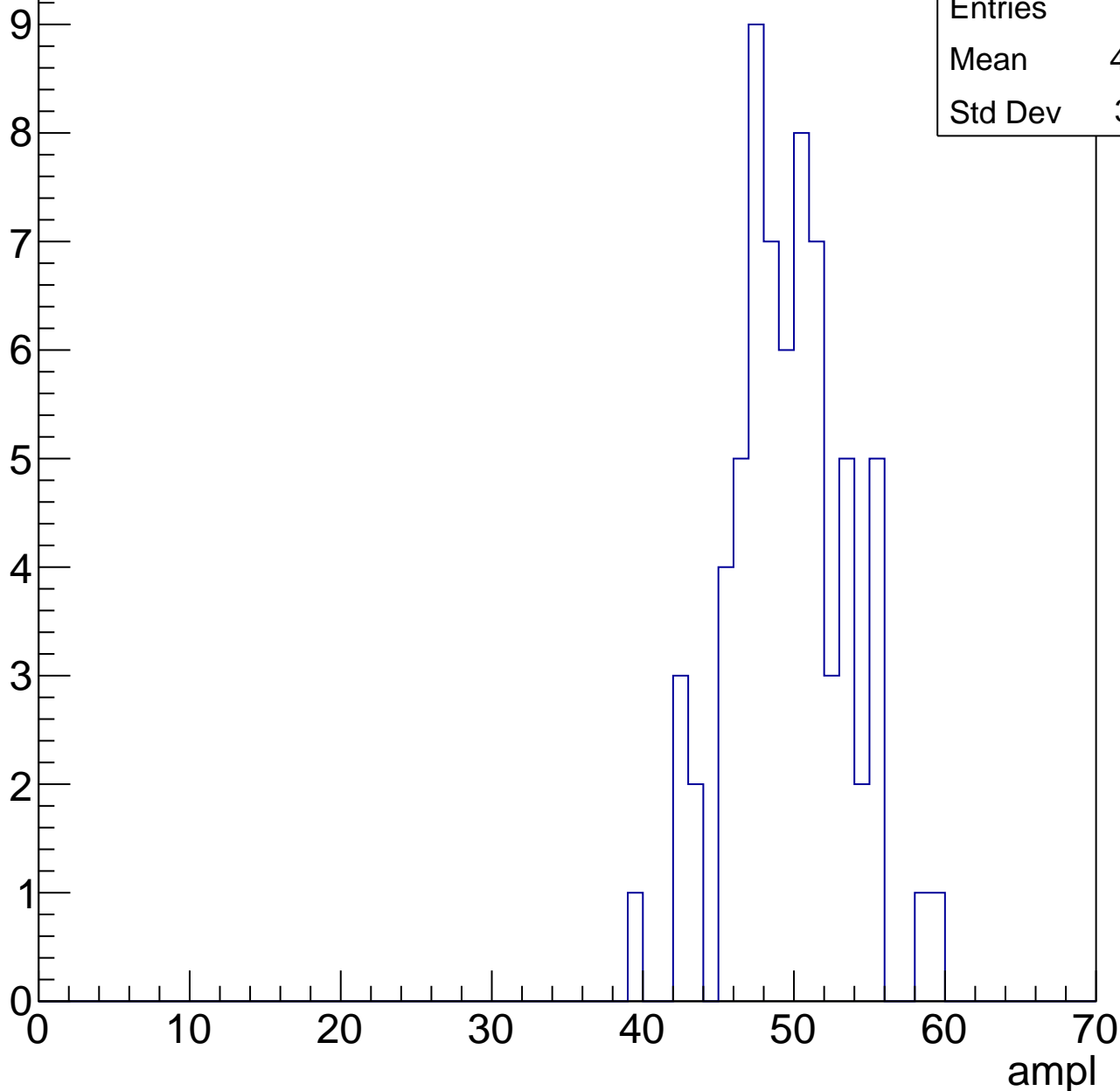


B1L102S, U19-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.16
Std Dev	3.851

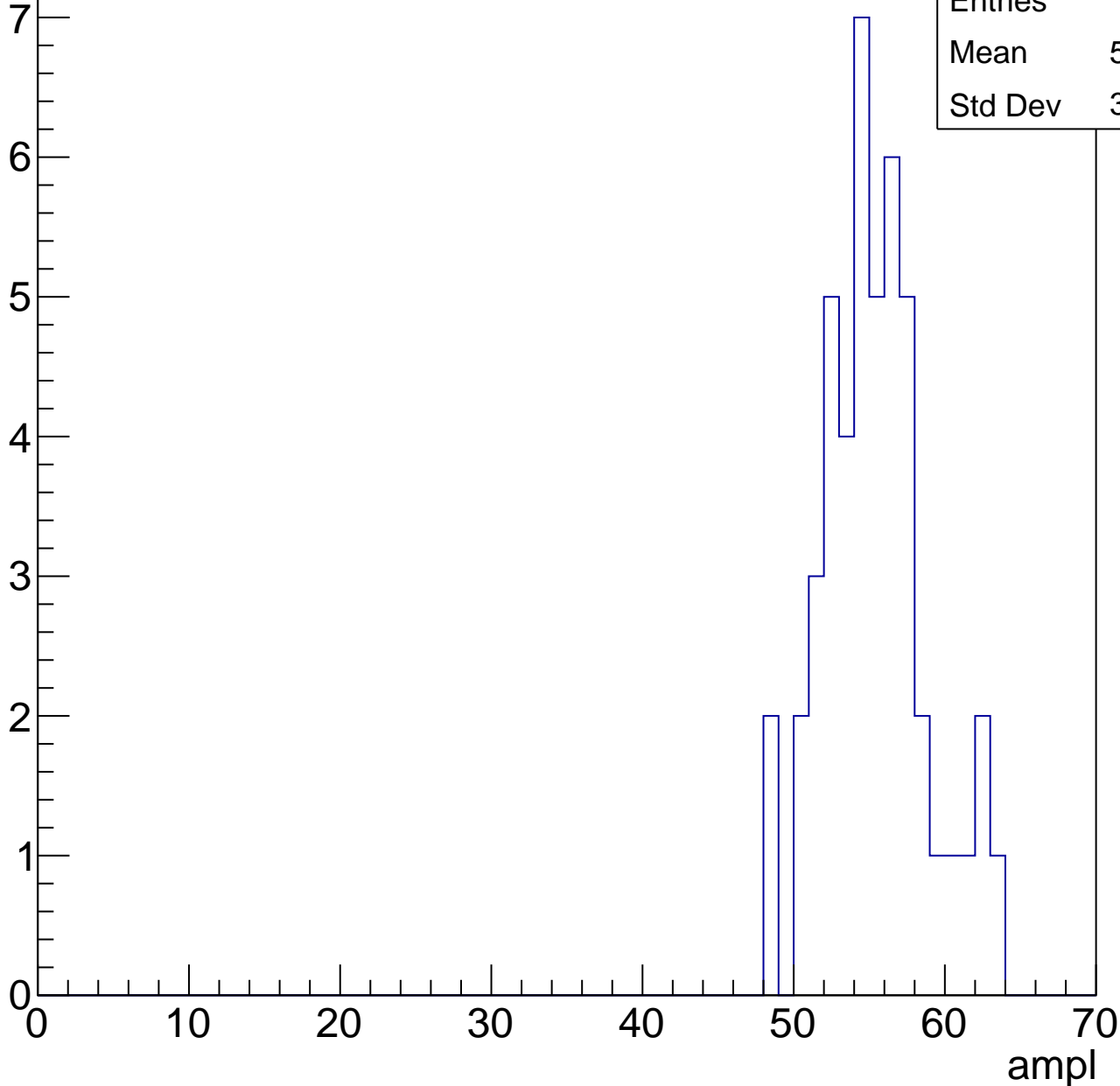


B1L102S, U19-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

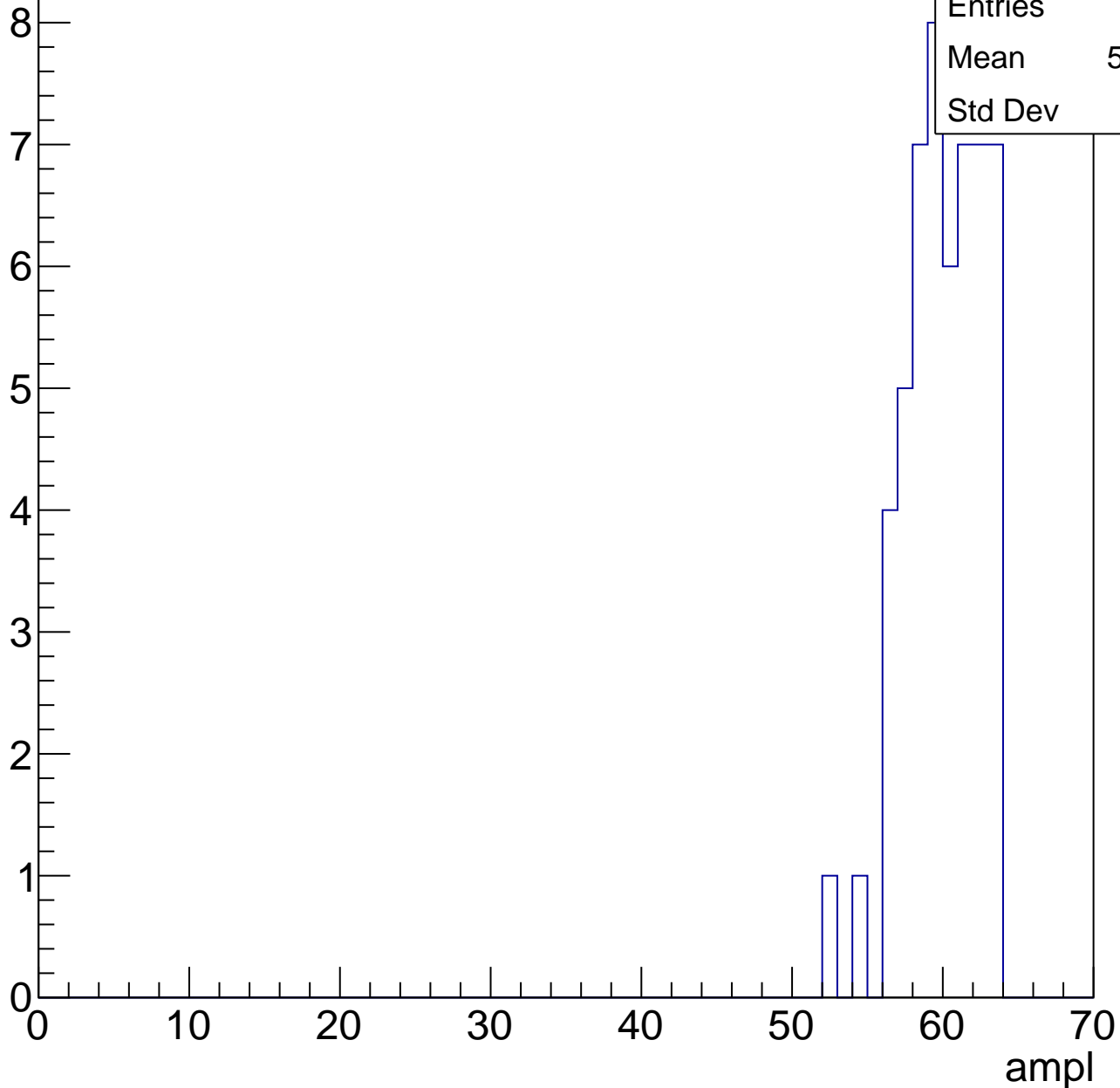
Entries	47
Mean	54.85
Std Dev	3.408



B1L102S, U19-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

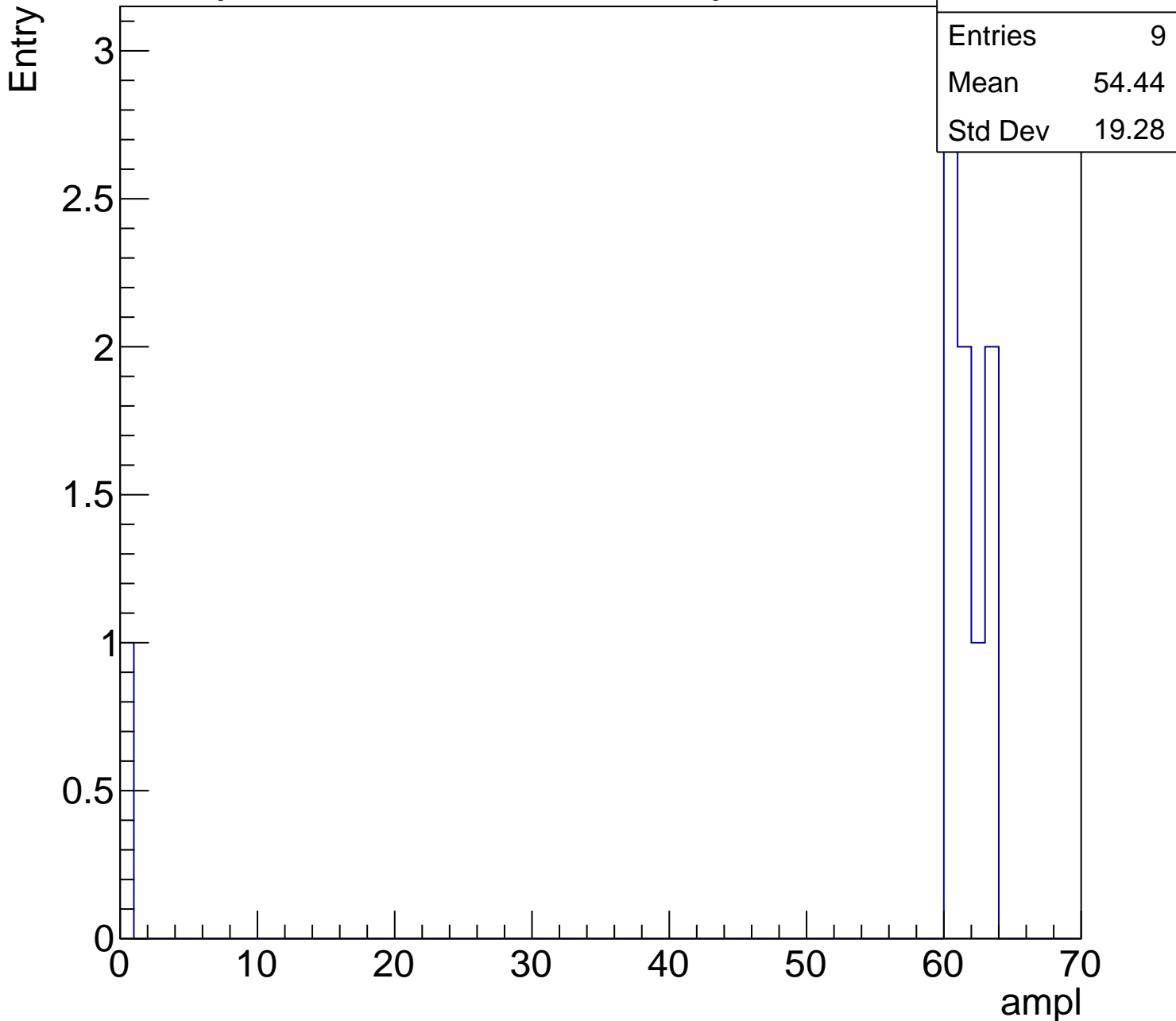
1

0.5

0

ampl

Entries	9
Mean	54.44
Std Dev	19.28



B1L102S, U19-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch75, adc0

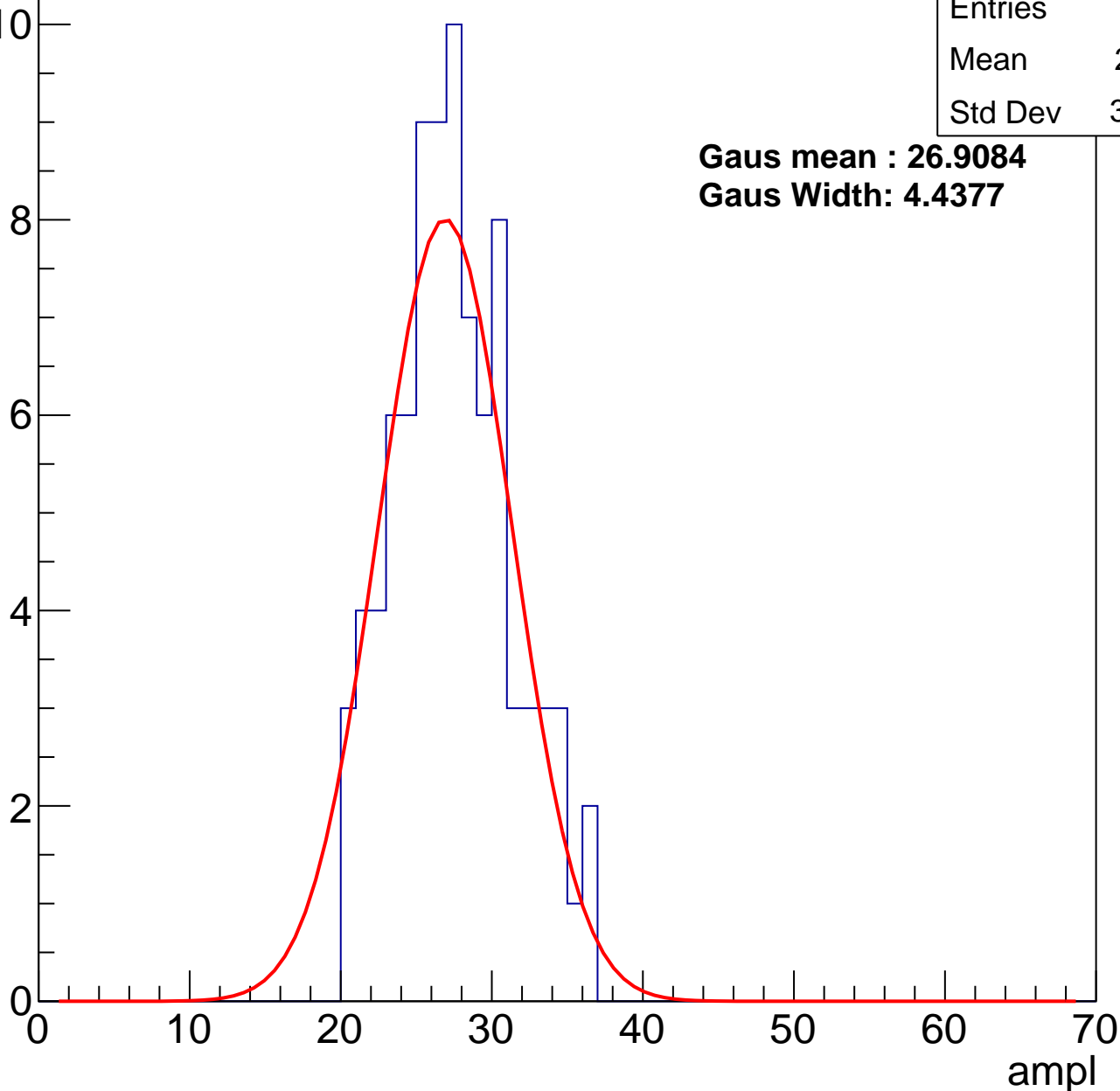
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	27.01
Std Dev	3.855

Gaus mean : 26.9084

Gaus Width: 4.4377



B1L102S, U19-ch75, adc1

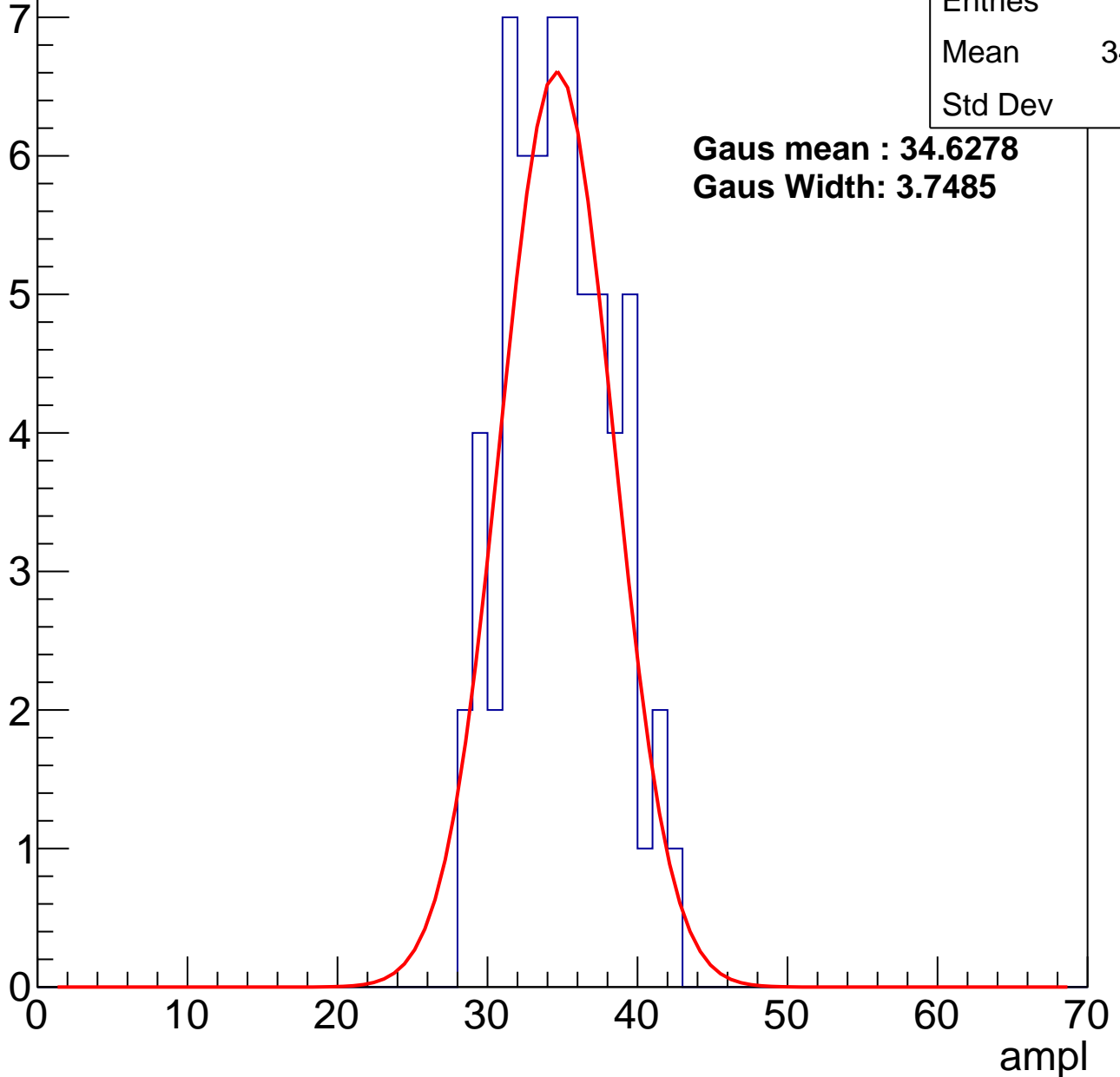
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.34
Std Dev	3.42

Gaus mean : 34.6278

Gaus Width: 3.7485



B1L102S, U19-ch75, adc2

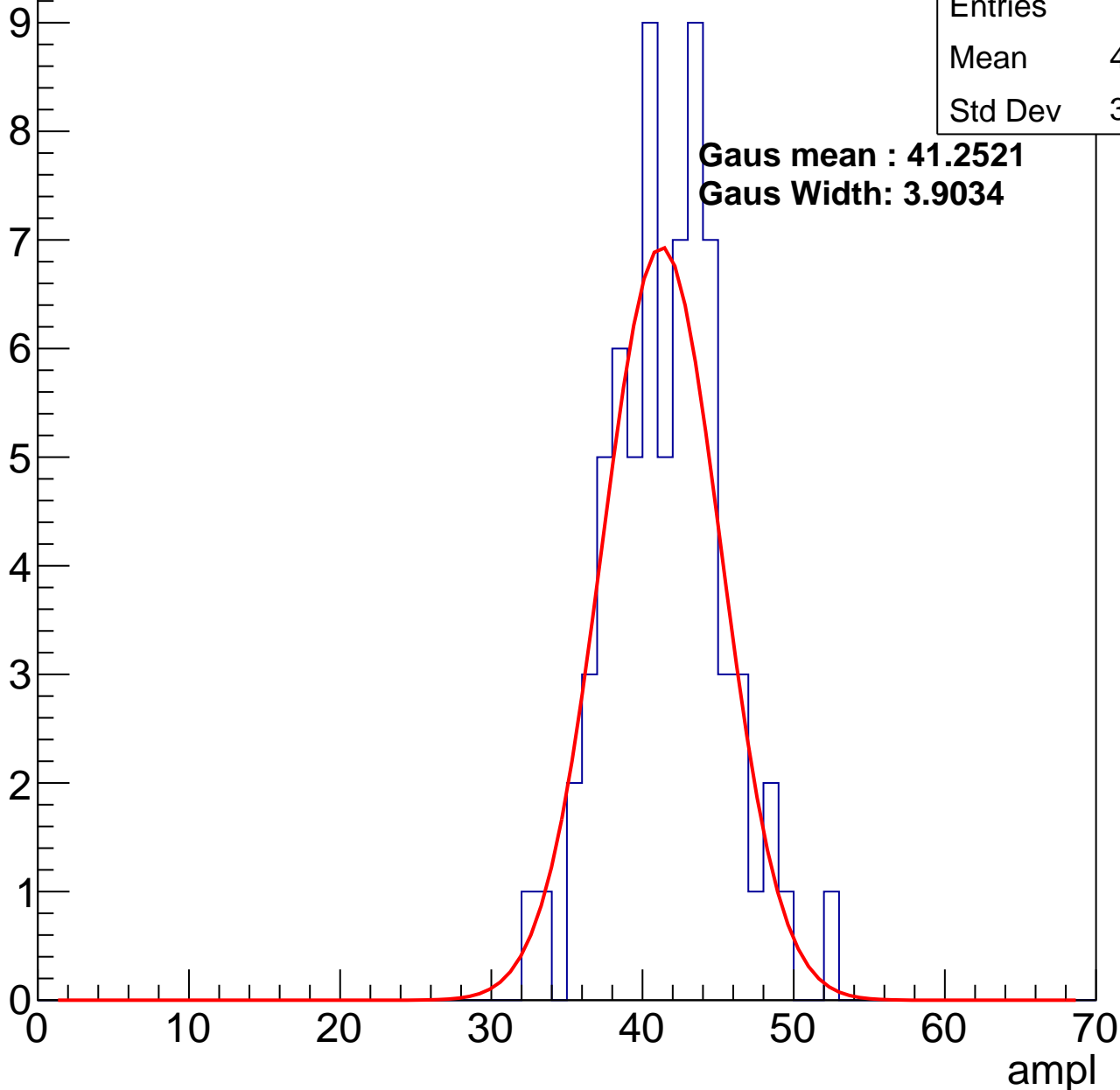
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.15
Std Dev	3.755

Gaus mean : 41.2521

Gaus Width: 3.9034

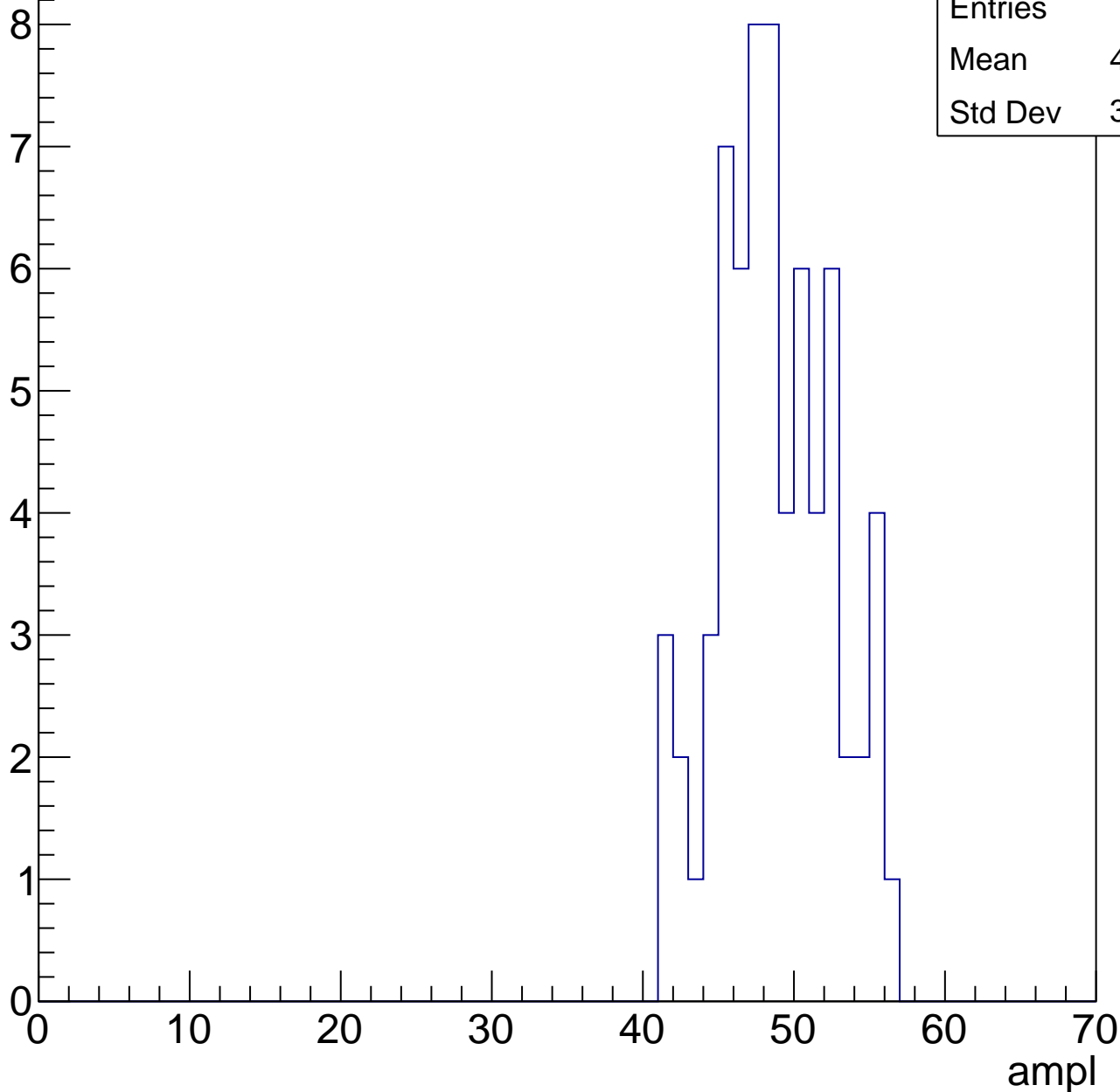


B1L102S, U19-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.28
Std Dev	3.713

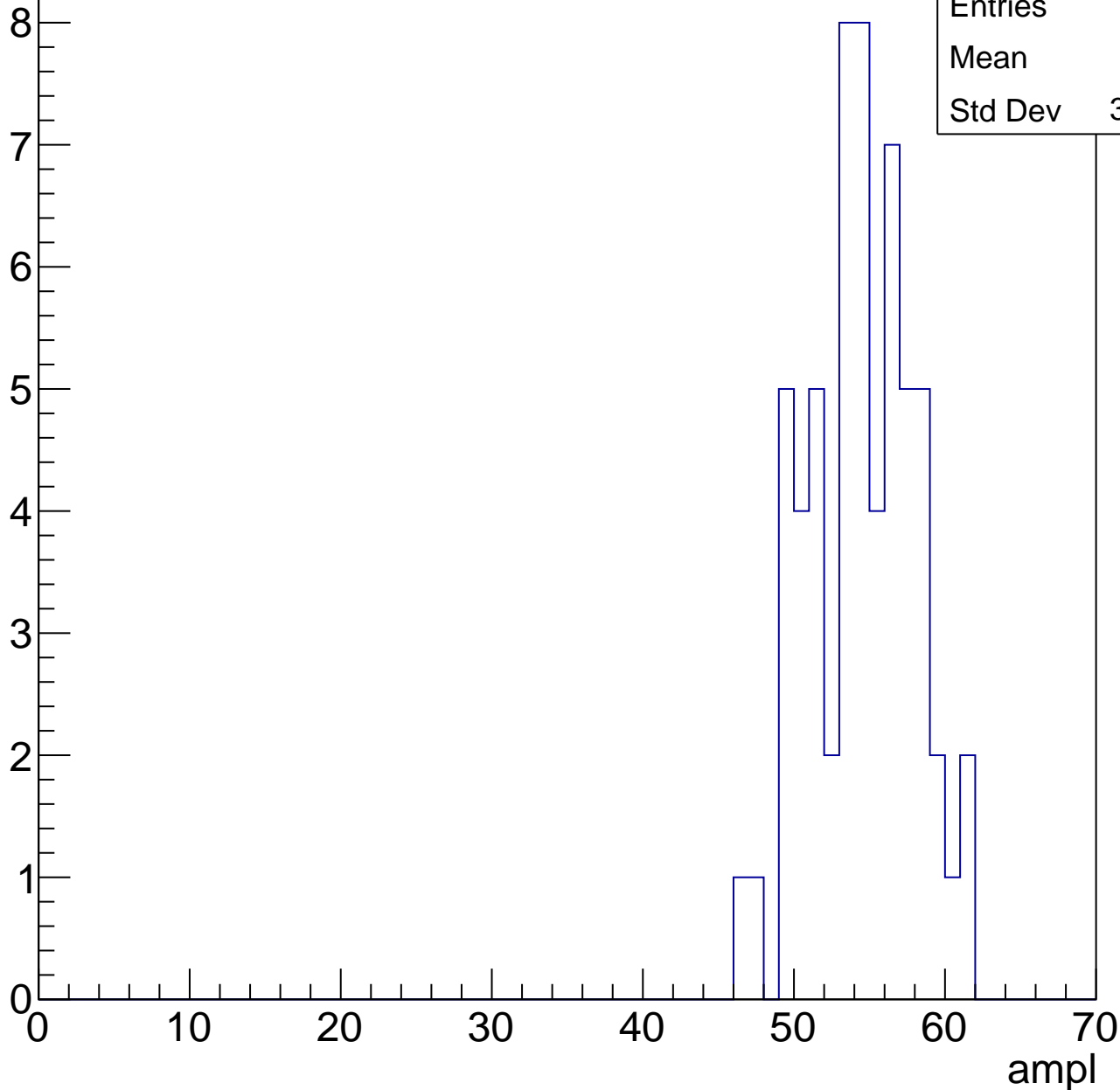


B1L102S, U19-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54
Std Dev	3.425



B1L102S, U19-ch75, adc5

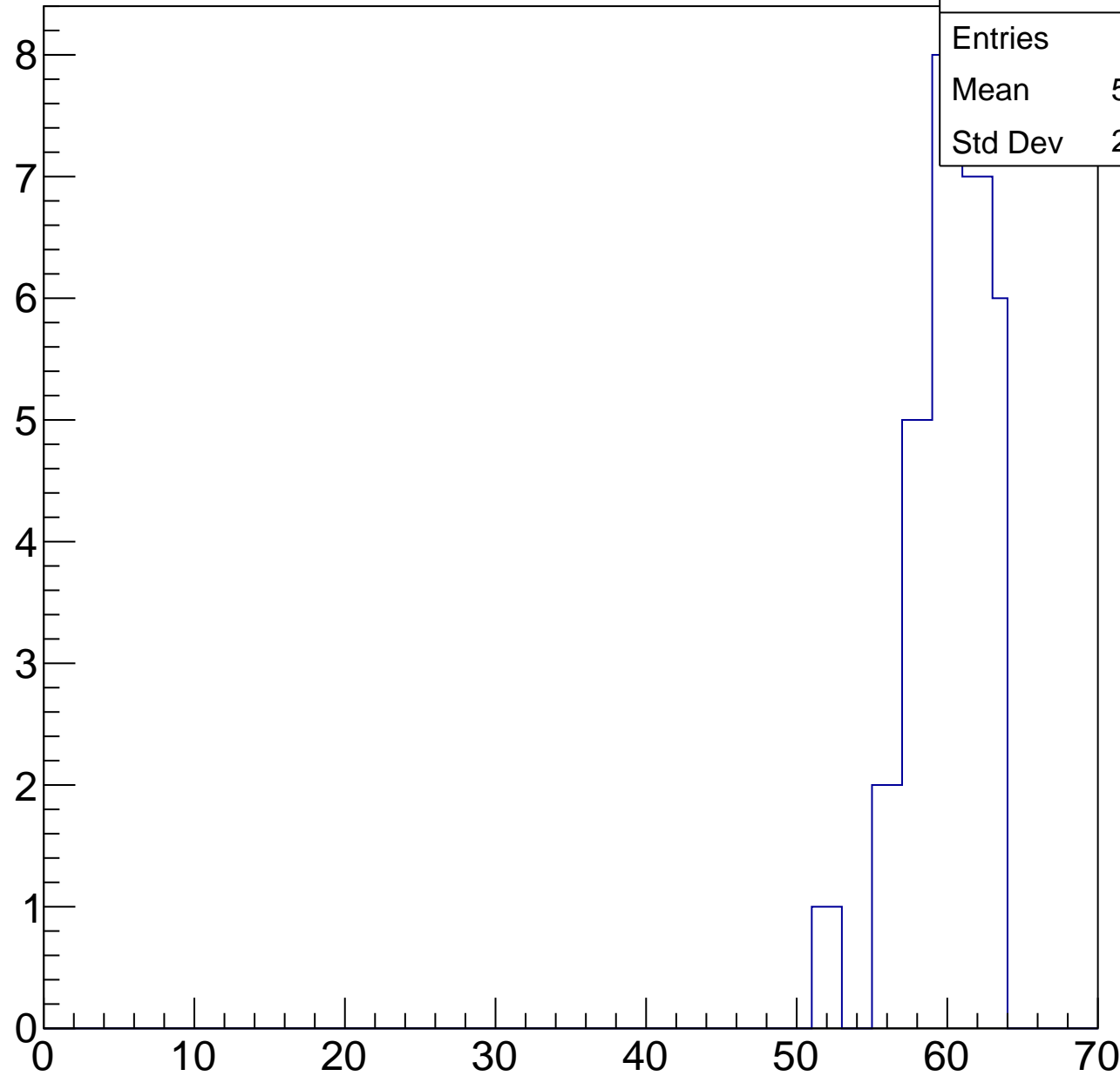
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.44
Std Dev	2.678

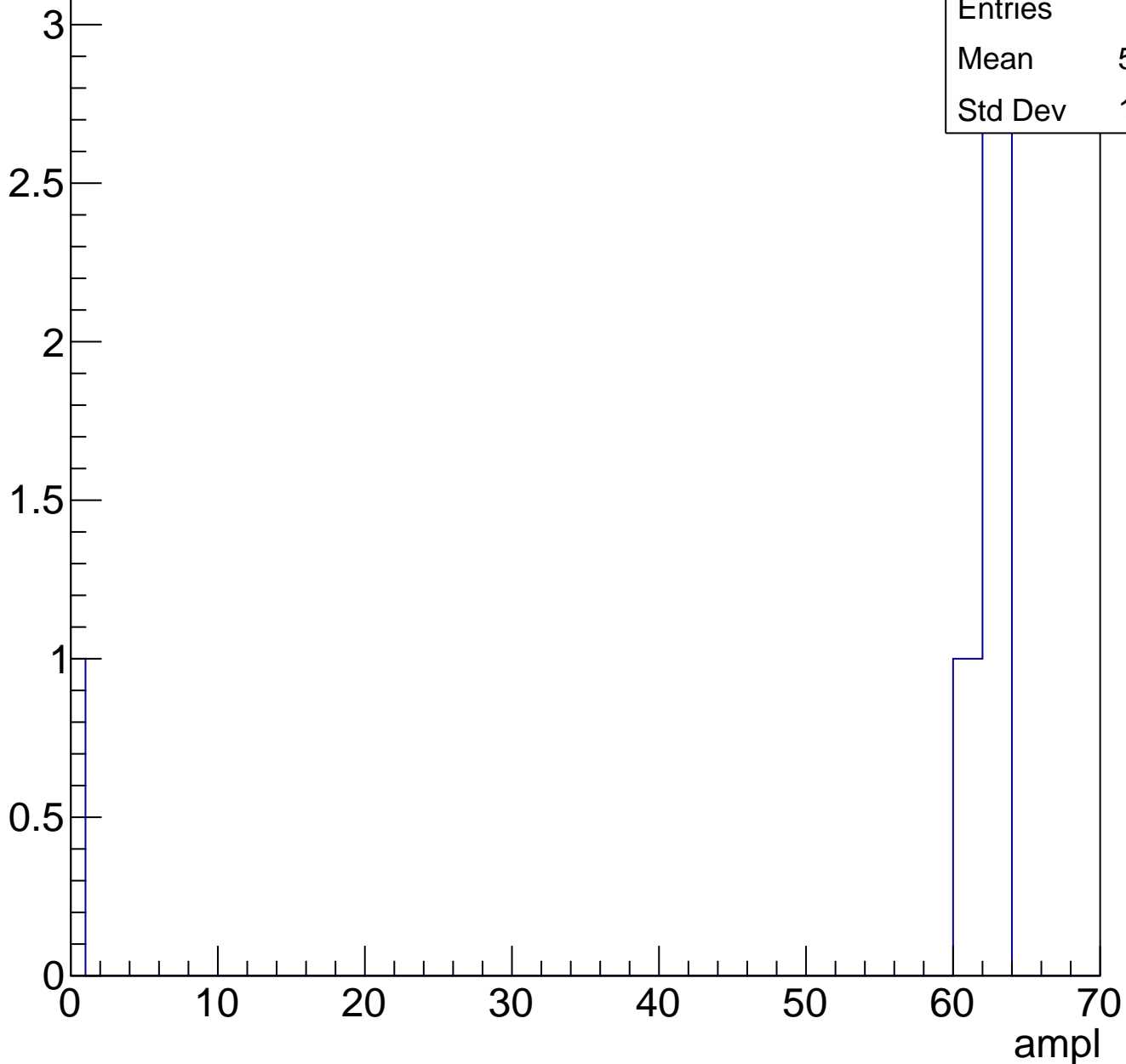
ampl



B1L102S, U19-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch76, adc0

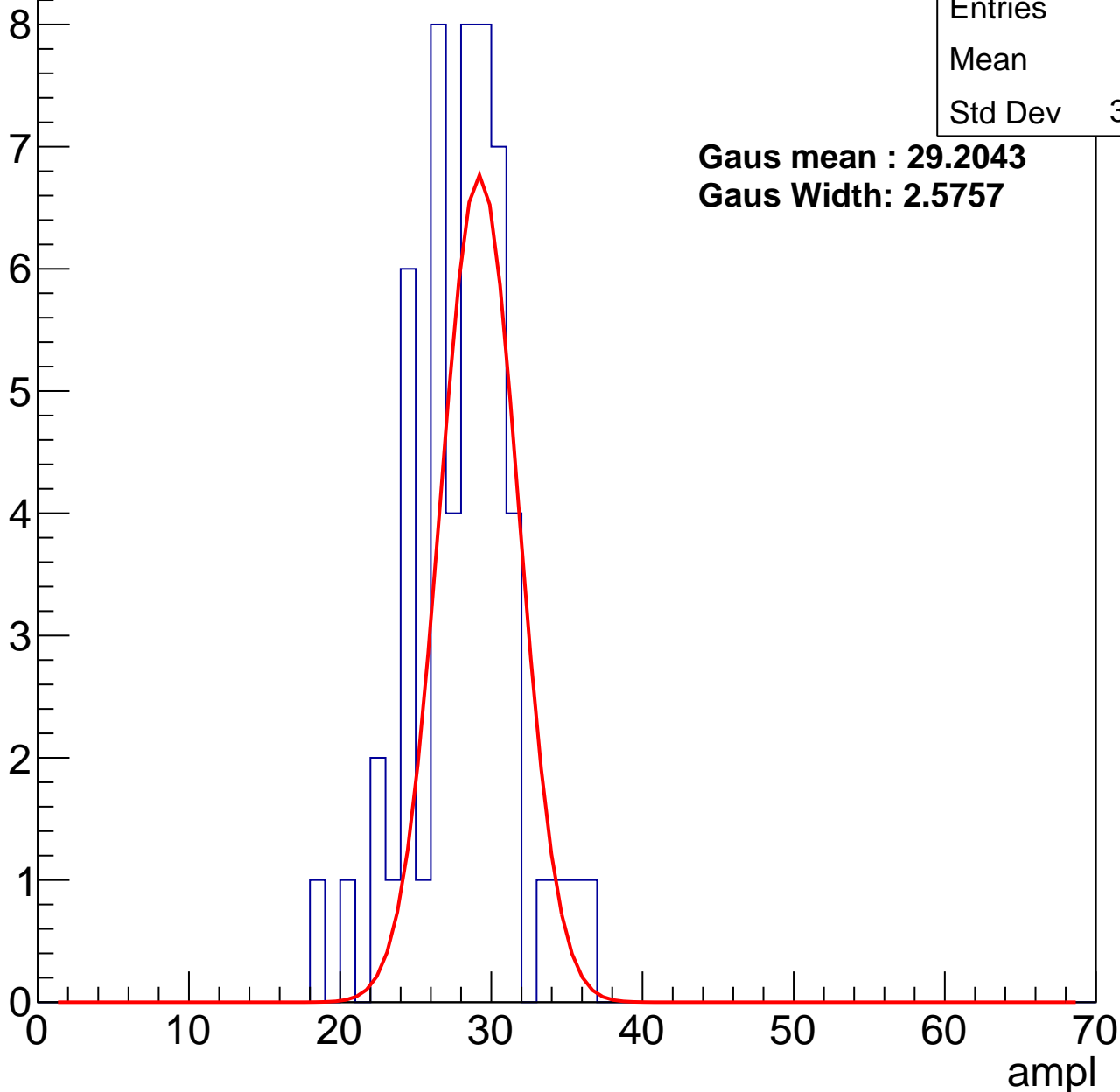
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	27.6
Std Dev	3.409

Gaus mean : 29.2043

Gaus Width: 2.5757



B1L102S, U19-ch76, adc1

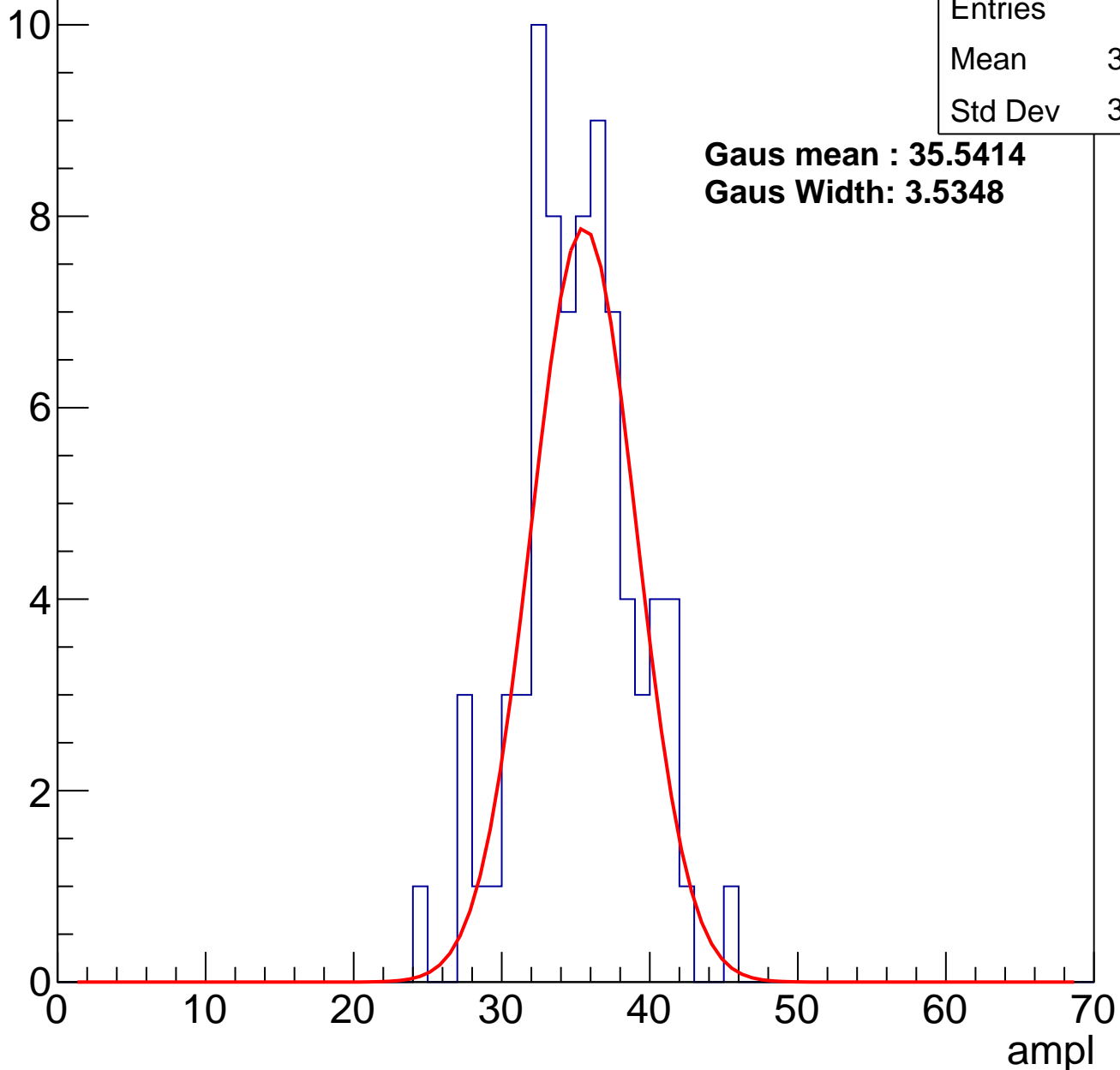
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	34.74
Std Dev	3.855

Gaus mean : 35.5414

Gaus Width: 3.5348

Entry



B1L102S, U19-ch76, adc2

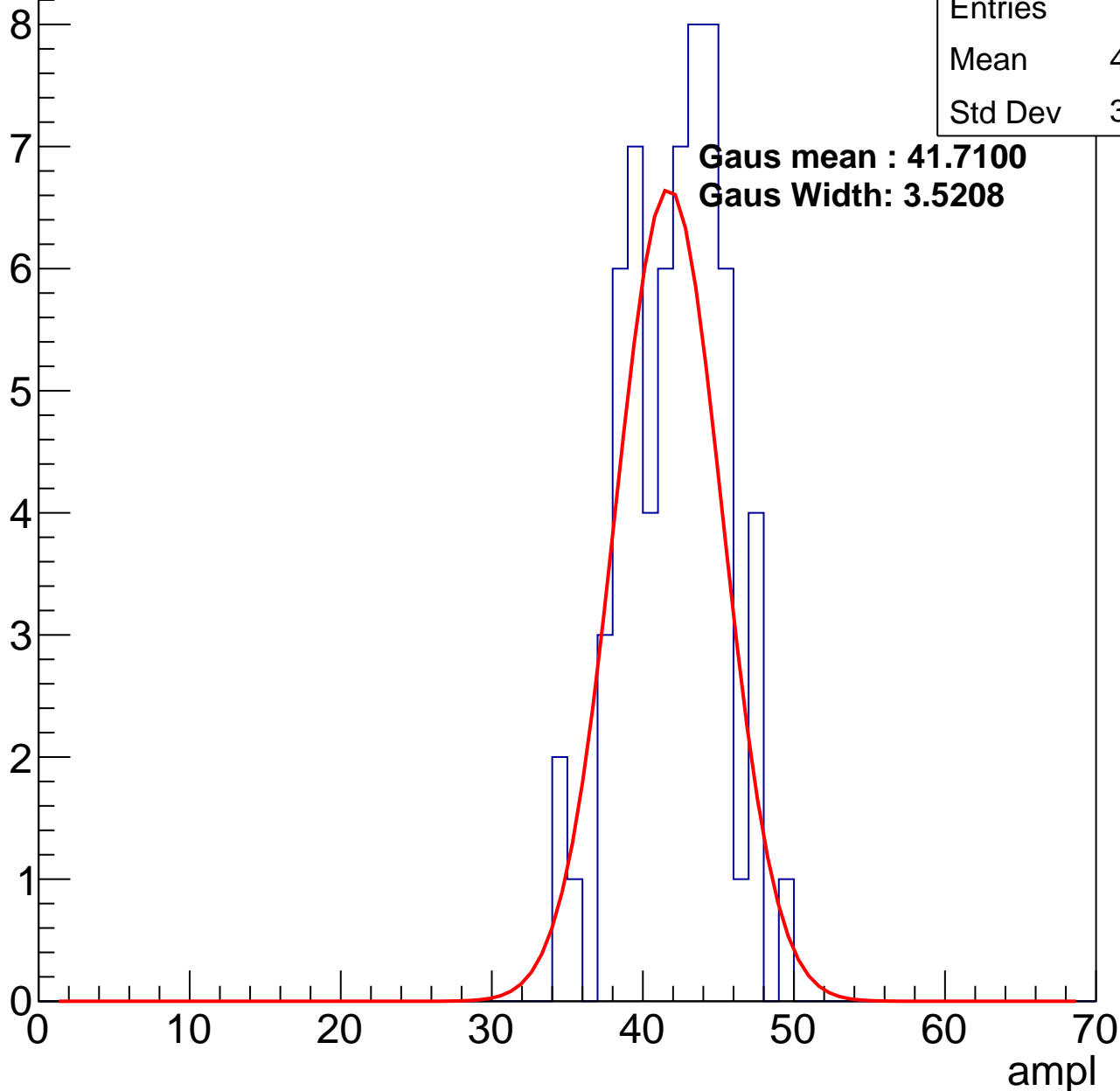
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.62
Std Dev	3.267

Gaus mean : 41.7100

Gaus Width: 3.5208

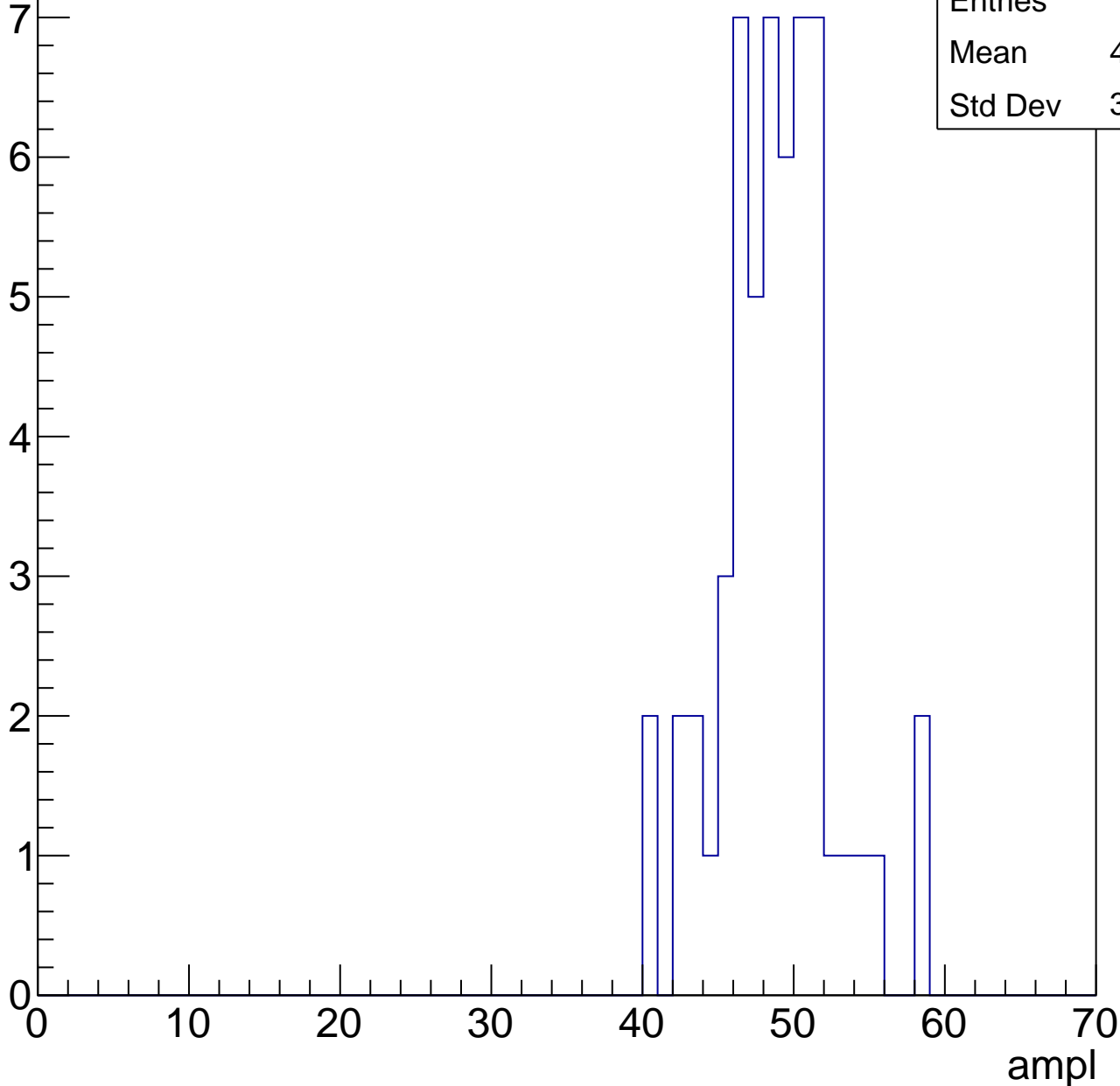


B1L102S, U19-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.24
Std Dev	3.683

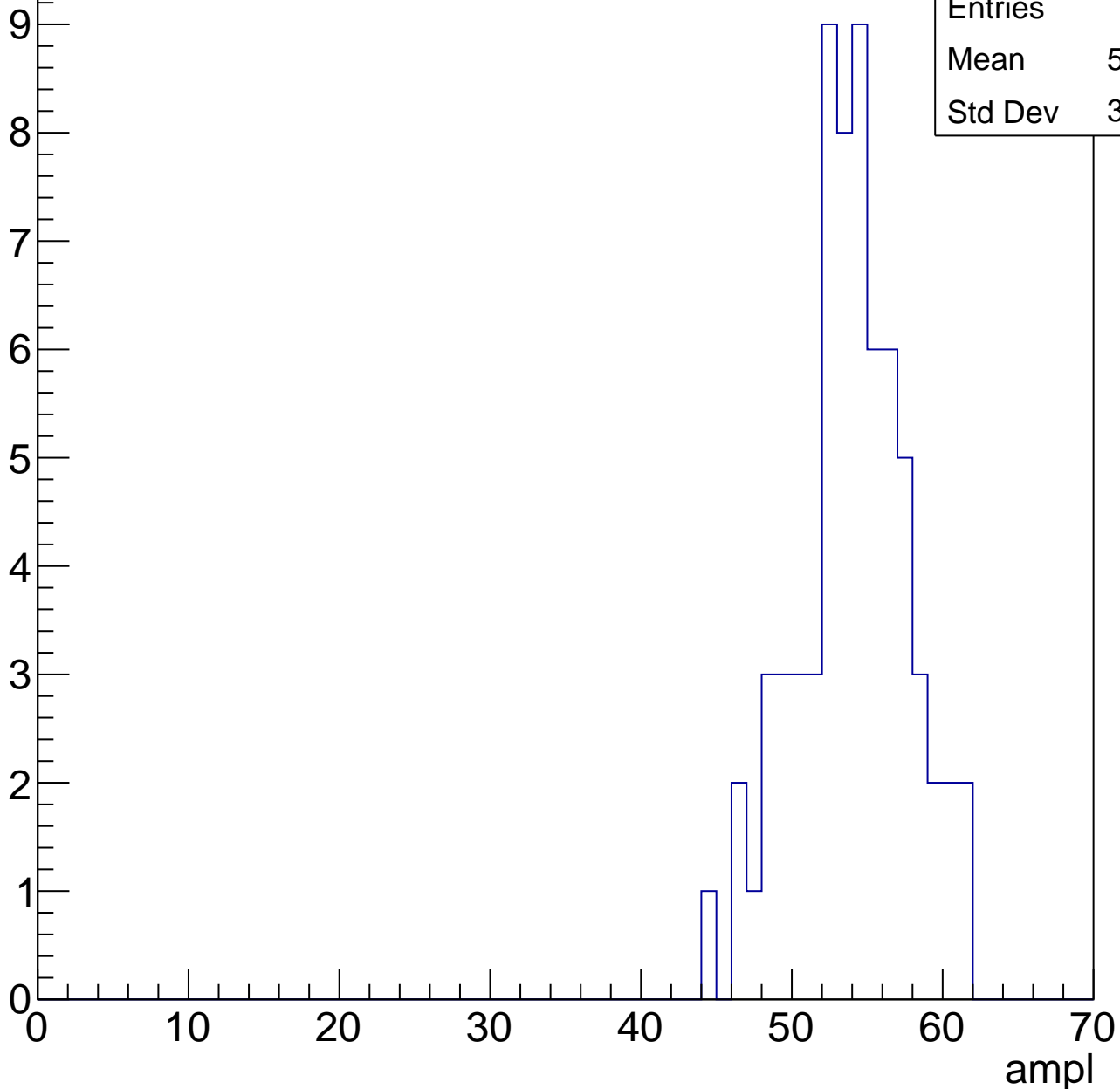


B1L102S, U19-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	53.53
Std Dev	3.652

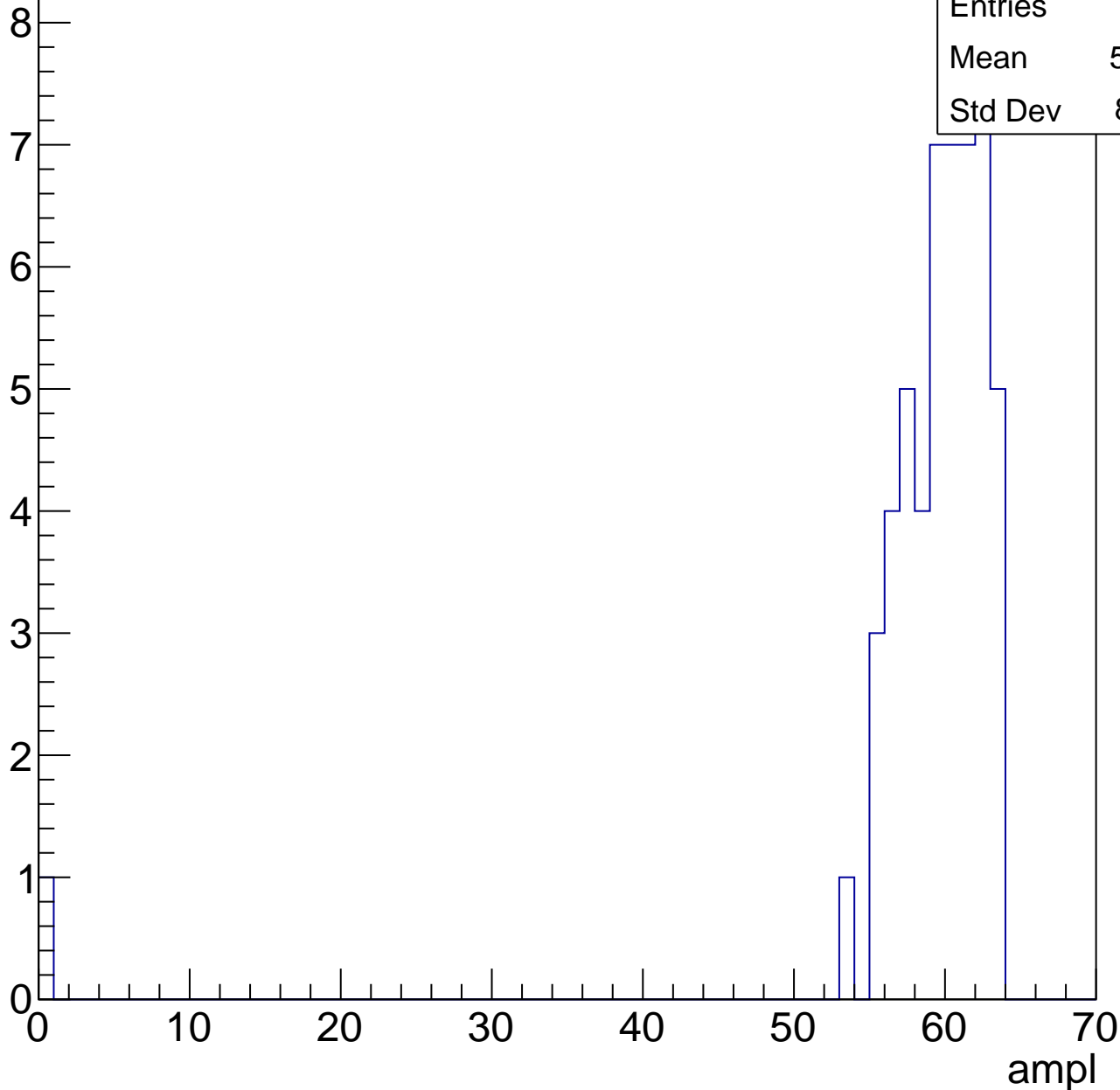


B1L102S, U19-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.27
Std Dev	8.531

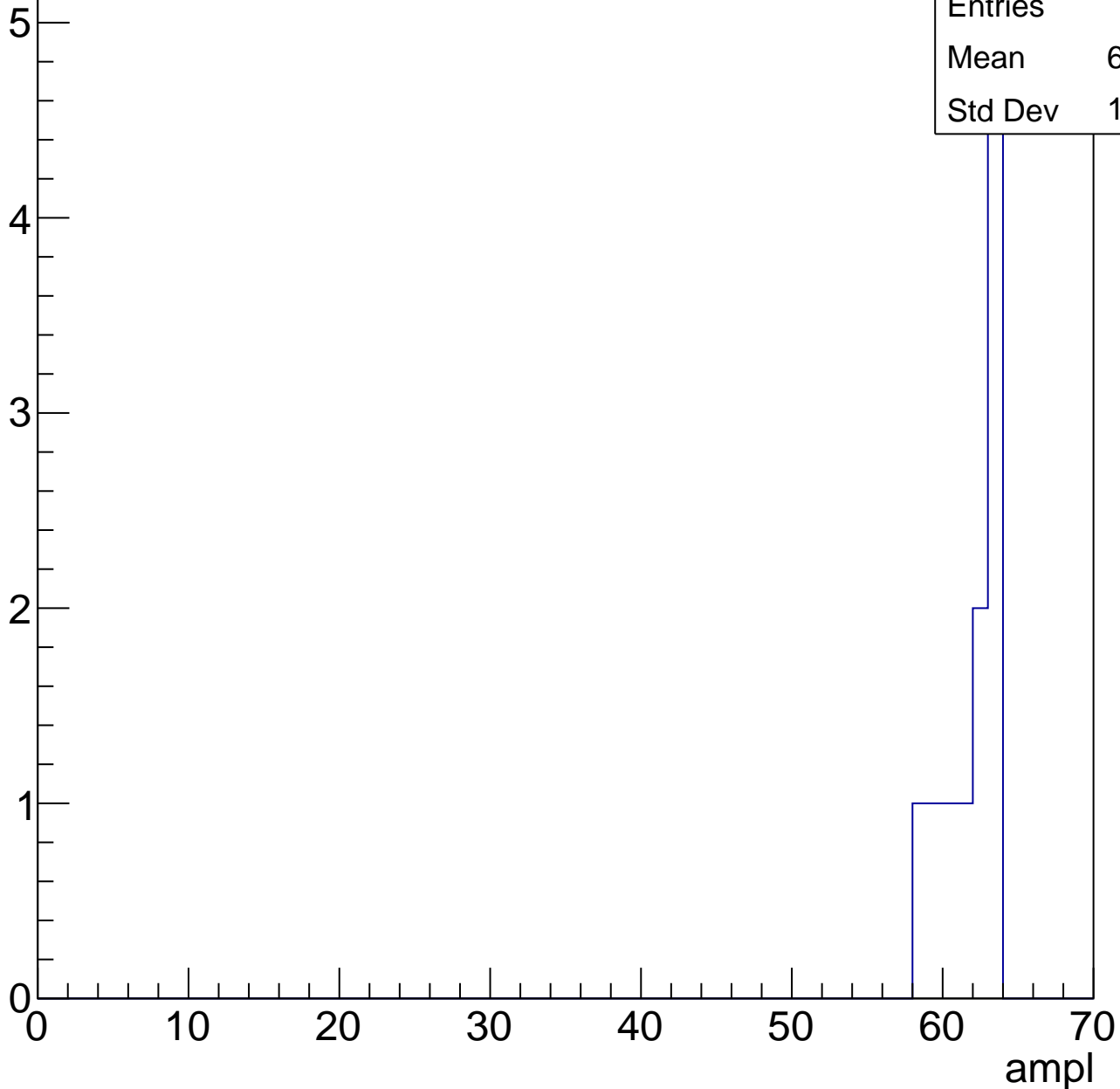


B1L102S, U19-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.55
Std Dev	1.725



B1L102S, U19-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch77, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	100
Mean	29.27
Std Dev	4.565

Gaus mean : 29.7595

Gaus Width: 4.2031

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

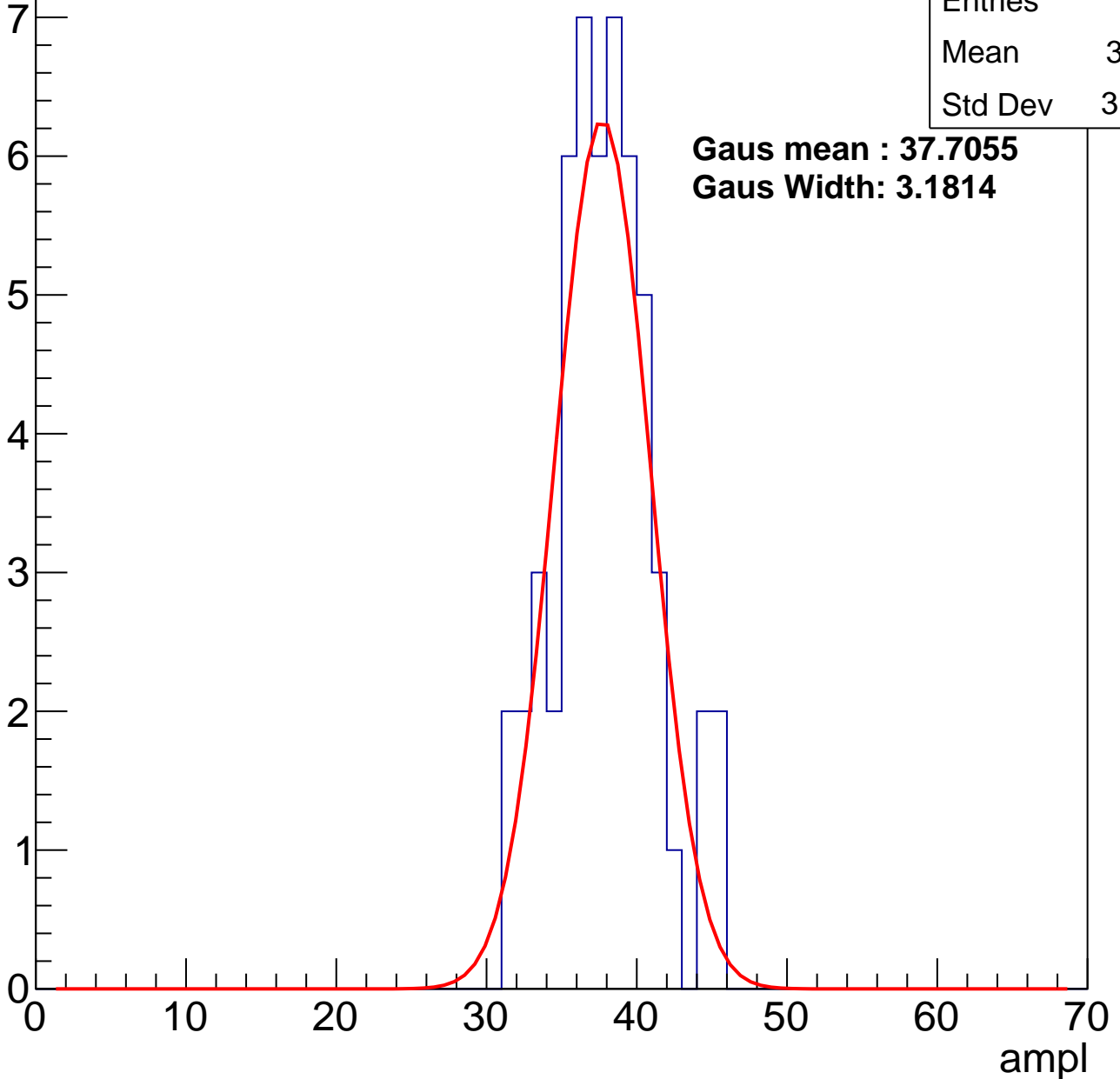
B1L102S, U19-ch77, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.41
Std Dev	3.292

Gaus mean : 37.7055
Gaus Width: 3.1814



B1L102S, U19-ch77, adc2

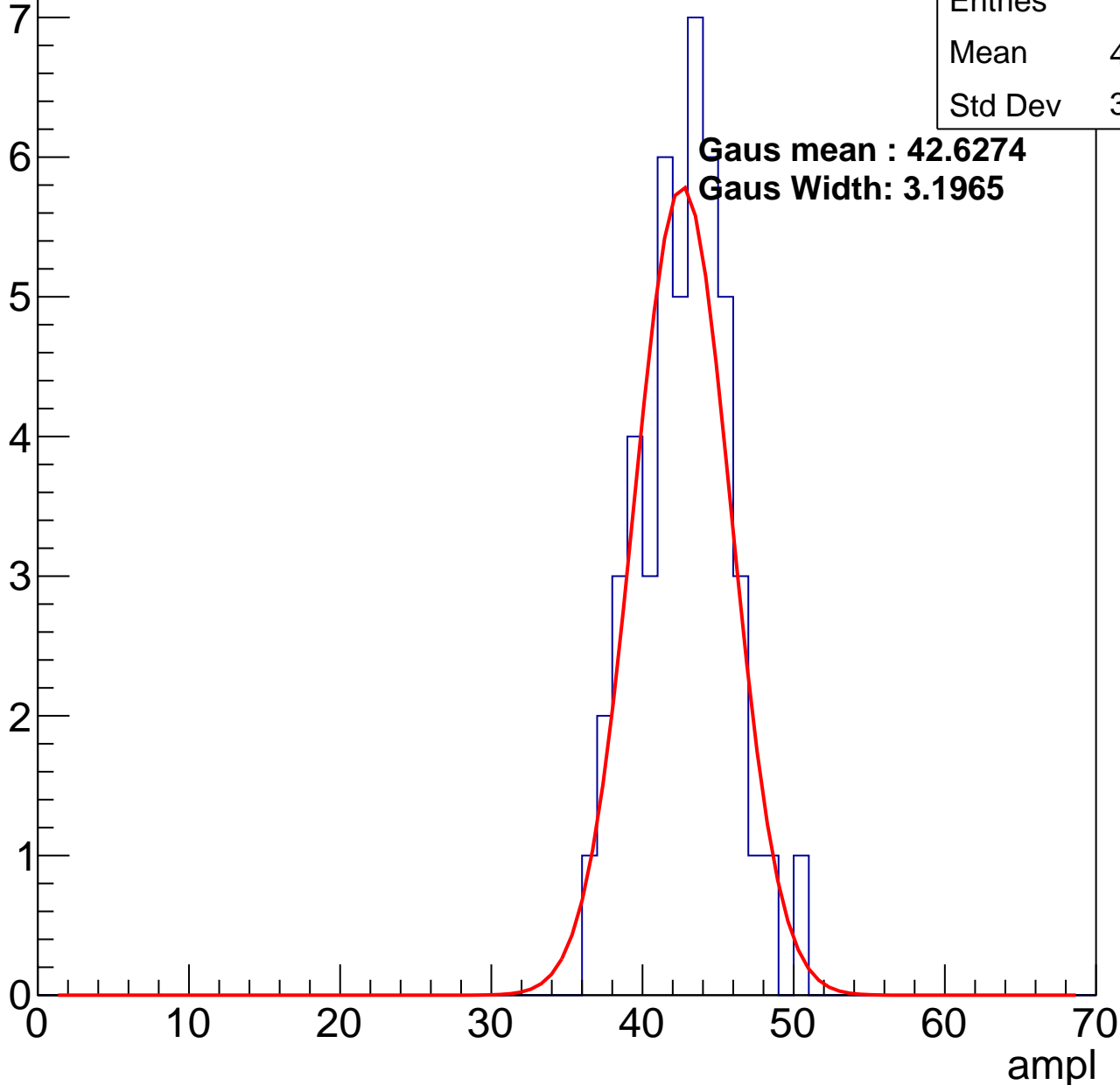
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.27
Std Dev	3.005

Gaus mean : 42.6274

Gaus Width: 3.1965

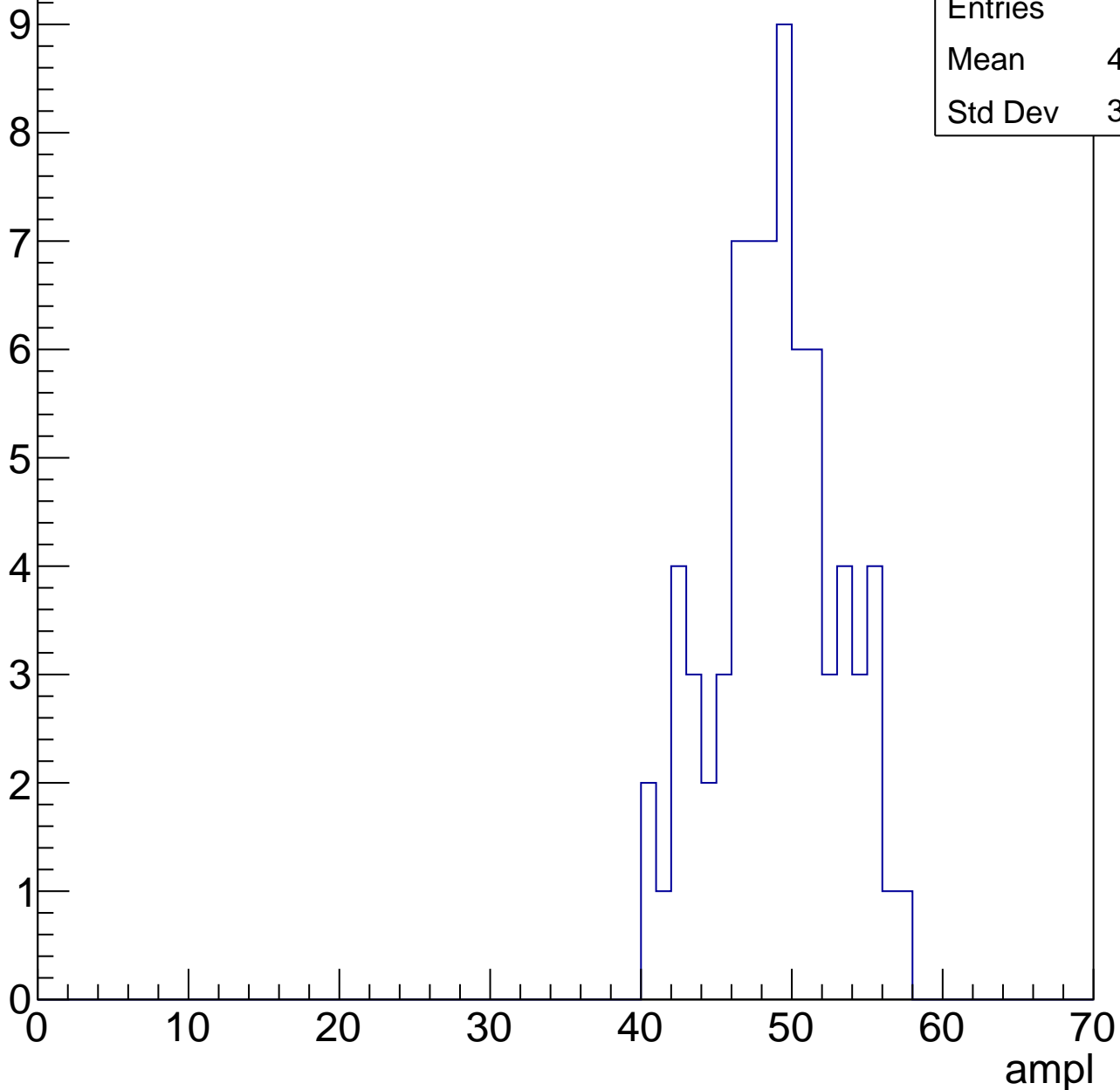


B1L102S, U19-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.47
Std Dev	3.983

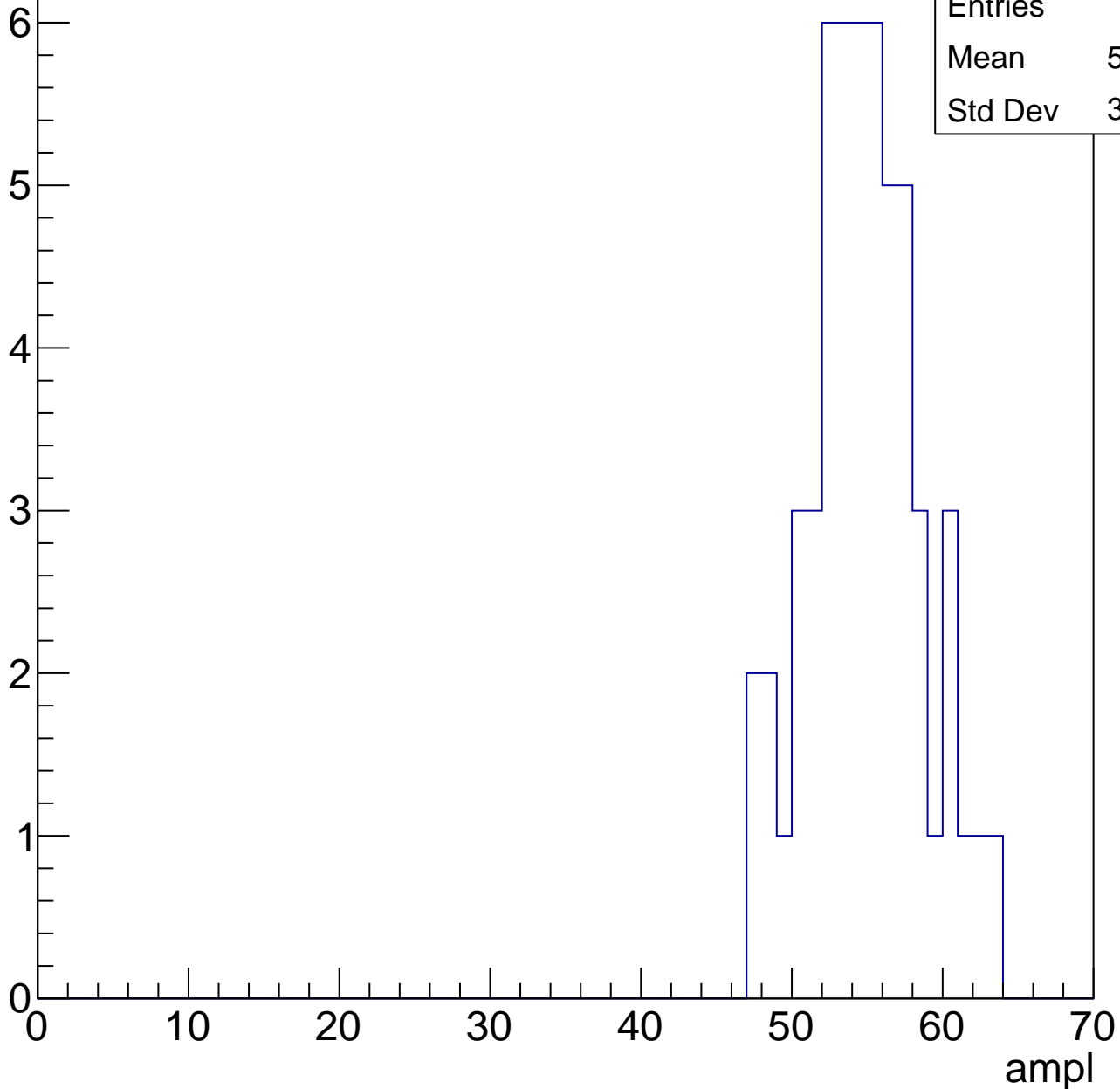


B1L102S, U19-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.36
Std Dev	3.665



B1L102S, U19-ch77, adc5

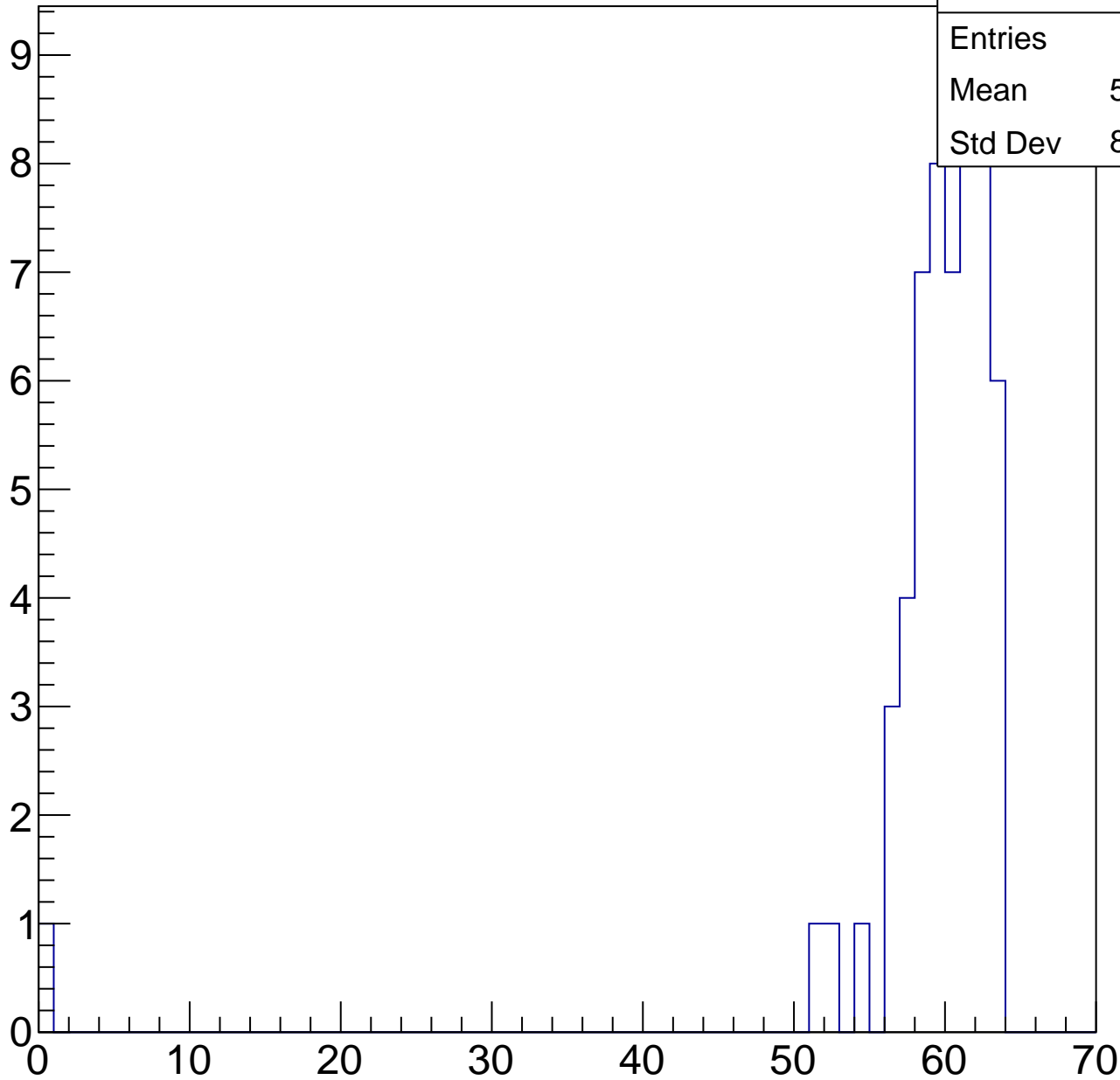
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.46
Std Dev	8.307

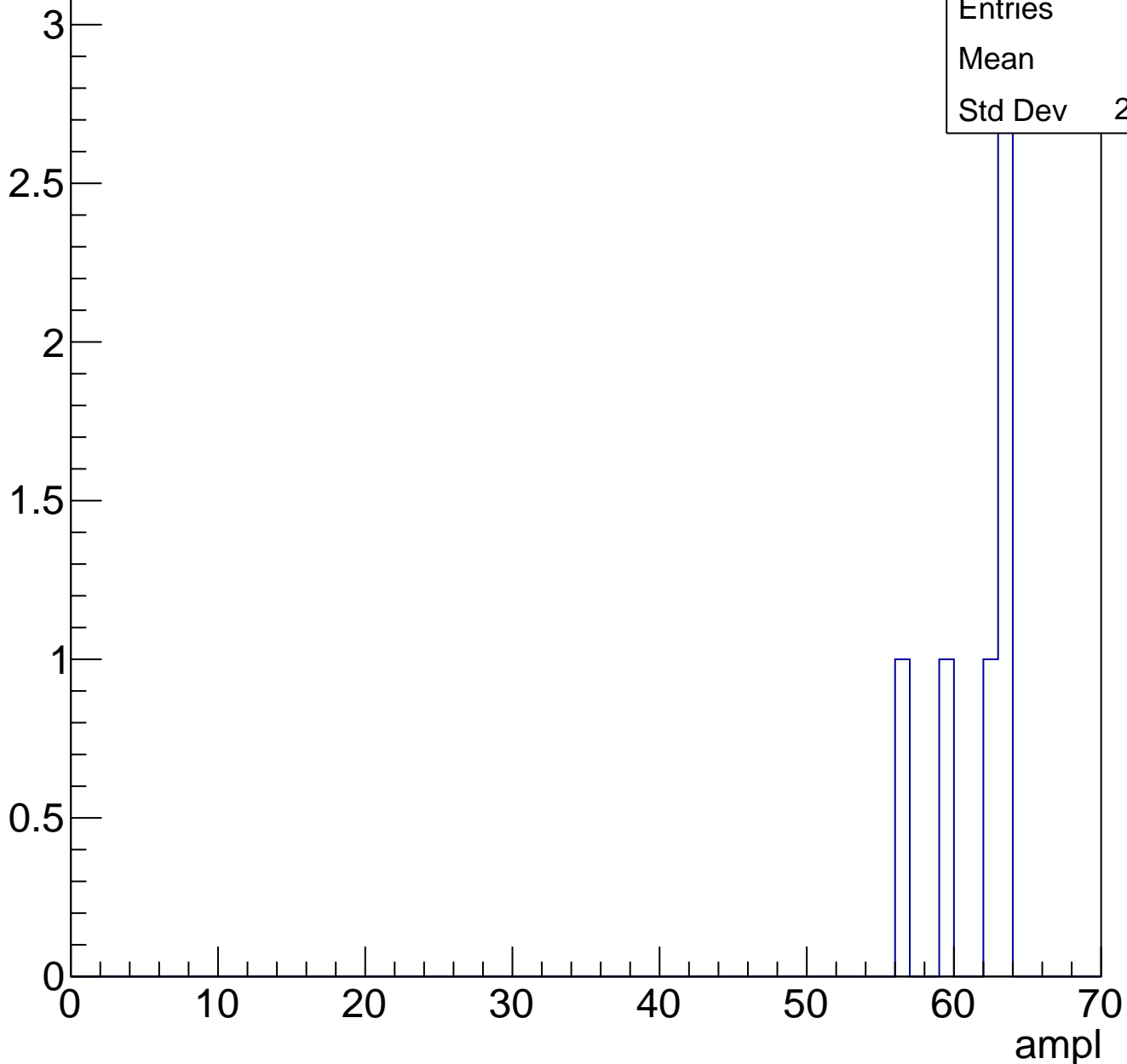
ampl



B1L102S, U19-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U19-ch78, adc0

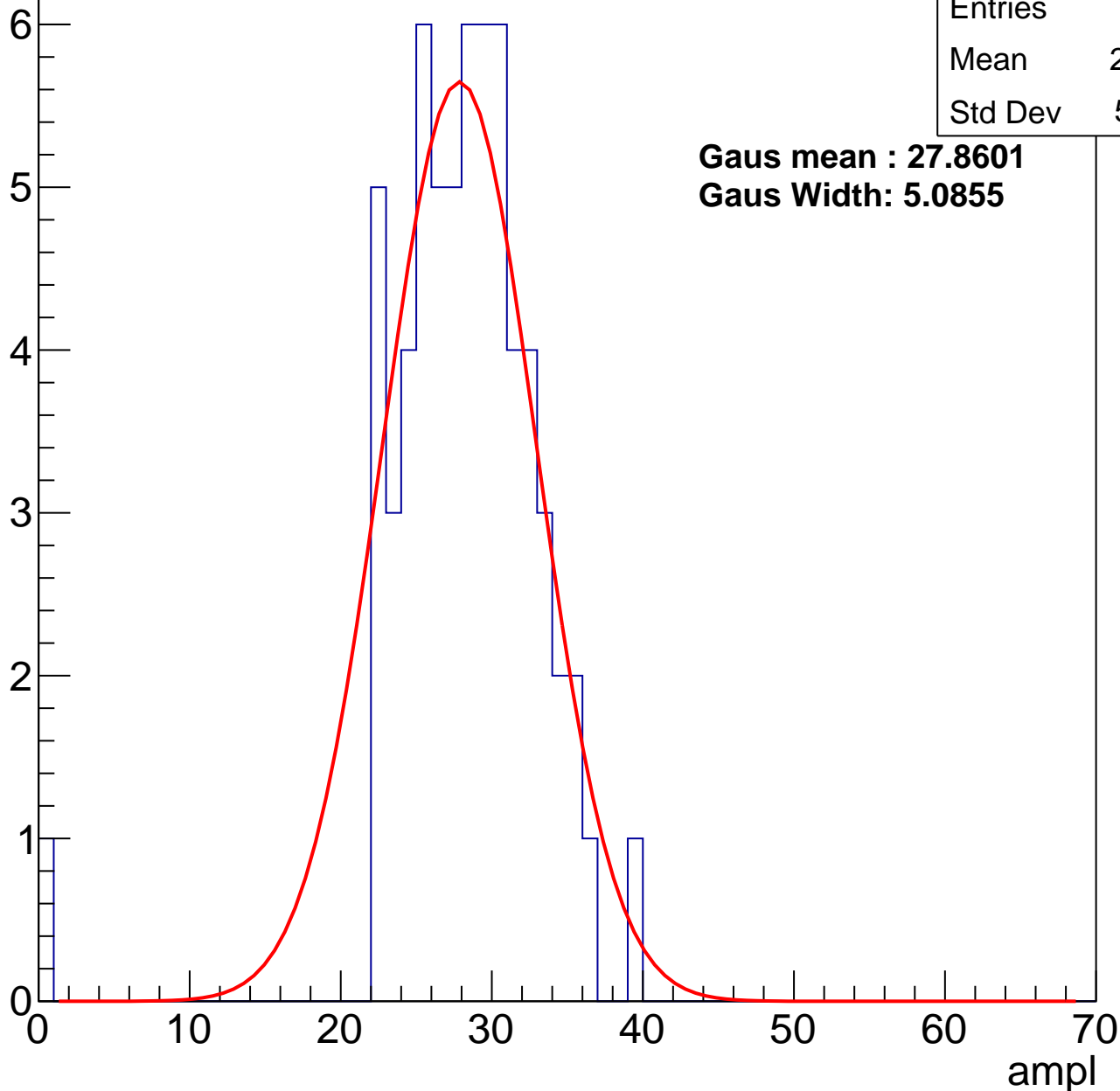
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.75
Std Dev	5.211

Gaus mean : 27.8601

Gaus Width: 5.0855



B1L102S, U19-ch78, adc1

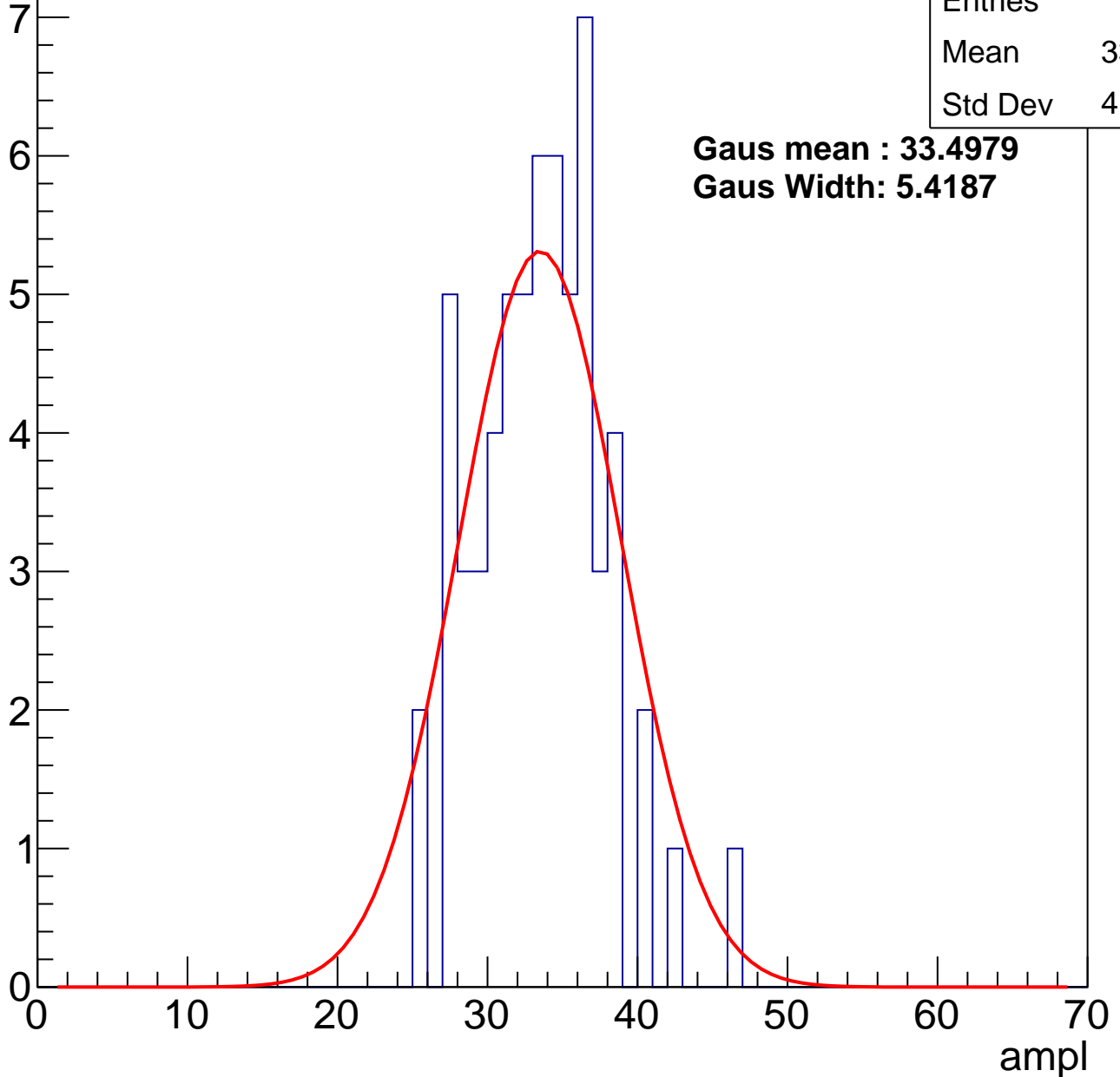
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.08
Std Dev	4.167

Gaus mean : 33.4979

Gaus Width: 5.4187



B1L102S, U19-ch78, adc2

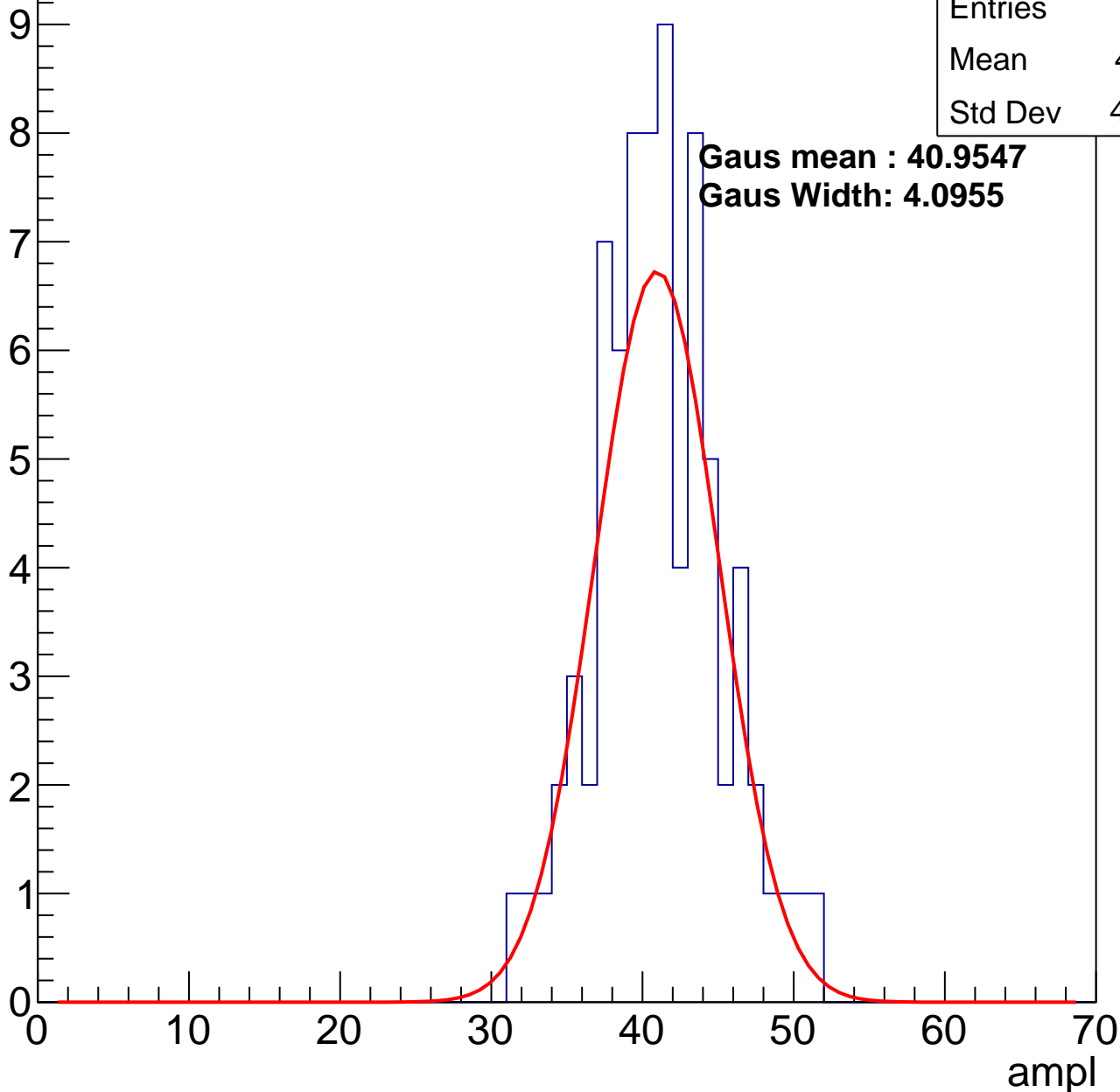
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	40.61
Std Dev	4.068

Gaus mean : 40.9547

Gaus Width: 4.0955

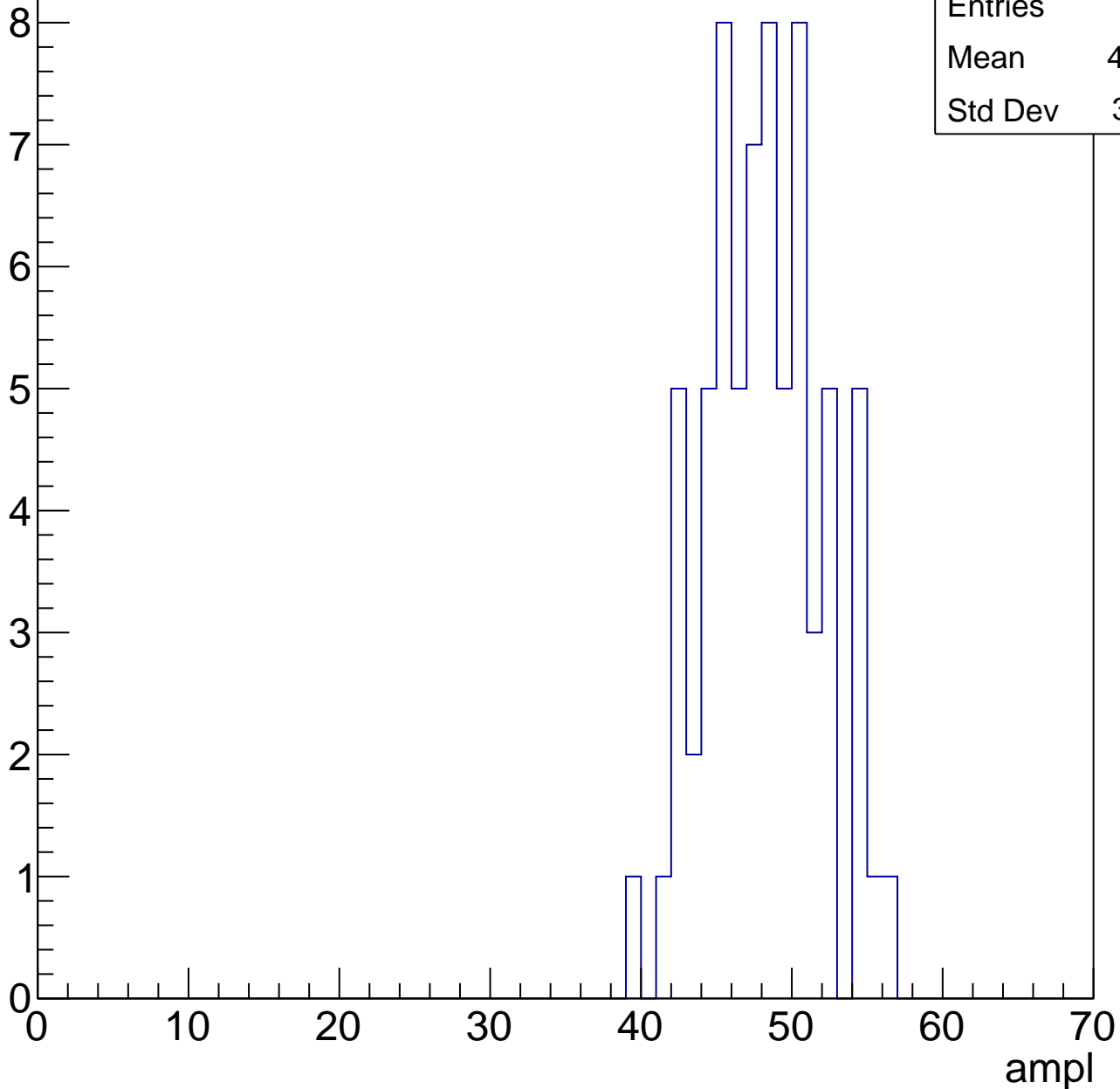


B1L102S, U19-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.69
Std Dev	3.721



B1L102S, U19-ch78, adc4

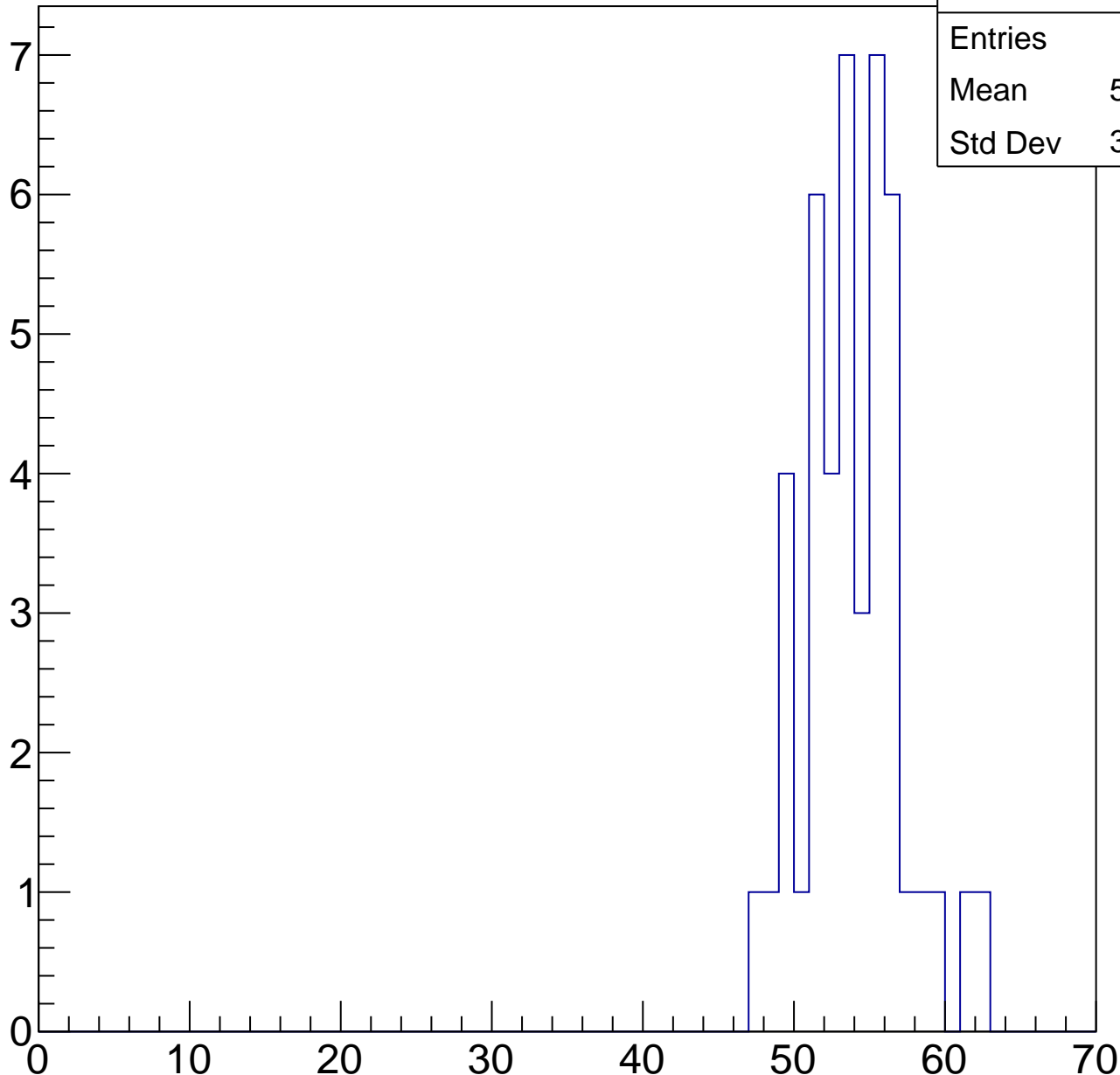
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	53.47
Std Dev	3.194

ampl

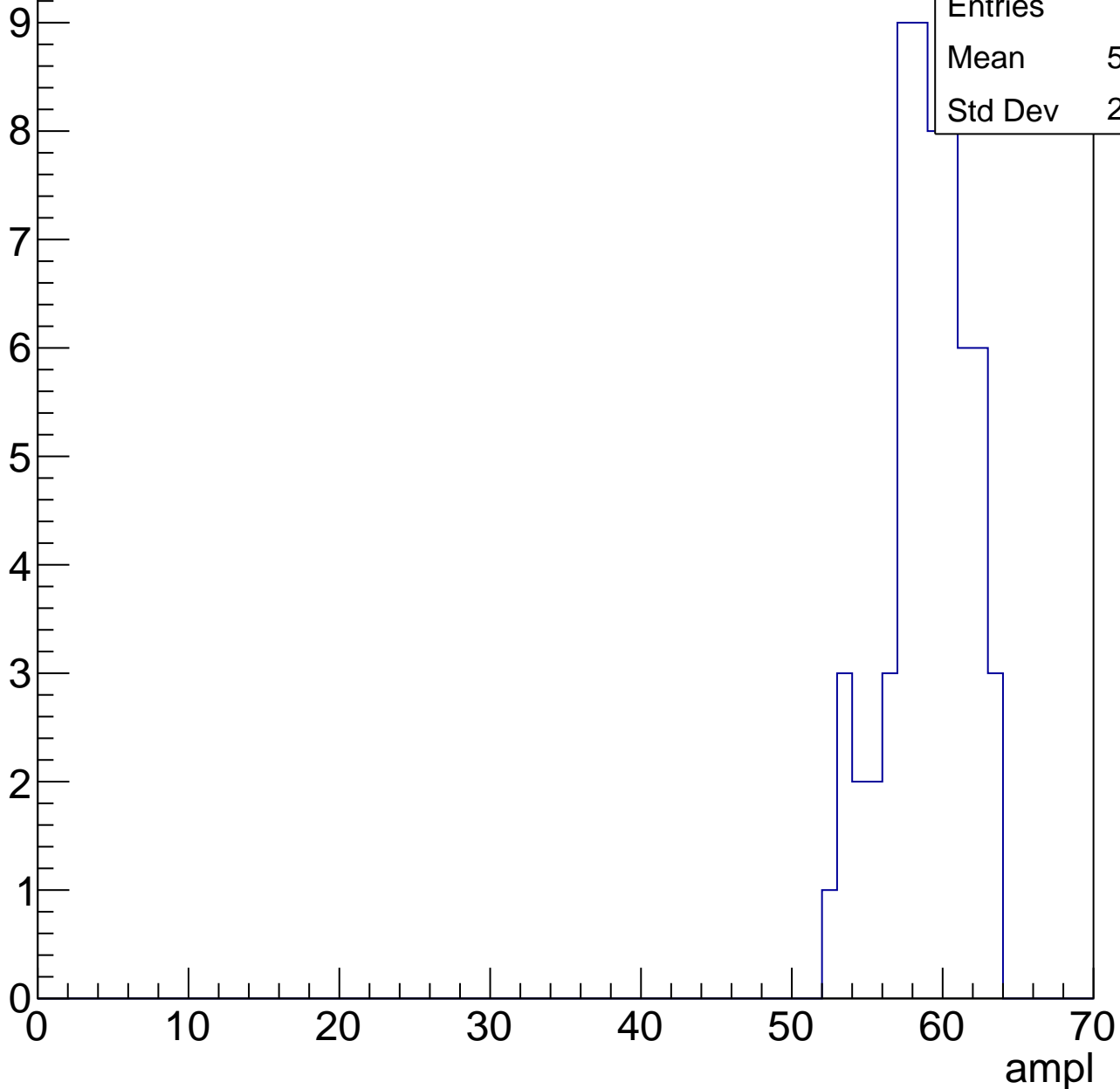


B1L102S, U19-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	58.54
Std Dev	2.665

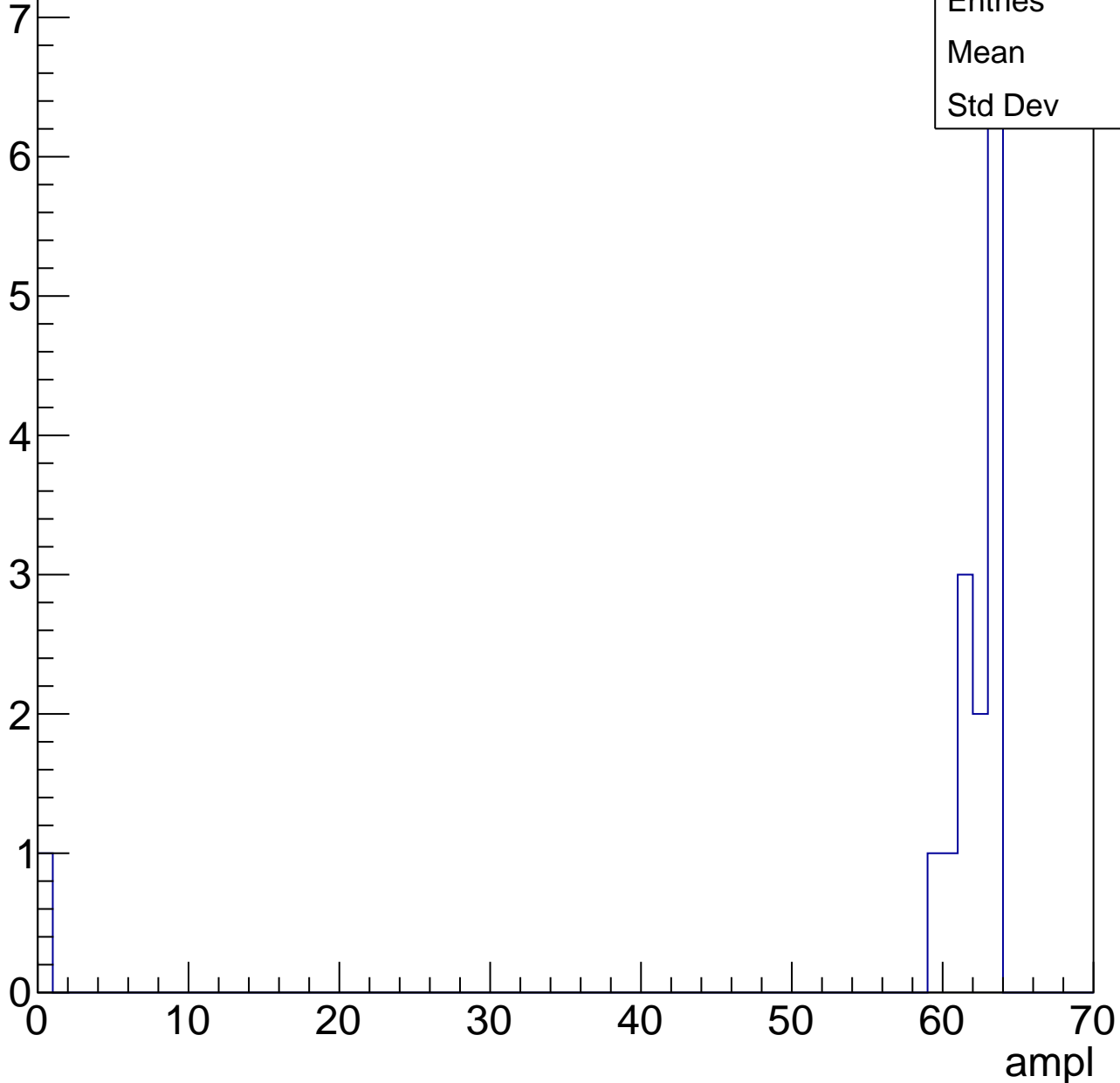


B1L102S, U19-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

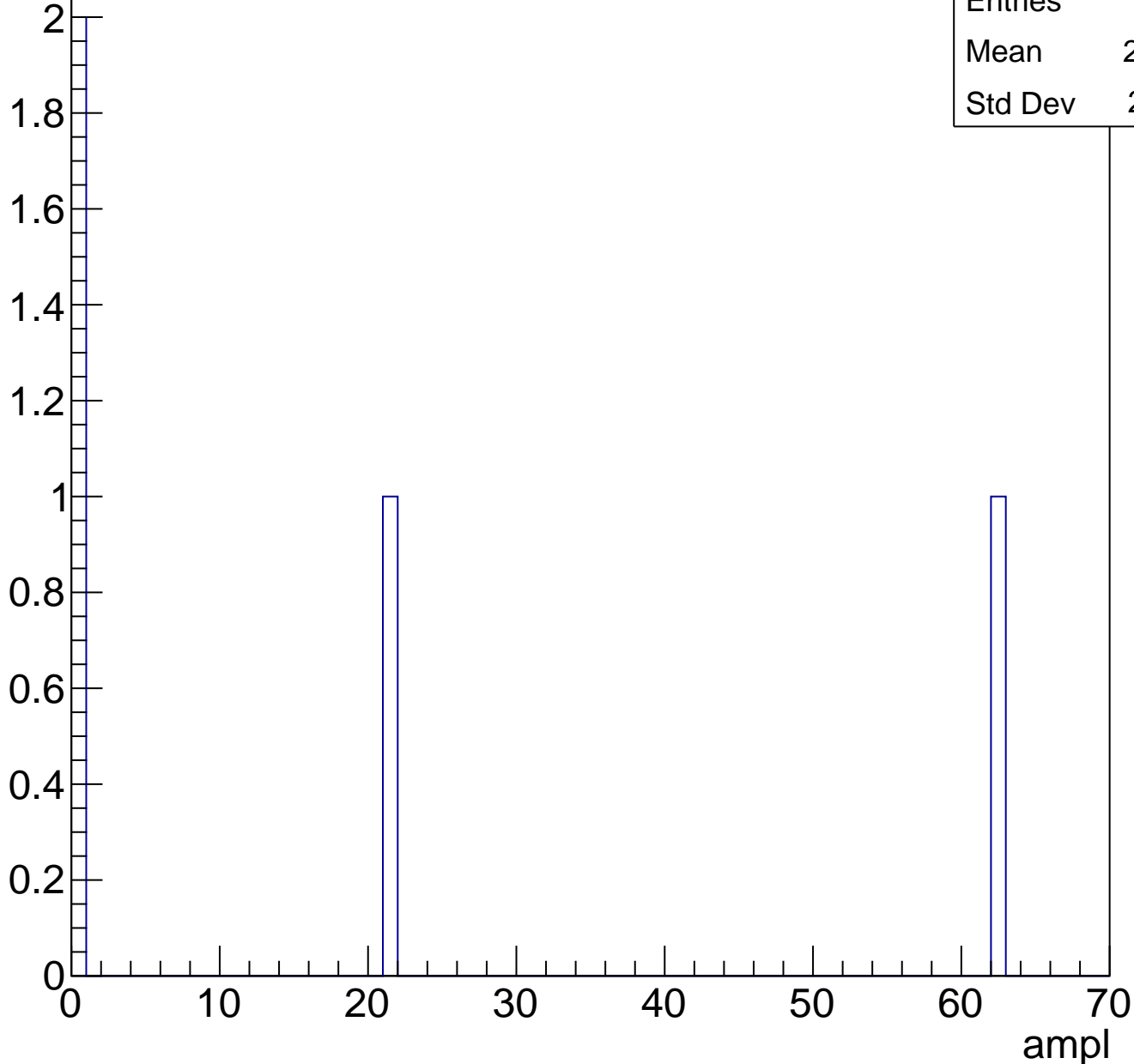
Entries	15
Mean	57.8
Std Dev	15.5



B1L102S, U19-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20.75
Std Dev	25.31

B1L102S, U19-ch79, adc0

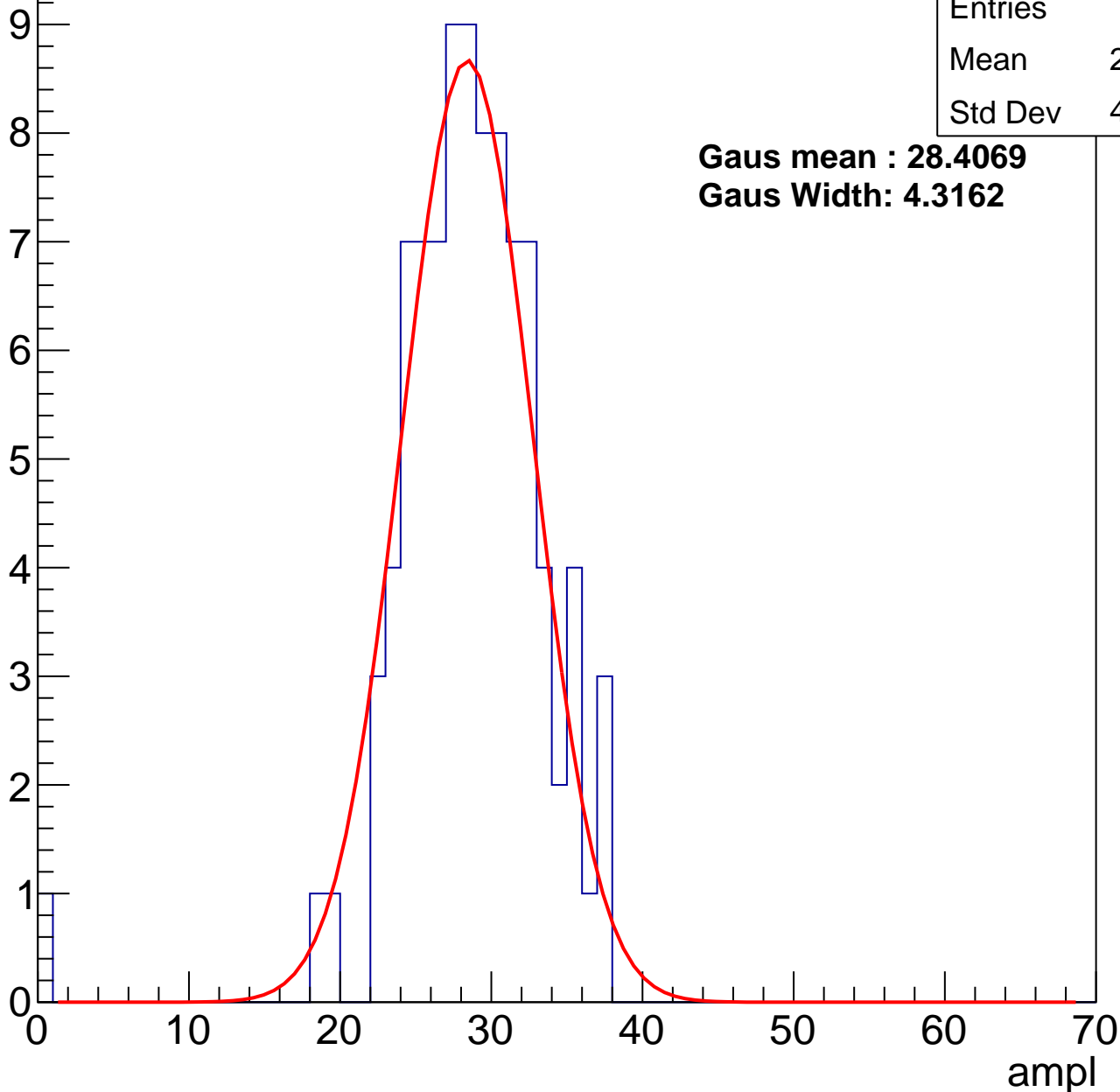
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	28.12
Std Dev	4.945

Gaus mean : 28.4069

Gaus Width: 4.3162



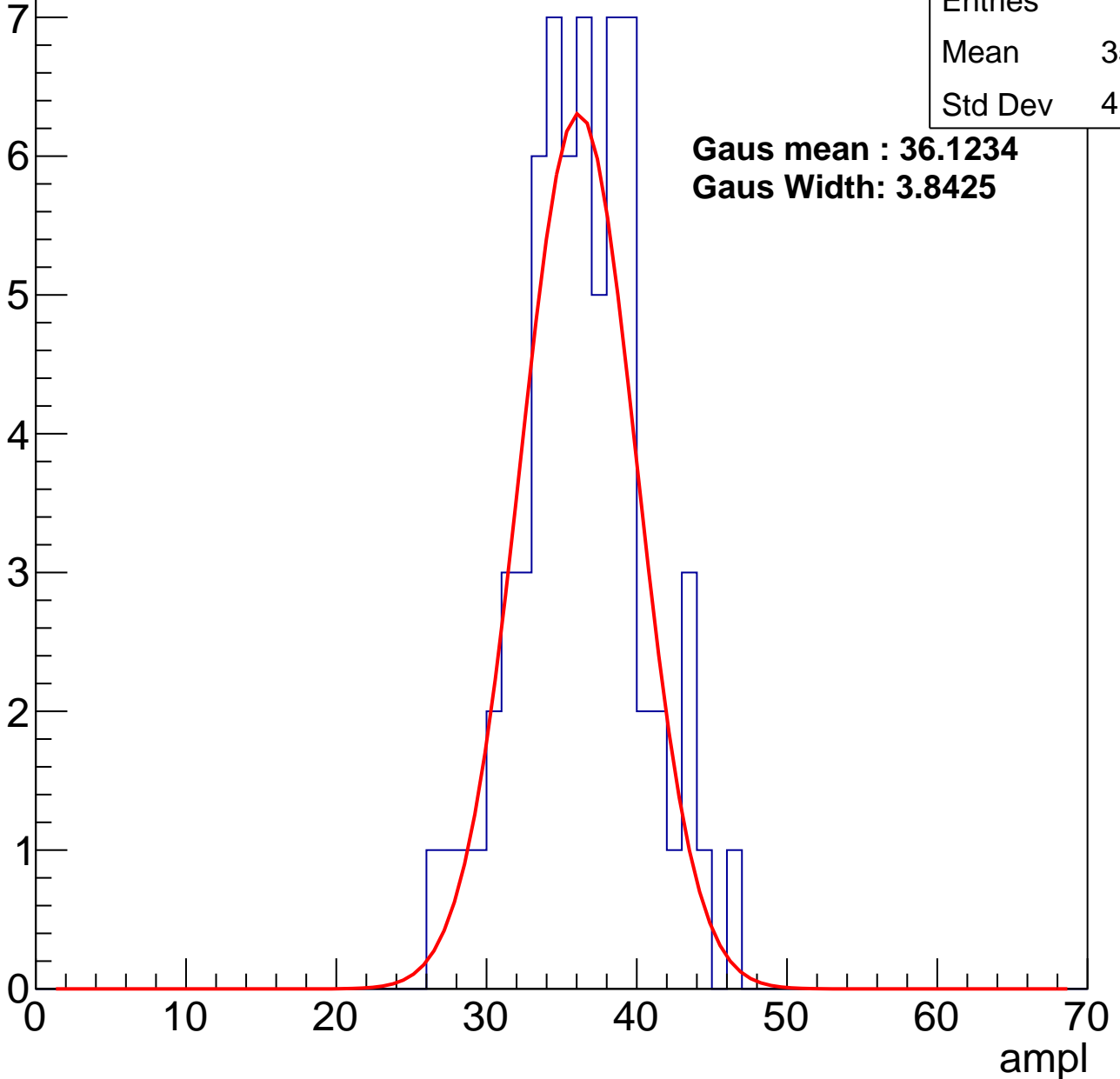
B1L102S, U19-ch79, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.88
Std Dev	4.054

Gaus mean : 36.1234
Gaus Width: 3.8425



B1L102S, U19-ch79, adc2

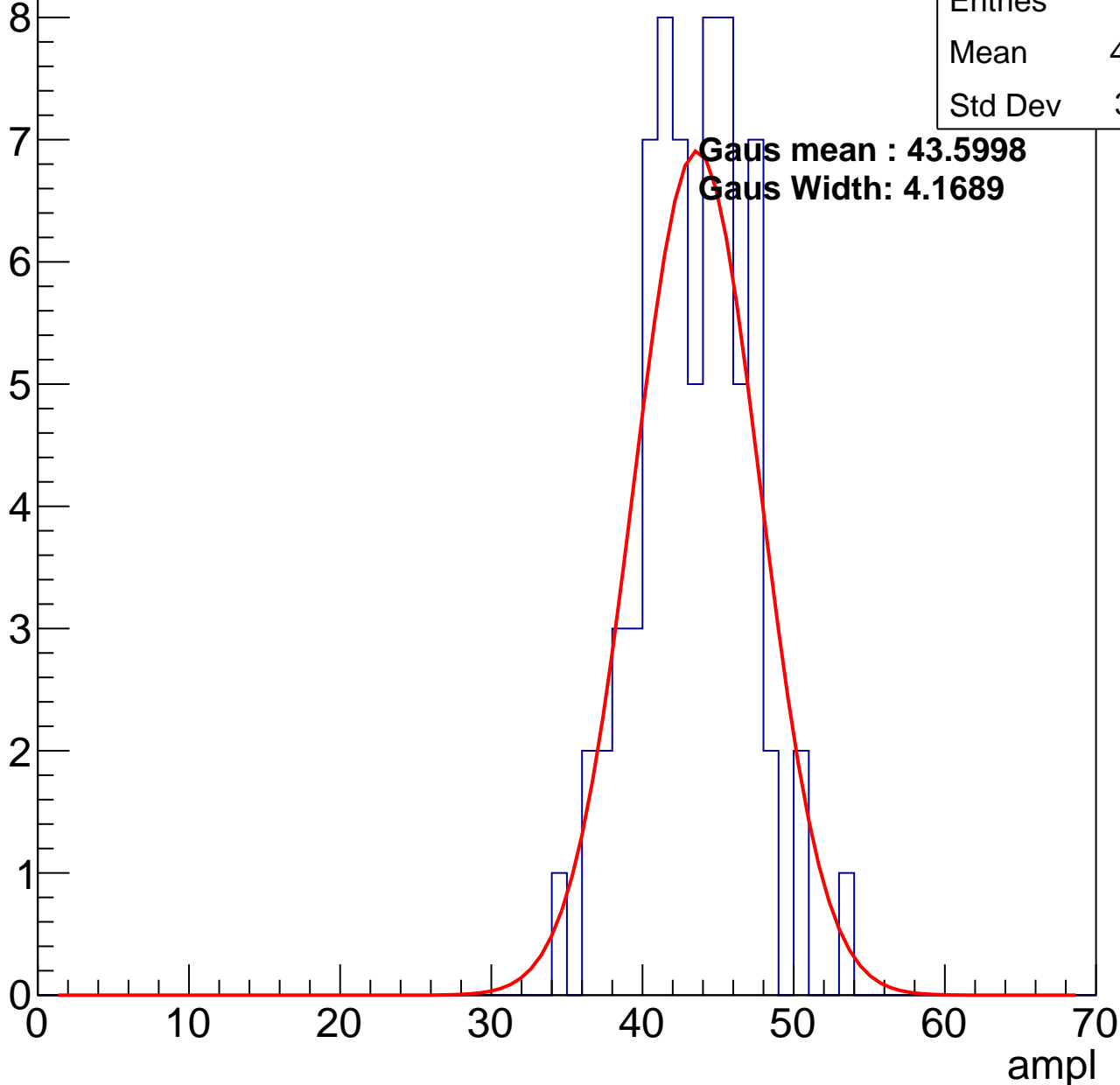
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.93
Std Dev	3.601

Gaus mean : 43.5998

Gaus Width: 4.1689

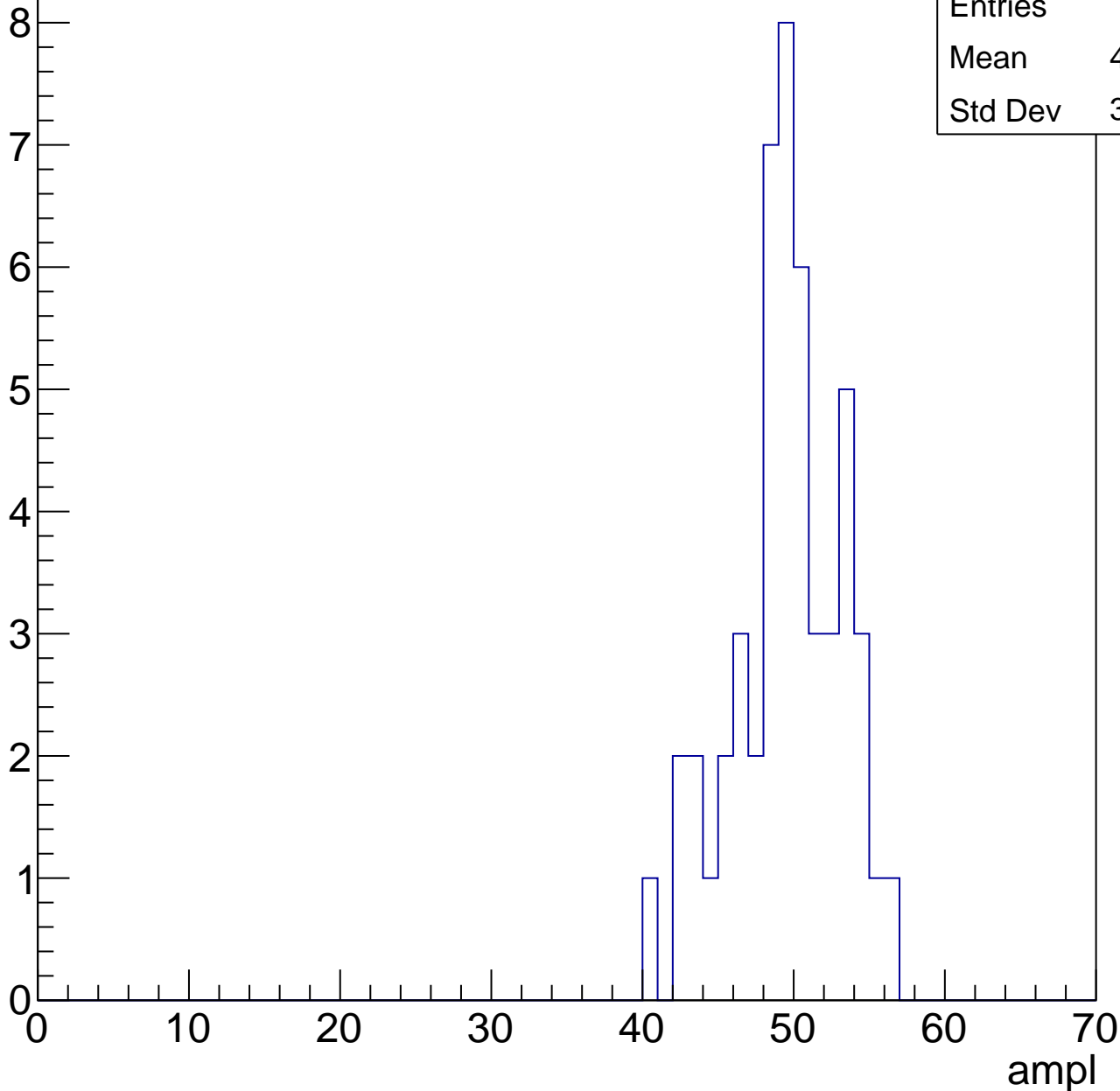


B1L102S, U19-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	49.02
Std Dev	3.564

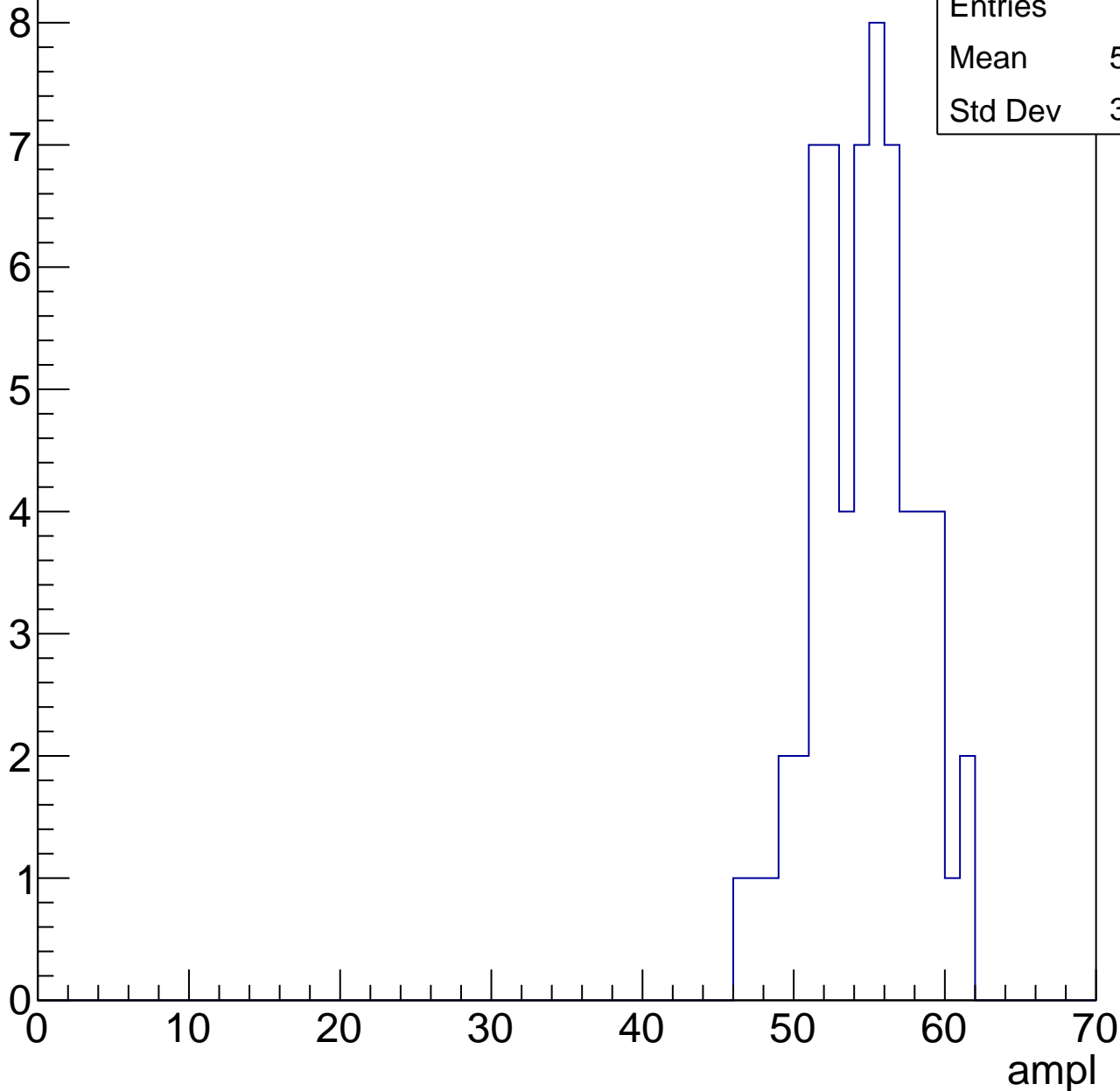


B1L102S, U19-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

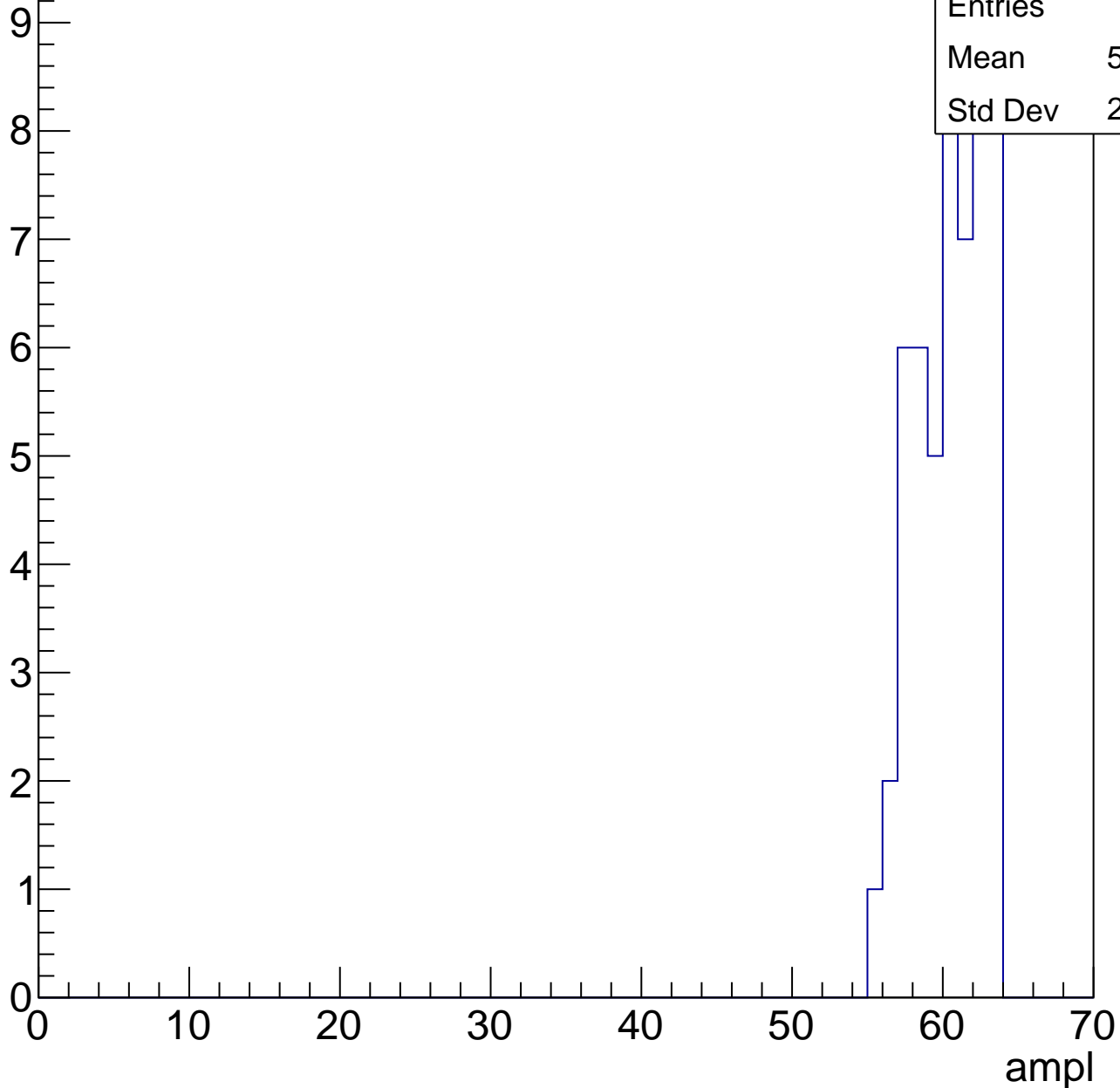
Entries	62
Mean	54.19
Std Dev	3.345



B1L102S, U19-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

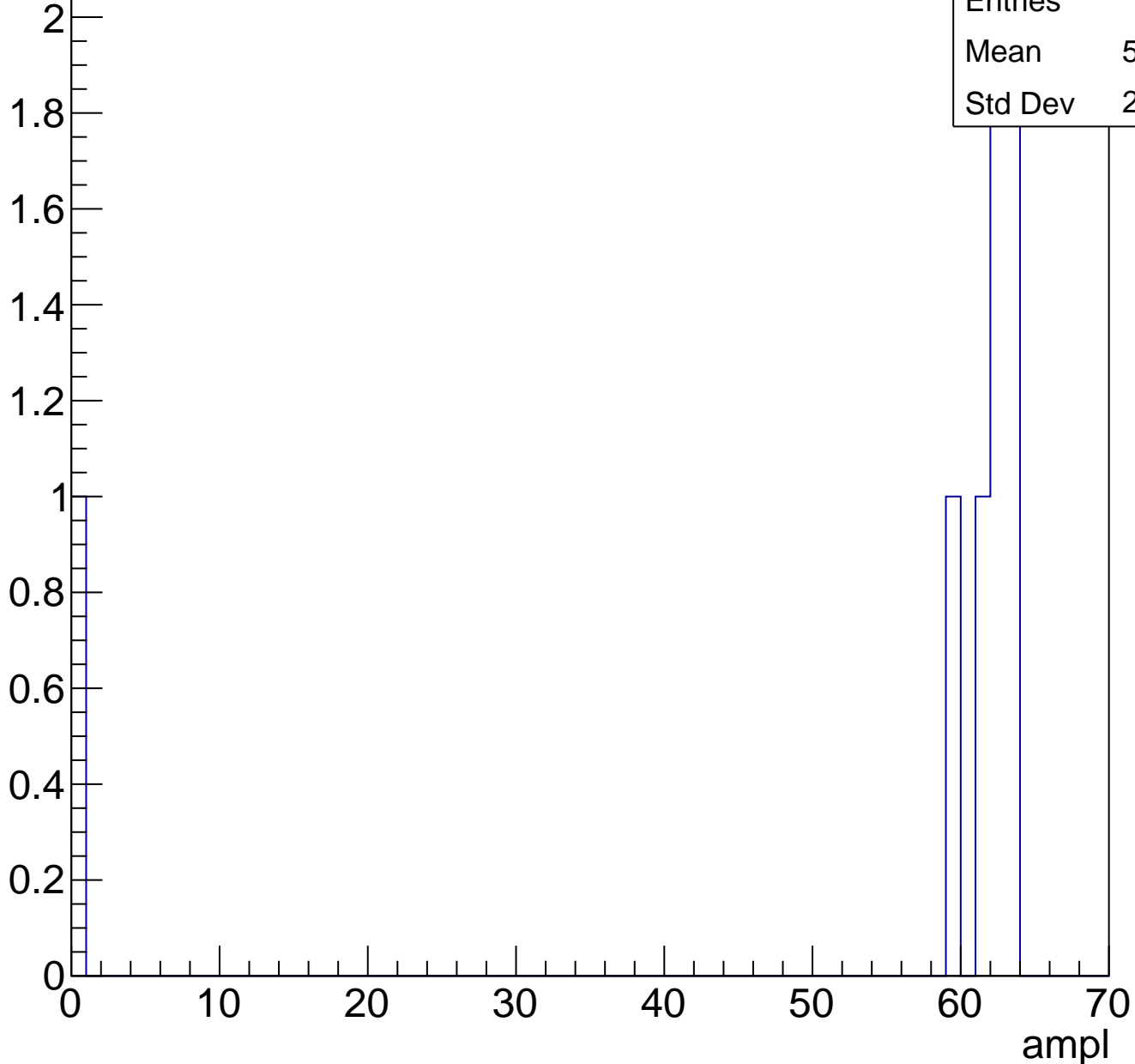
Entry



B1L102S, U19-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch80, adc0

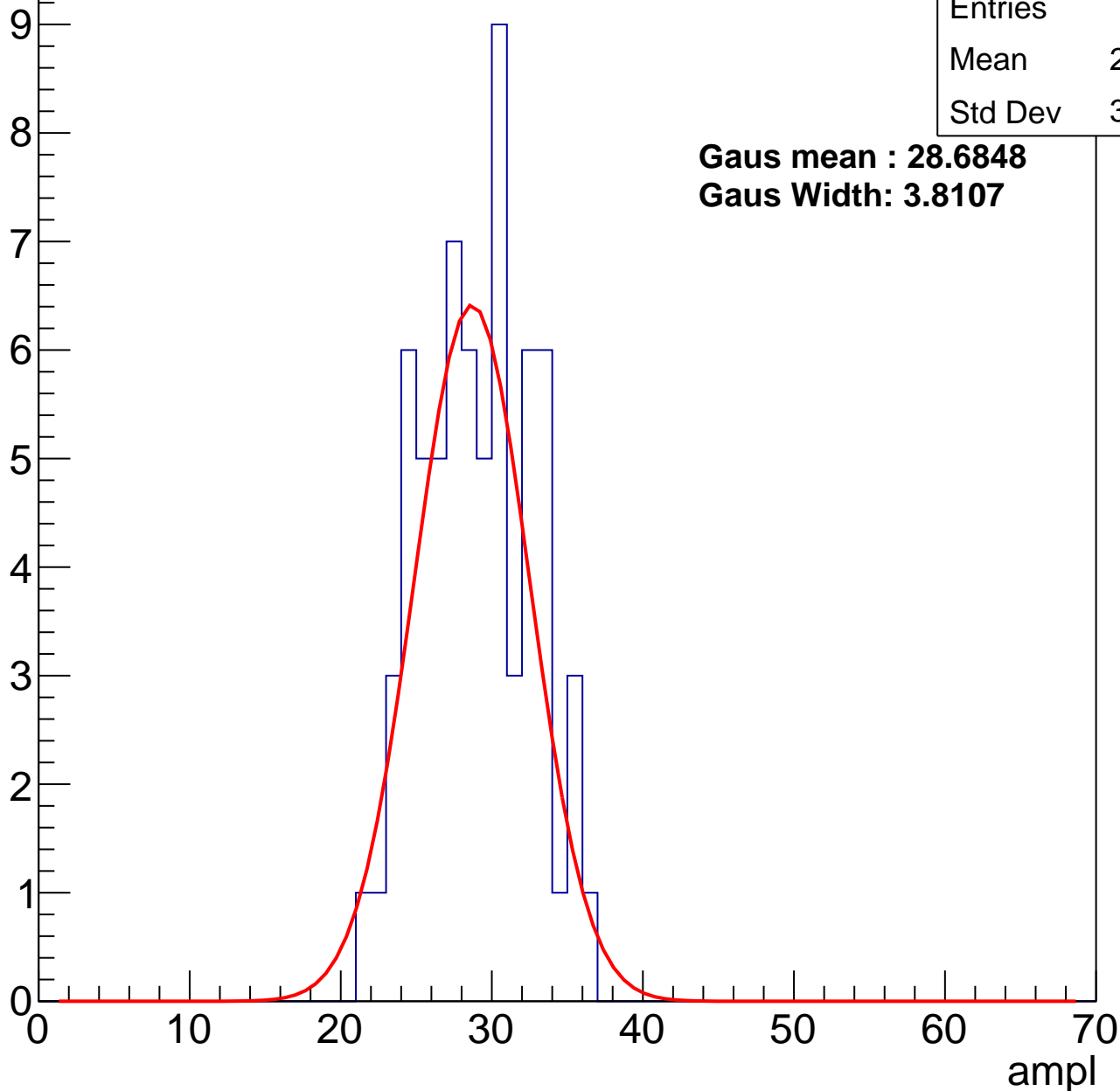
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.54
Std Dev	3.575

Gaus mean : 28.6848

Gaus Width: 3.8107



B1L102S, U19-ch80, adc1

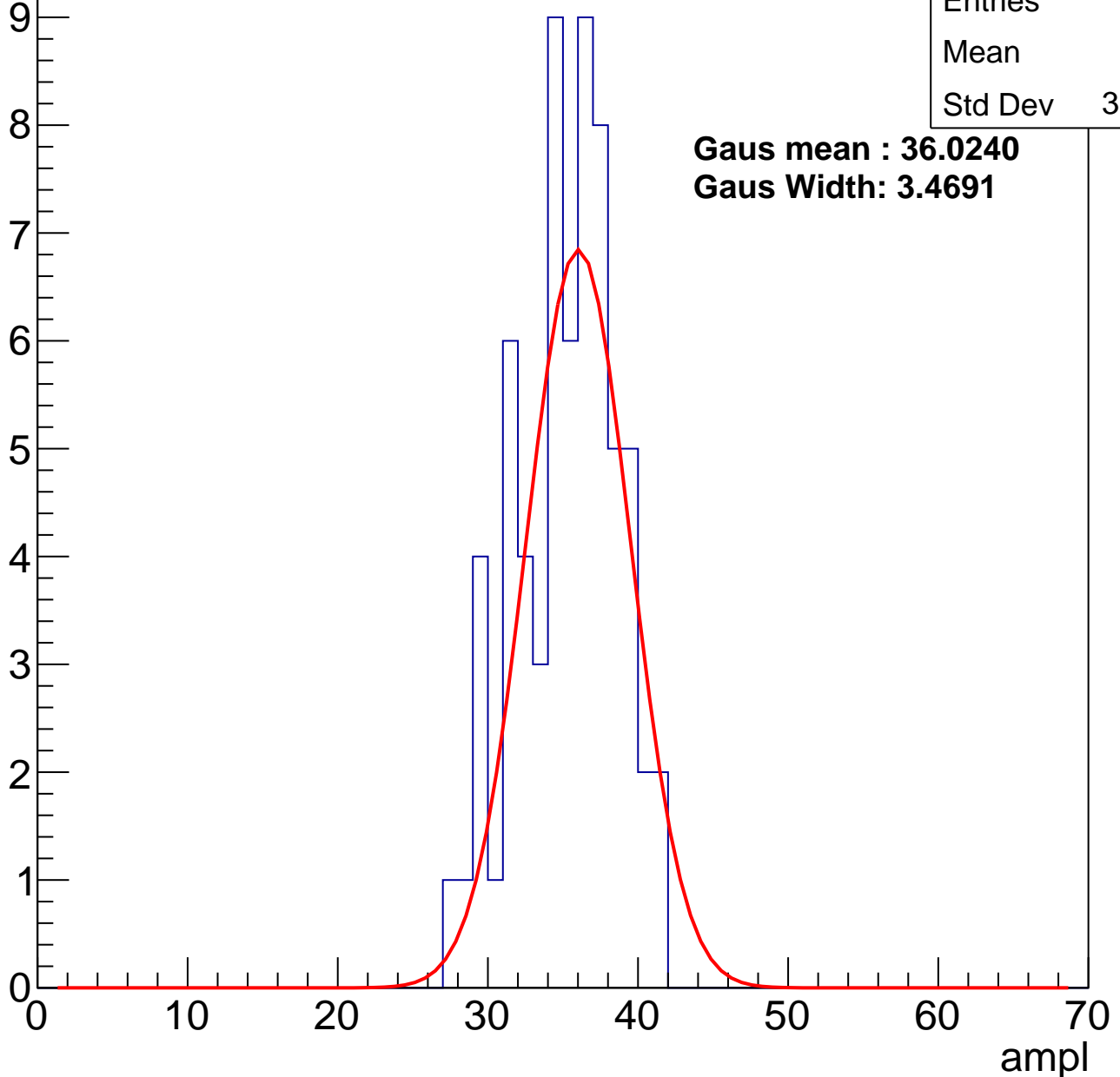
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.8
Std Dev	3.304

Gaus mean : 36.0240

Gaus Width: 3.4691



B1L102S, U19-ch80, adc2

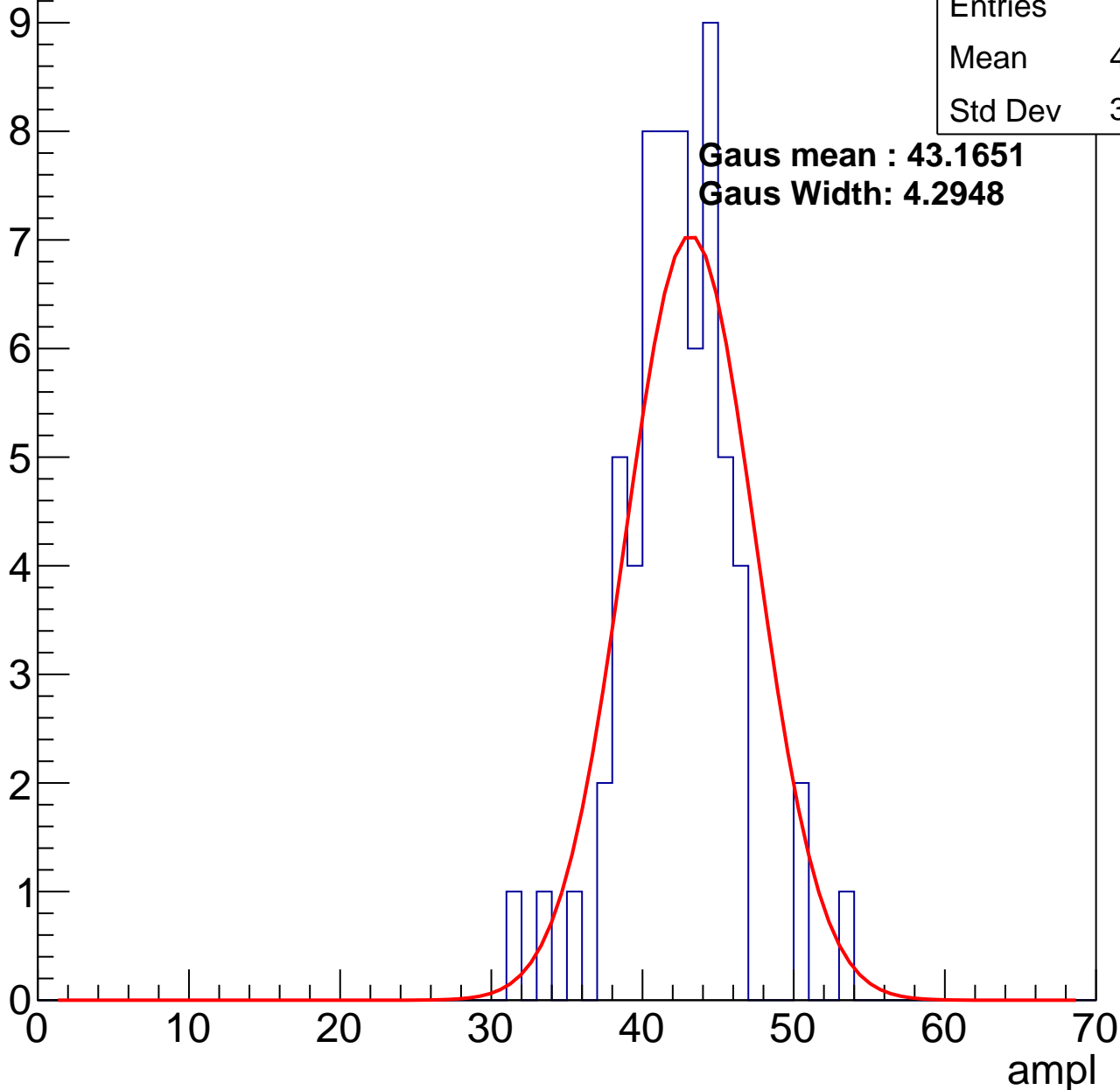
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.83
Std Dev	3.627

Gaus mean : 43.1651

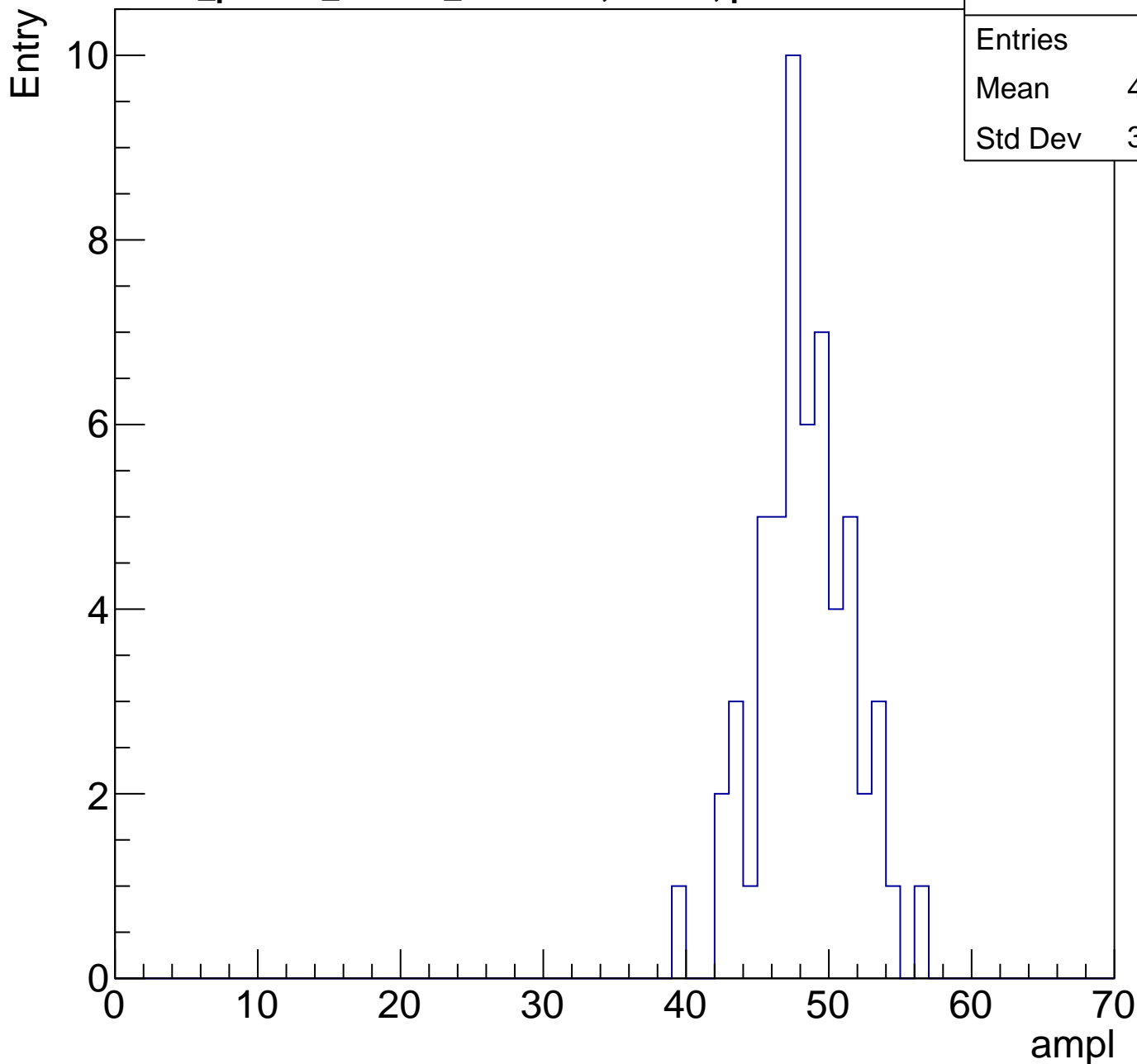
Gaus Width: 4.2948



B1L102S, U19-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	56
Mean	47.86
Std Dev	3.254

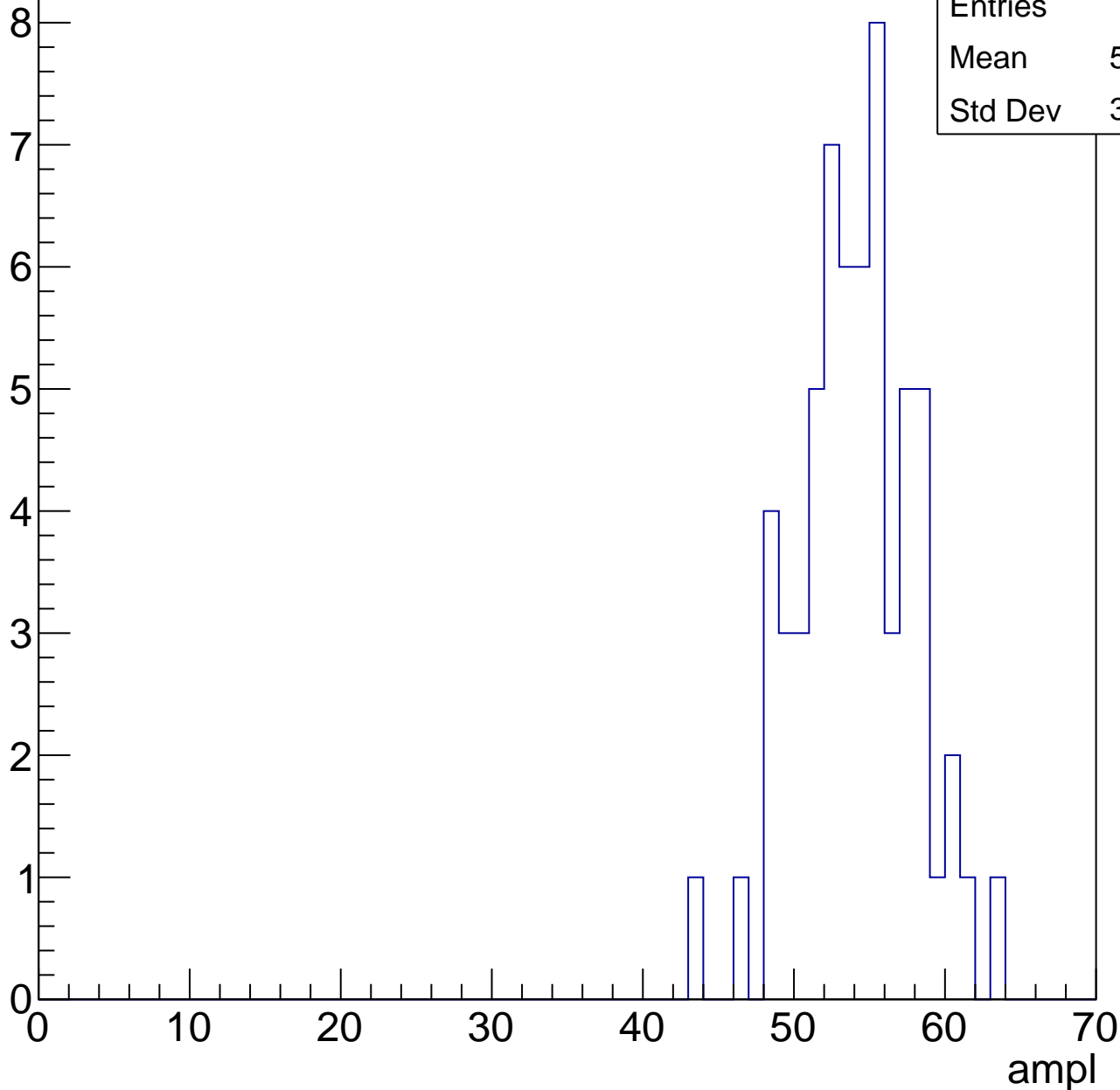


B1L102S, U19-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.63
Std Dev	3.798

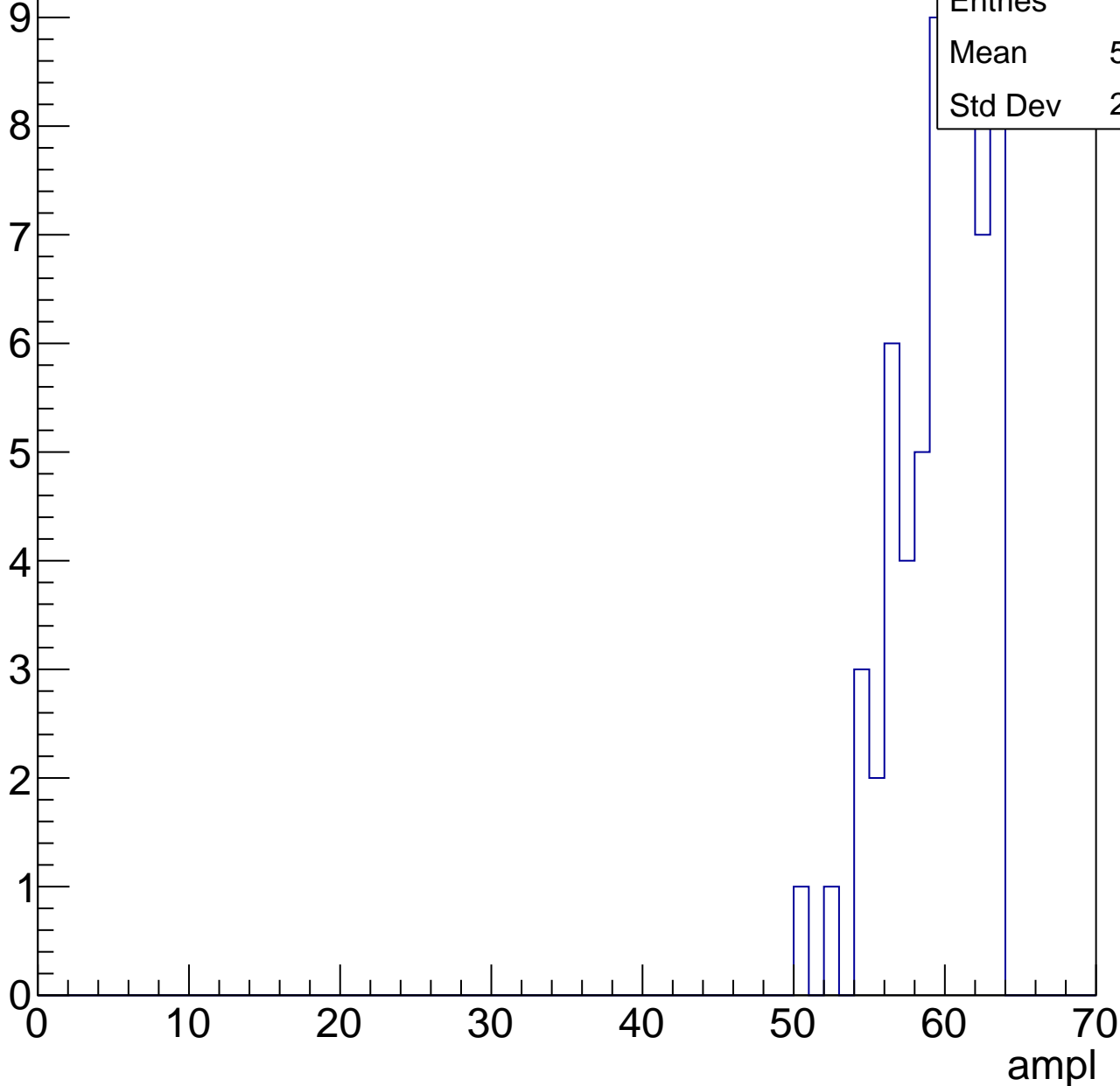


B1L102S, U19-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

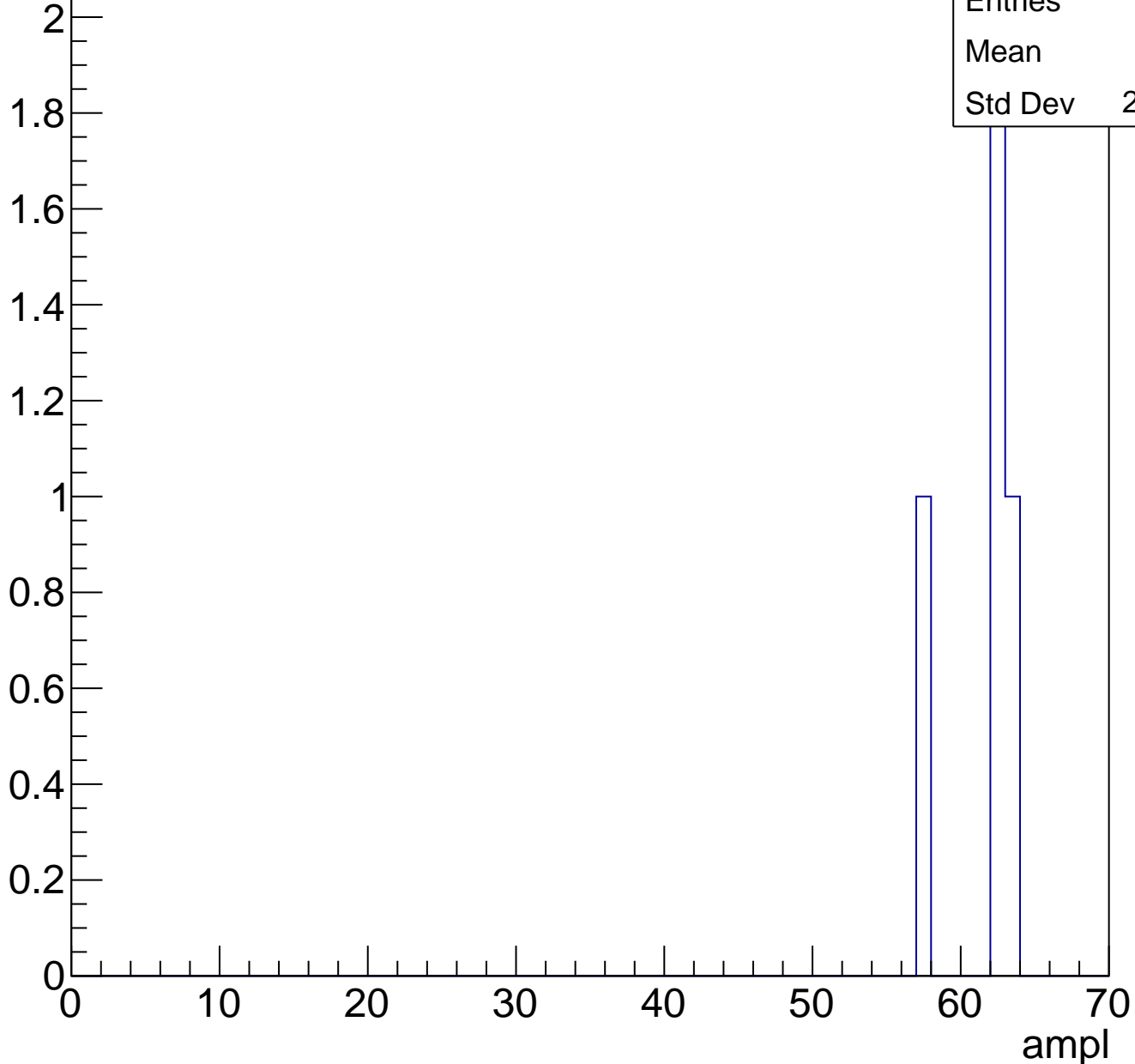
Entries	63
Mean	59.14
Std Dev	2.943



B1L102S, U19-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

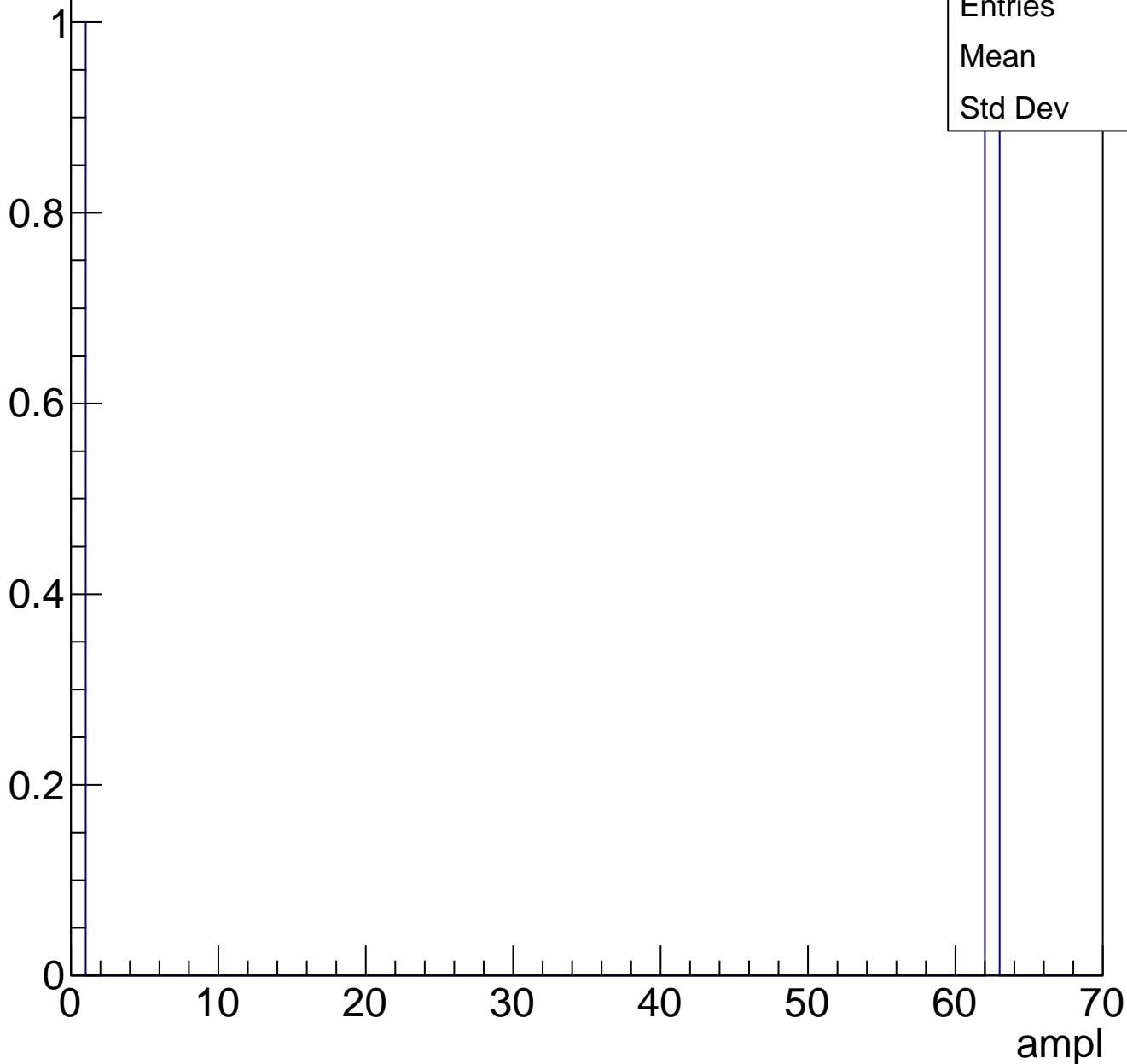
Entry



B1L102S, U19-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch81, adc0

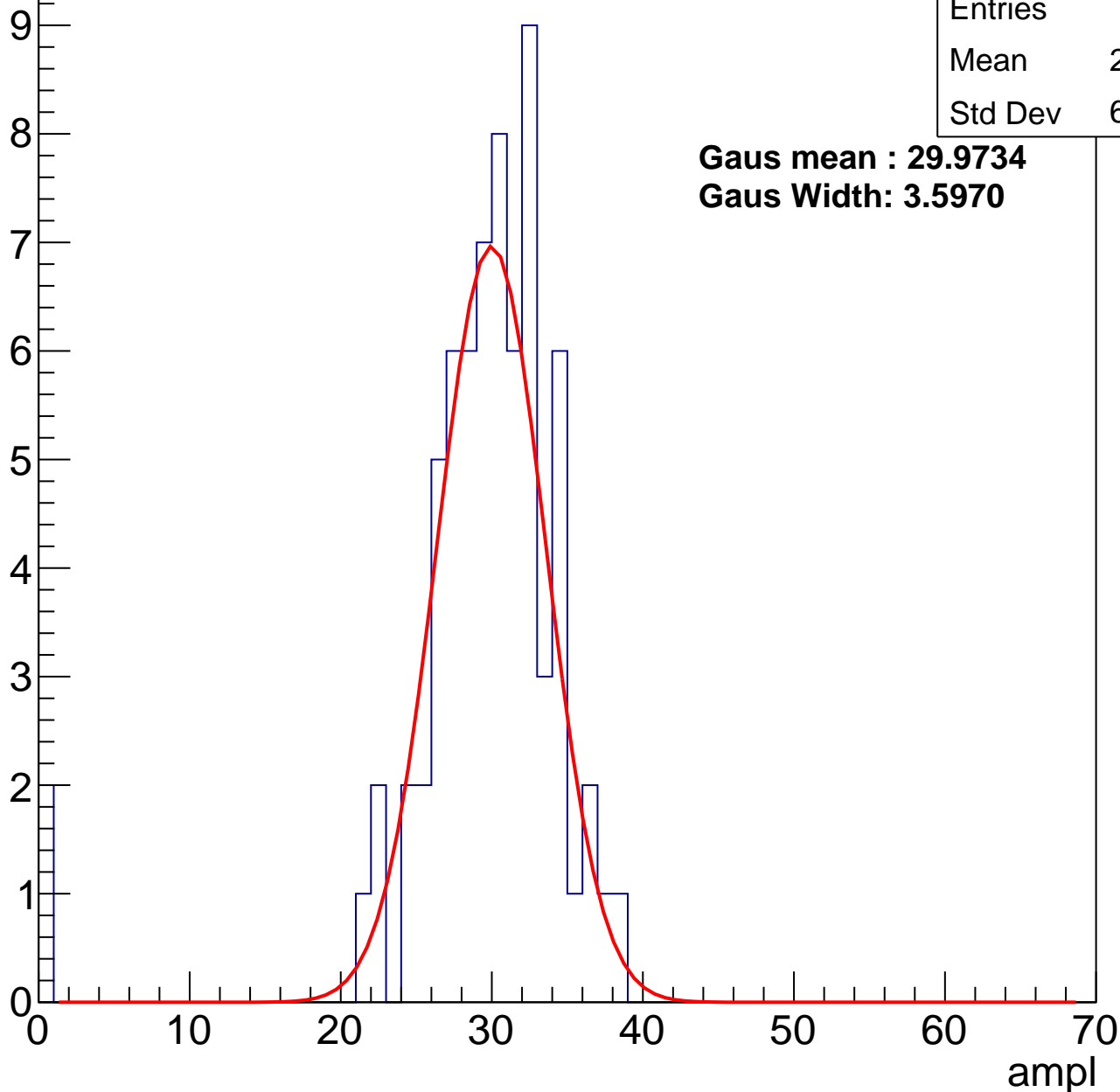
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.93
Std Dev	6.084

Gaus mean : 29.9734

Gaus Width: 3.5970



B1L102S, U19-ch81, adc1

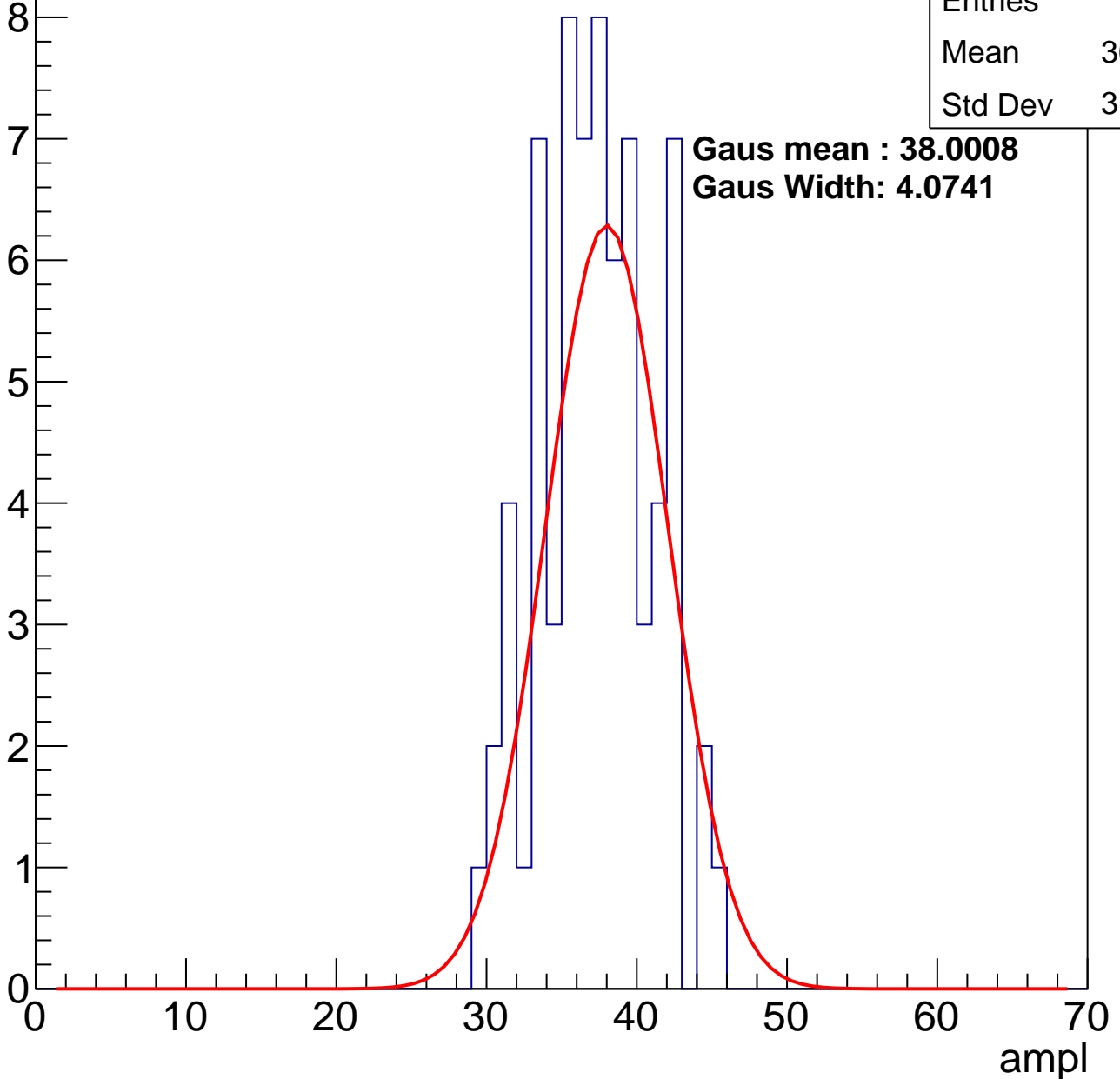
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.87
Std Dev	3.696

Gaus mean : 38.0008

Gaus Width: 4.0741



B1L102S, U19-ch81, adc2

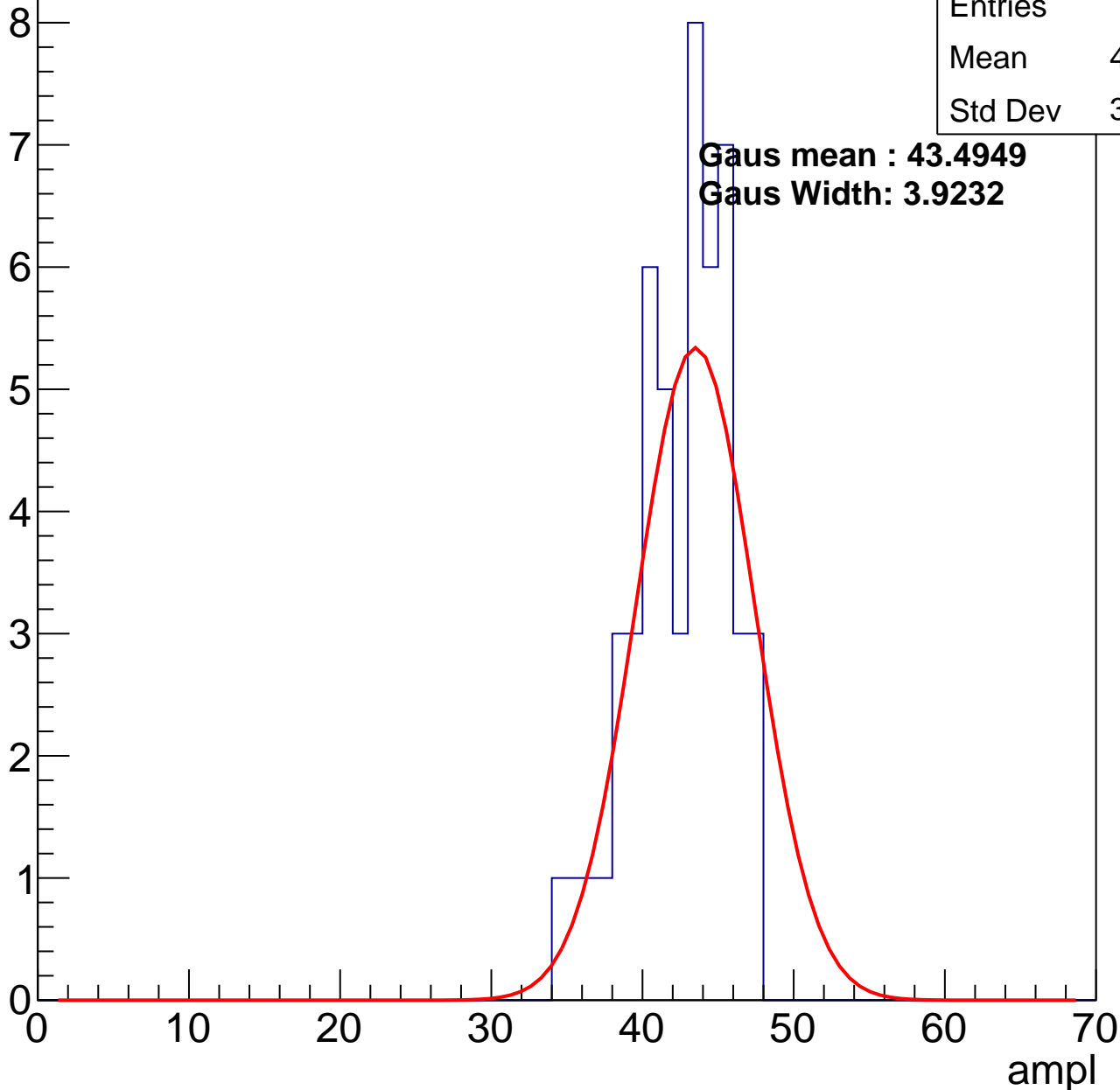
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.08
Std Dev	3.118

Gaus mean : 43.4949

Gaus Width: 3.9232

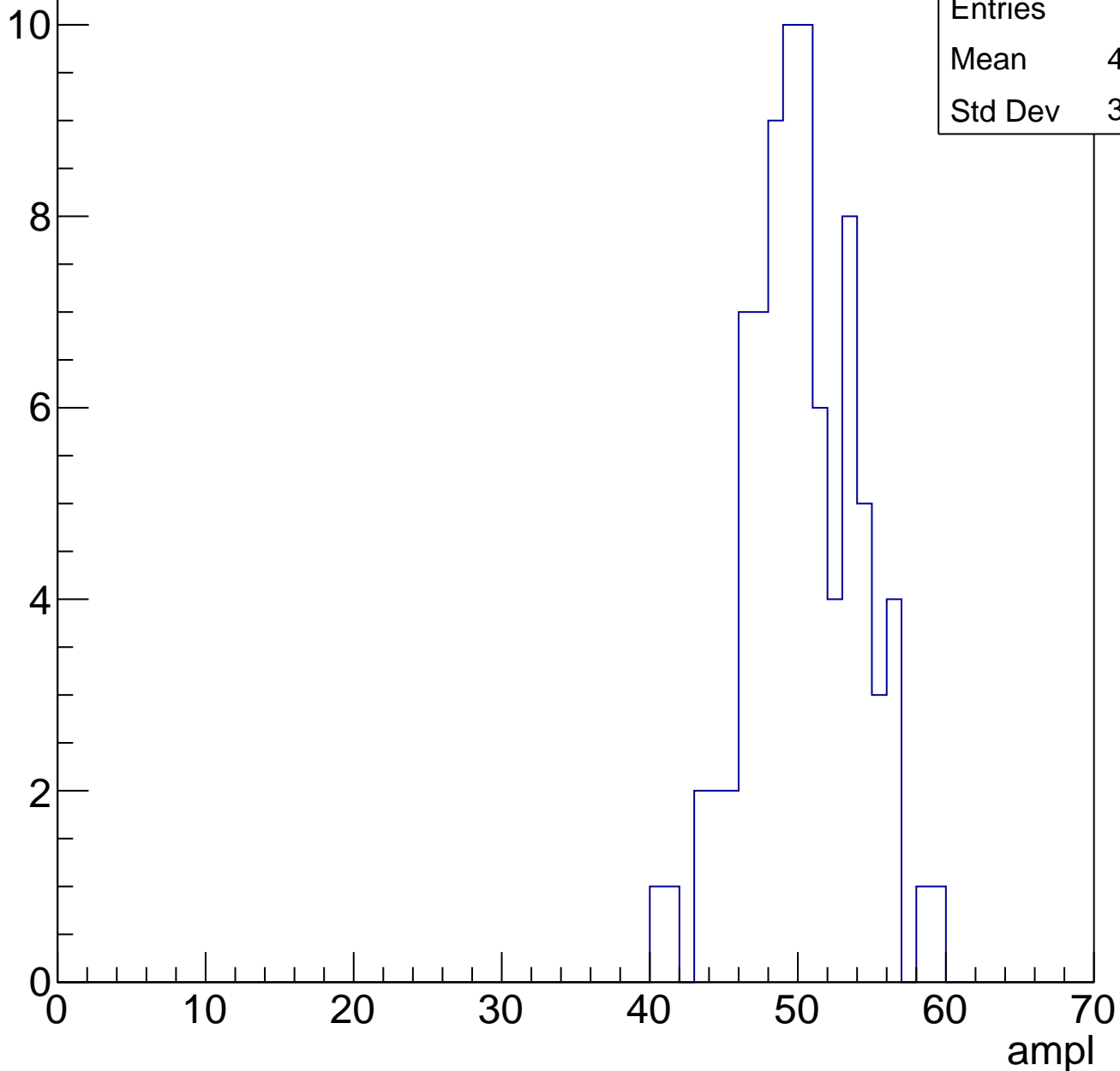


B1L102S, U19-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	49.78
Std Dev	3.732

Entry

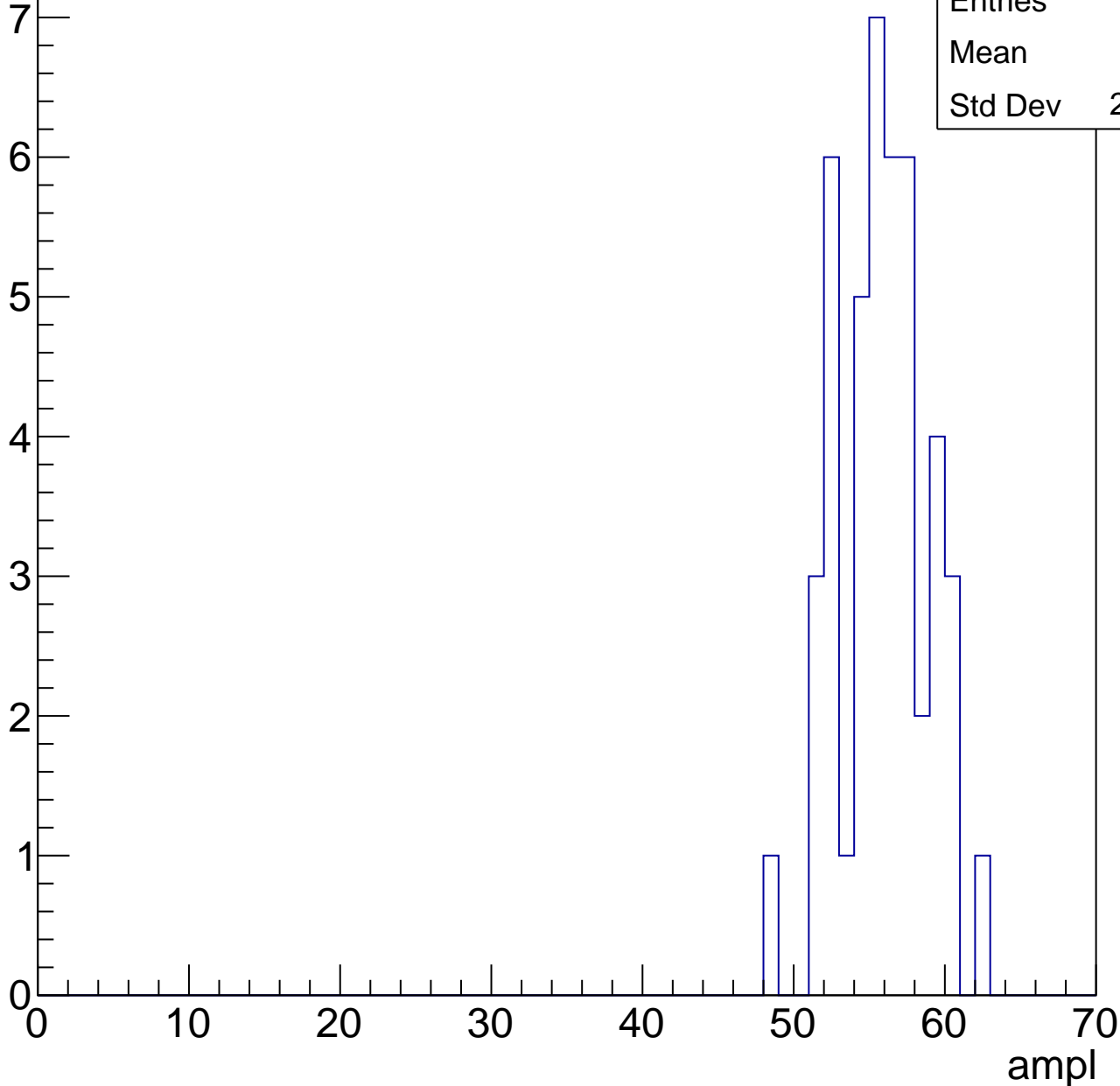


B1L102S, U19-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	55.4
Std Dev	2.939

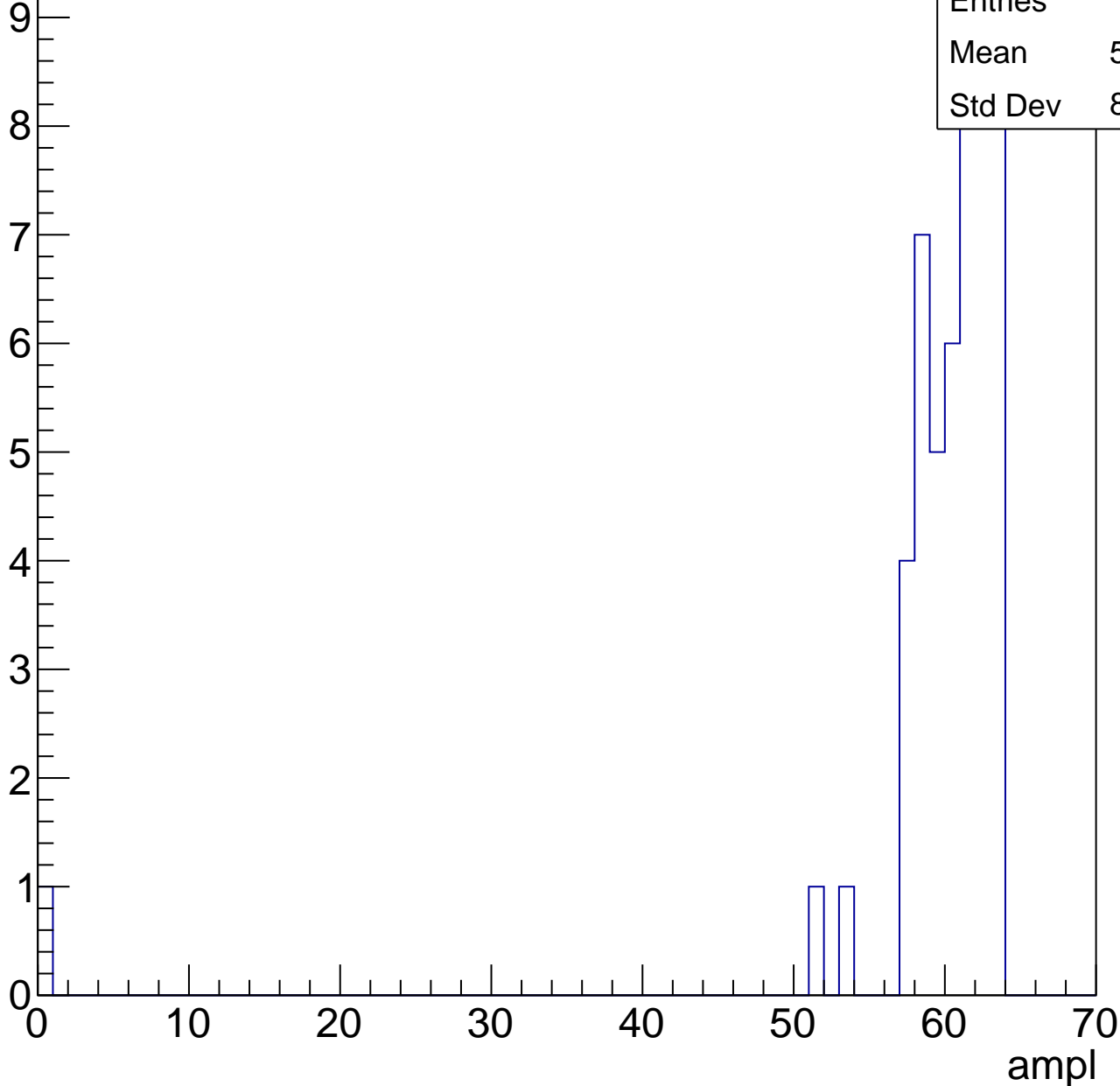


B1L102S, U19-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

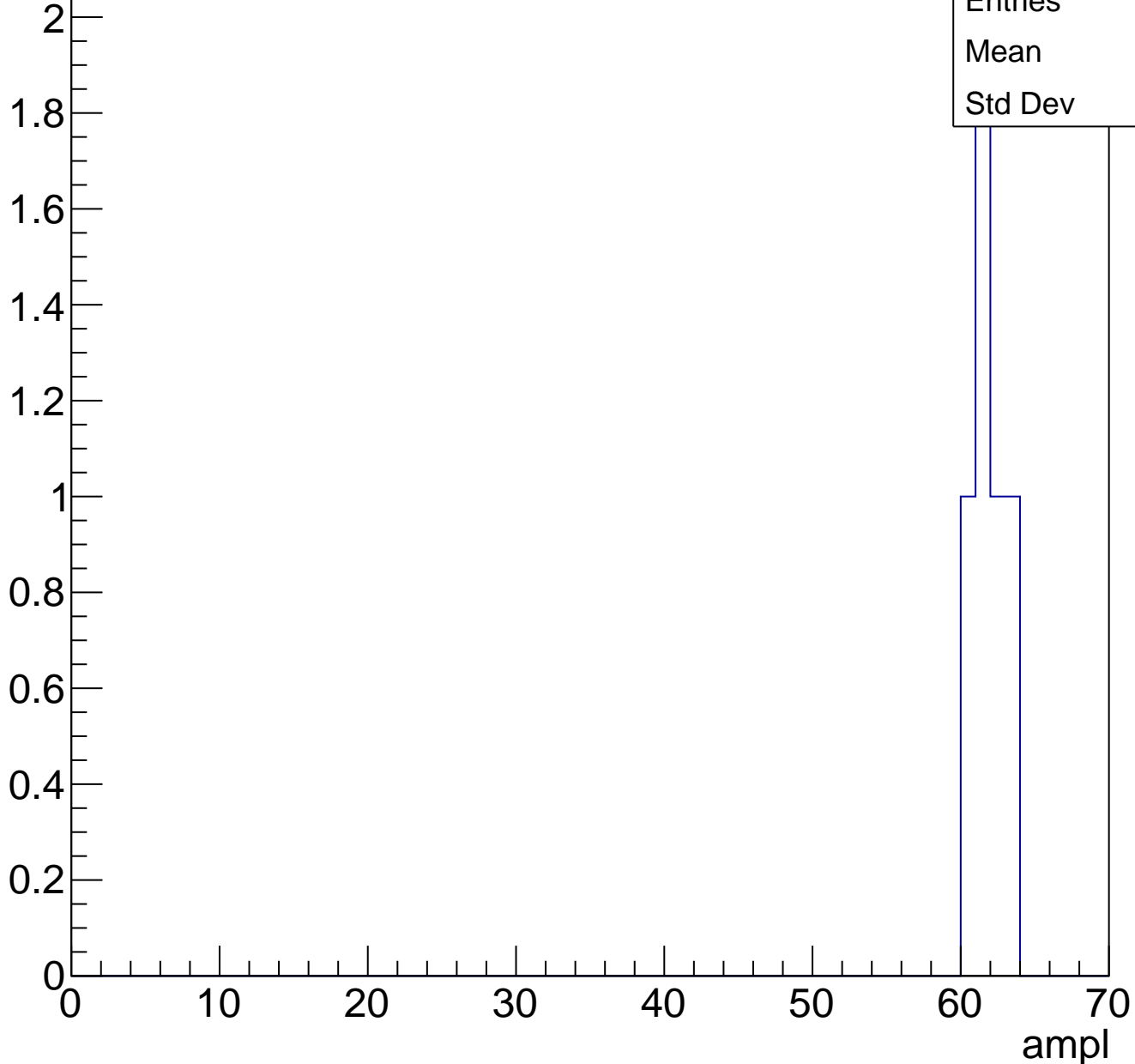
Entries	50
Mean	58.88
Std Dev	8.783



B1L102S, U19-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch82, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	29.78
Std Dev	3.48

Gaus mean : 30.1213

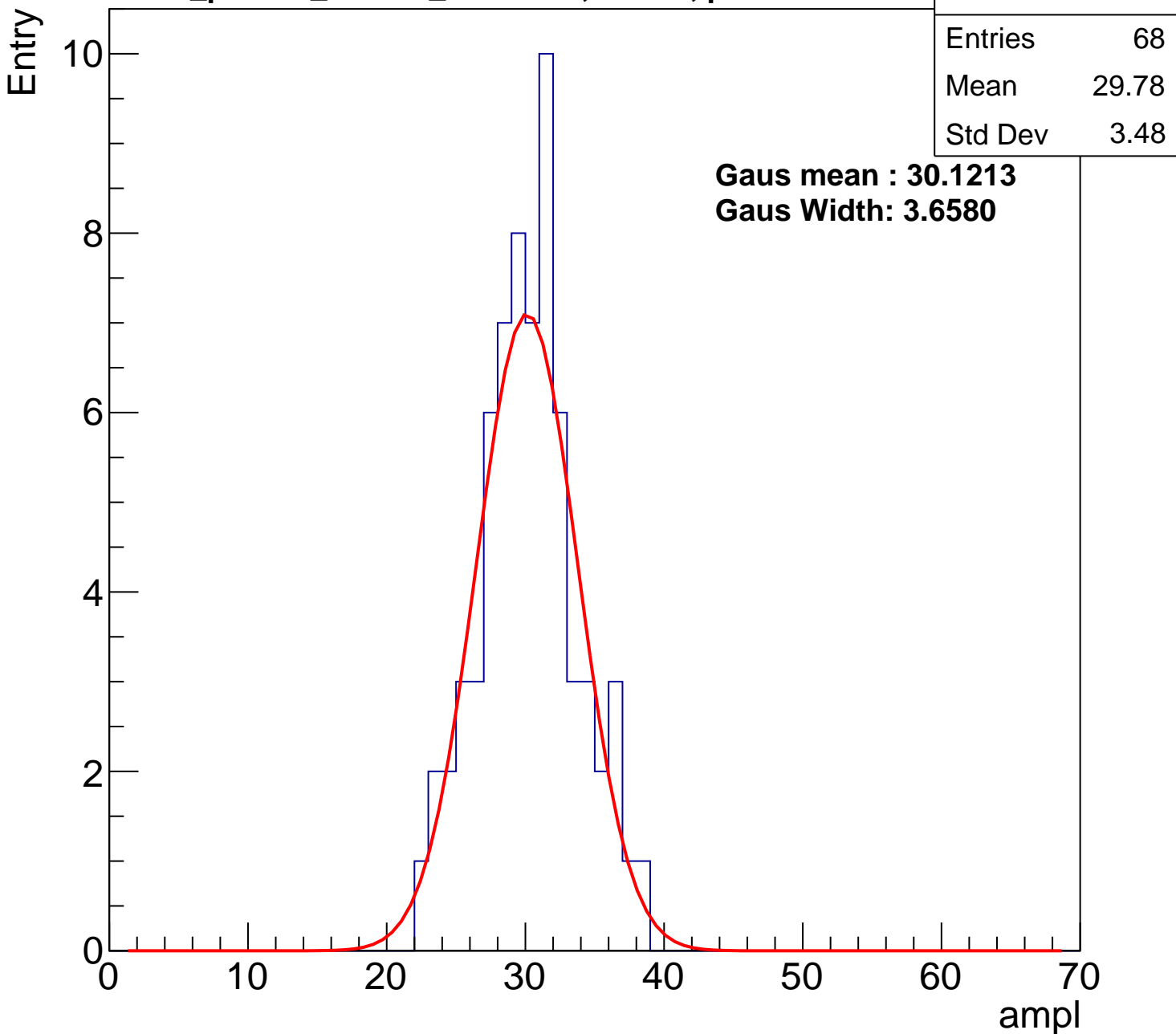
Gaus Width: 3.6580

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch82, adc1

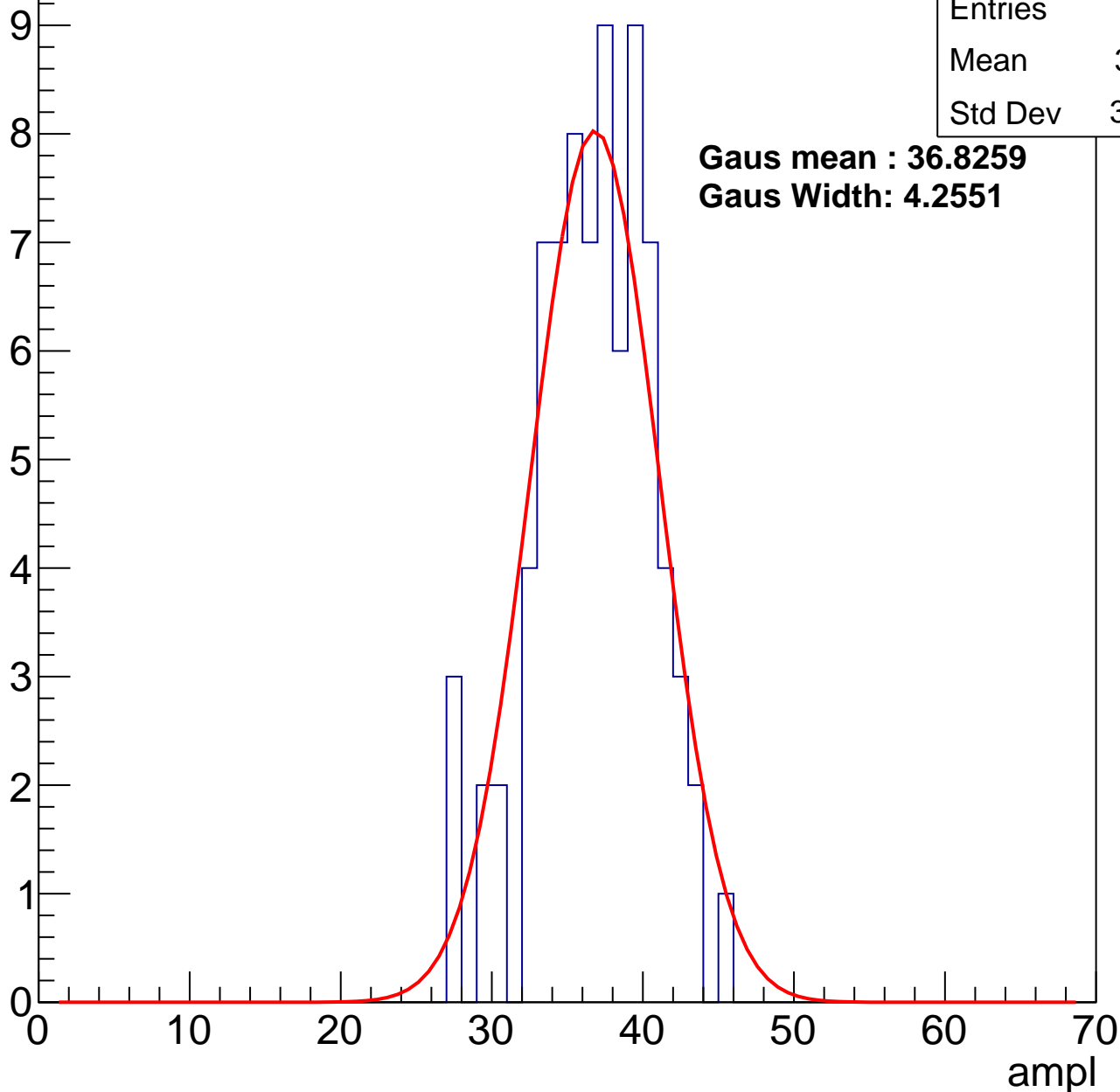
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.31
Std Dev	3.819

Gaus mean : 36.8259

Gaus Width: 4.2551



B1L102S, U19-ch82, adc2

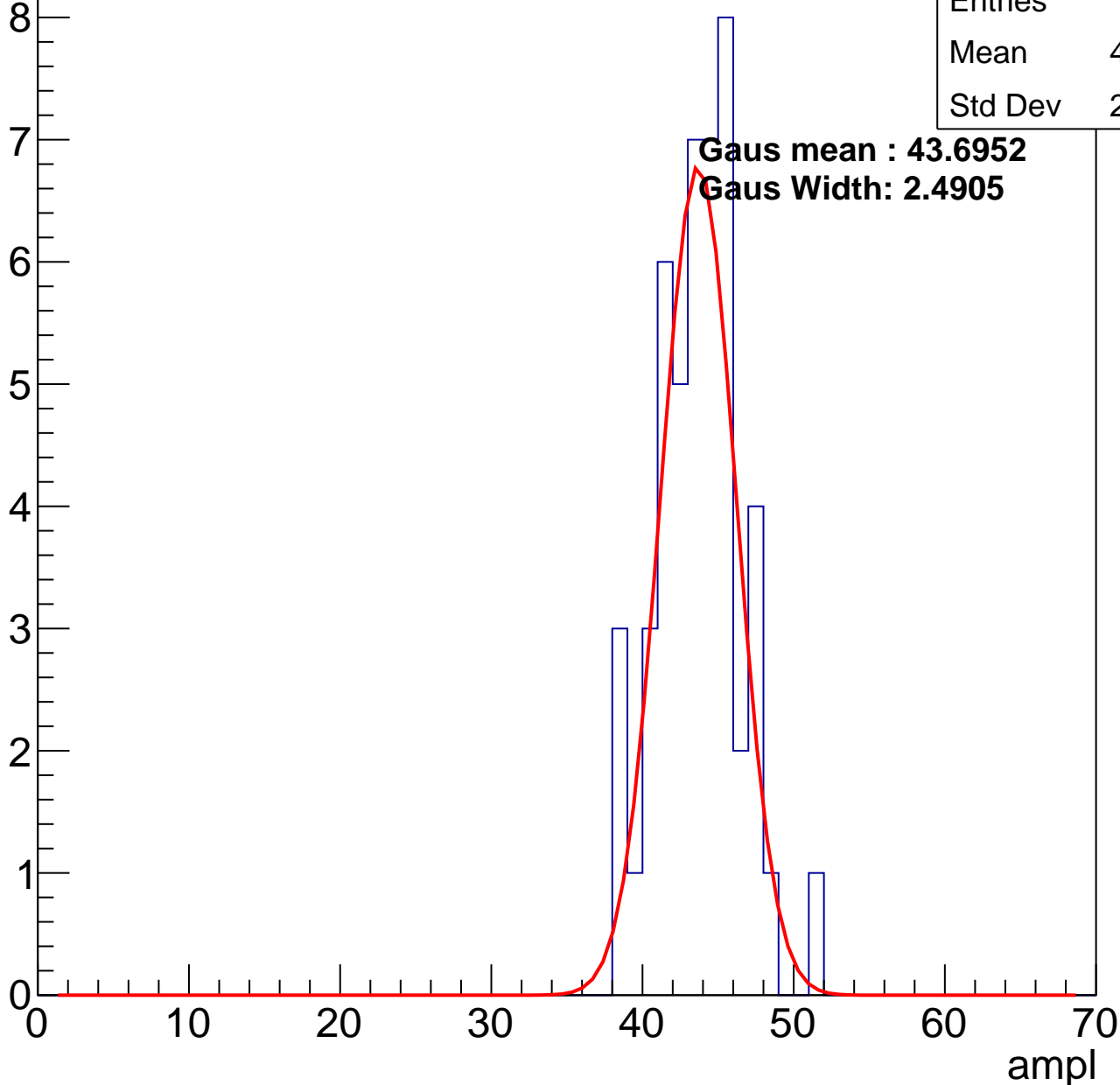
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	43.27
Std Dev	2.714

Gaus mean : 43.6952

Gaus Width: 2.4905

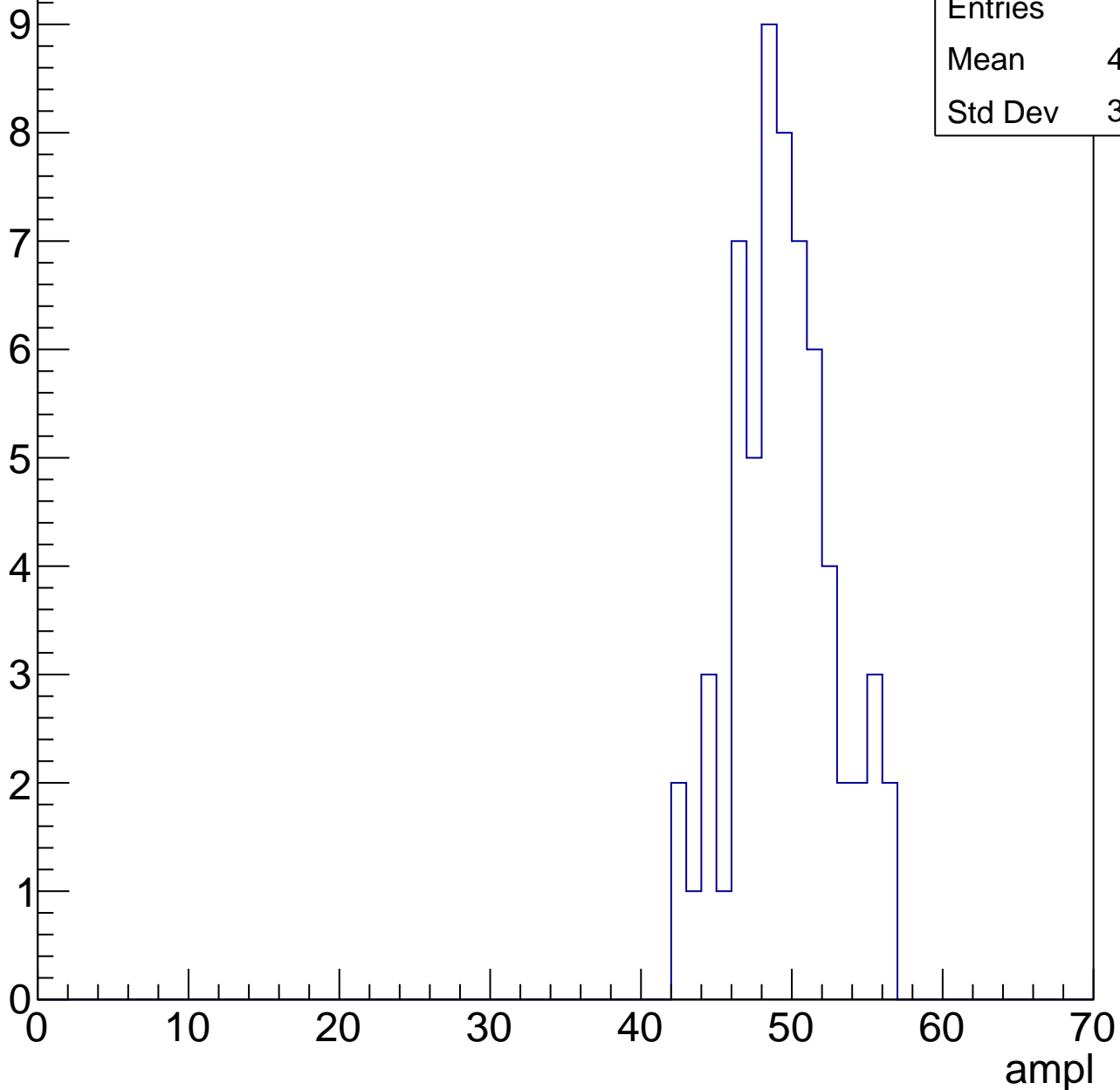


B1L102S, U19-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.03
Std Dev	3.292

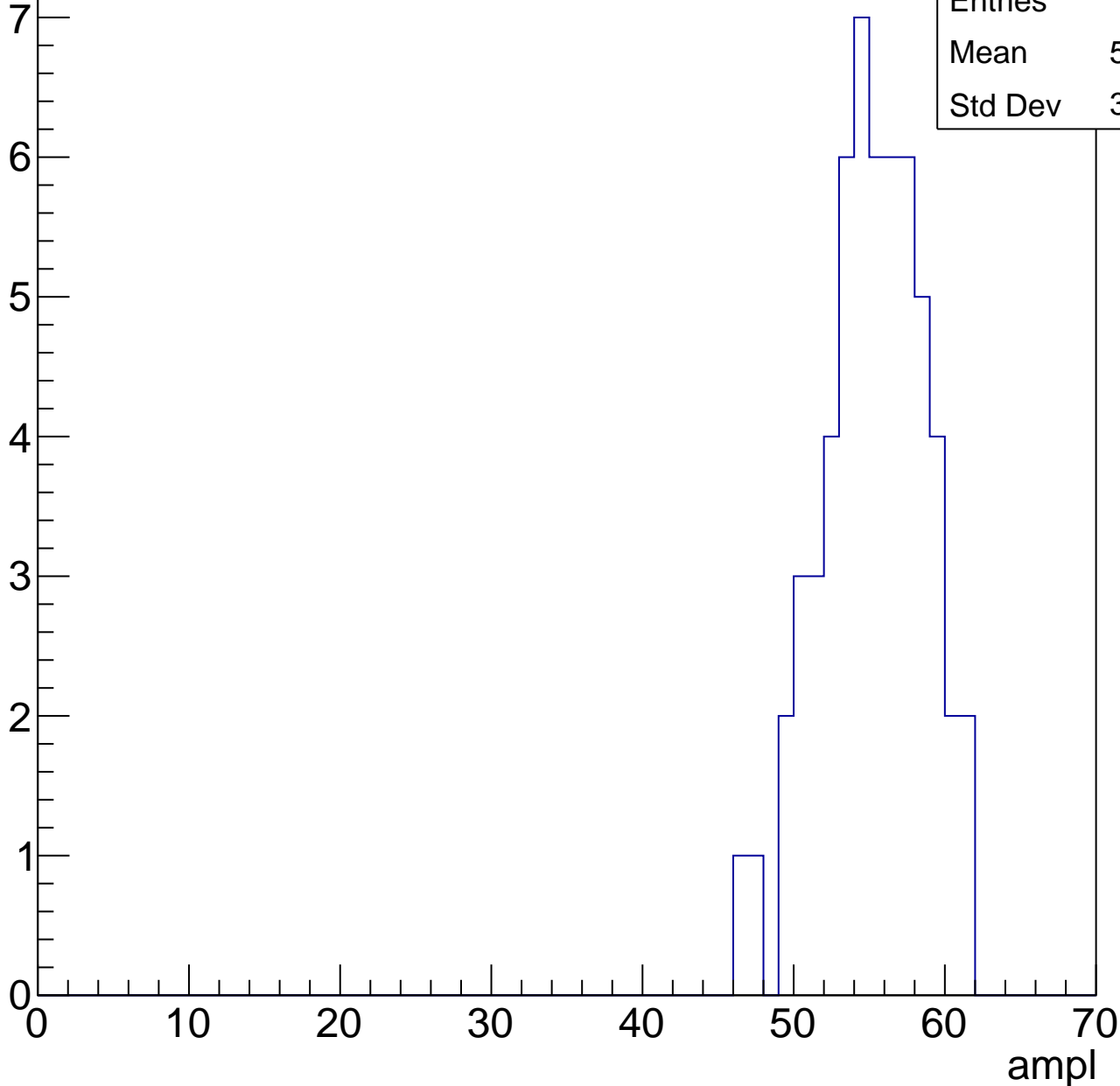


B1L102S, U19-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

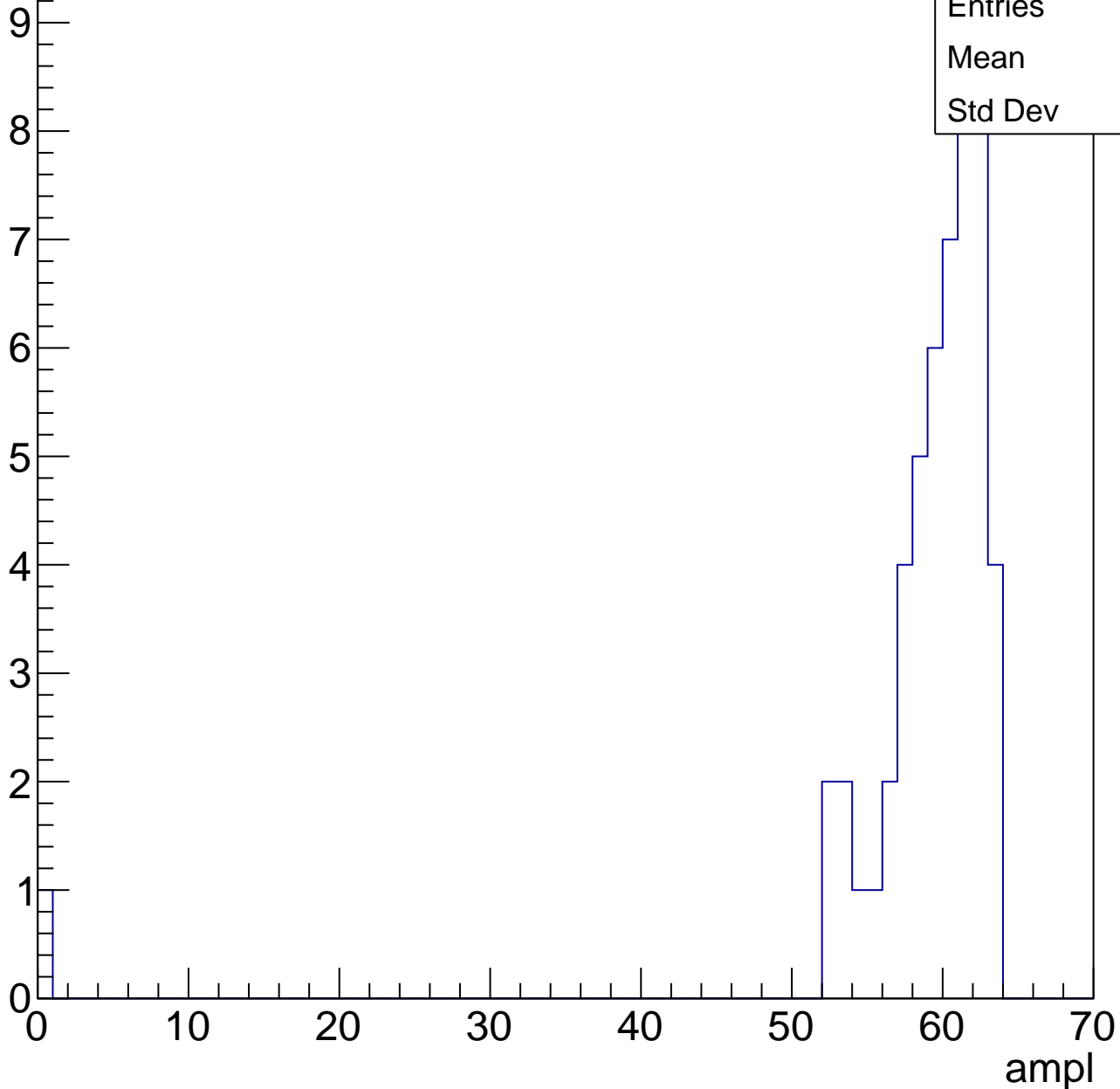
Entries	58
Mean	54.72
Std Dev	3.382



B1L102S, U19-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

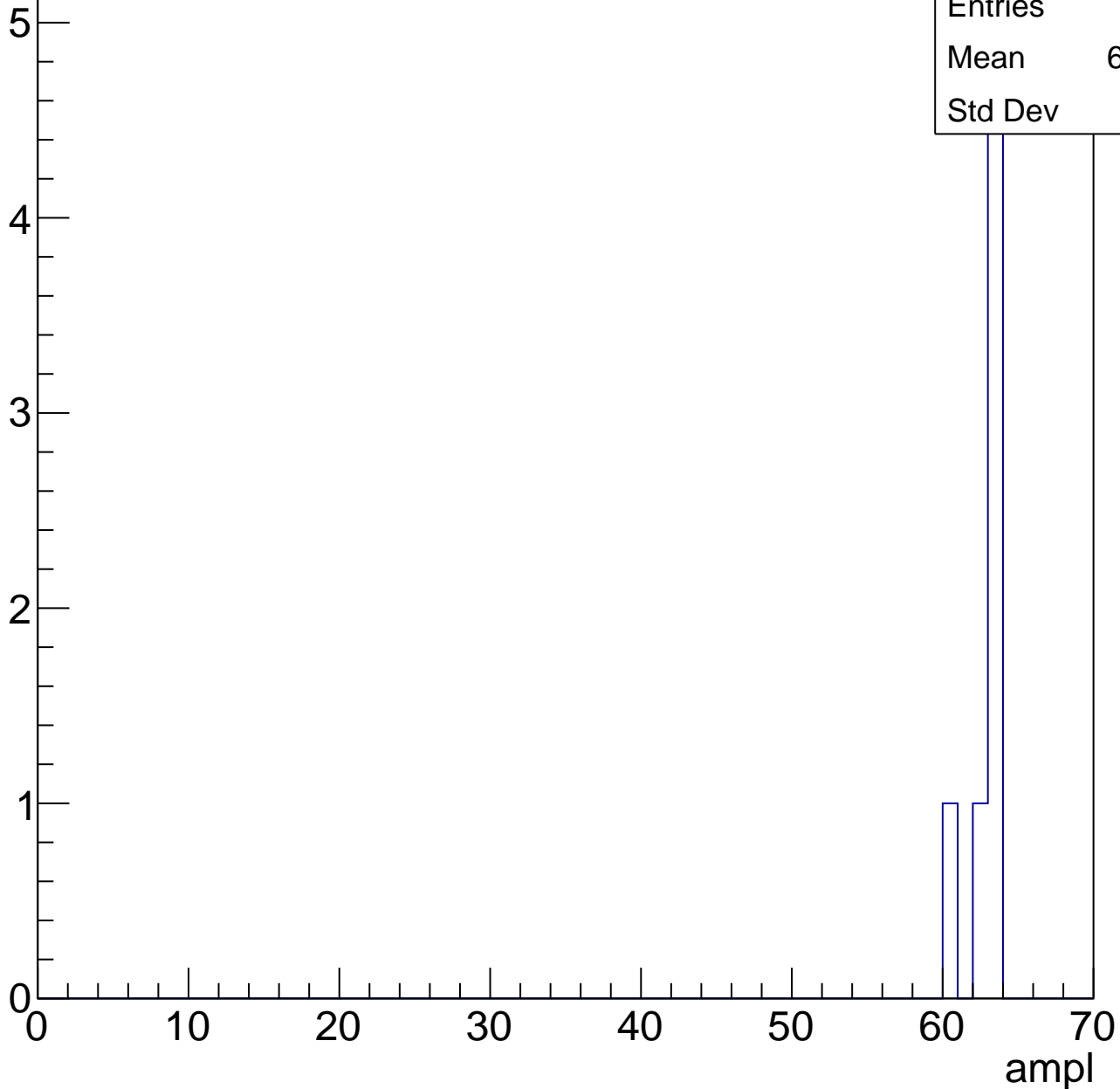


B1L102S, U19-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.43
Std Dev	1.05



B1L102S, U19-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U19-ch83, adc0

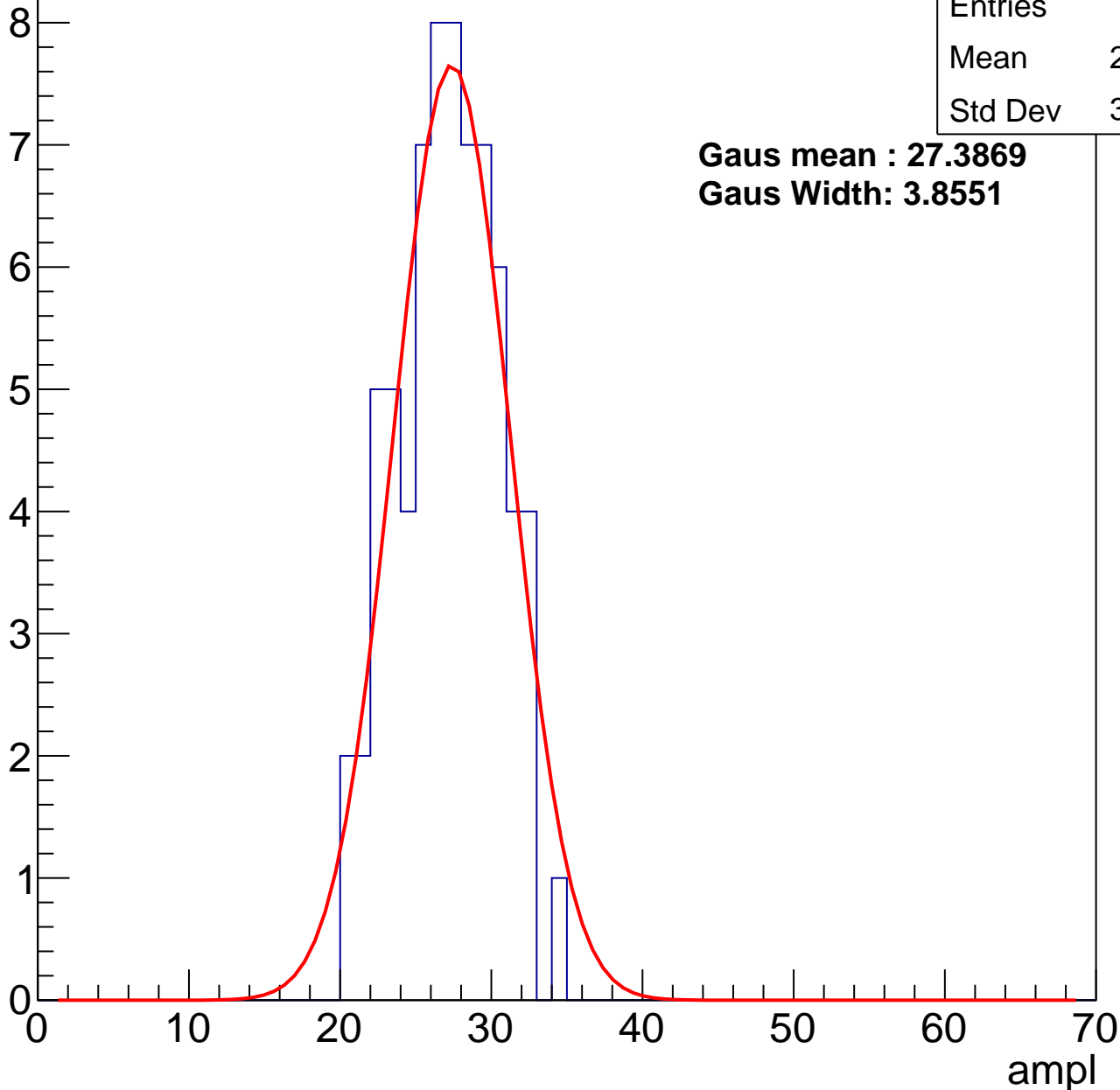
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	26.67
Std Dev	3.255

Gaus mean : 27.3869

Gaus Width: 3.8551



B1L102S, U19-ch83, adc1

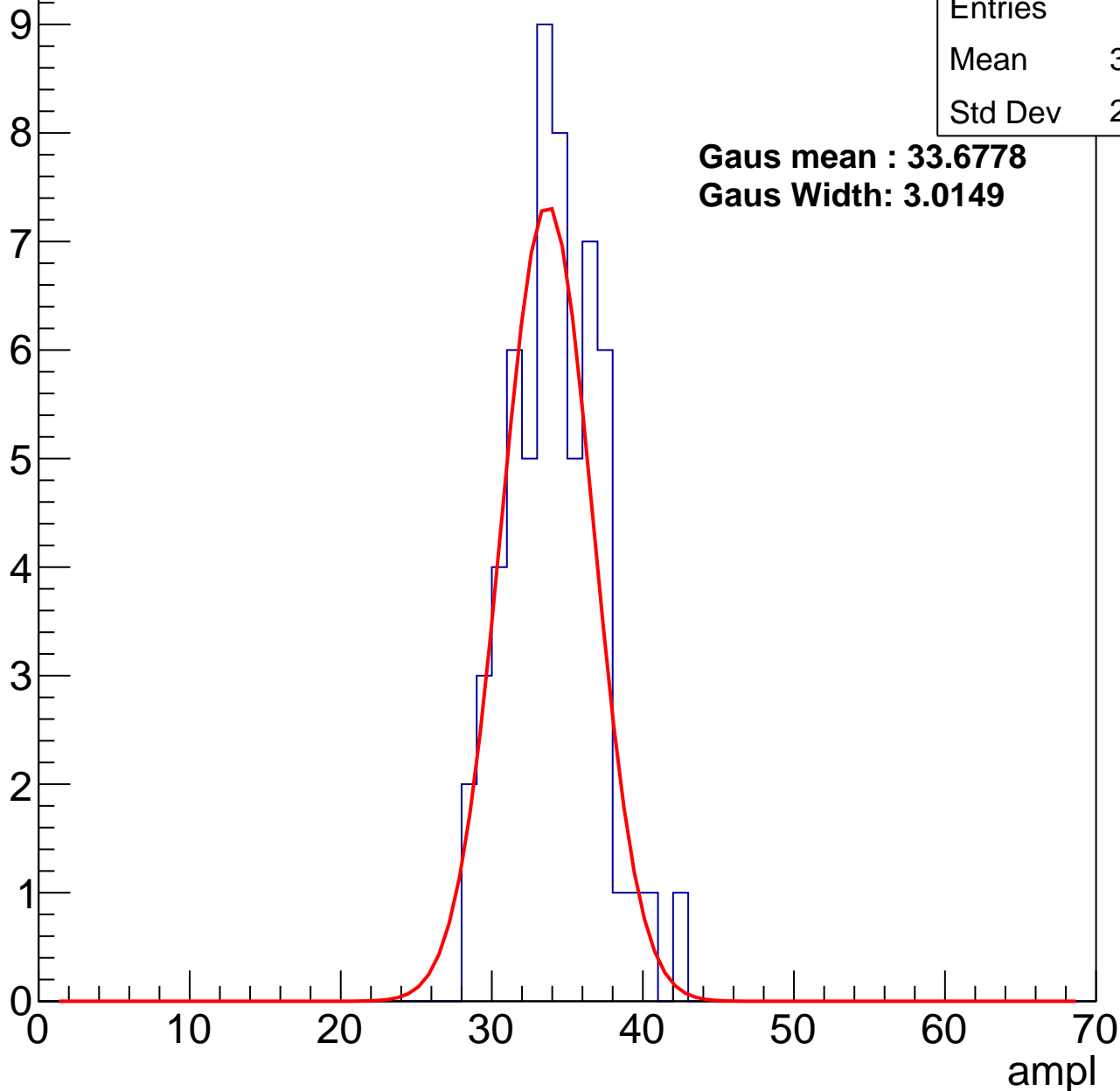
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	33.66
Std Dev	2.955

Gaus mean : 33.6778

Gaus Width: 3.0149



B1L102S, U19-ch83, adc2

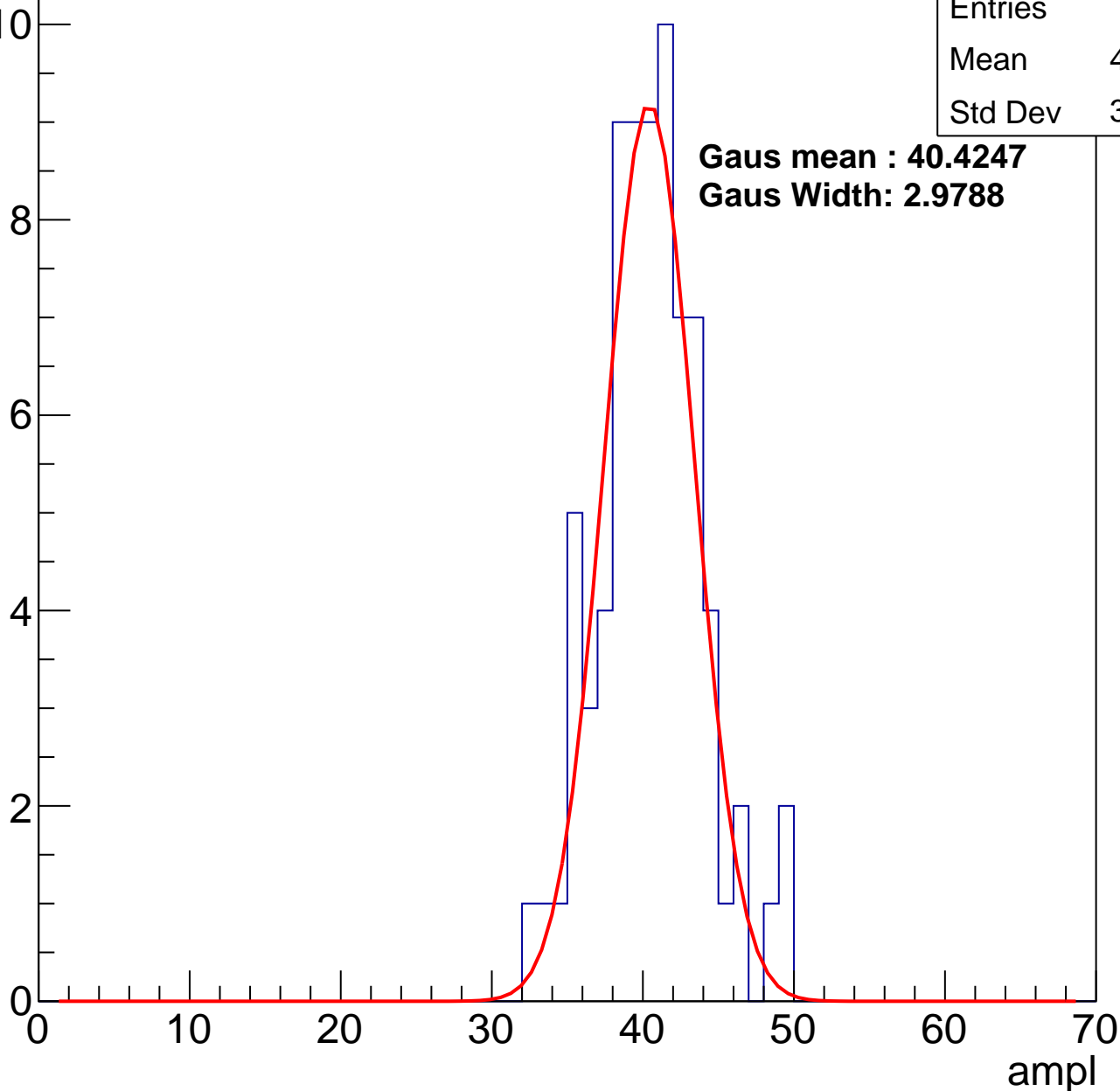
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	40.09
Std Dev	3.423

Gaus mean : 40.4247

Gaus Width: 2.9788

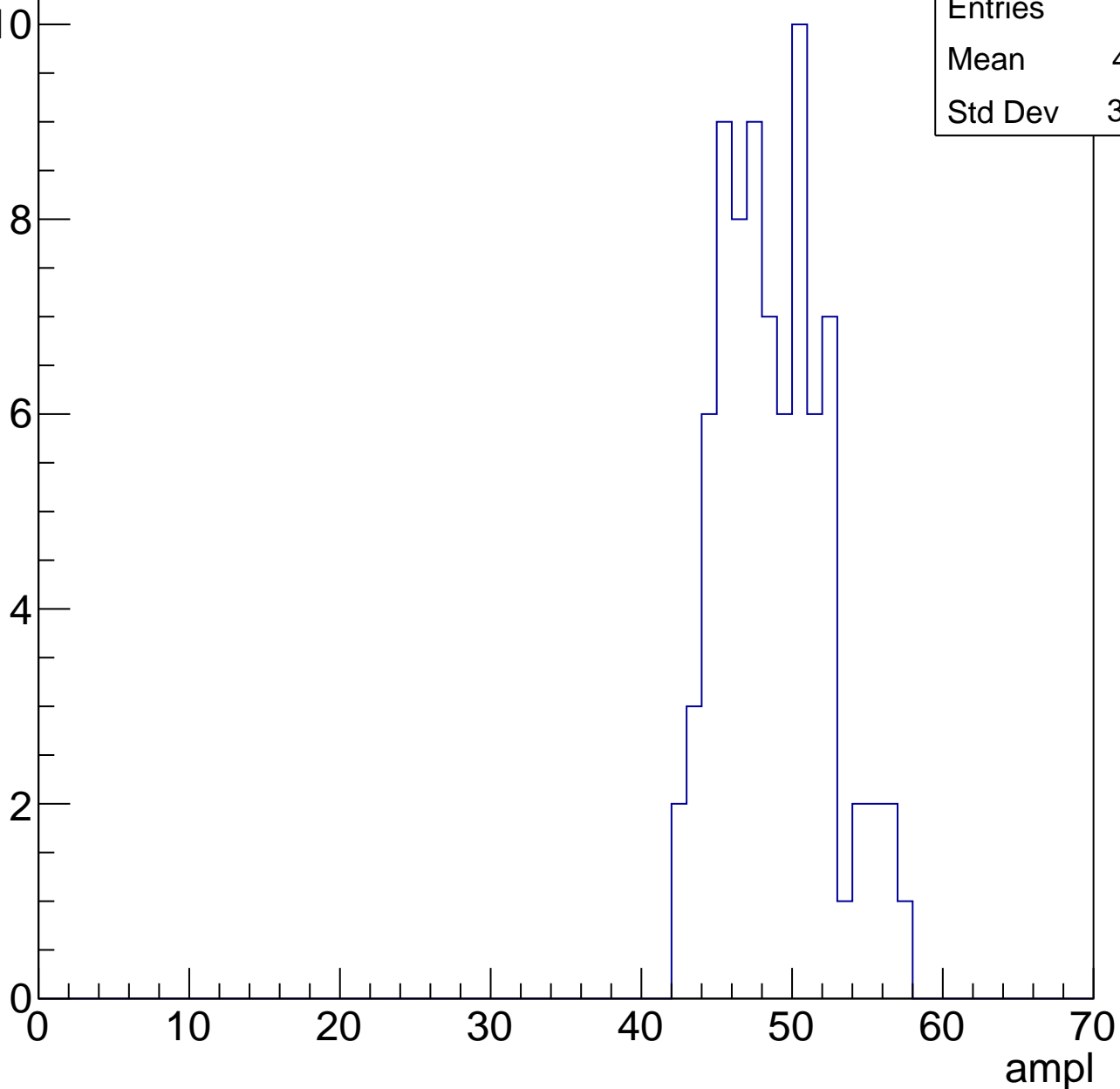


B1L102S, U19-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	48.31
Std Dev	3.466

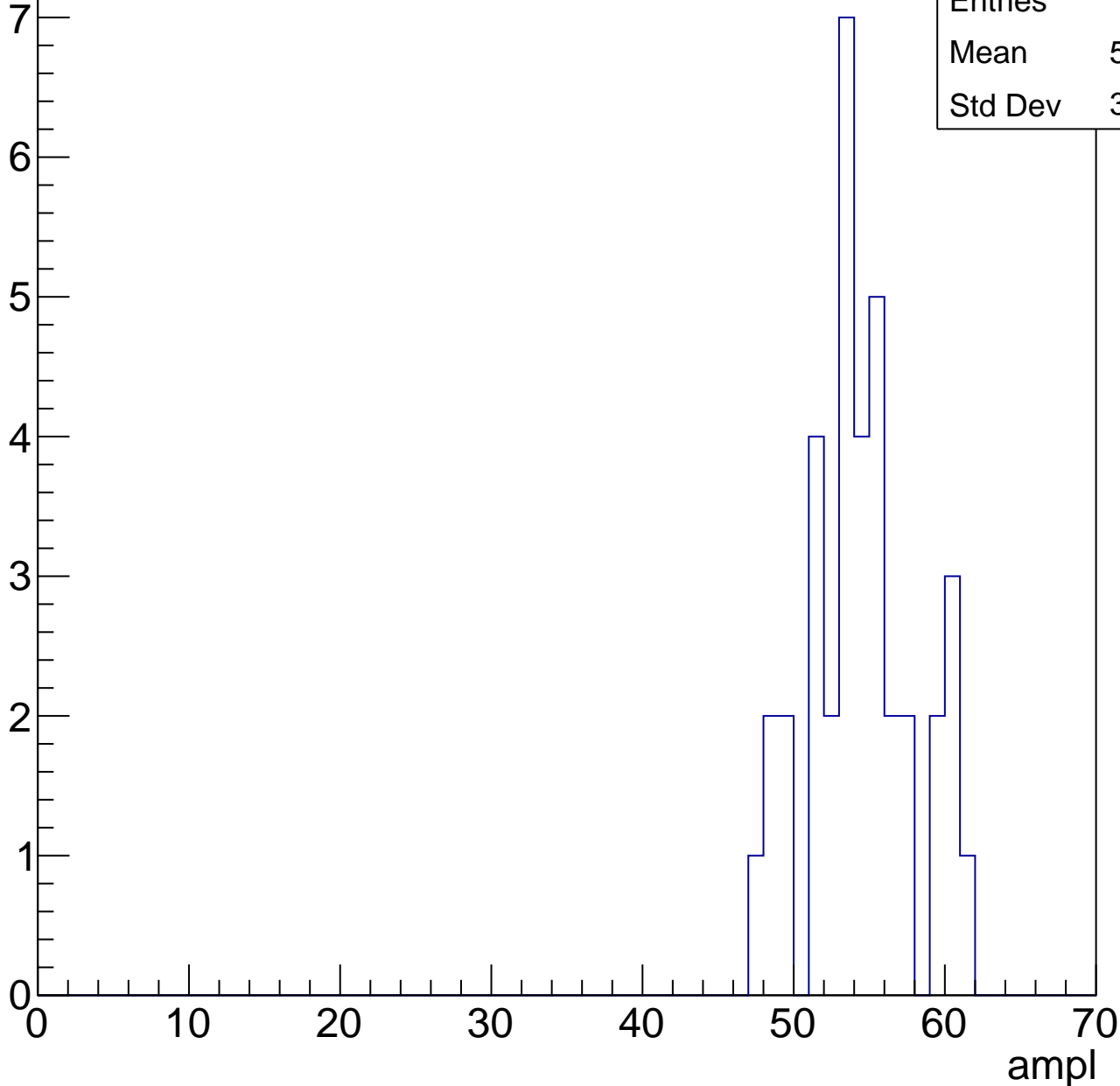


B1L102S, U19-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	53.95
Std Dev	3.526

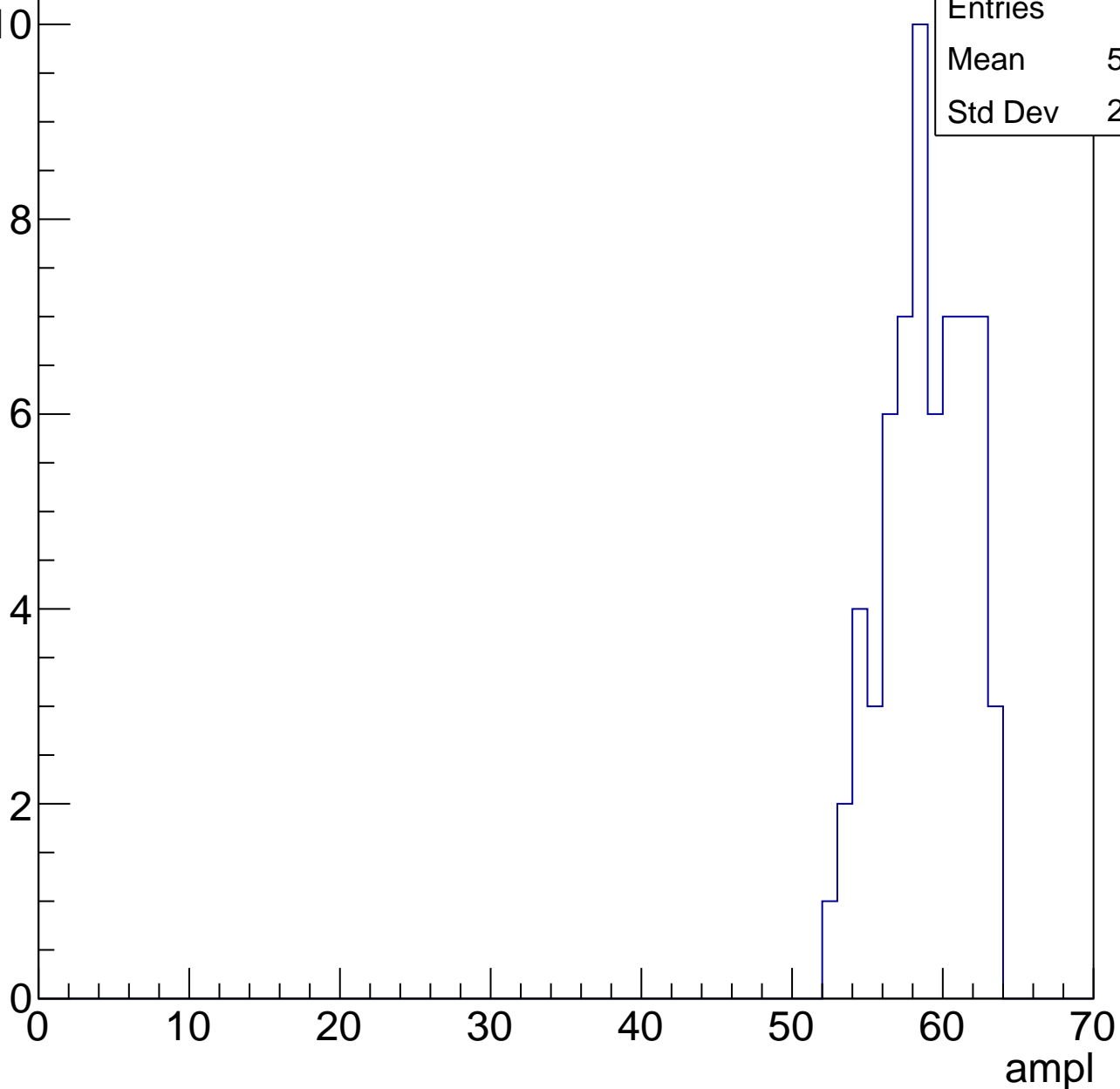


B1L102S, U19-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	58.38
Std Dev	2.768

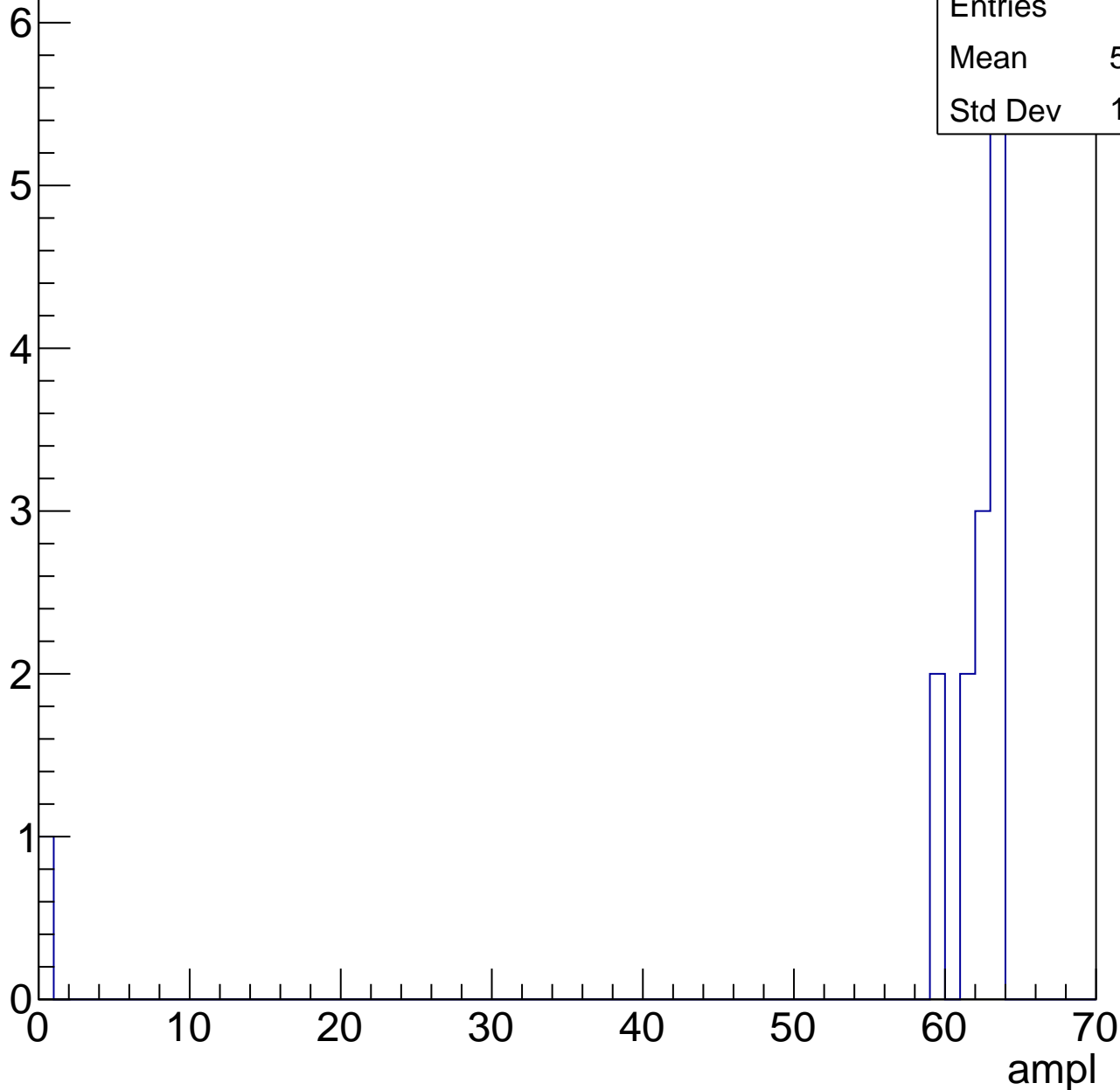


B1L102S, U19-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.43
Std Dev	15.99



B1L102S, U19-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch84, adc0

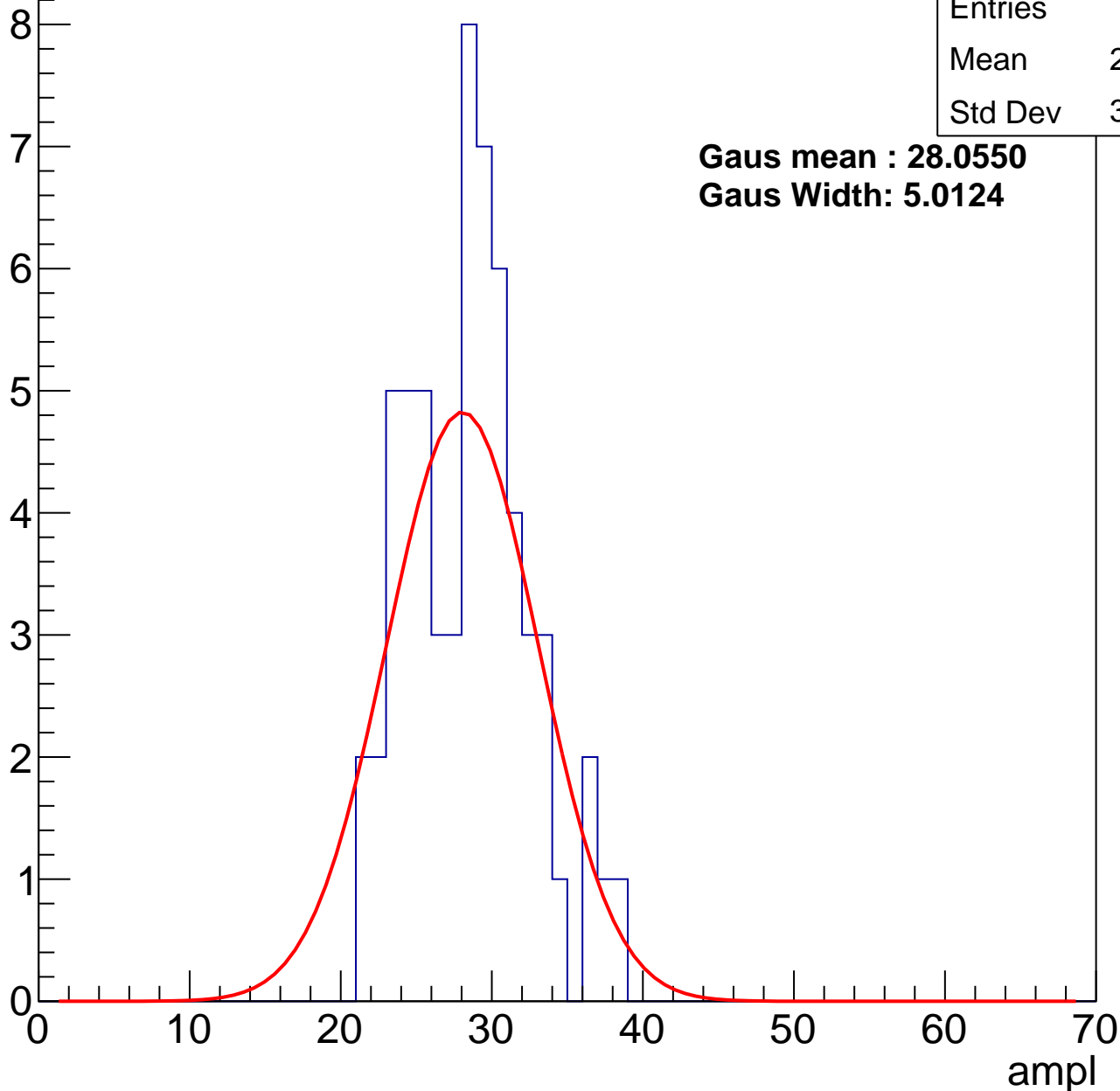
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.07
Std Dev	3.987

Gaus mean : 28.0550

Gaus Width: 5.0124



B1L102S, U19-ch84, adc1

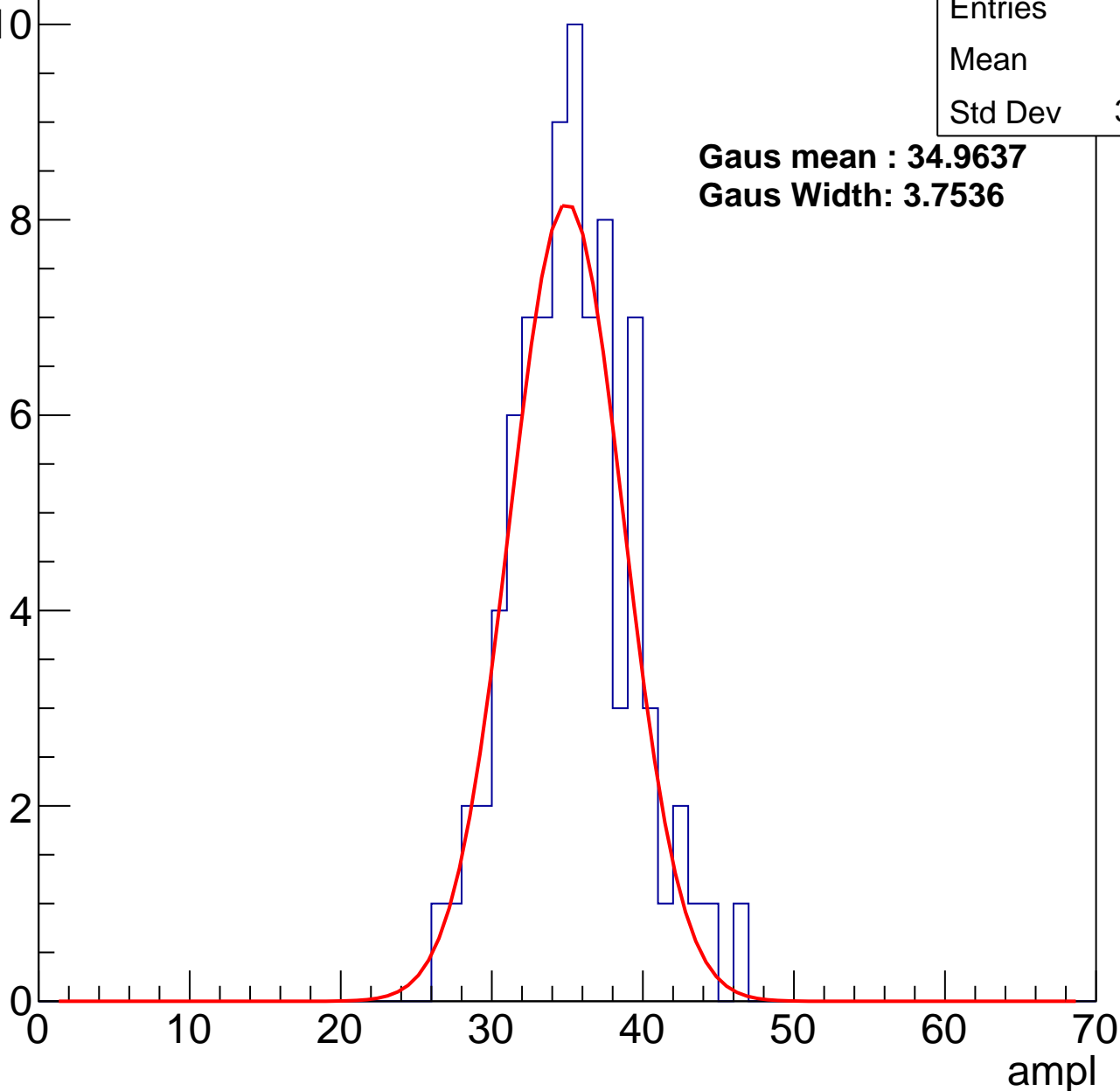
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	34.9
Std Dev	3.901

Gaus mean : 34.9637

Gaus Width: 3.7536



B1L102S, U19-ch84, adc2

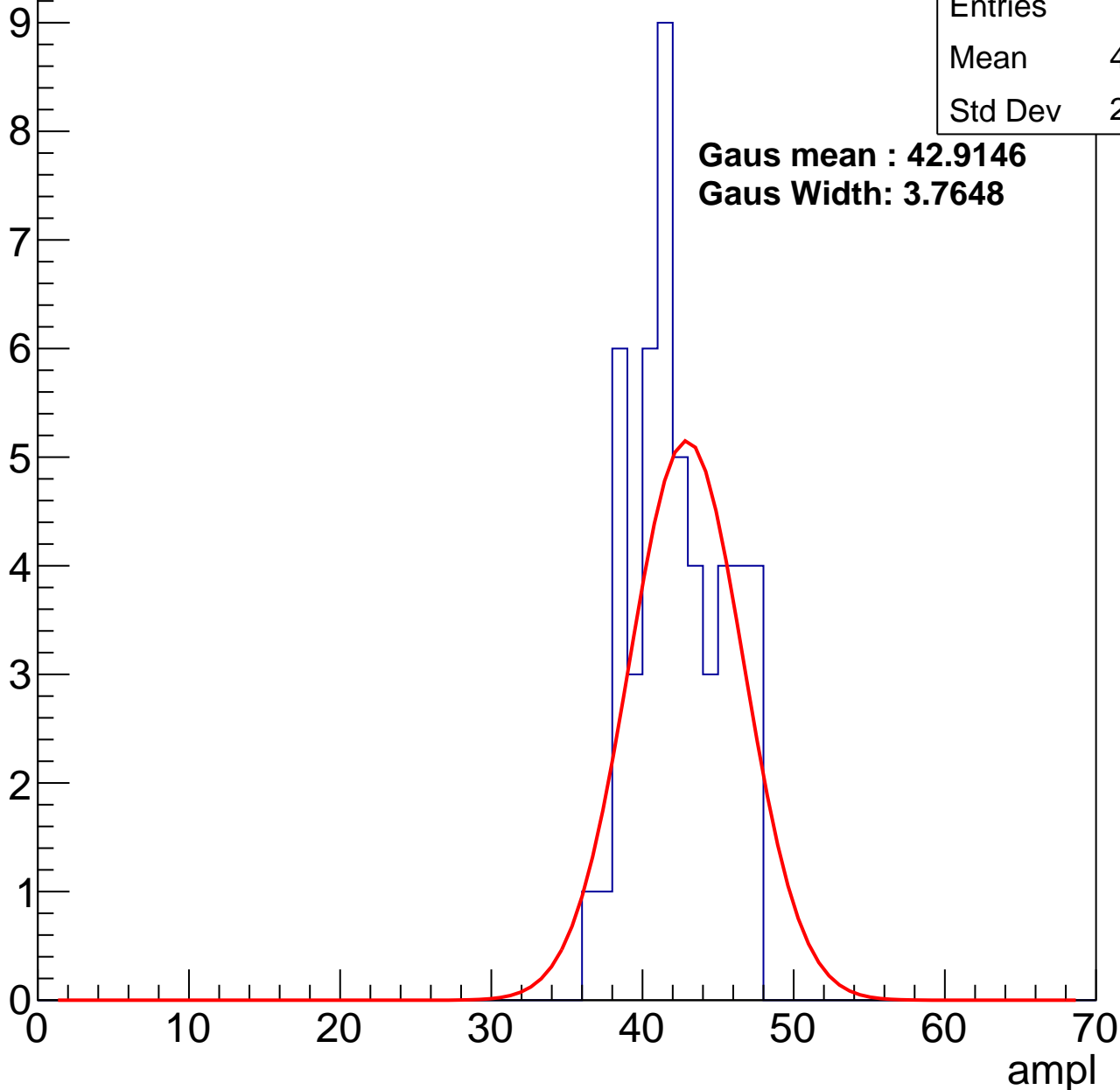
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	41.86
Std Dev	2.939

Gaus mean : 42.9146

Gaus Width: 3.7648

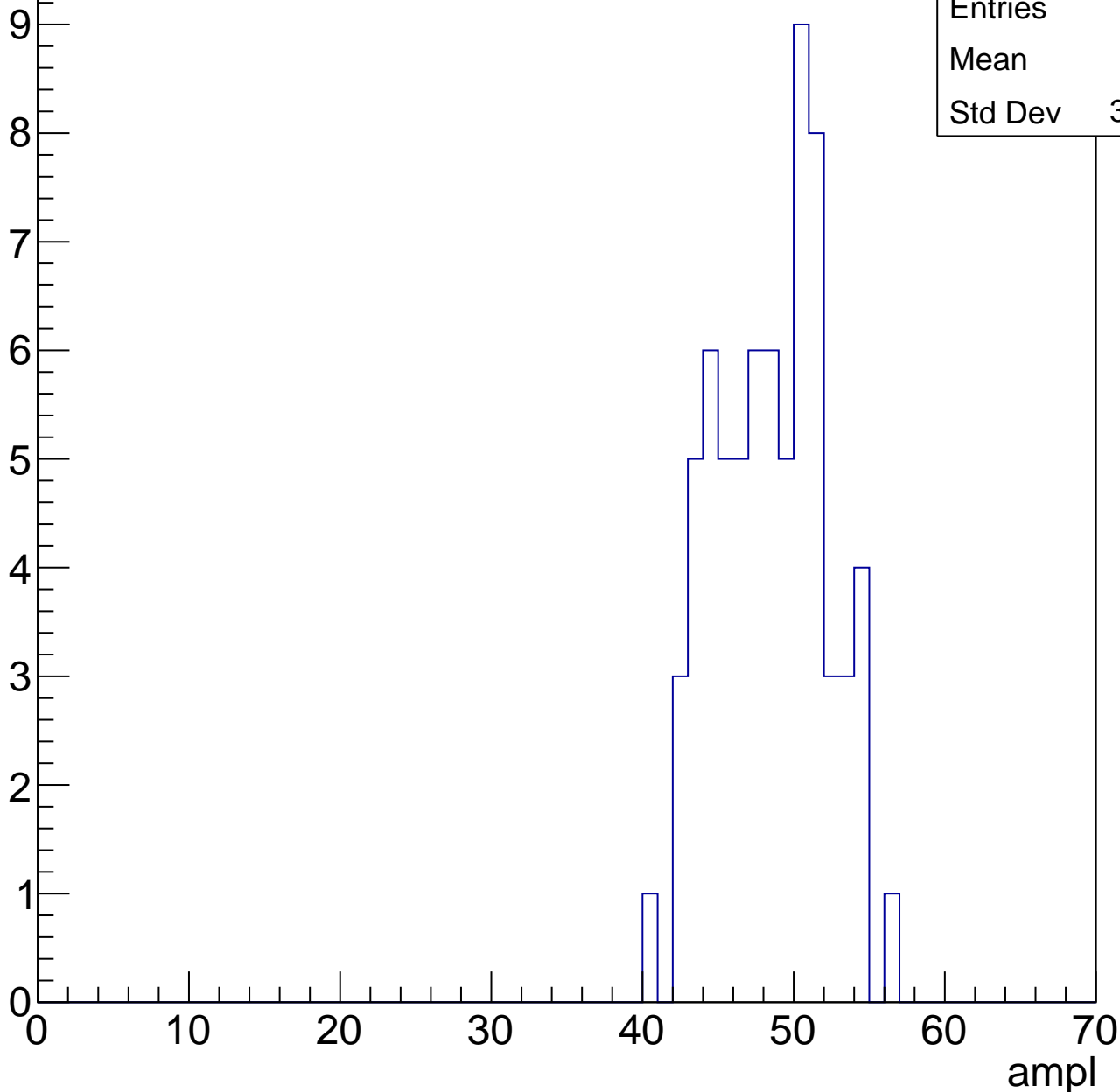


B1L102S, U19-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48
Std Dev	3.602

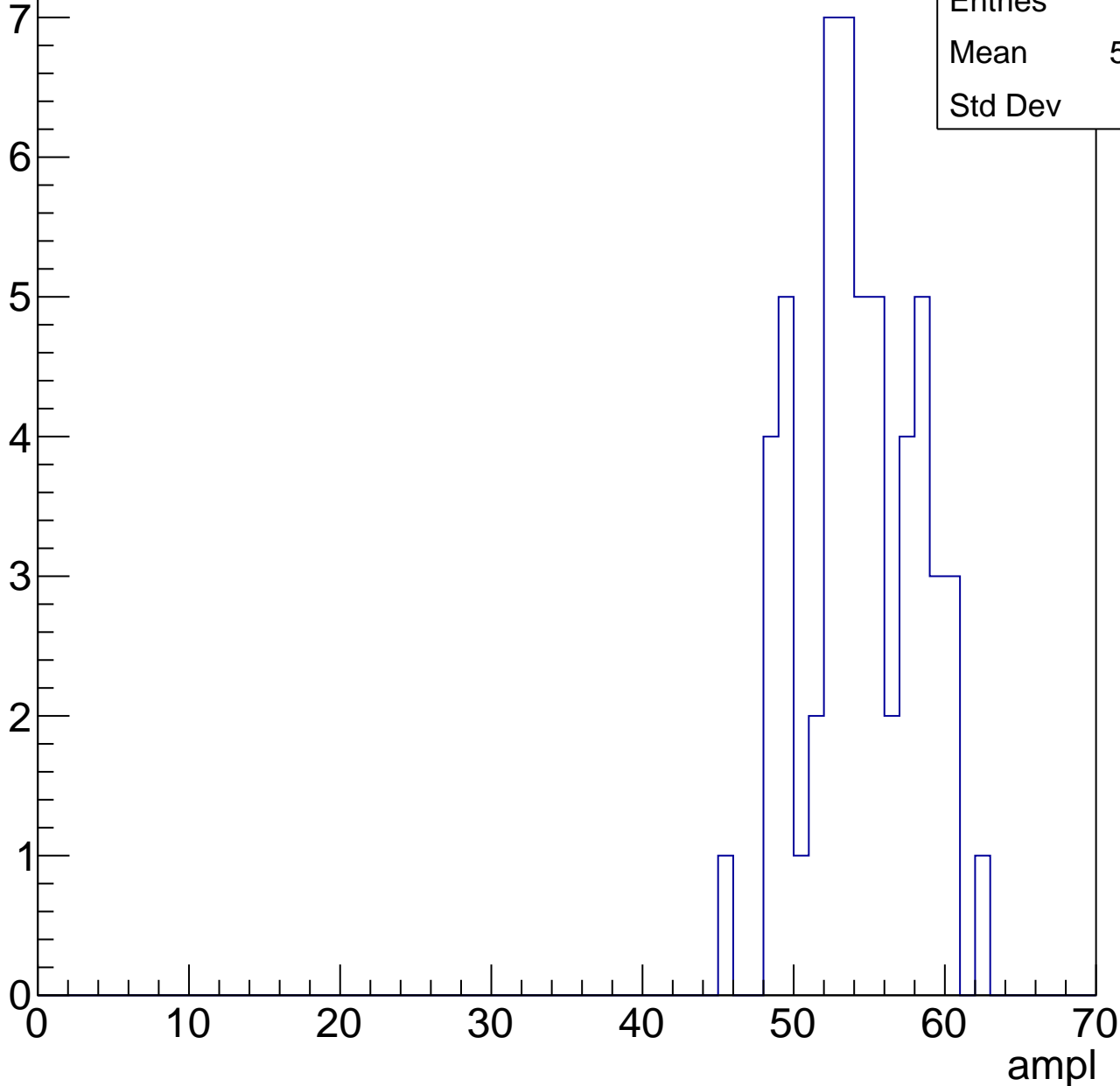


B1L102S, U19-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.87
Std Dev	3.8

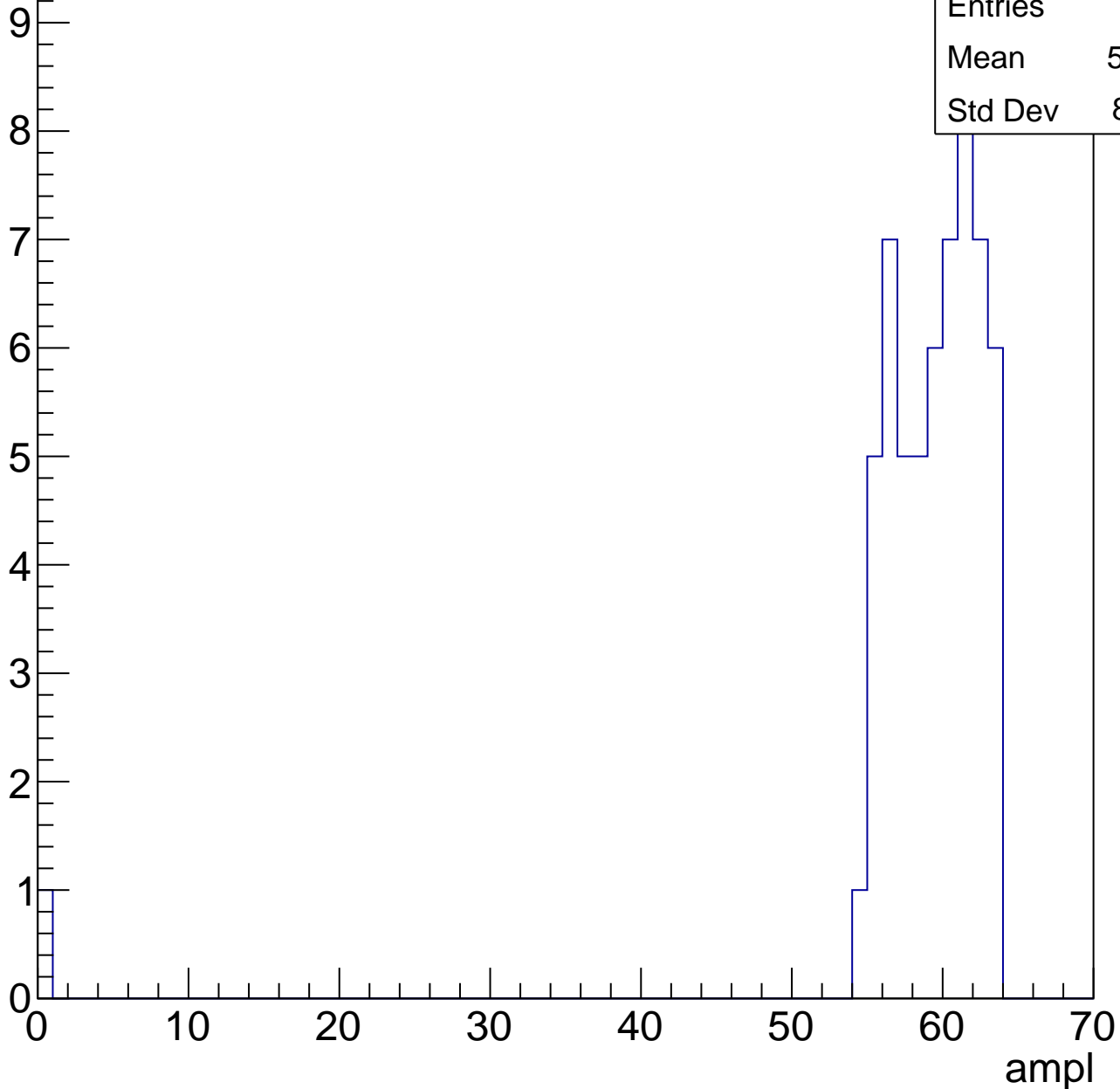


B1L102S, U19-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

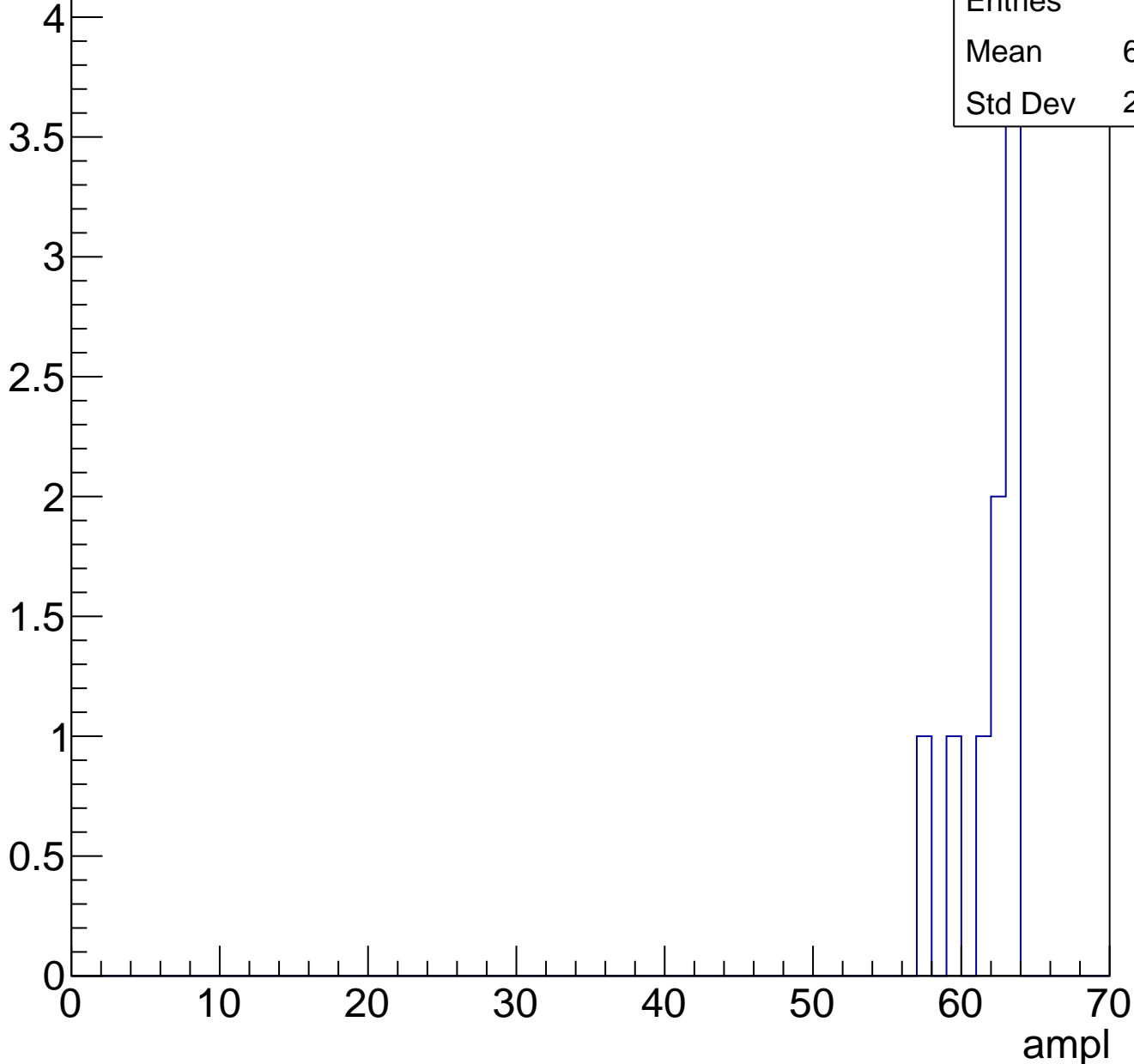
Entries	59
Mean	58.15
Std Dev	8.061



B1L102S, U19-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch85, adc0

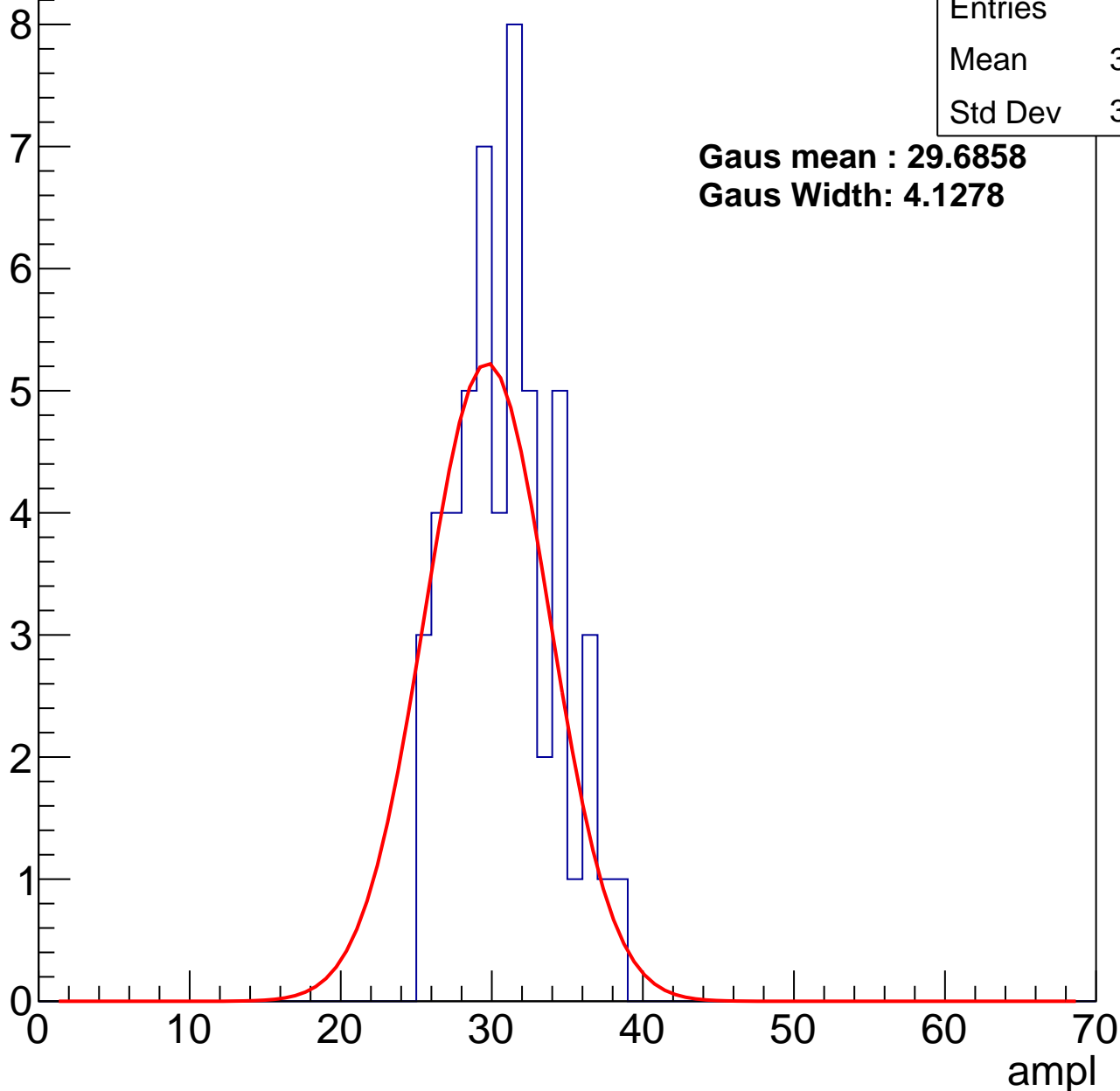
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	30.42
Std Dev	3.265

Gaus mean : 29.6858

Gaus Width: 4.1278



B1L102S, U19-ch85, adc1

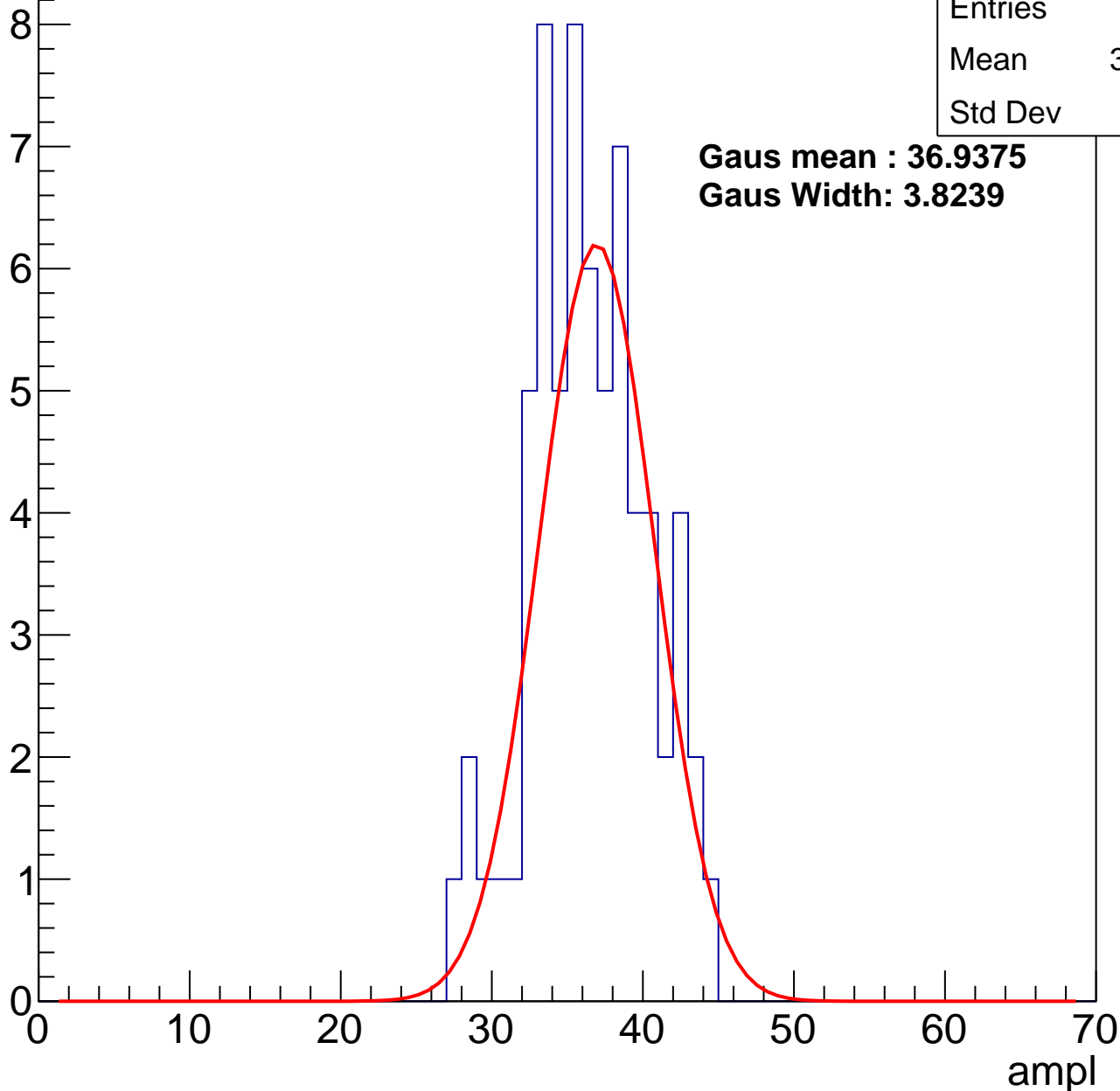
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.97
Std Dev	3.84

Gaus mean : 36.9375

Gaus Width: 3.8239



B1L102S, U19-ch85, adc2

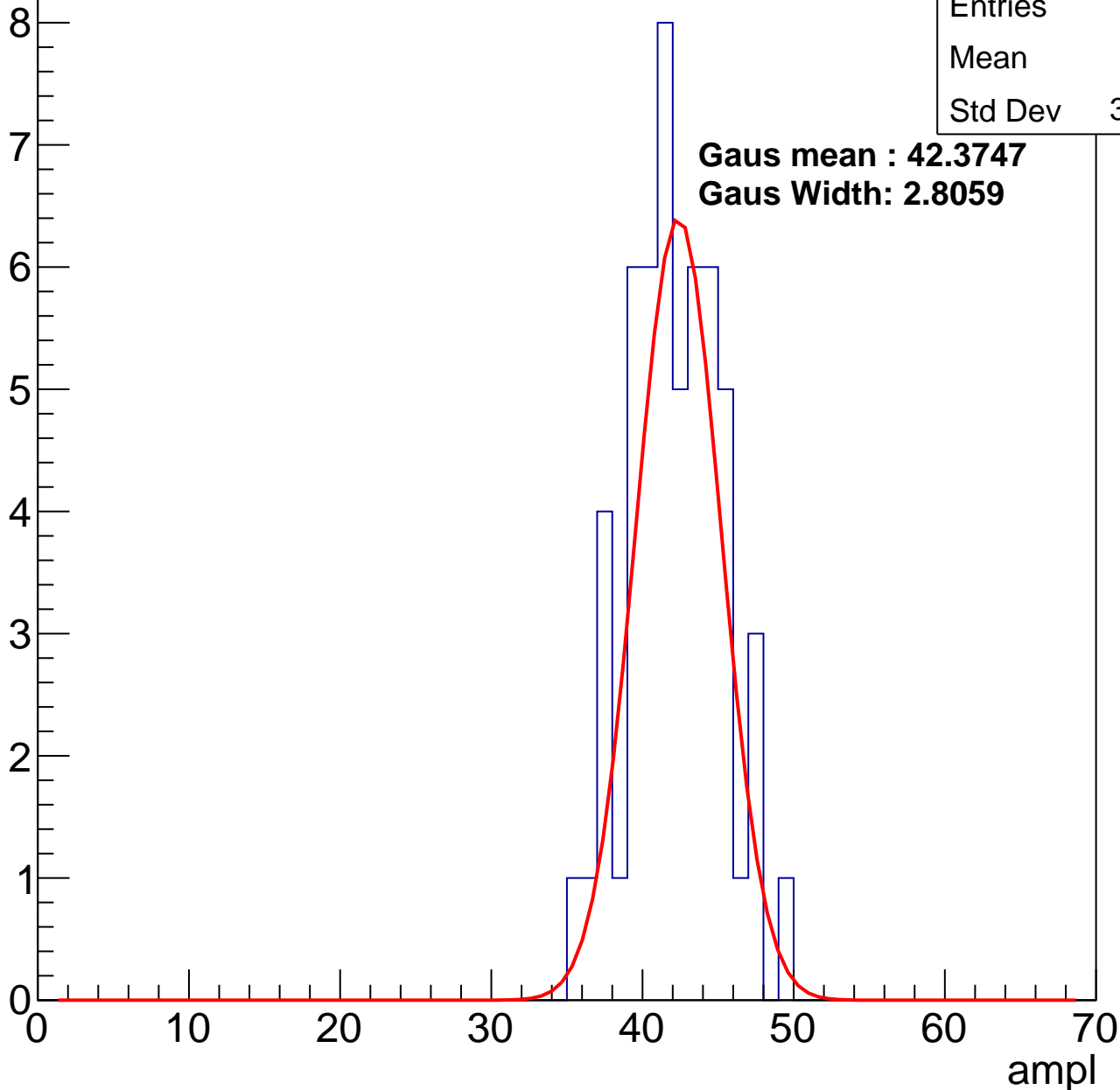
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.7
Std Dev	3.035

Gaus mean : 42.3747

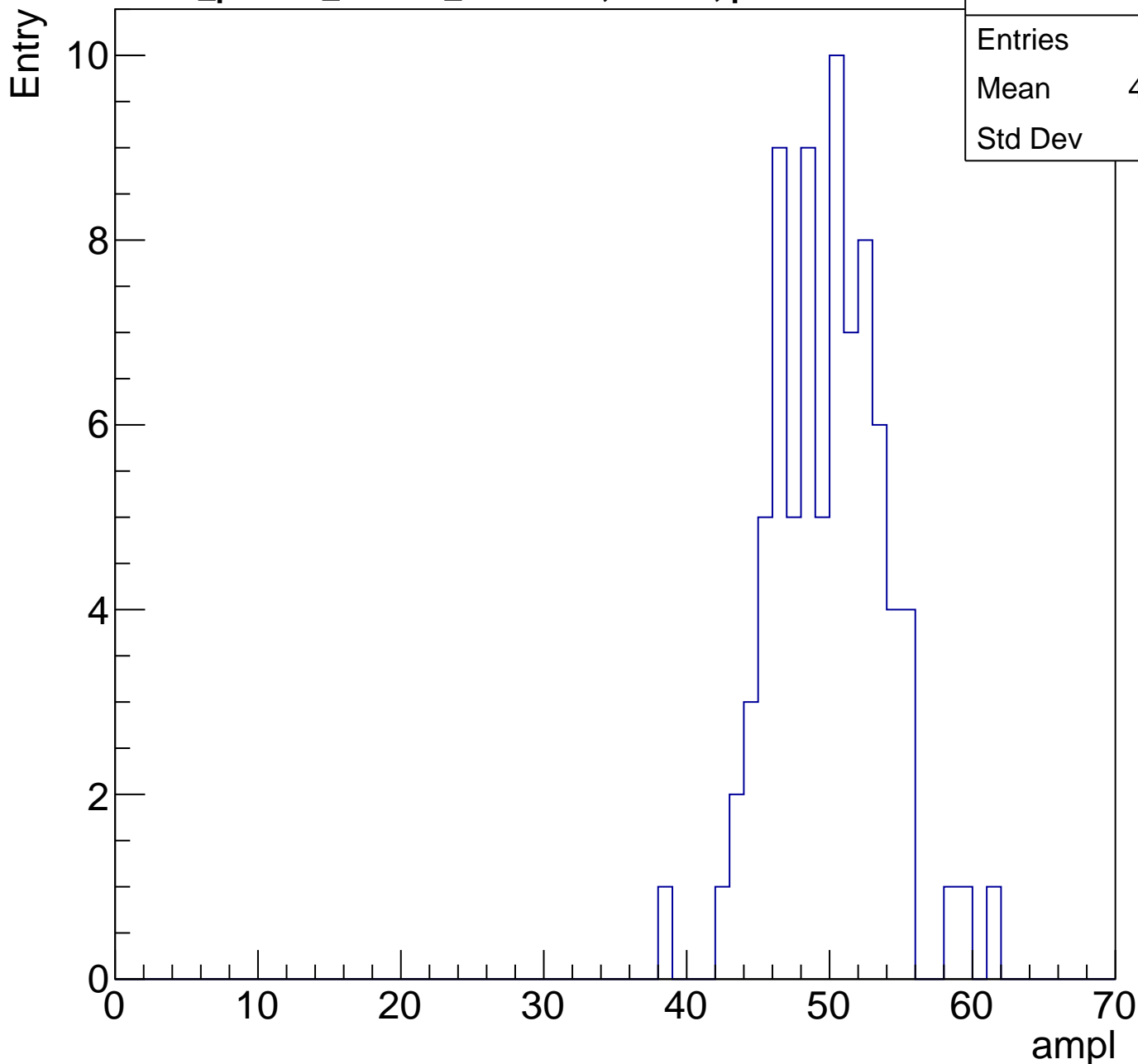
Gaus Width: 2.8059



B1L102S, U19-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	49.44
Std Dev	3.93

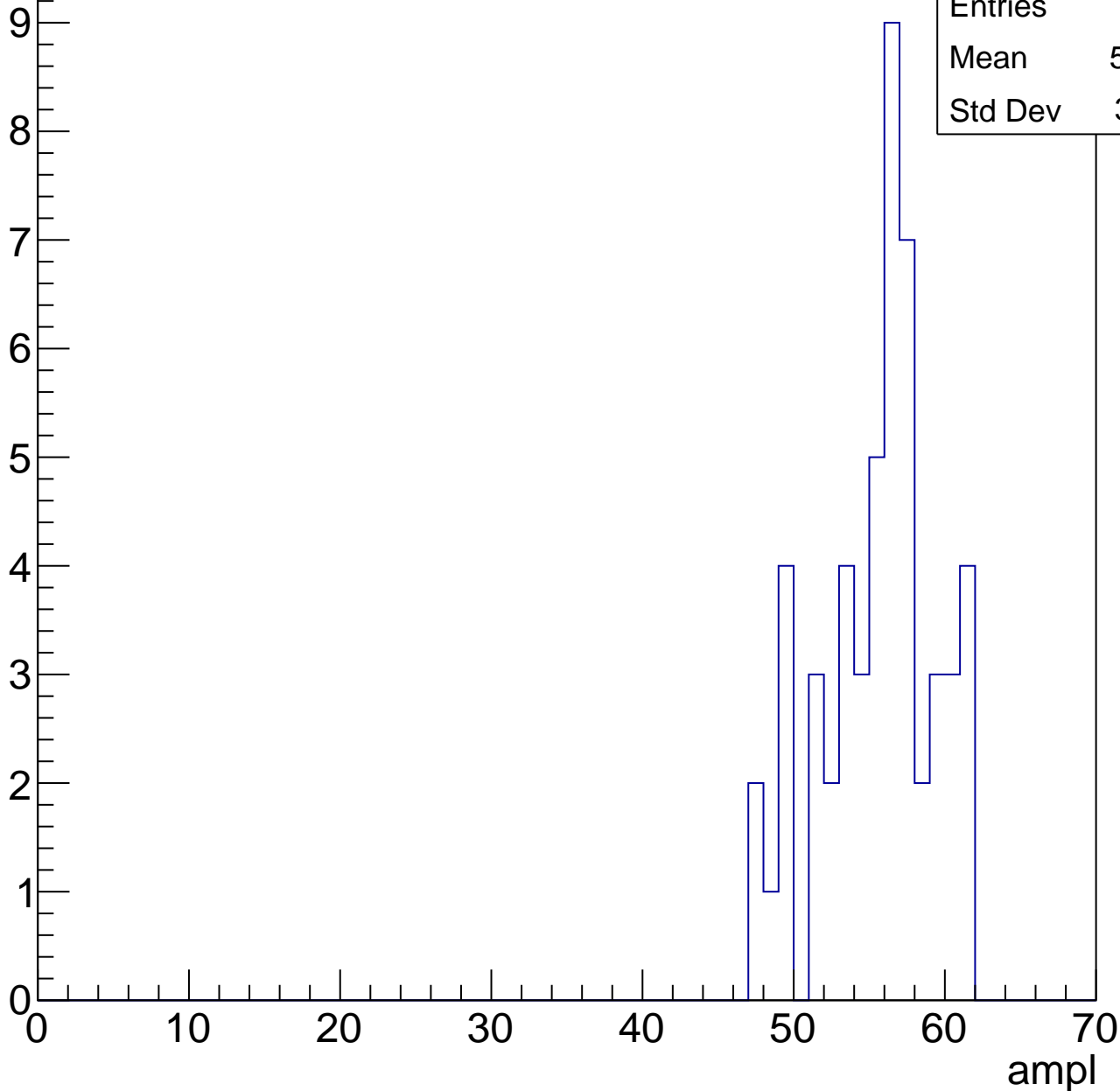


B1L102S, U19-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.08
Std Dev	3.741

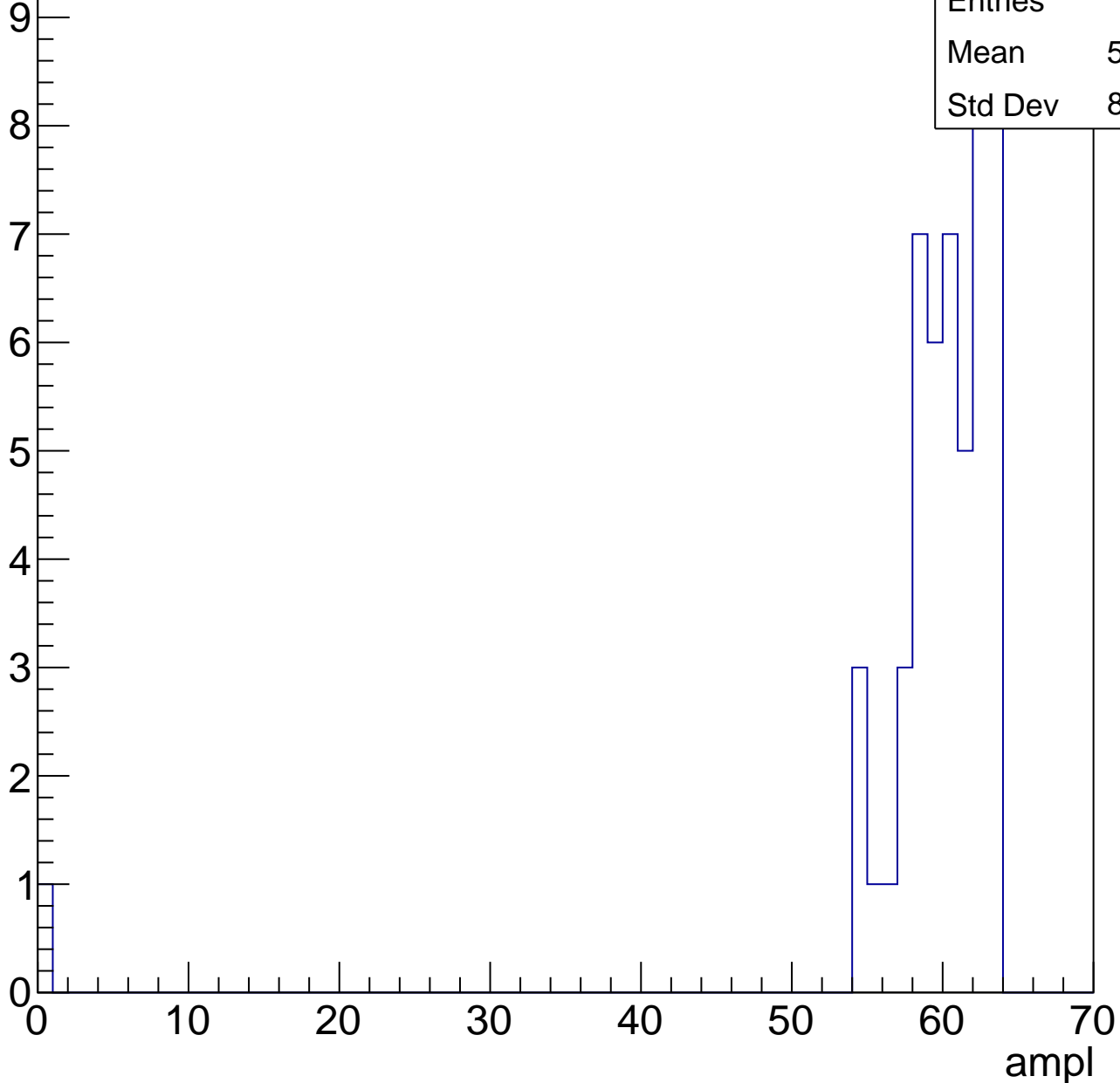


B1L102S, U19-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.73
Std Dev	8.603



B1L102S, U19-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch86, adc0

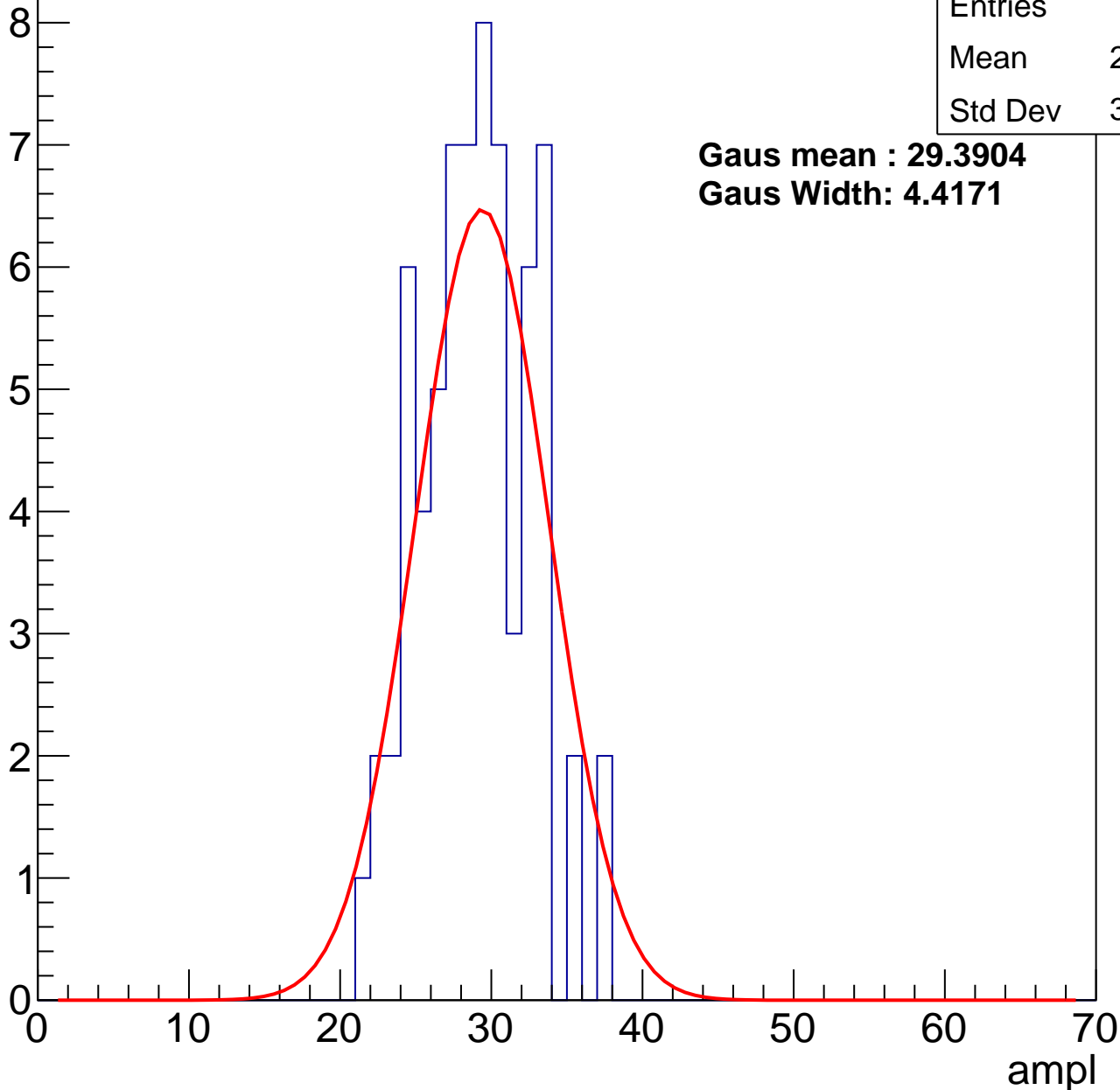
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.58
Std Dev	3.613

Gaus mean : 29.3904

Gaus Width: 4.4171



B1L102S, U19-ch86, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.83
Std Dev	4.037

Gaus mean : 36.1107

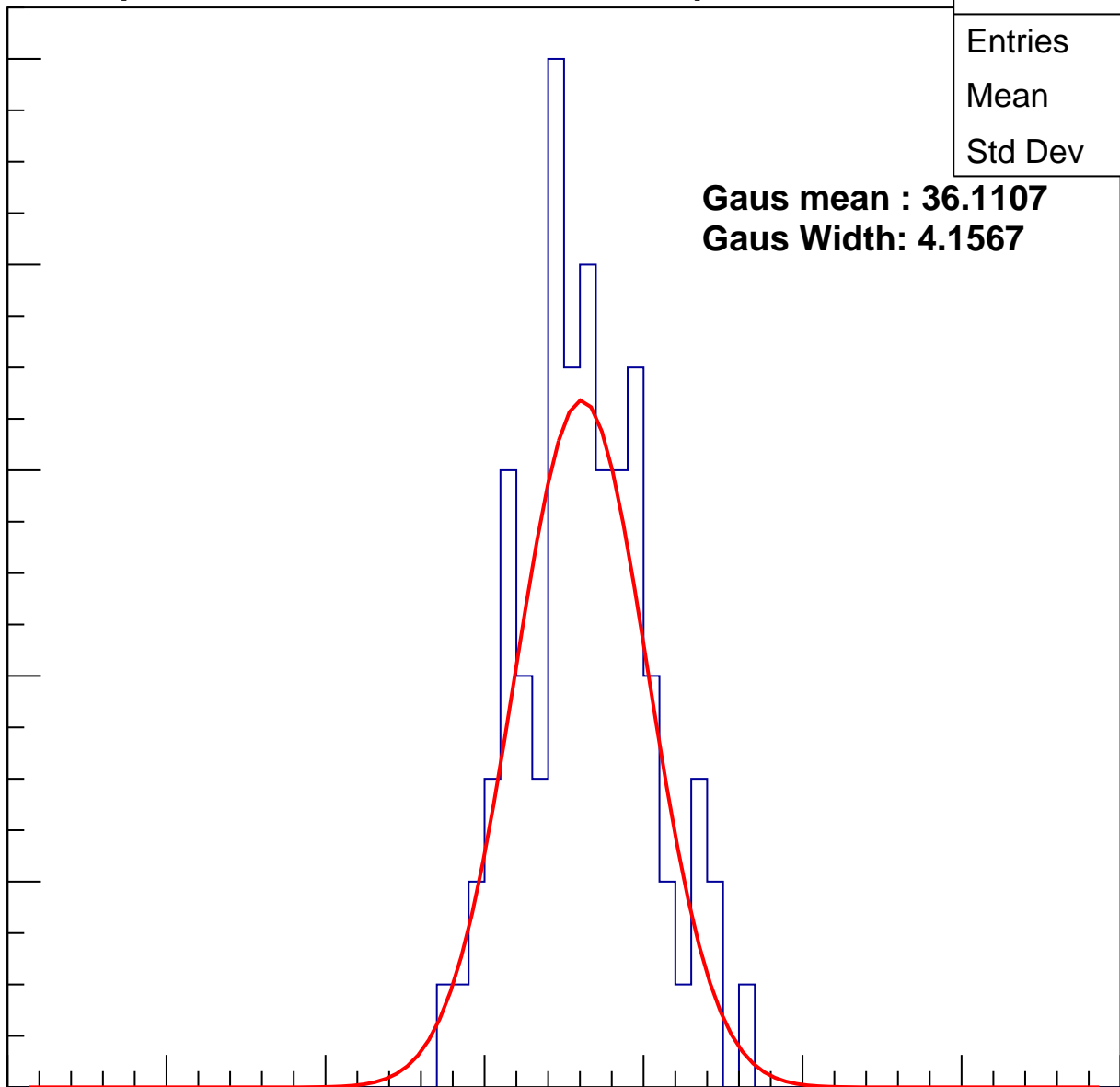
Gaus Width: 4.1567

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch86, adc2

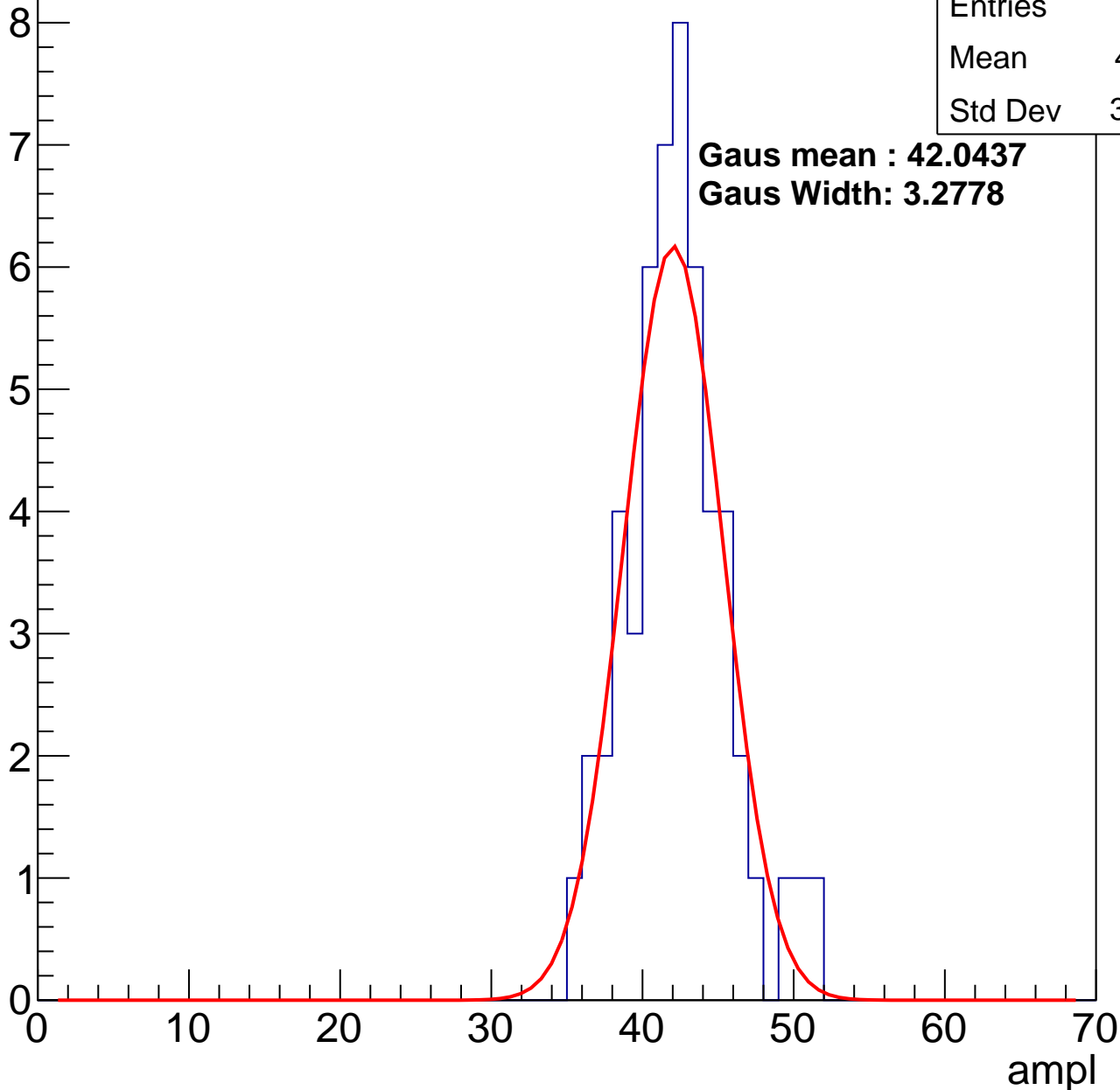
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.81
Std Dev	3.365

Gaus mean : 42.0437

Gaus Width: 3.2778

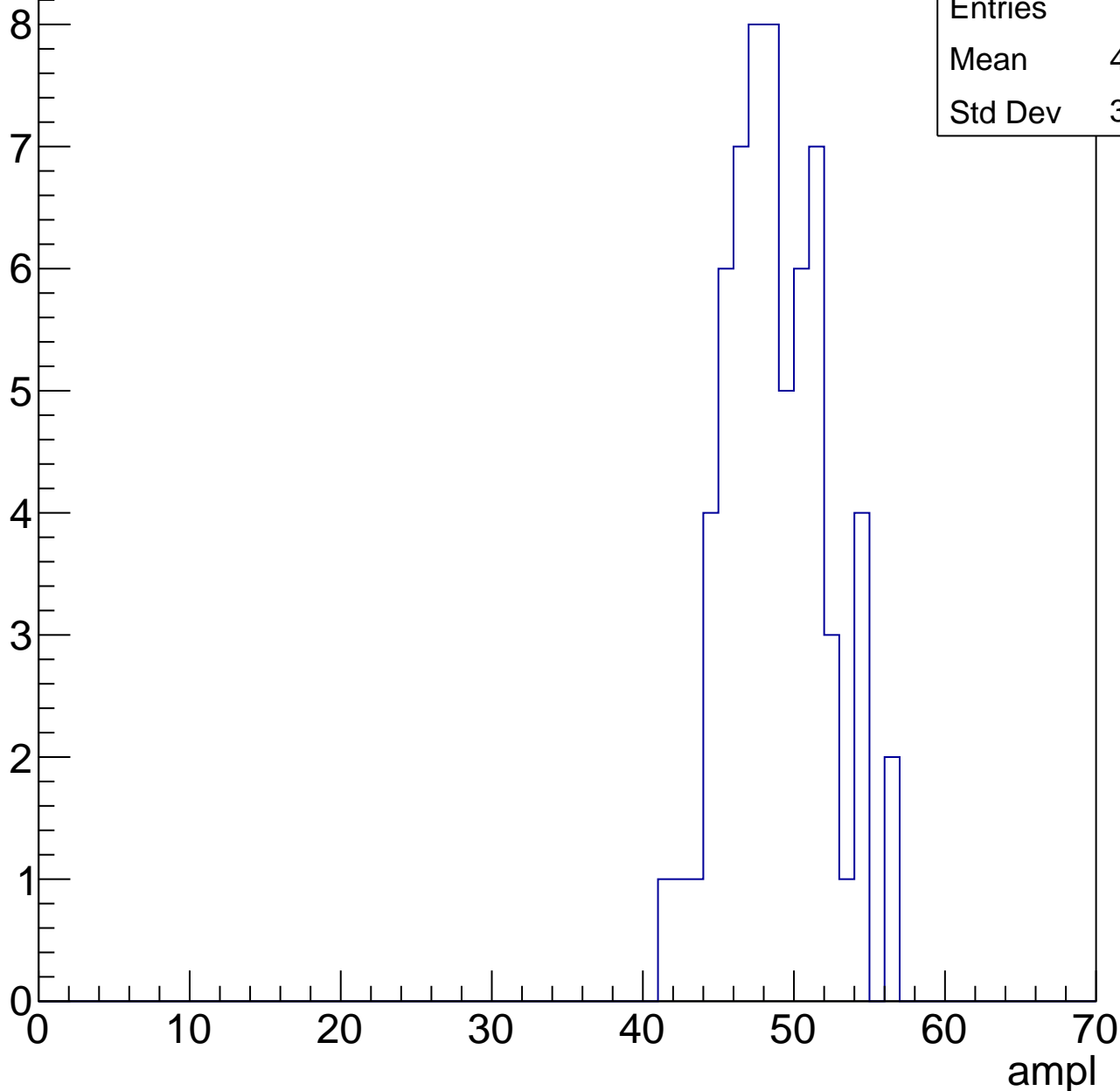


B1L102S, U19-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.33
Std Dev	3.293

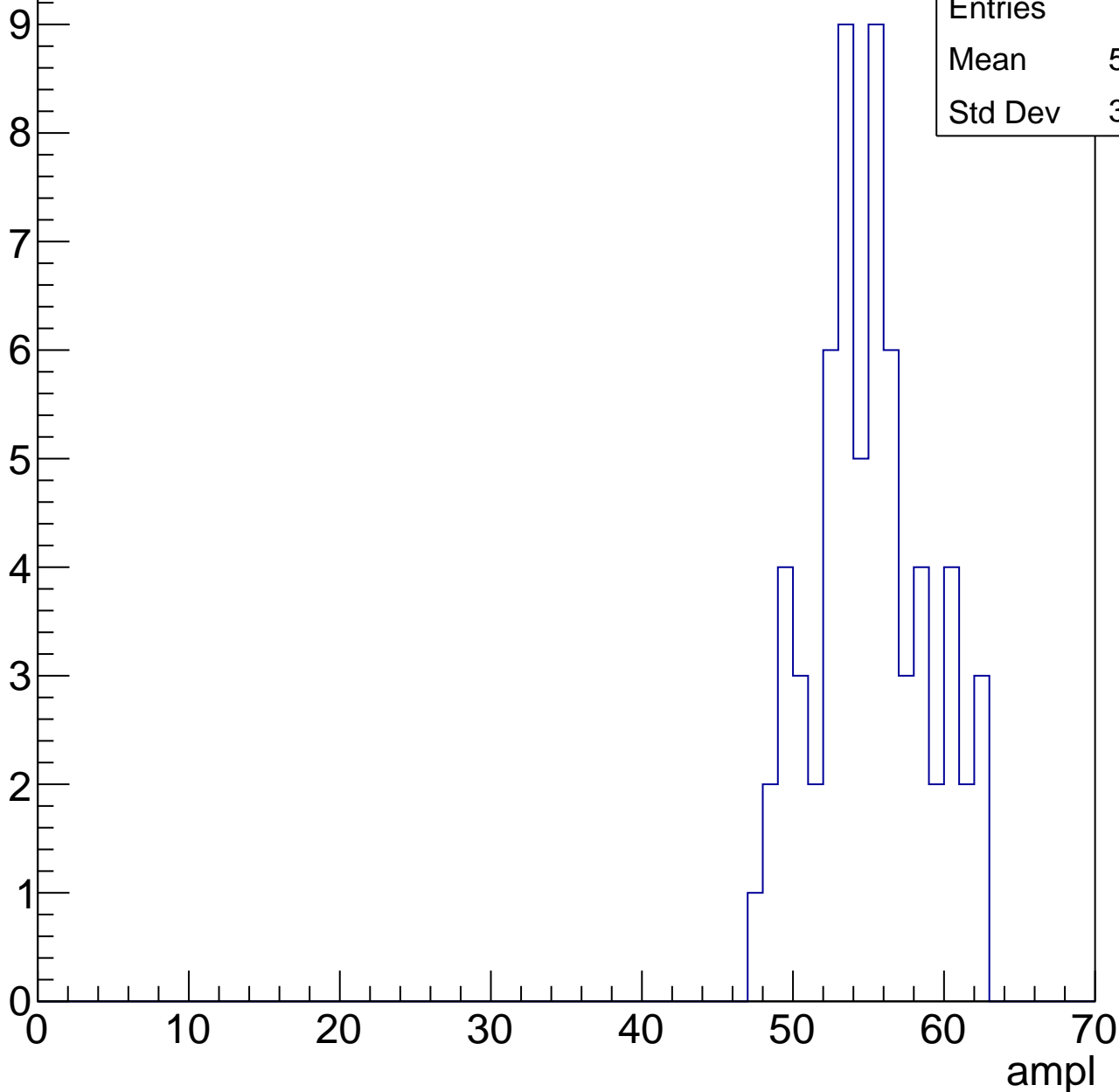


B1L102S, U19-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.62
Std Dev	3.728



B1L102S, U19-ch86, adc5

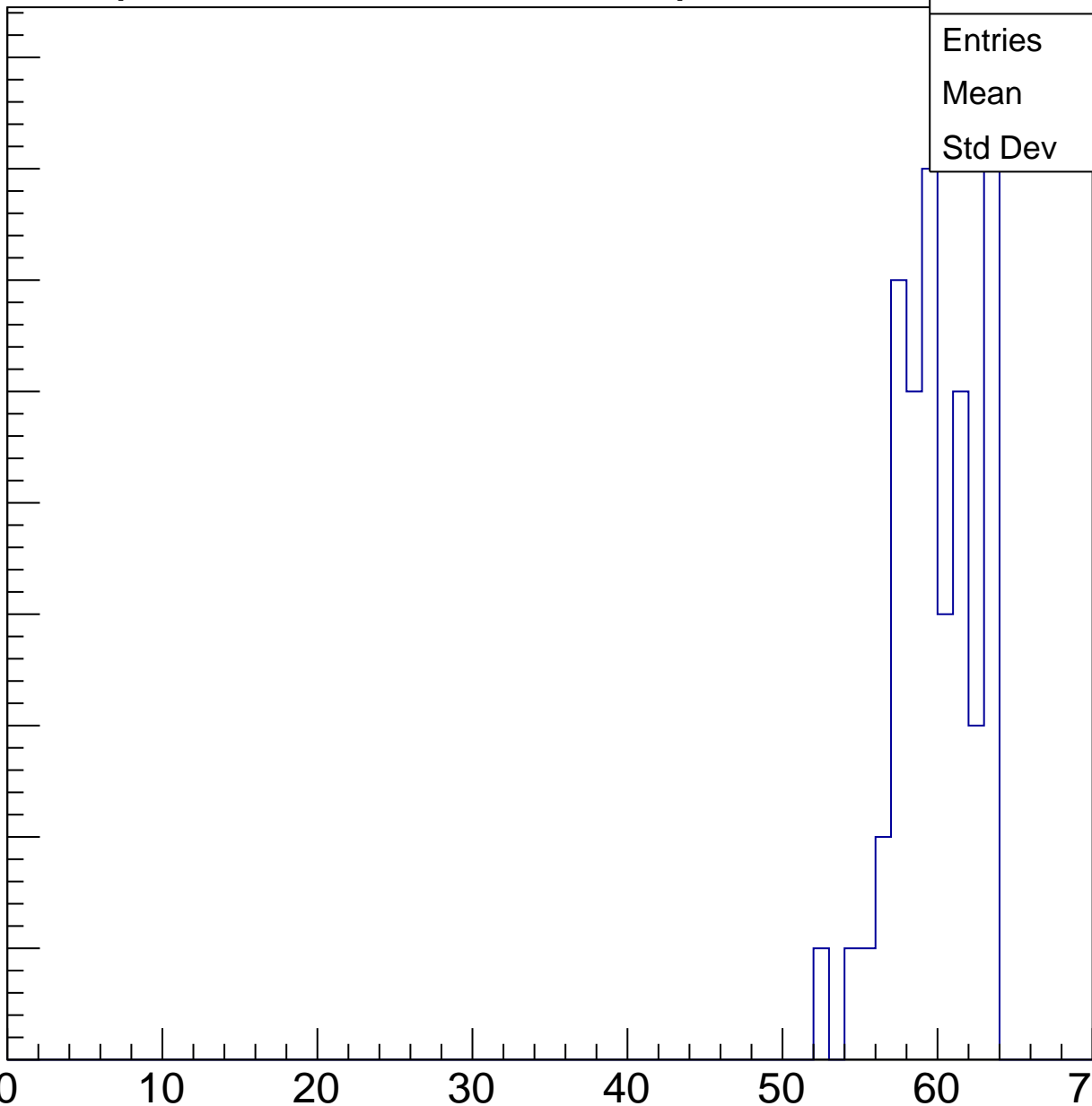
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.4
Std Dev	2.636

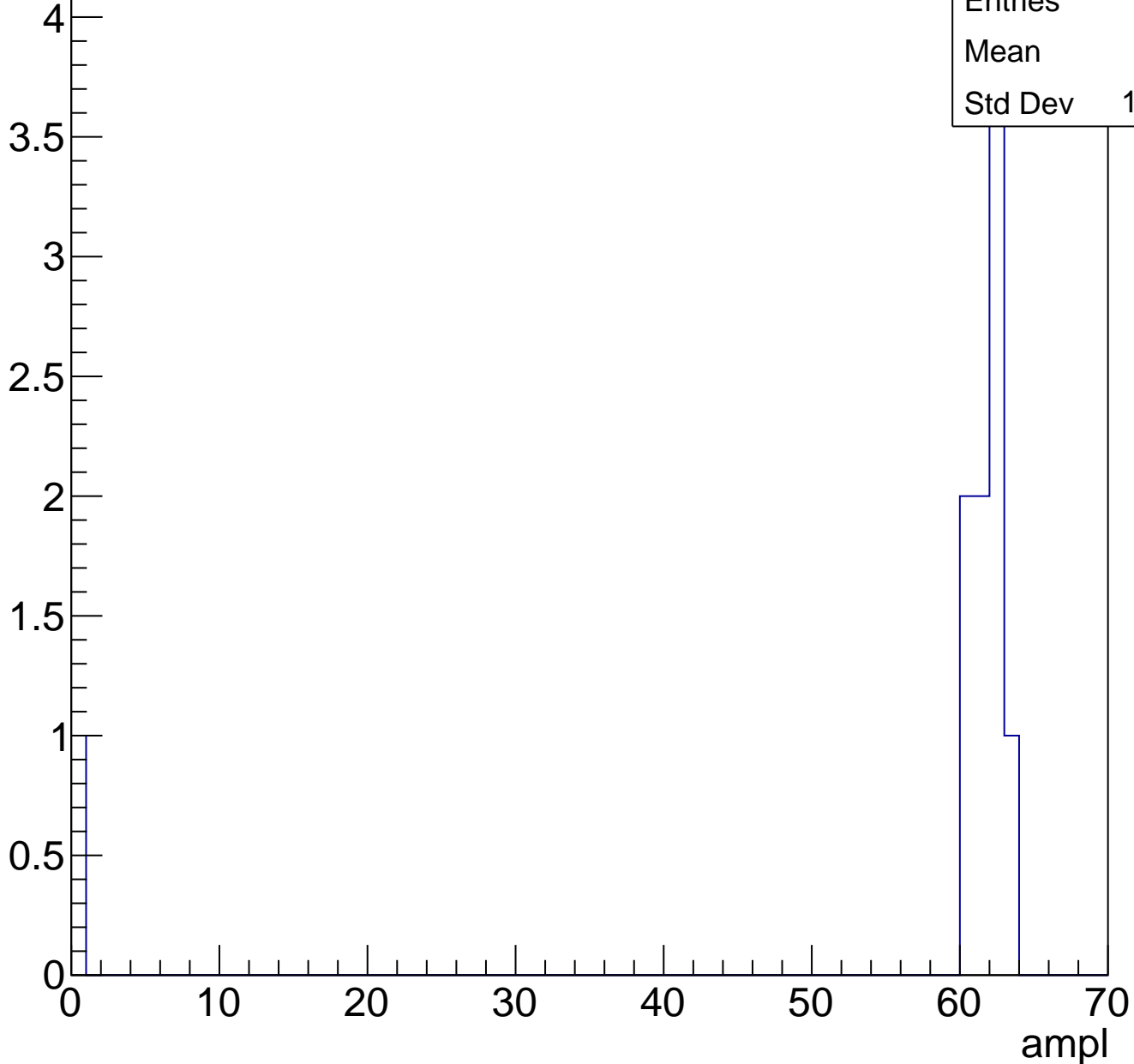
ampl



B1L102S, U19-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	55.3
Std Dev	18.46

B1L102S, U19-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch87, adc0

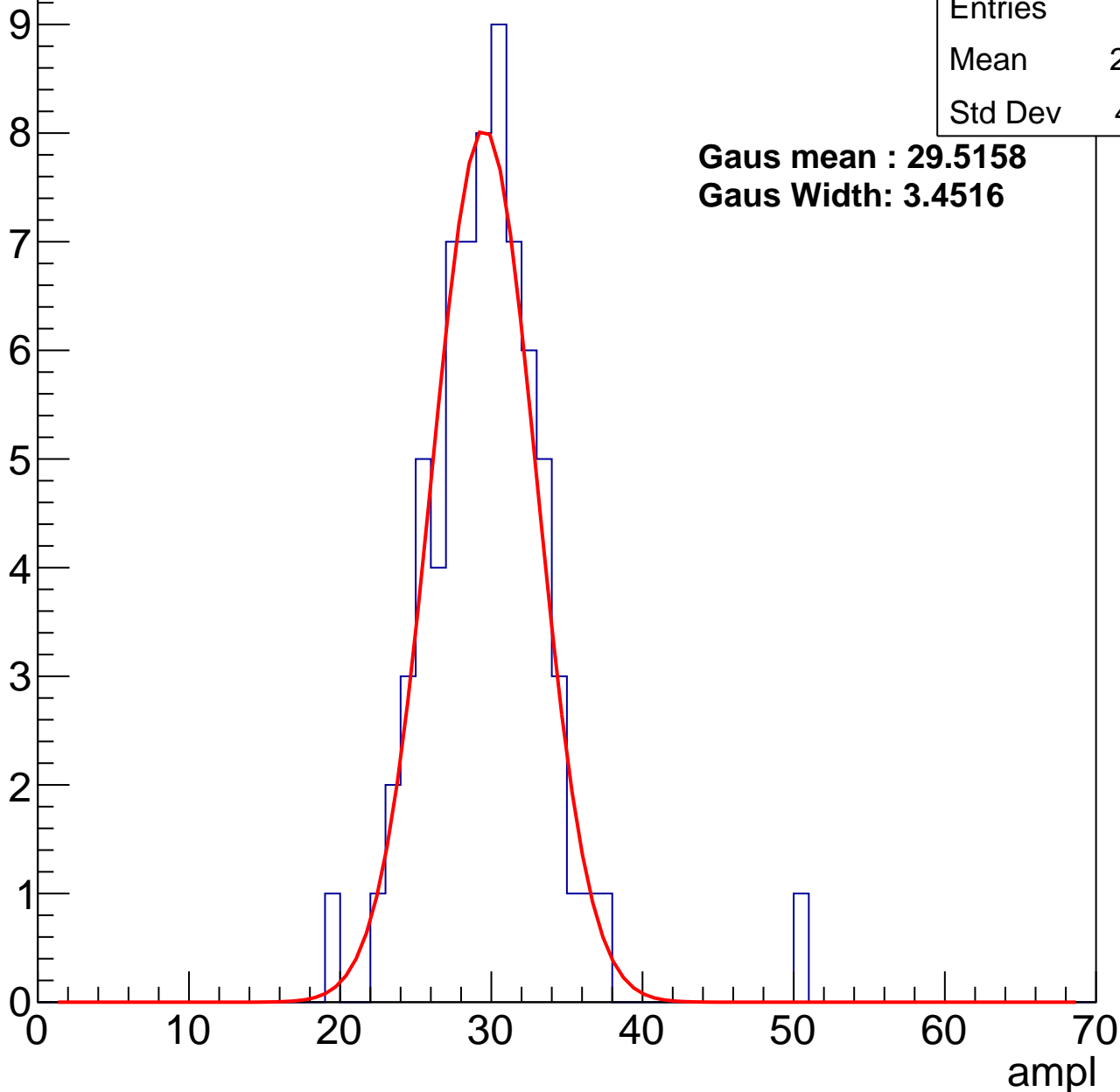
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.29
Std Dev	4.211

Gaus mean : 29.5158

Gaus Width: 3.4516



B1L102S, U19-ch87, adc1

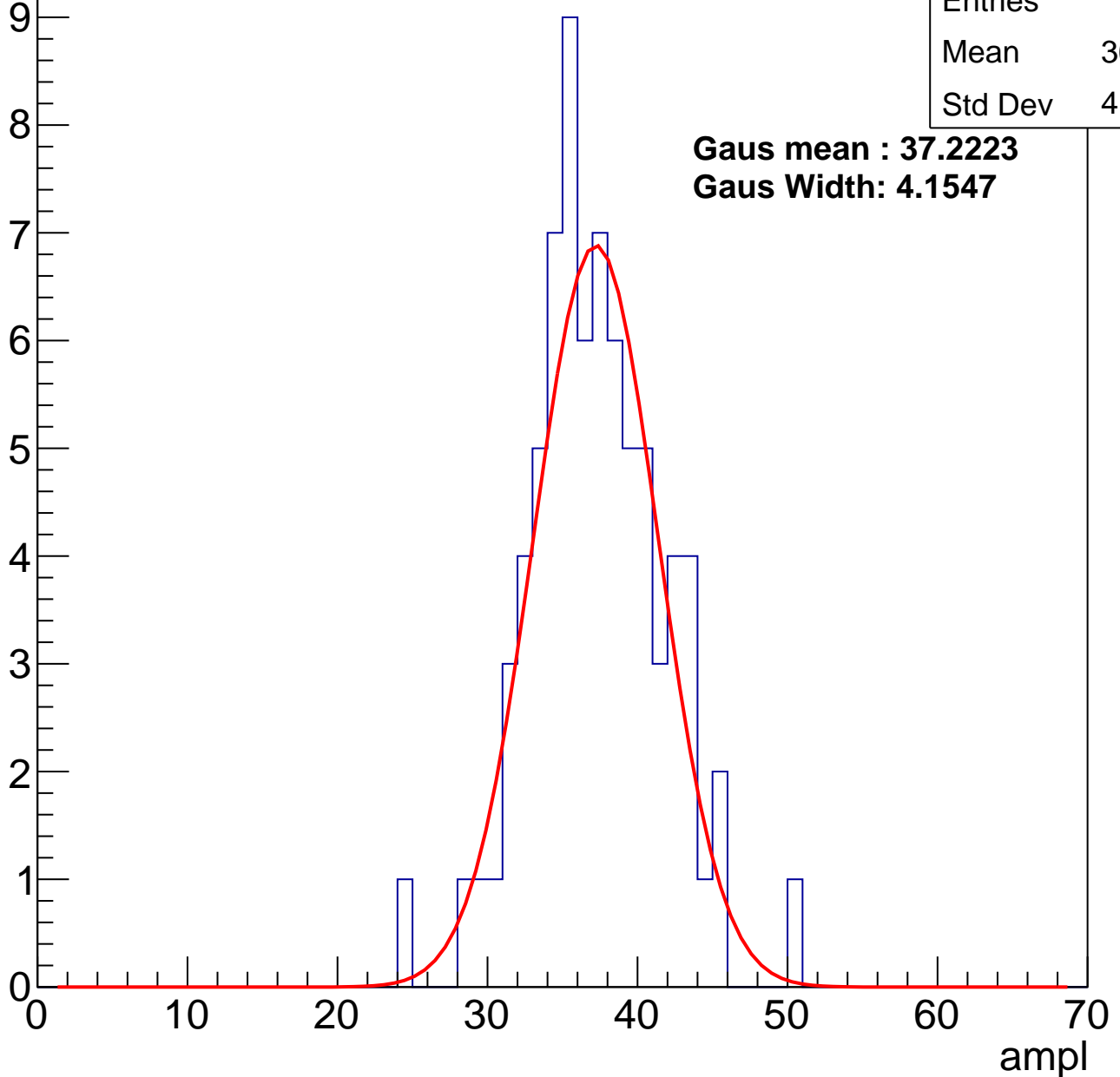
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.78
Std Dev	4.385

Gaus mean : 37.2223

Gaus Width: 4.1547



B1L102S, U19-ch87, adc2

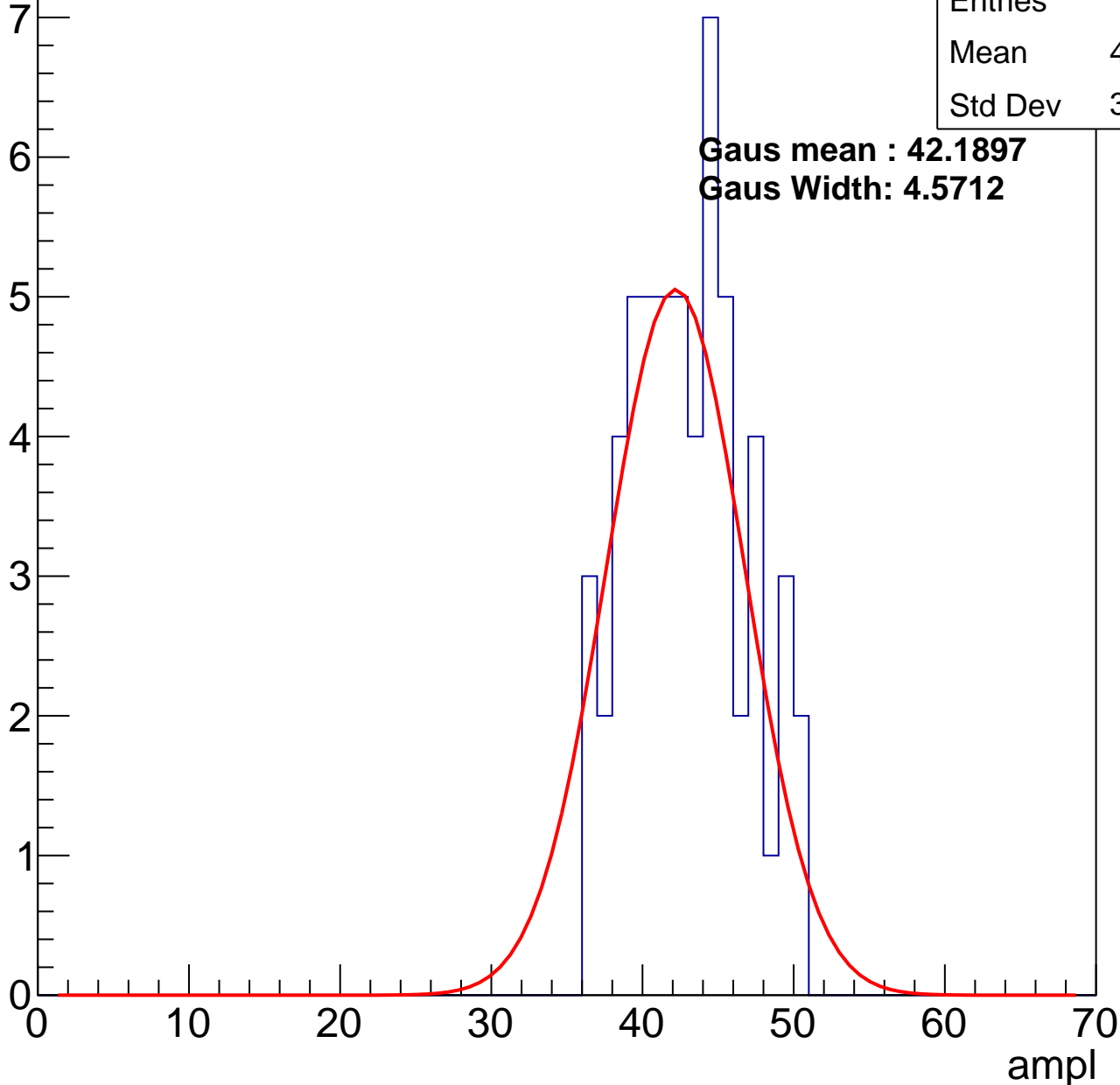
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.53
Std Dev	3.737

Gaus mean : 42.1897

Gaus Width: 4.5712

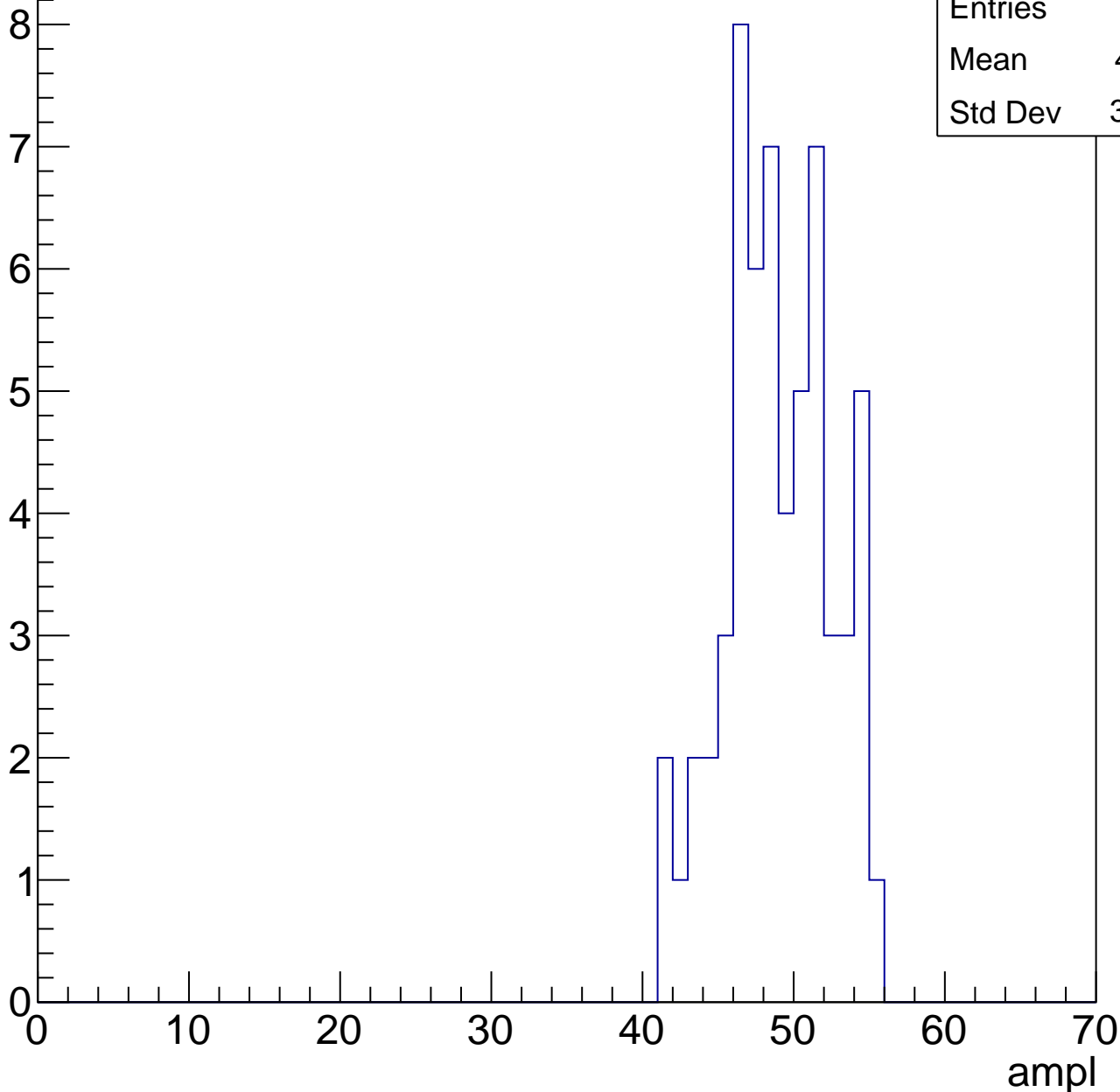


B1L102S, U19-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.51
Std Dev	3.456

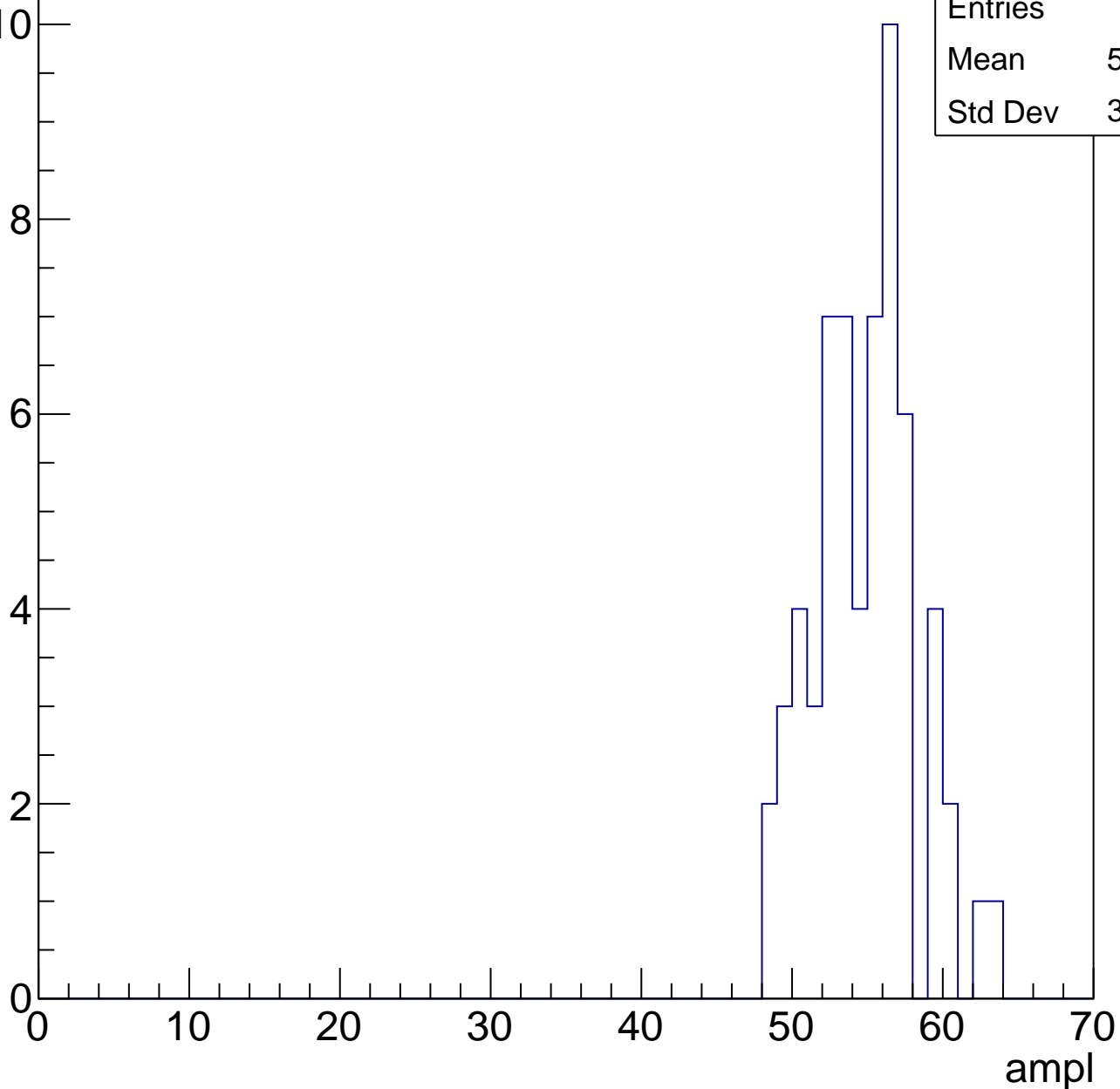


B1L102S, U19-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.34
Std Dev	3.348

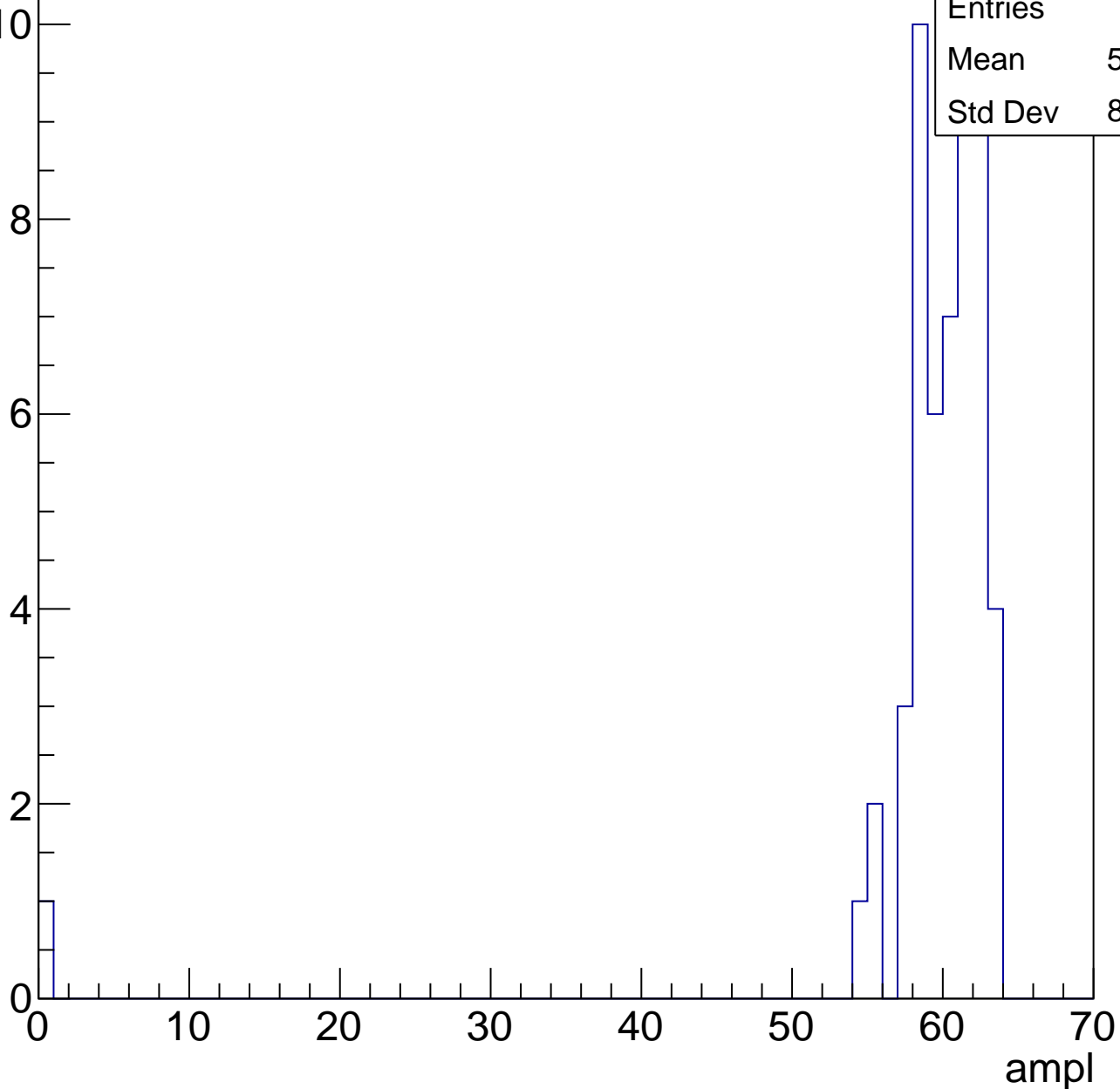


B1L102S, U19-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.66
Std Dev	8.407

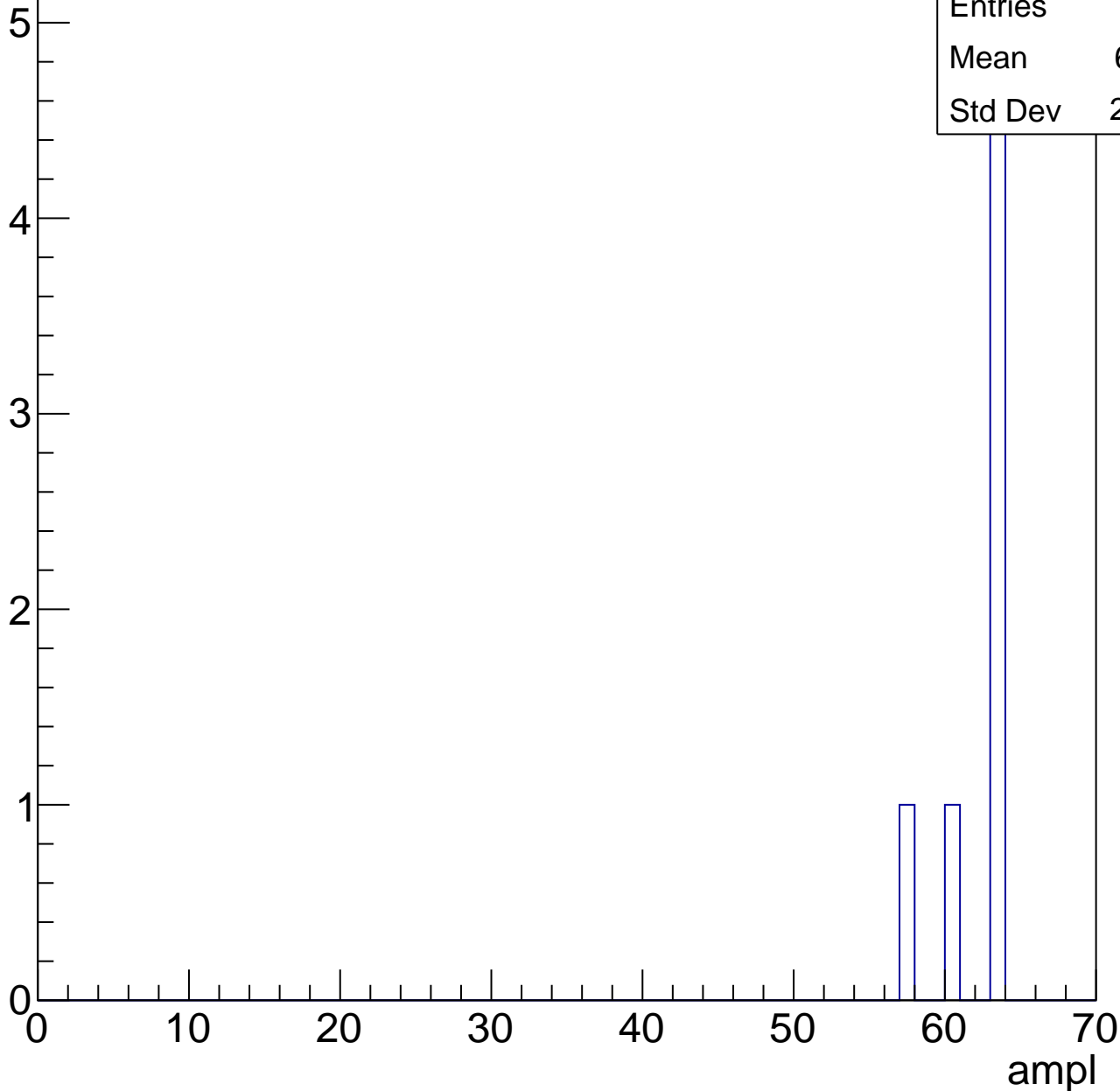


B1L102S, U19-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	61.71
Std Dev	2.185



B1L102S, U19-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch88, adc0

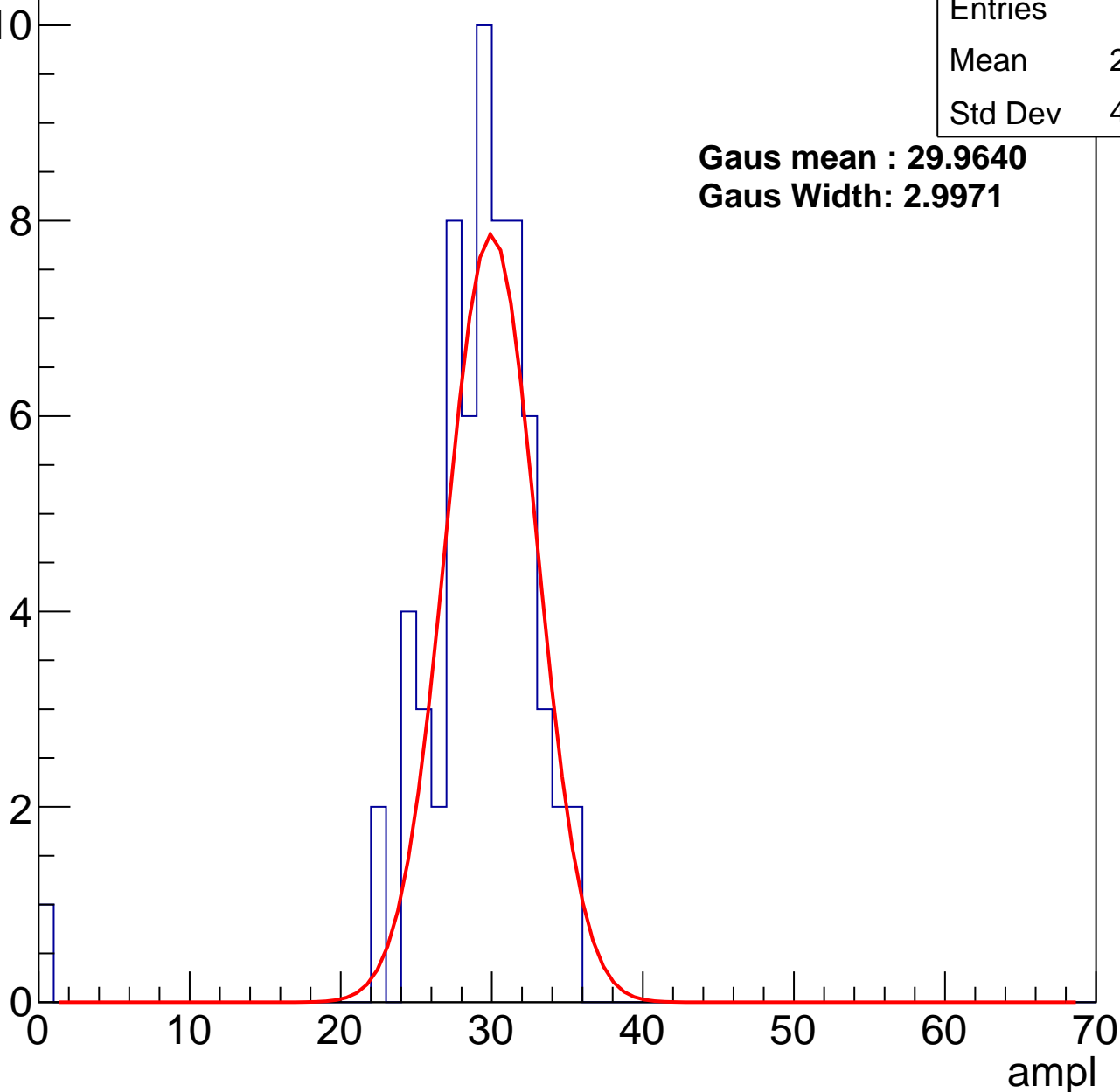
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.58
Std Dev	4.634

Gaus mean : 29.9640

Gaus Width: 2.9971



B1L102S, U19-ch88, adc1

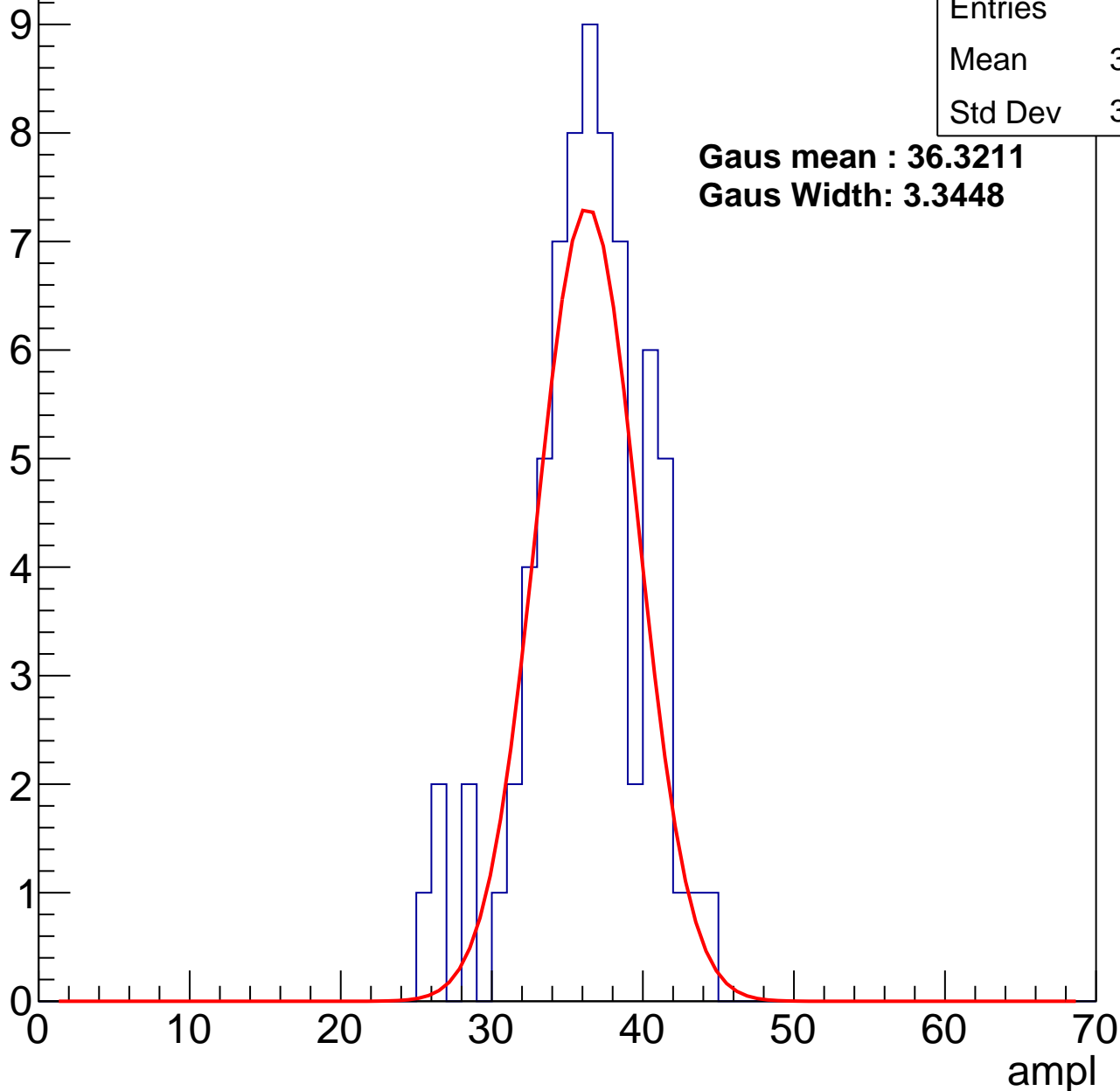
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.75
Std Dev	3.908

Gaus mean : 36.3211

Gaus Width: 3.3448



B1L102S, U19-ch88, adc2

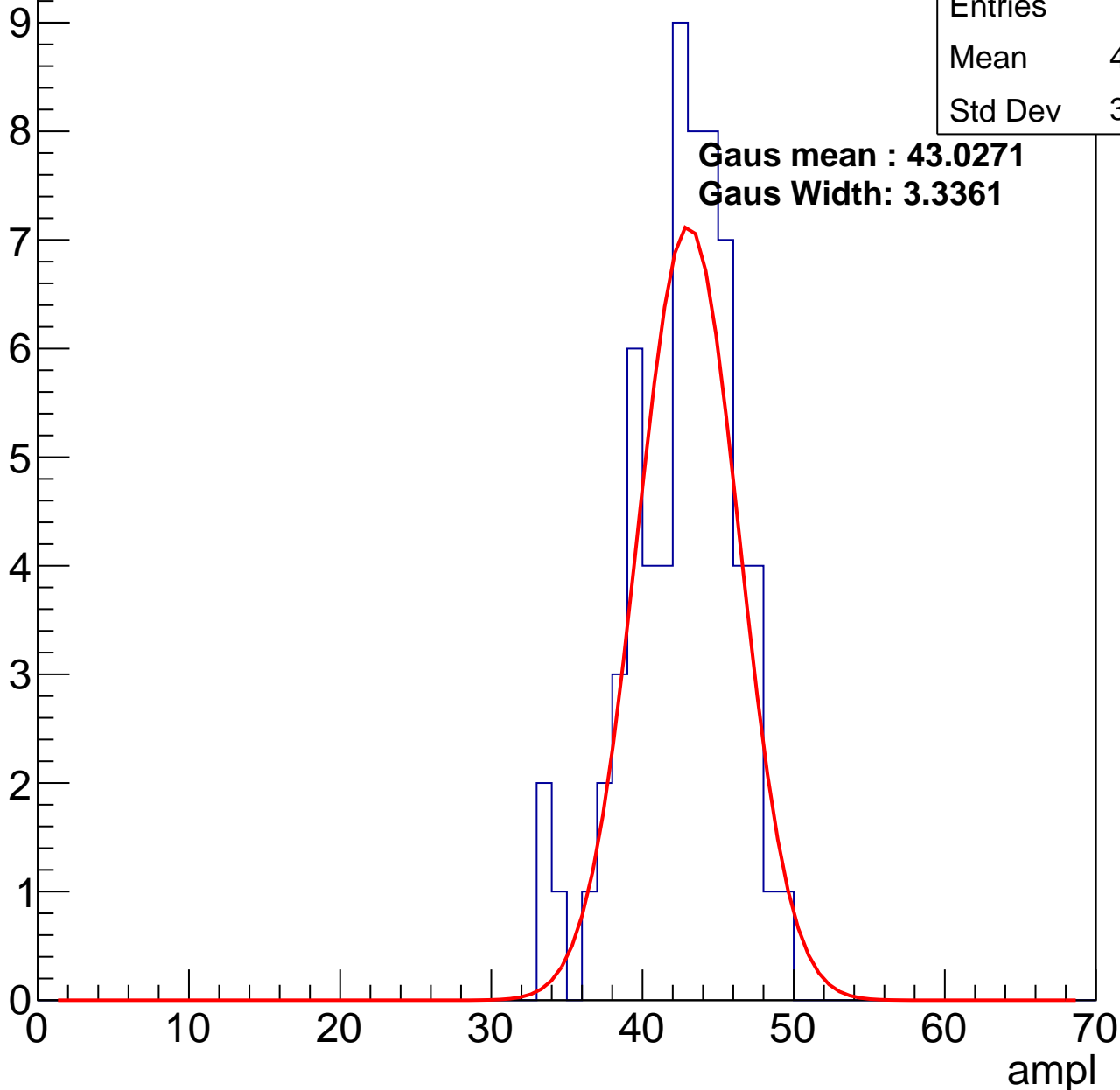
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.15
Std Dev	3.474

Gaus mean : 43.0271

Gaus Width: 3.3361

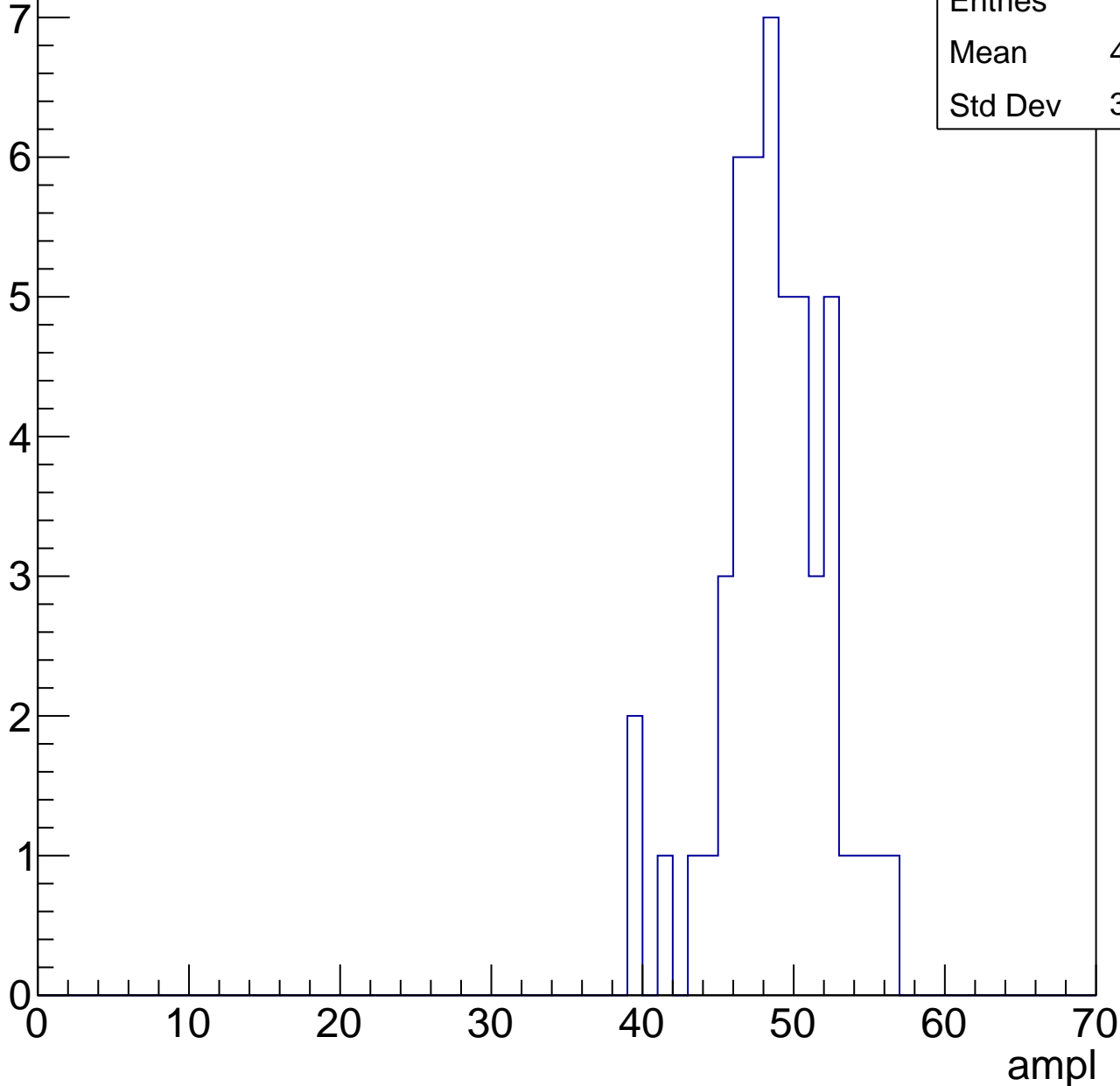


B1L102S, U19-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

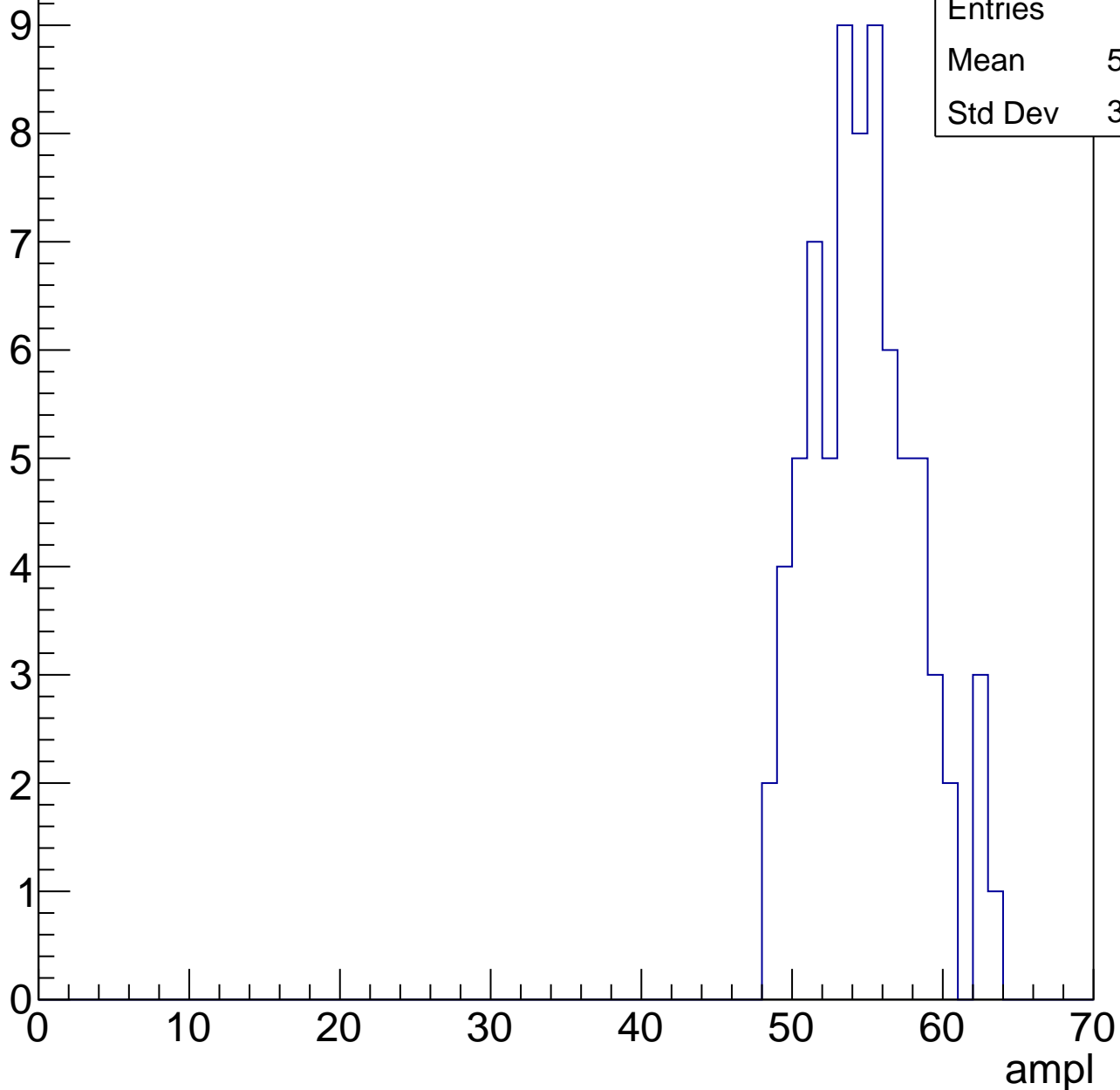
Entries	49
Mean	48.18
Std Dev	3.538



B1L102S, U19-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



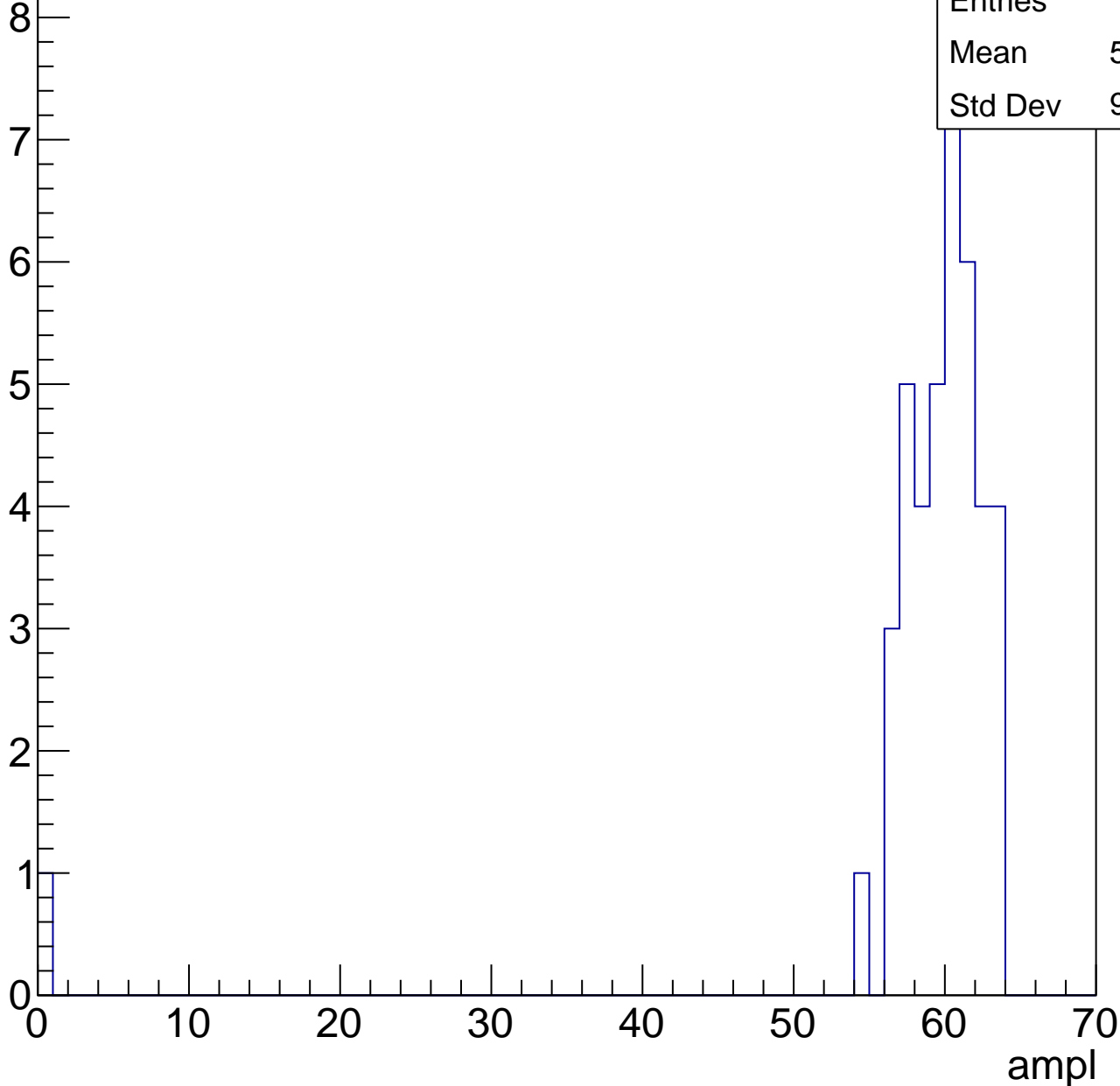
Entries	74
Mean	54.32
Std Dev	3.515

B1L102S, U19-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.05
Std Dev	9.438

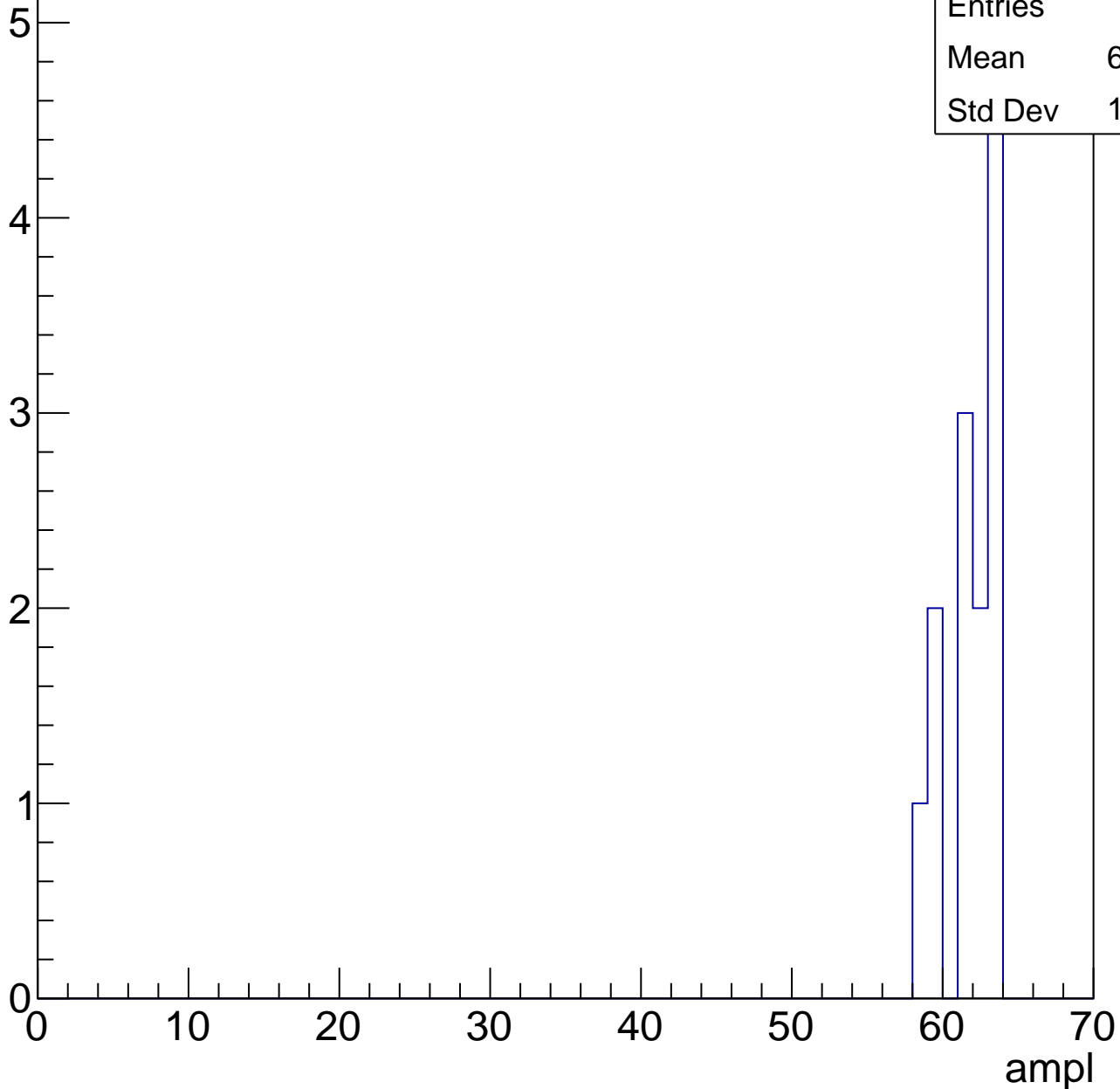


B1L102S, U19-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

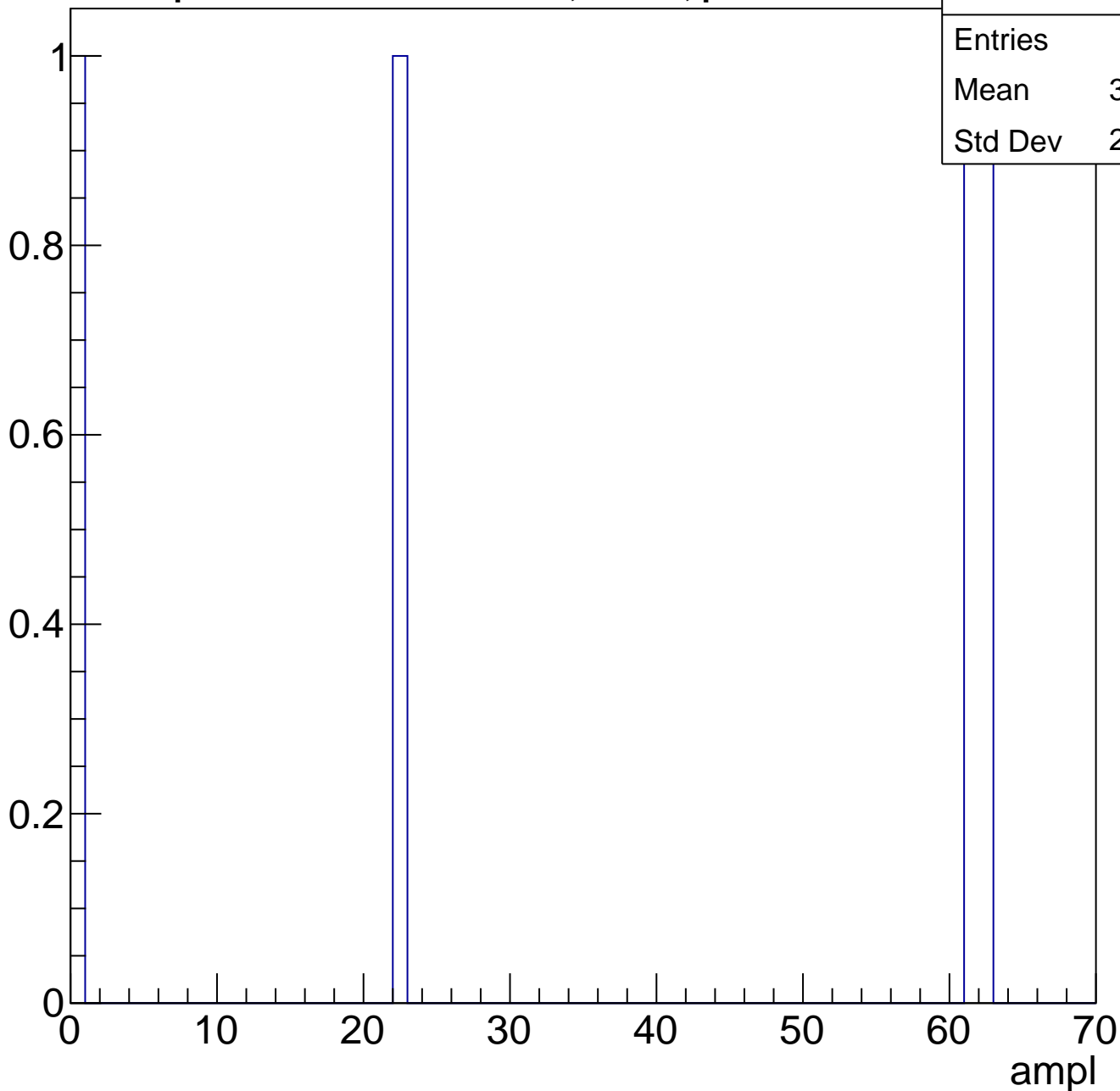
Entries	13
Mean	61.38
Std Dev	1.689



B1L102S, U19-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch89, adc0

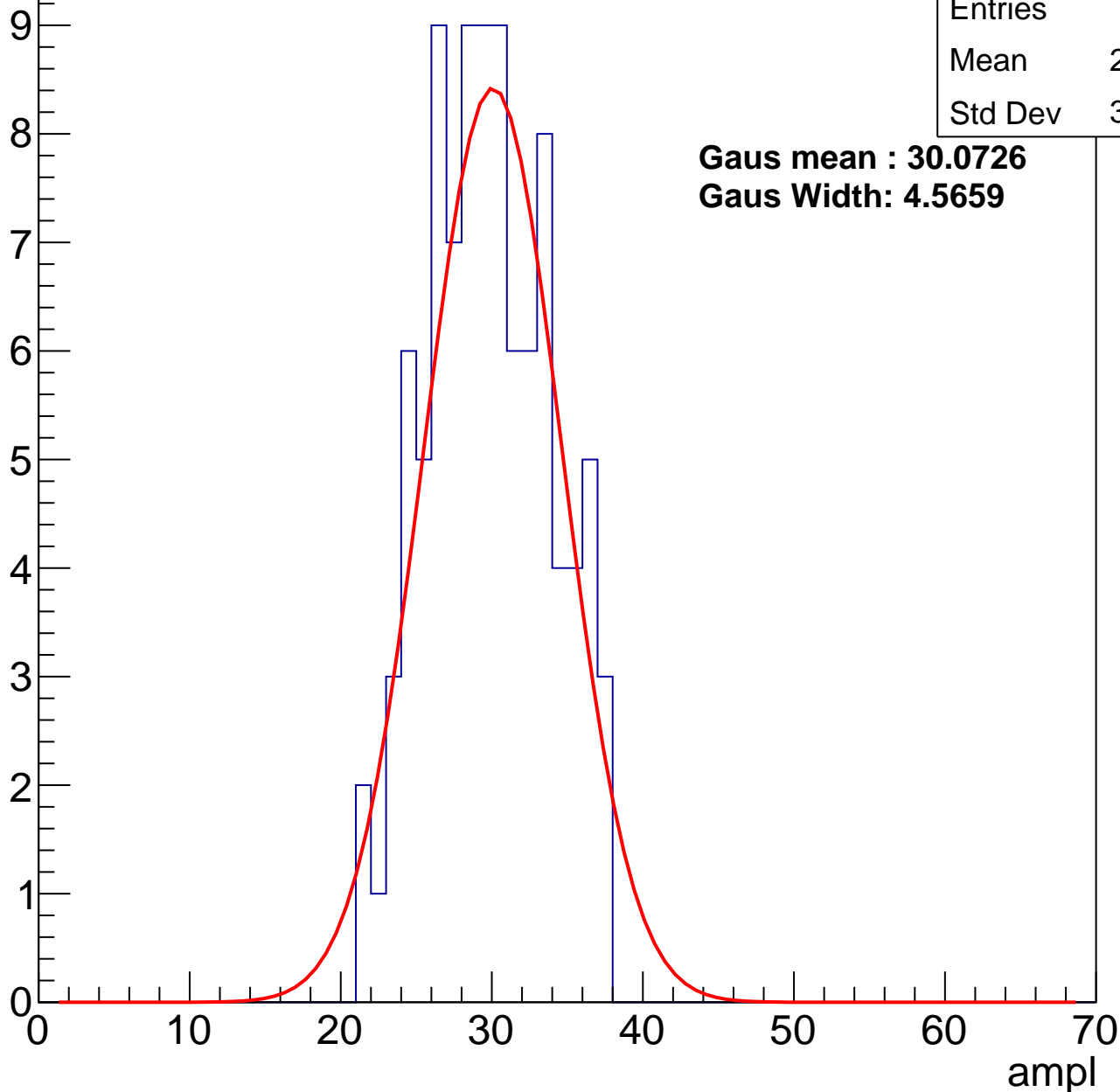
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	96
Mean	29.34
Std Dev	3.976

Gaus mean : 30.0726

Gaus Width: 4.5659



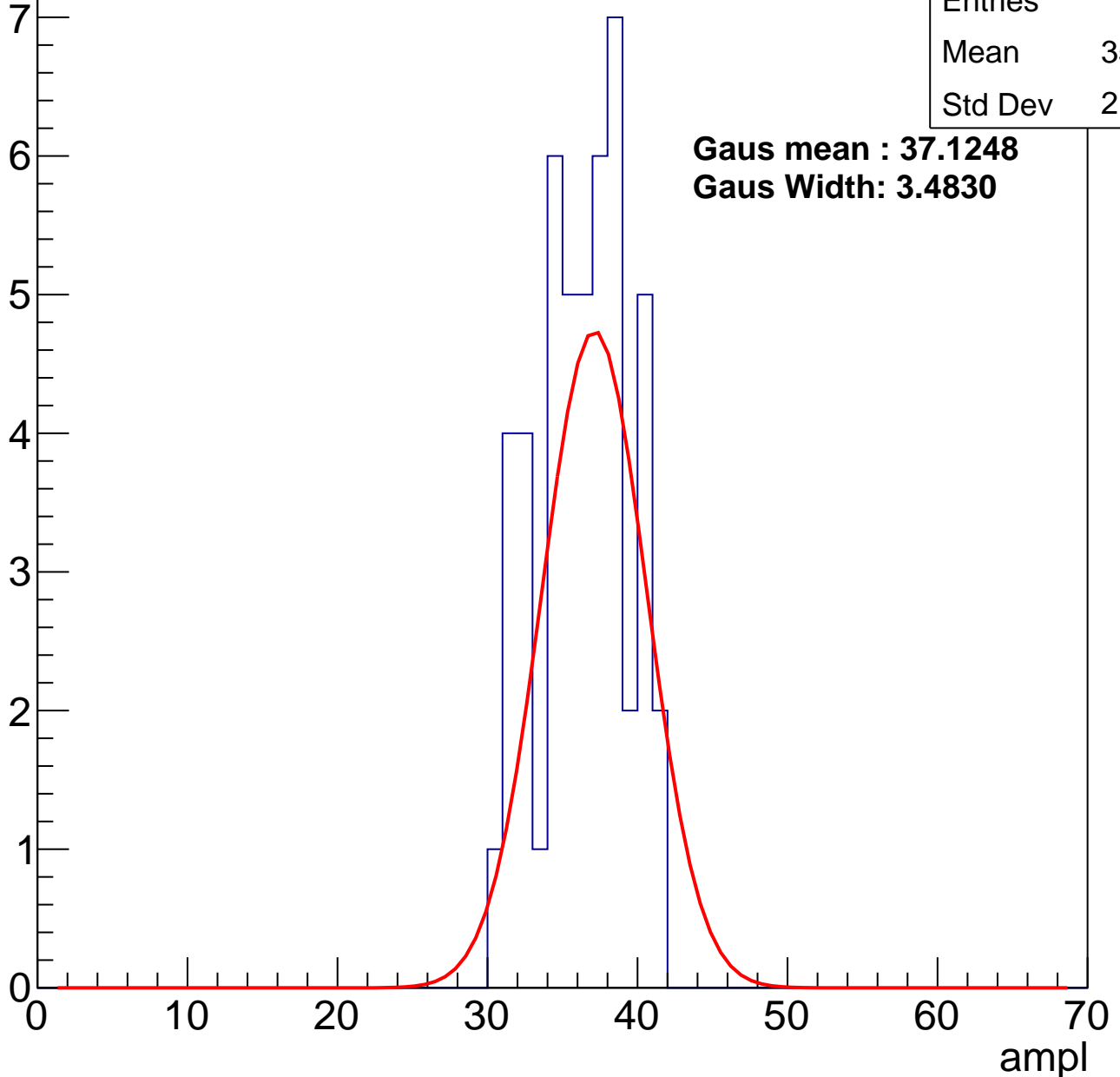
B1L102S, U19-ch89, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	35.88
Std Dev	2.955

Gaus mean : 37.1248
Gaus Width: 3.4830



B1L102S, U19-ch89, adc2

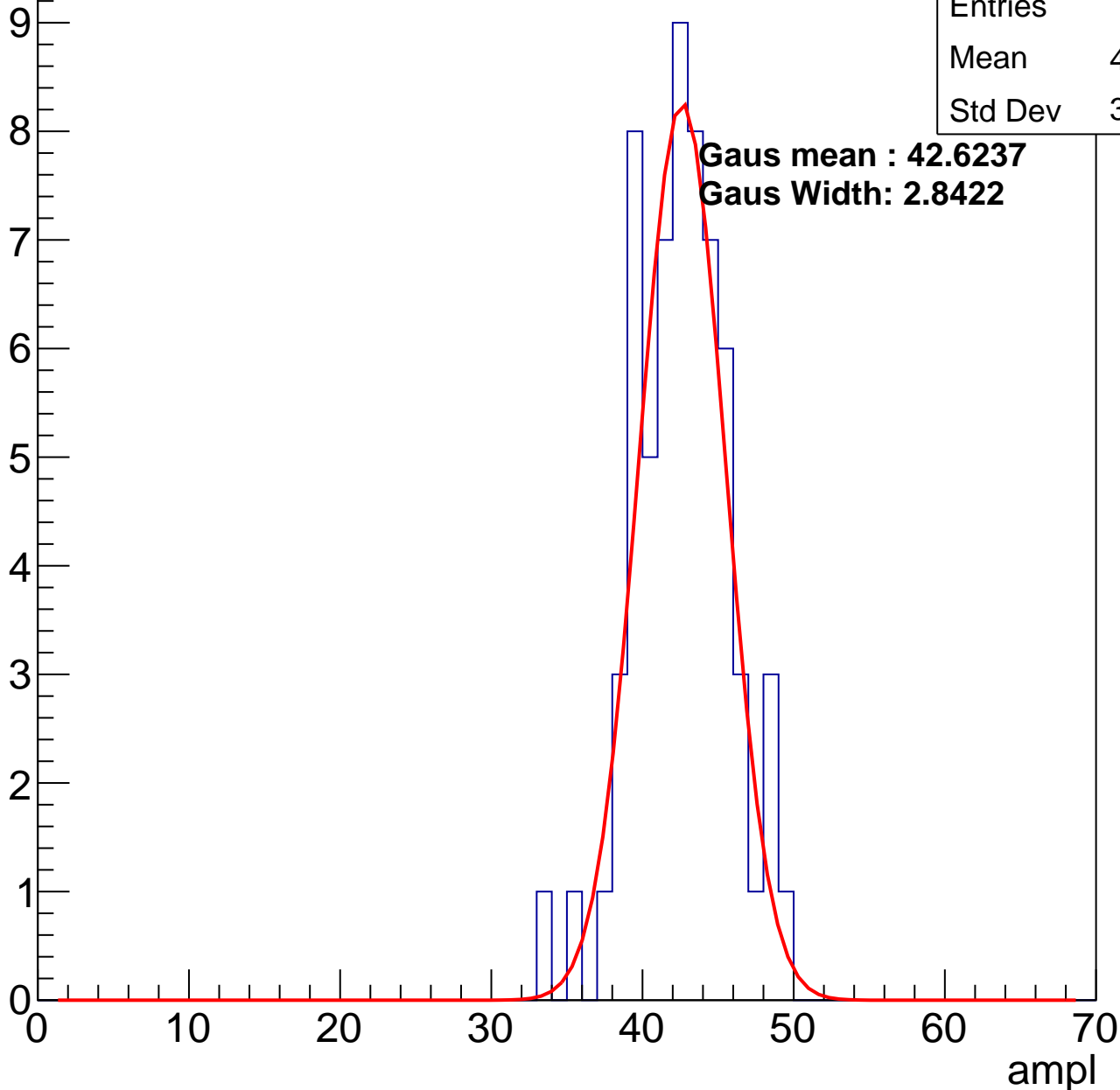
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.12
Std Dev	3.115

Gaus mean : 42.6237

Gaus Width: 2.8422



B1L102S, U19-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	49.51
Std Dev	3.461

Entry

10

8

6

4

2

0

0

10

20

30

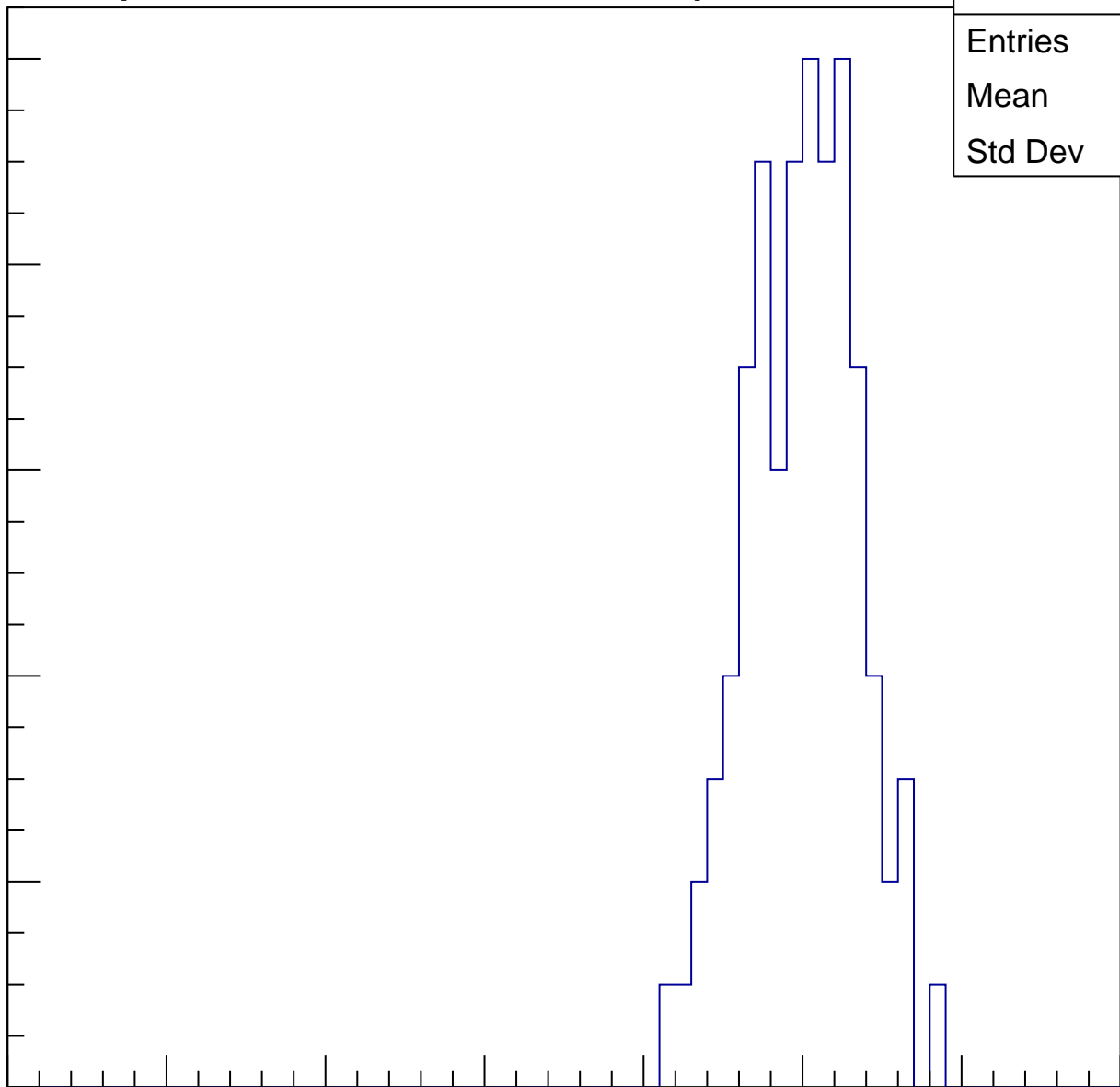
40

50

60

ampl

70

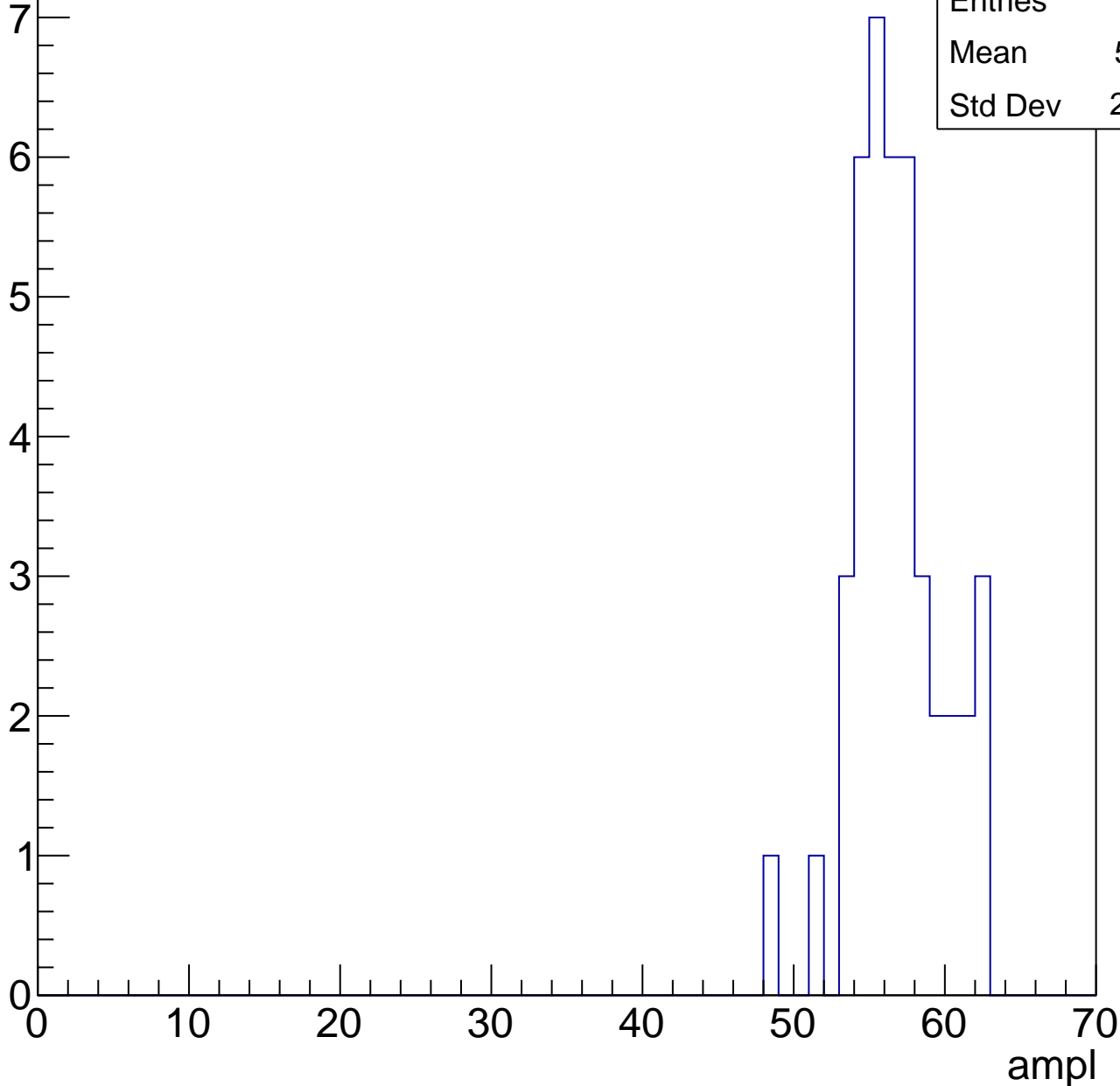


B1L102S, U19-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	56.31
Std Dev	2.964



B1L102S, U19-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

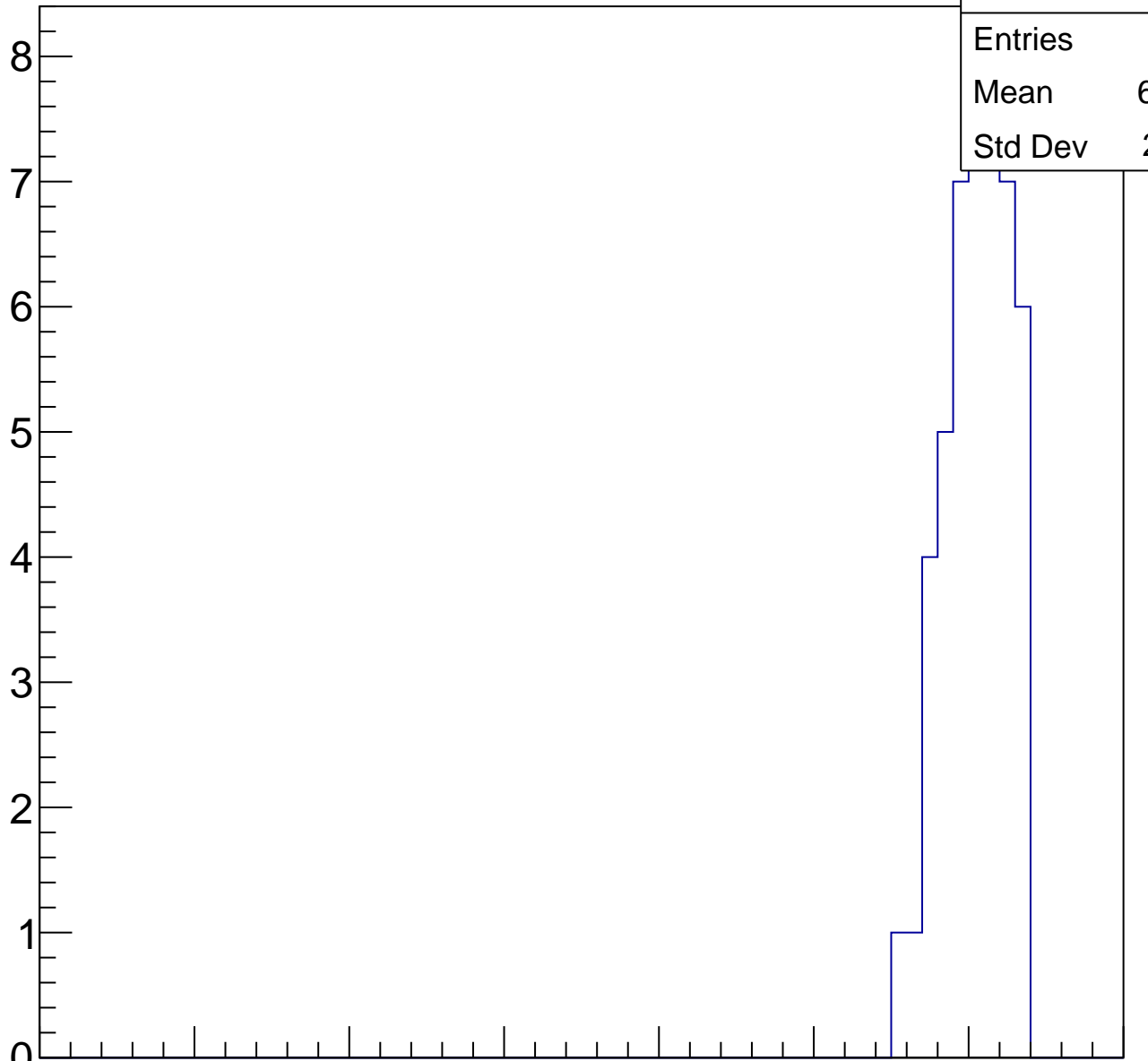
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.04
Std Dev	2.031

ampl

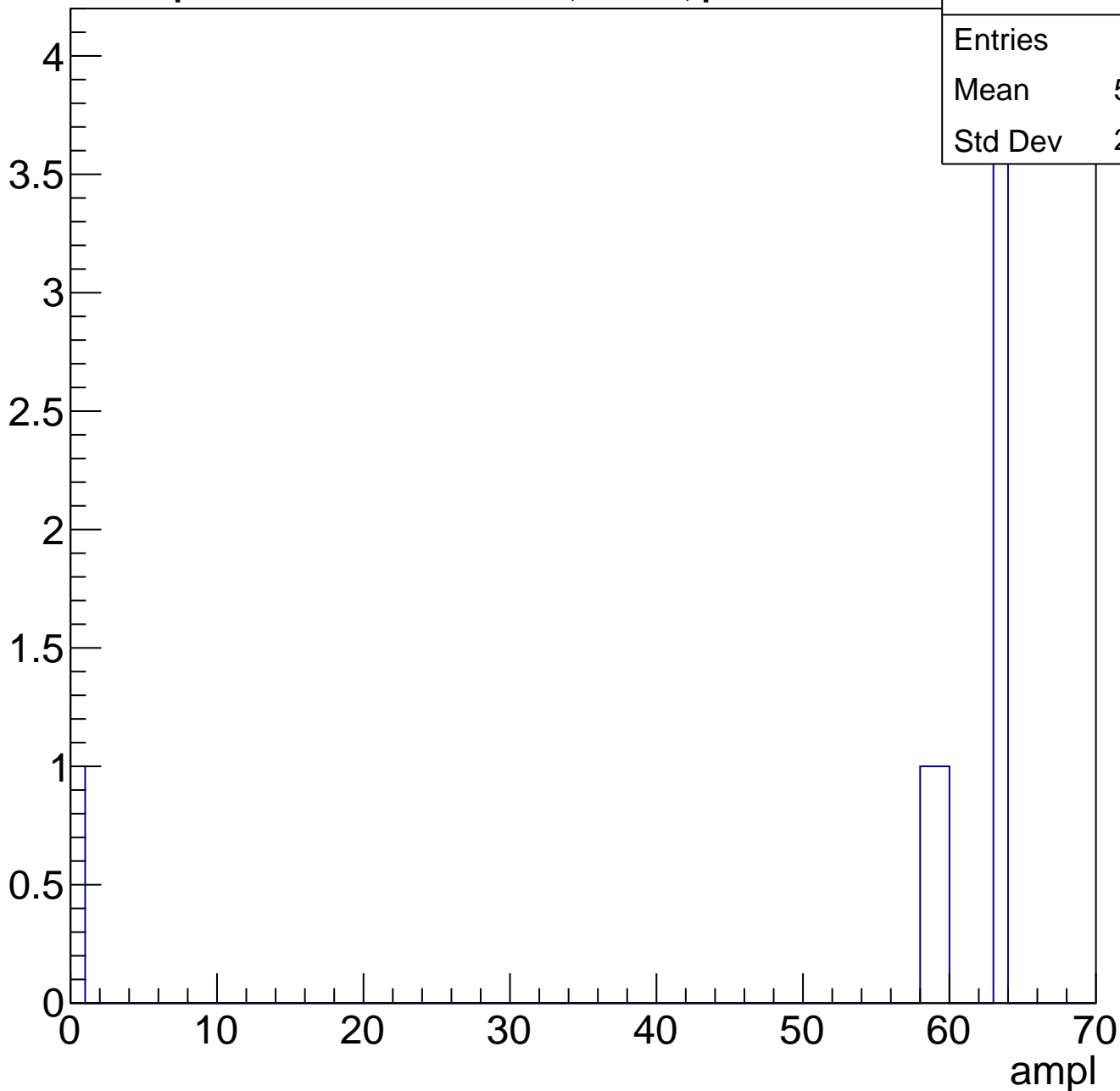
0 10 20 30 40 50 60 70



B1L102S, U19-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch90, adc0

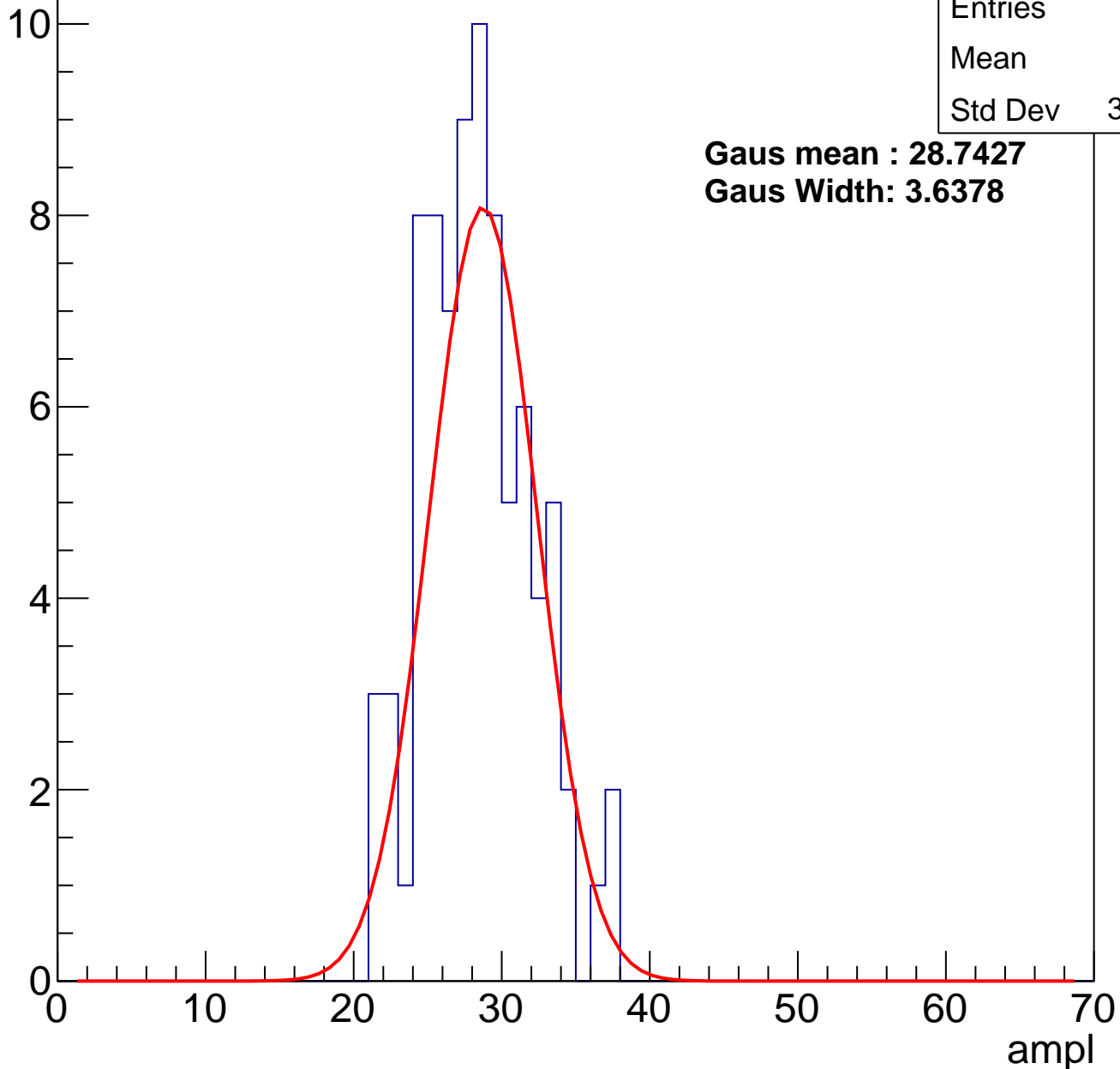
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	27.9
Std Dev	3.638

Gaus mean : 28.7427

Gaus Width: 3.6378

Entry



B1L102S, U19-ch90, adc1

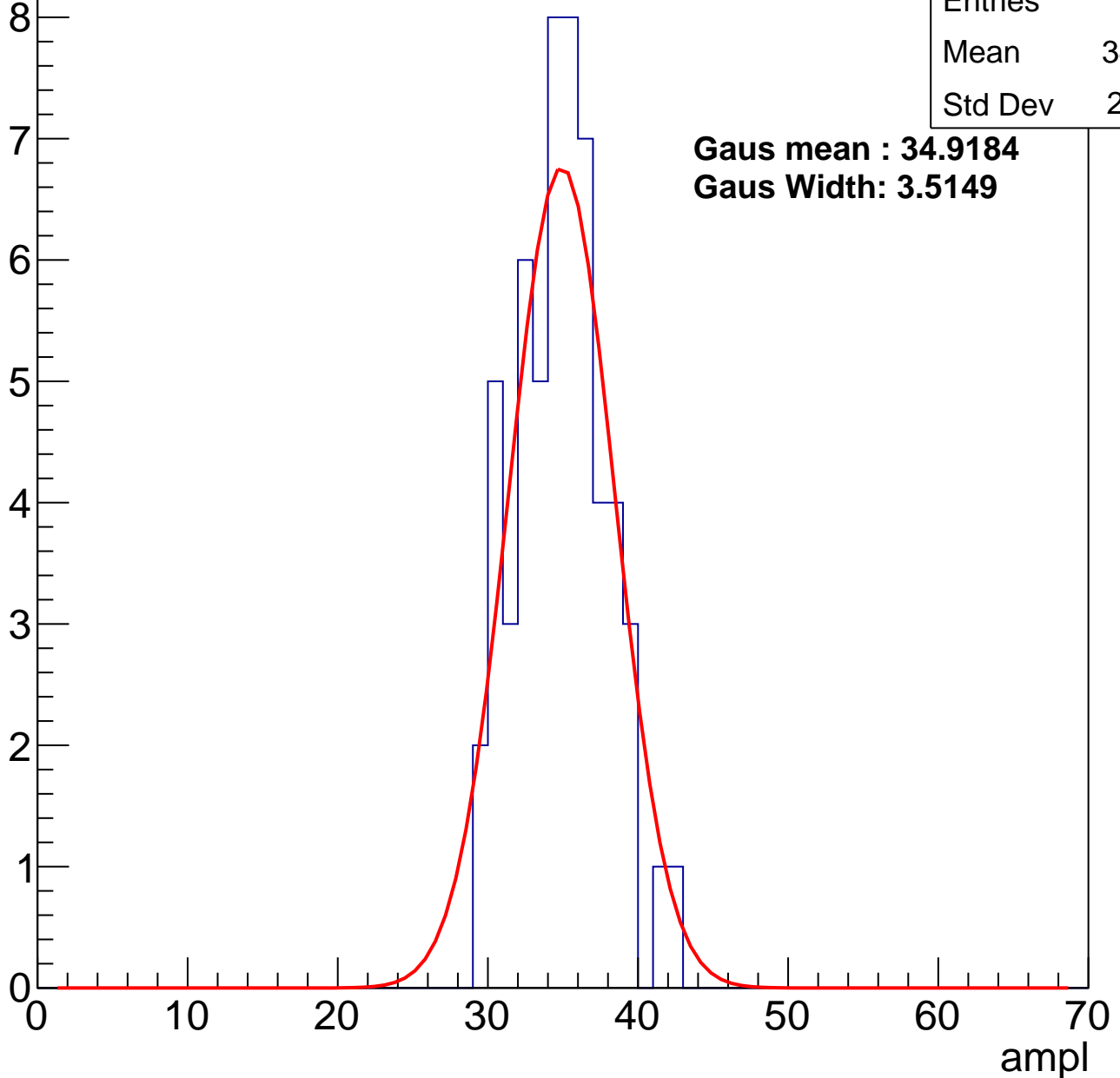
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.42
Std Dev	2.961

Gaus mean : 34.9184

Gaus Width: 3.5149



B1L102S, U19-ch90, adc2

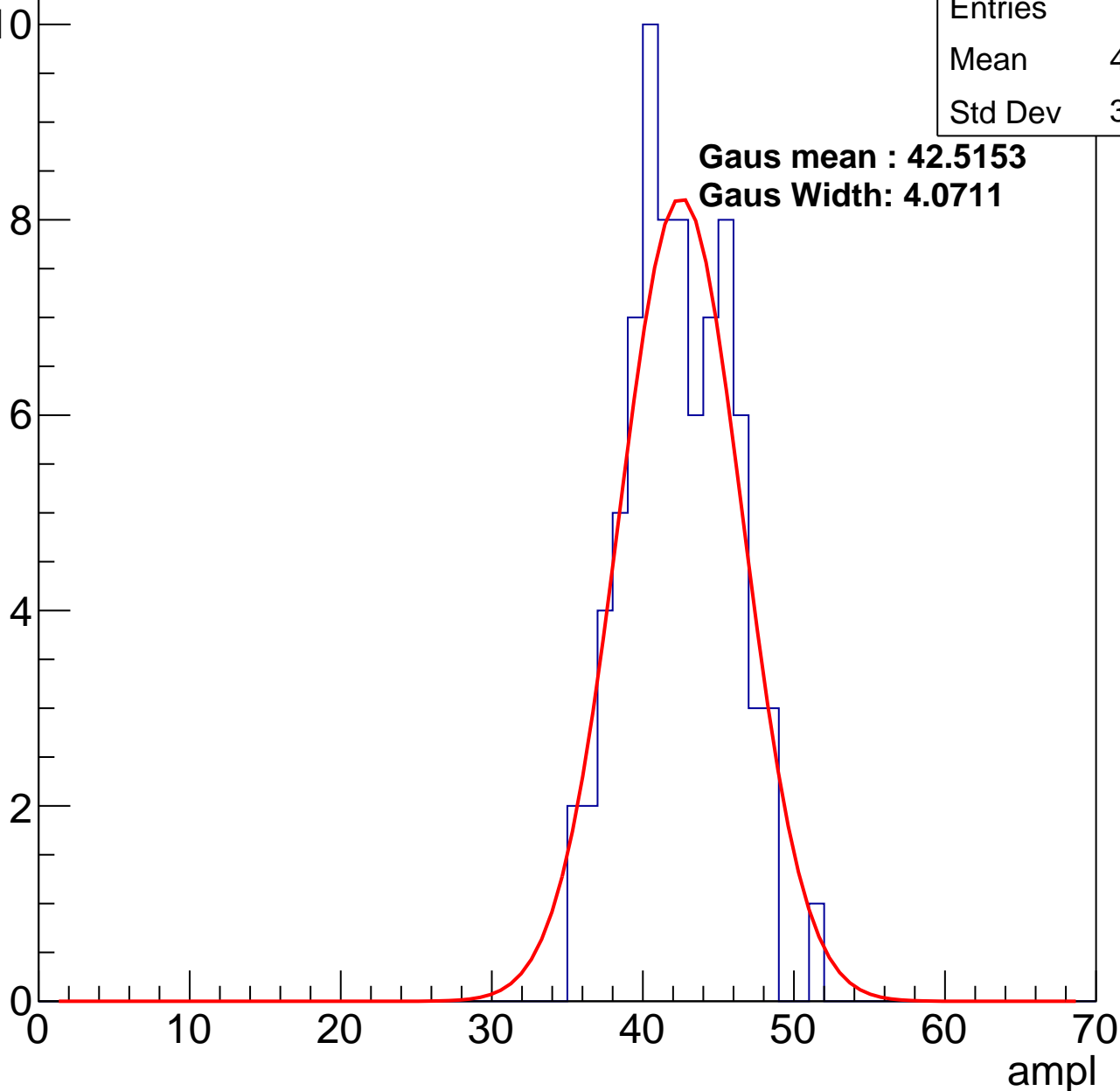
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.94
Std Dev	3.403

Gaus mean : 42.5153

Gaus Width: 4.0711

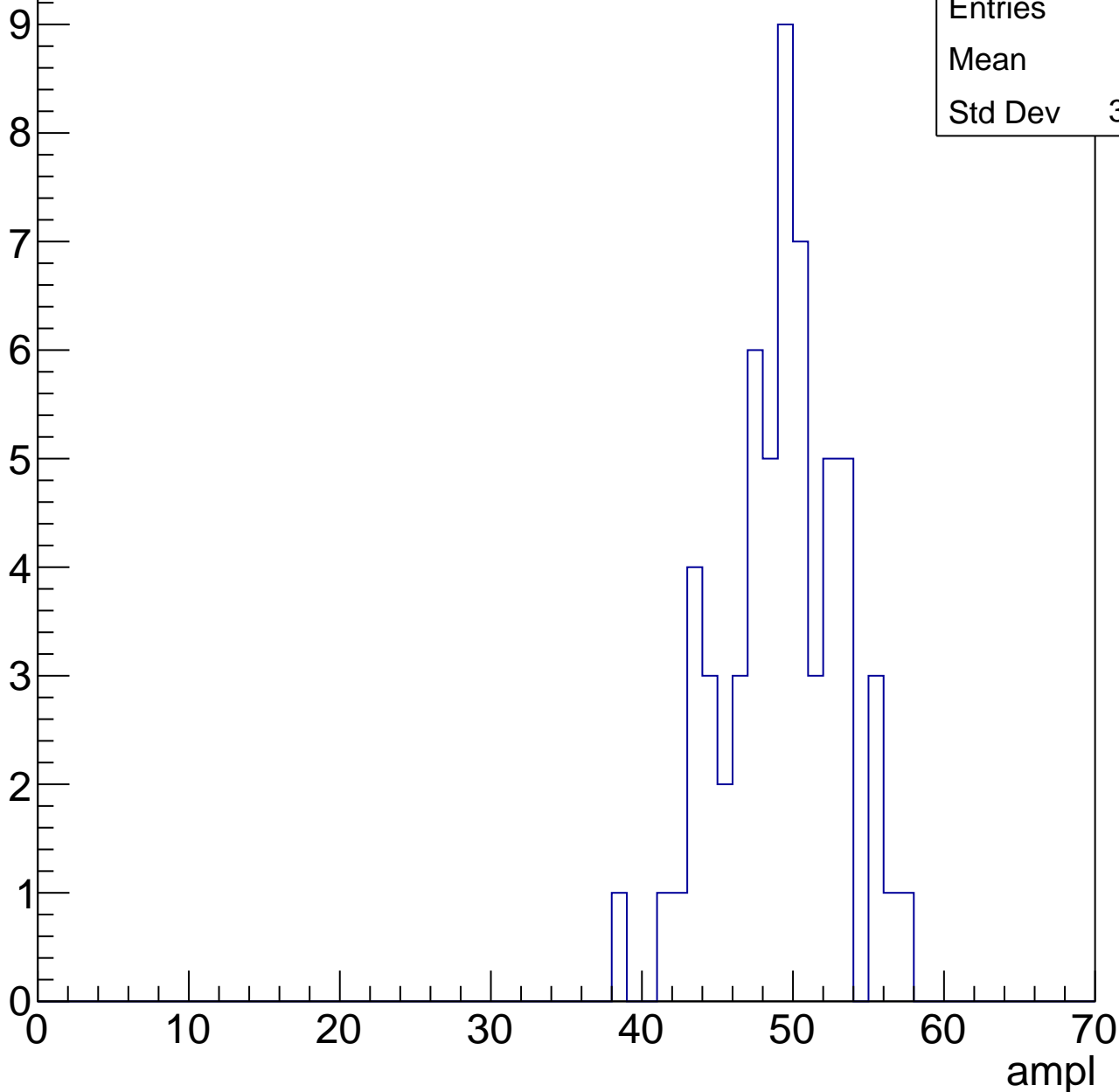


B1L102S, U19-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.7
Std Dev	3.874

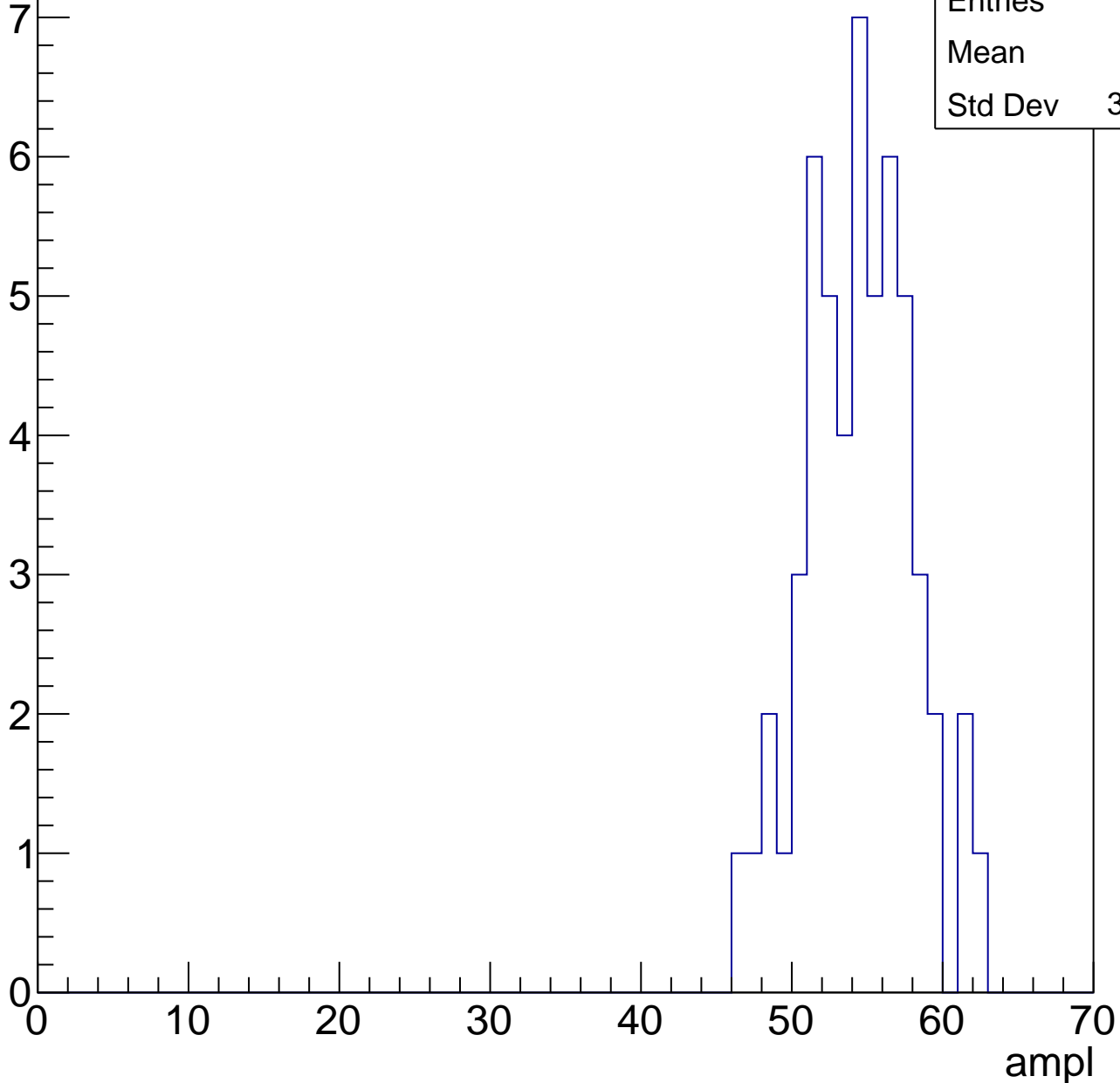


B1L102S, U19-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54
Std Dev	3.522



B1L102S, U19-ch90, adc5

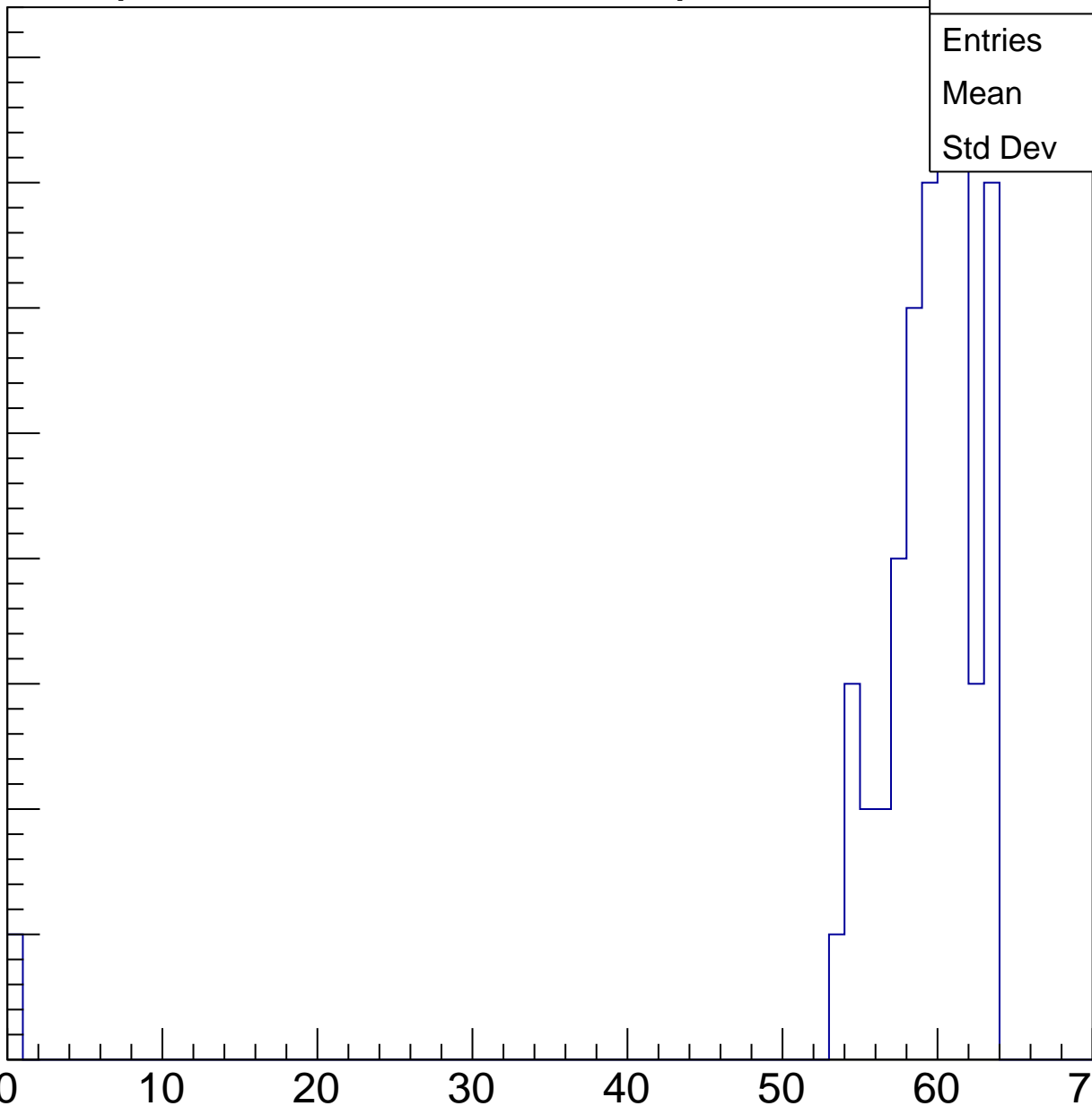
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.1
Std Dev	8.549

ampl

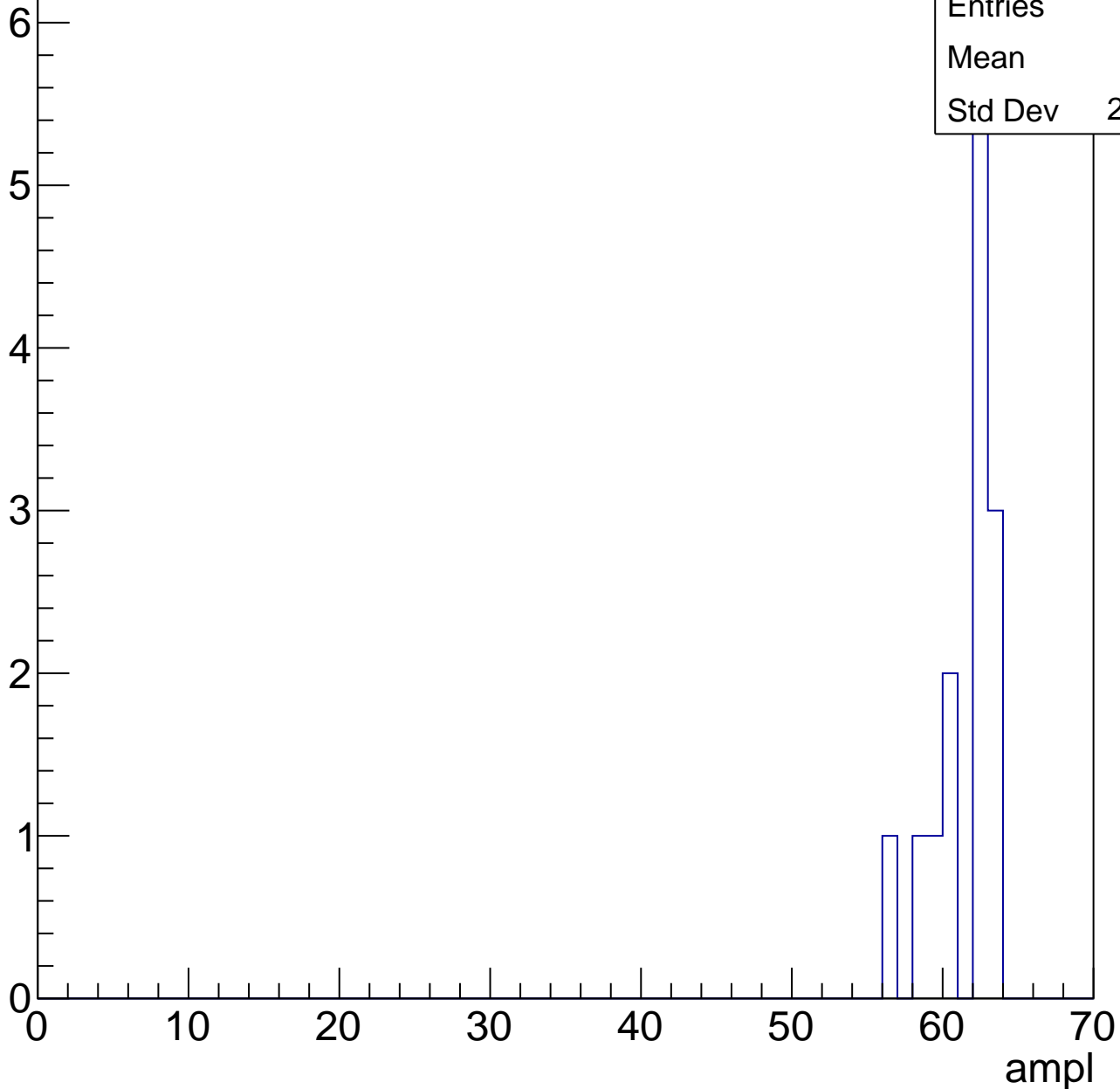


B1L102S, U19-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61
Std Dev	2.035



B1L102S, U19-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch91, adc0

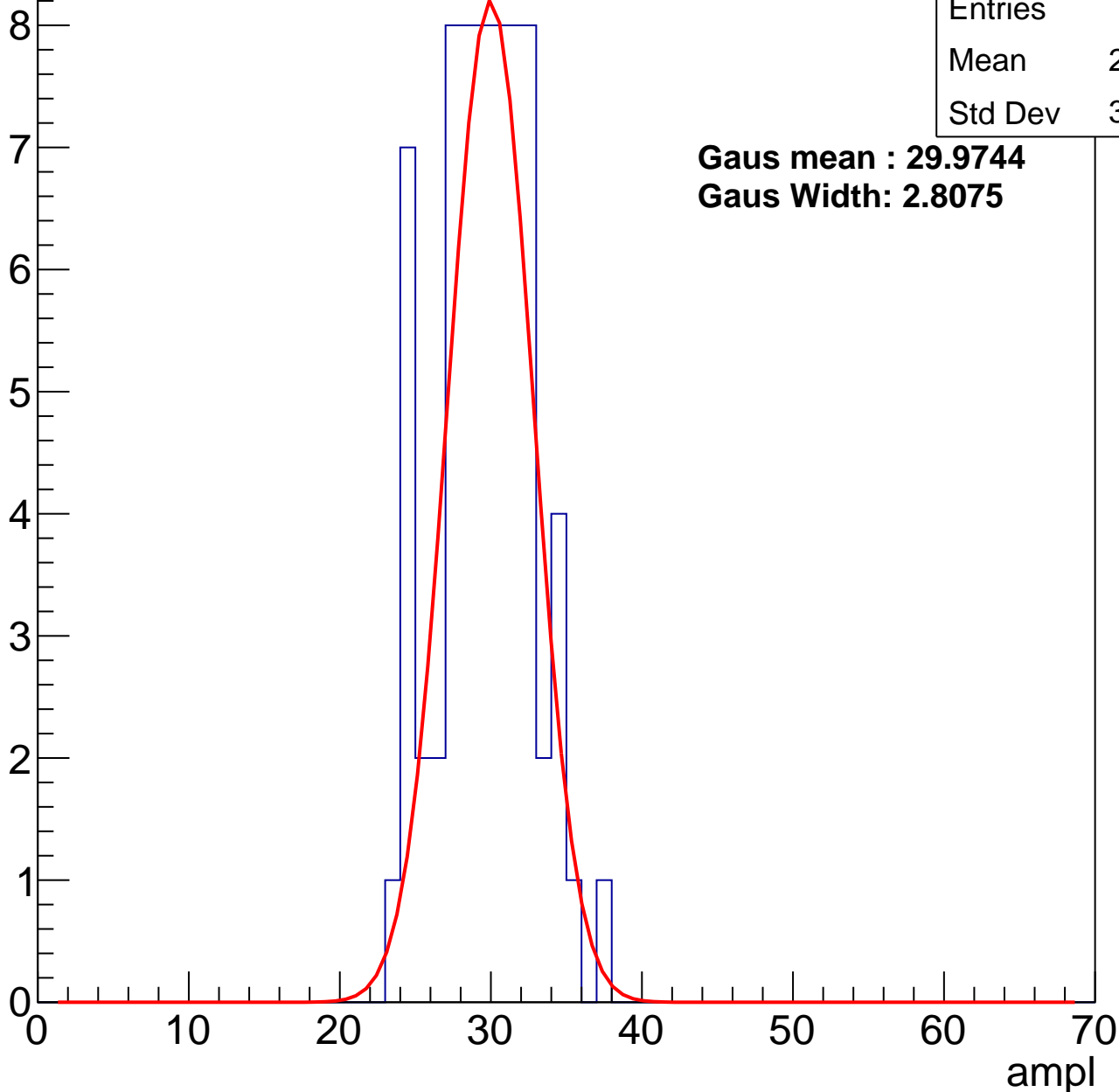
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.16
Std Dev	3.076

Gaus mean : 29.9744

Gaus Width: 2.8075



B1L102S, U19-ch91, adc1

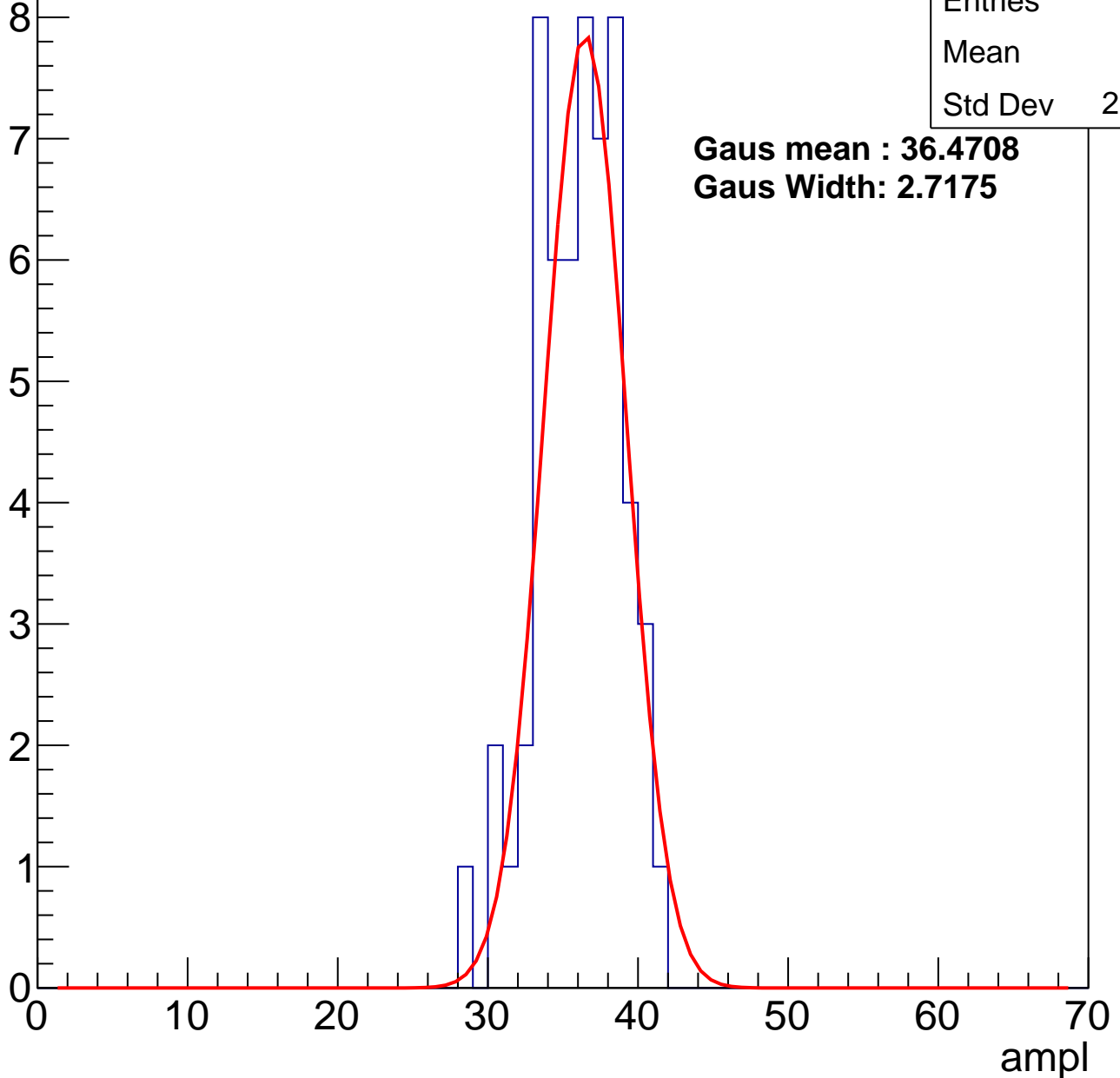
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.6
Std Dev	2.758

Gaus mean : 36.4708

Gaus Width: 2.7175



B1L102S, U19-ch91, adc2

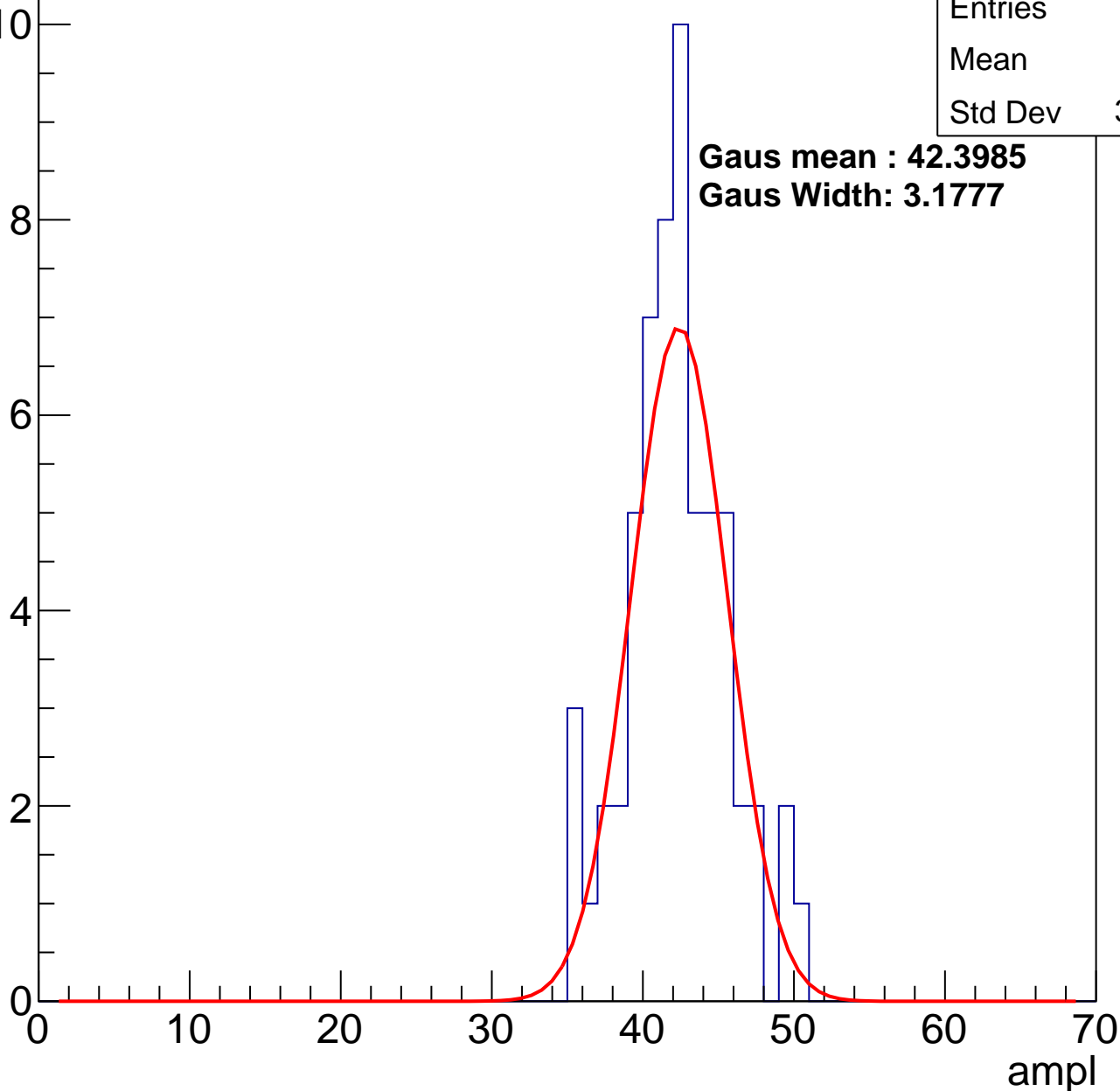
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.8
Std Dev	3.311

Gaus mean : 42.3985

Gaus Width: 3.1777



B1L102S, U19-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	47.74
Std Dev	3.625

Entry

10

8

6

4

2

0

0

10

20

30

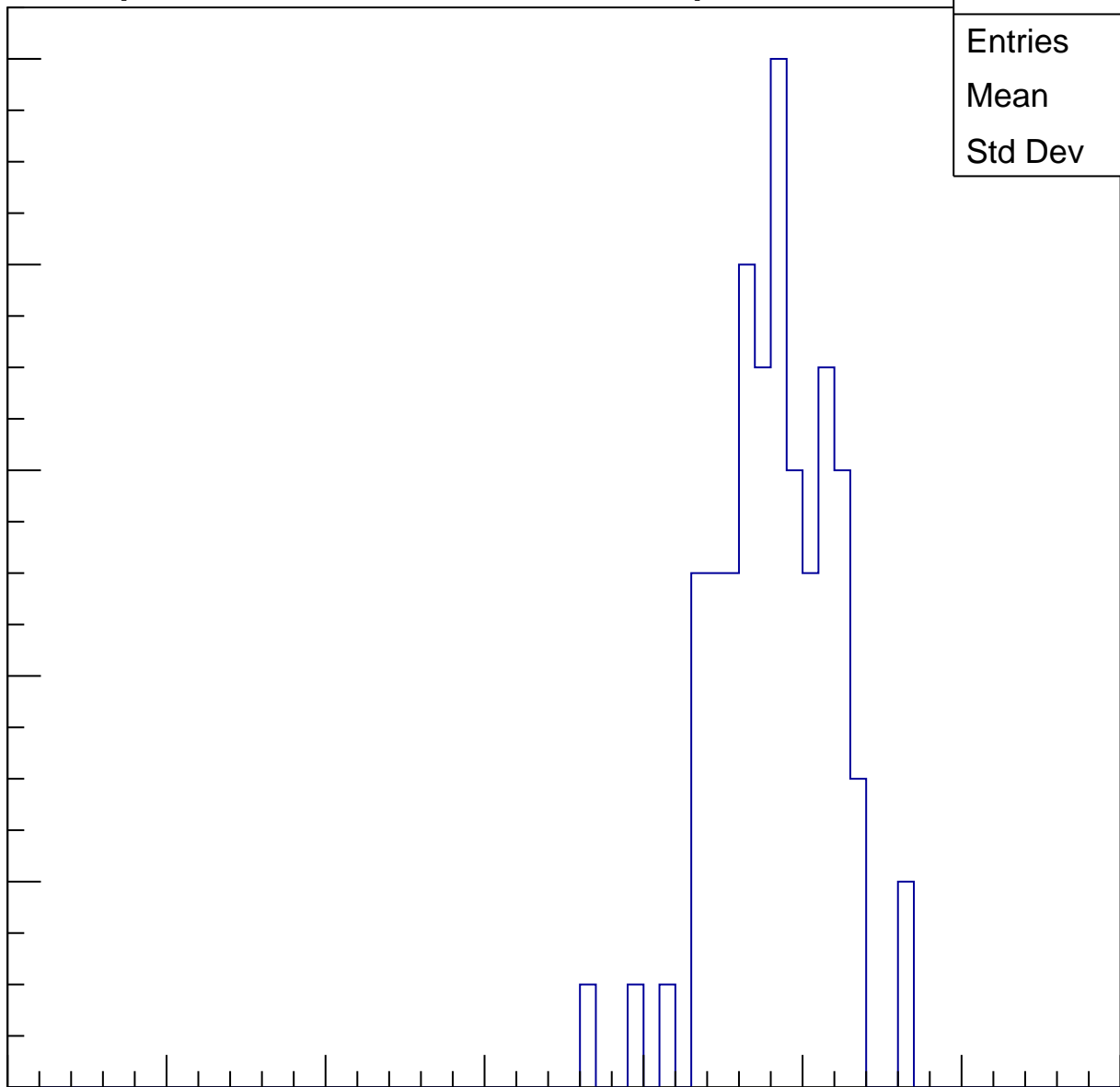
40

50

60

ampl

70

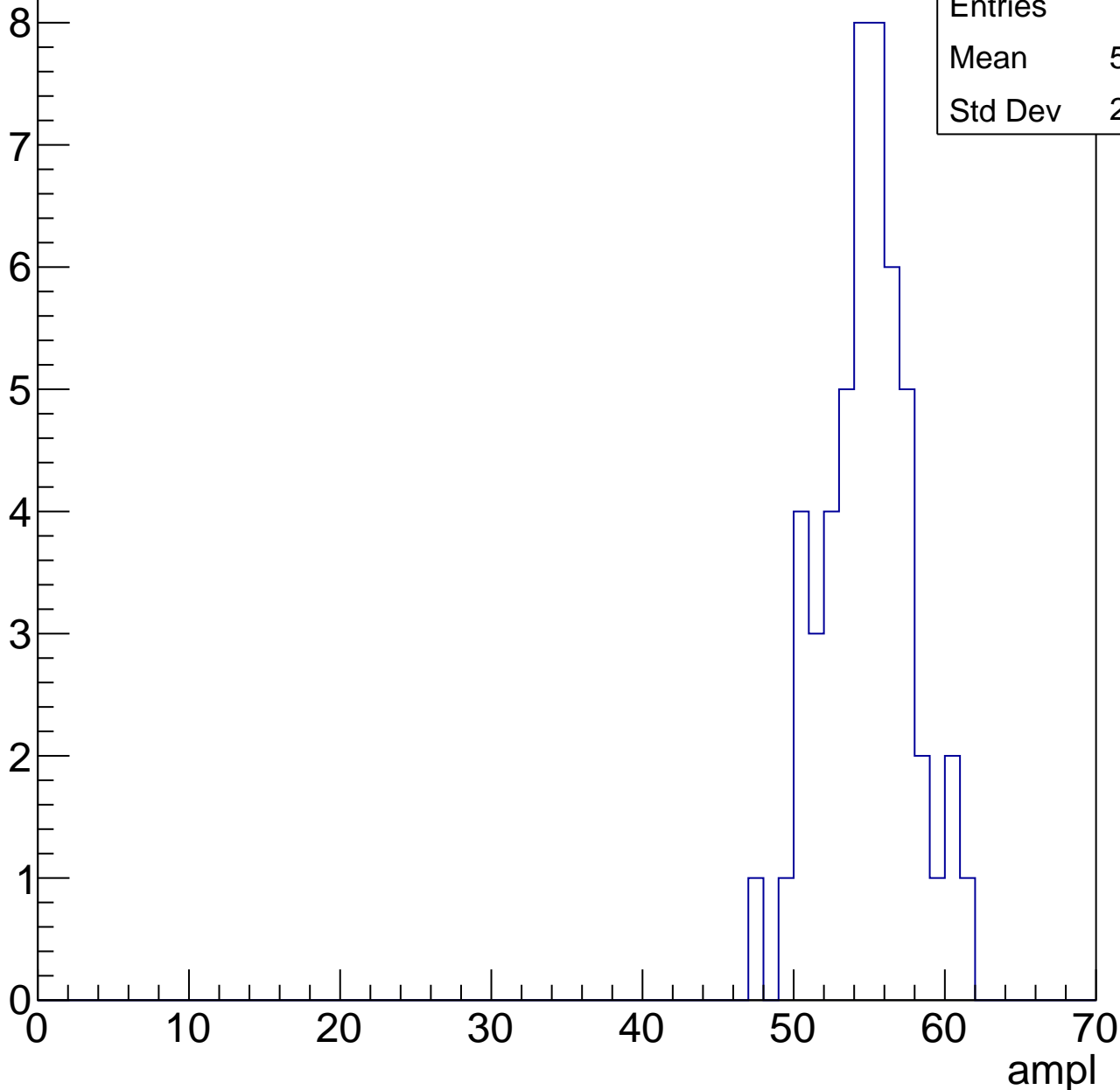


B1L102S, U19-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.33
Std Dev	2.922



B1L102S, U19-ch91, adc5

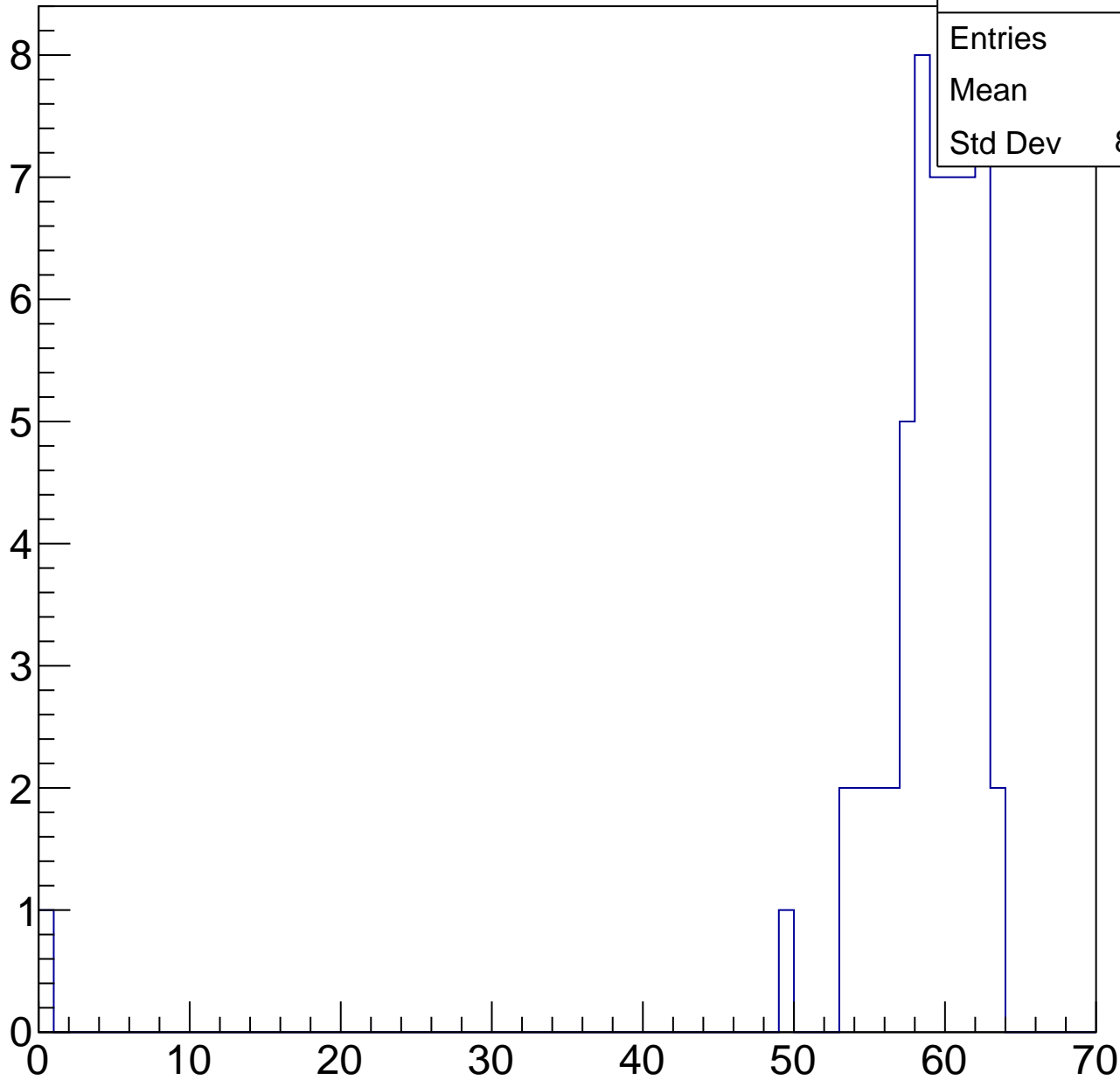
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	57.7
Std Dev	8.421

ampl

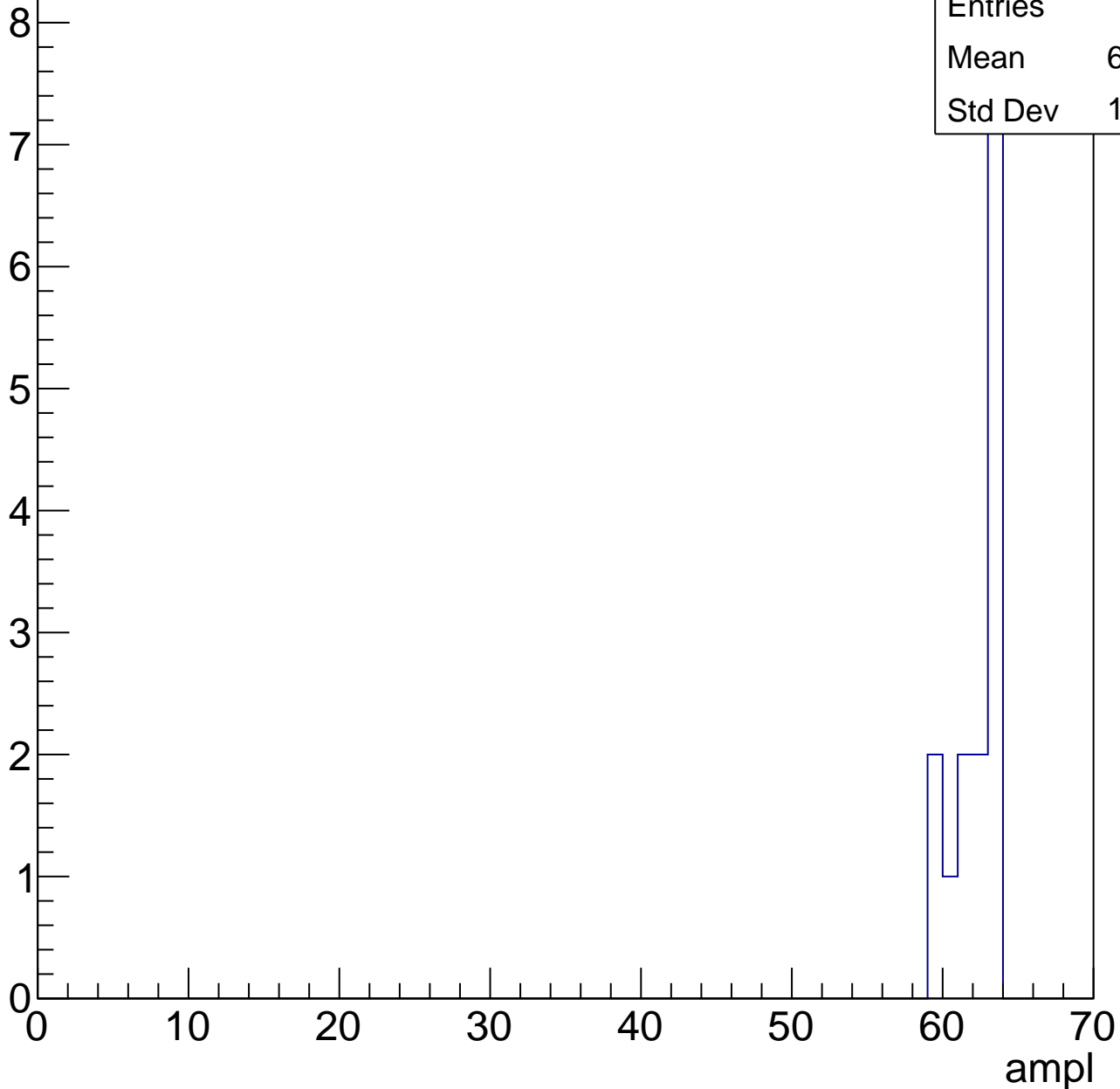


B1L102S, U19-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.87
Std Dev	1.454



B1L102S, U19-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch92, adc0

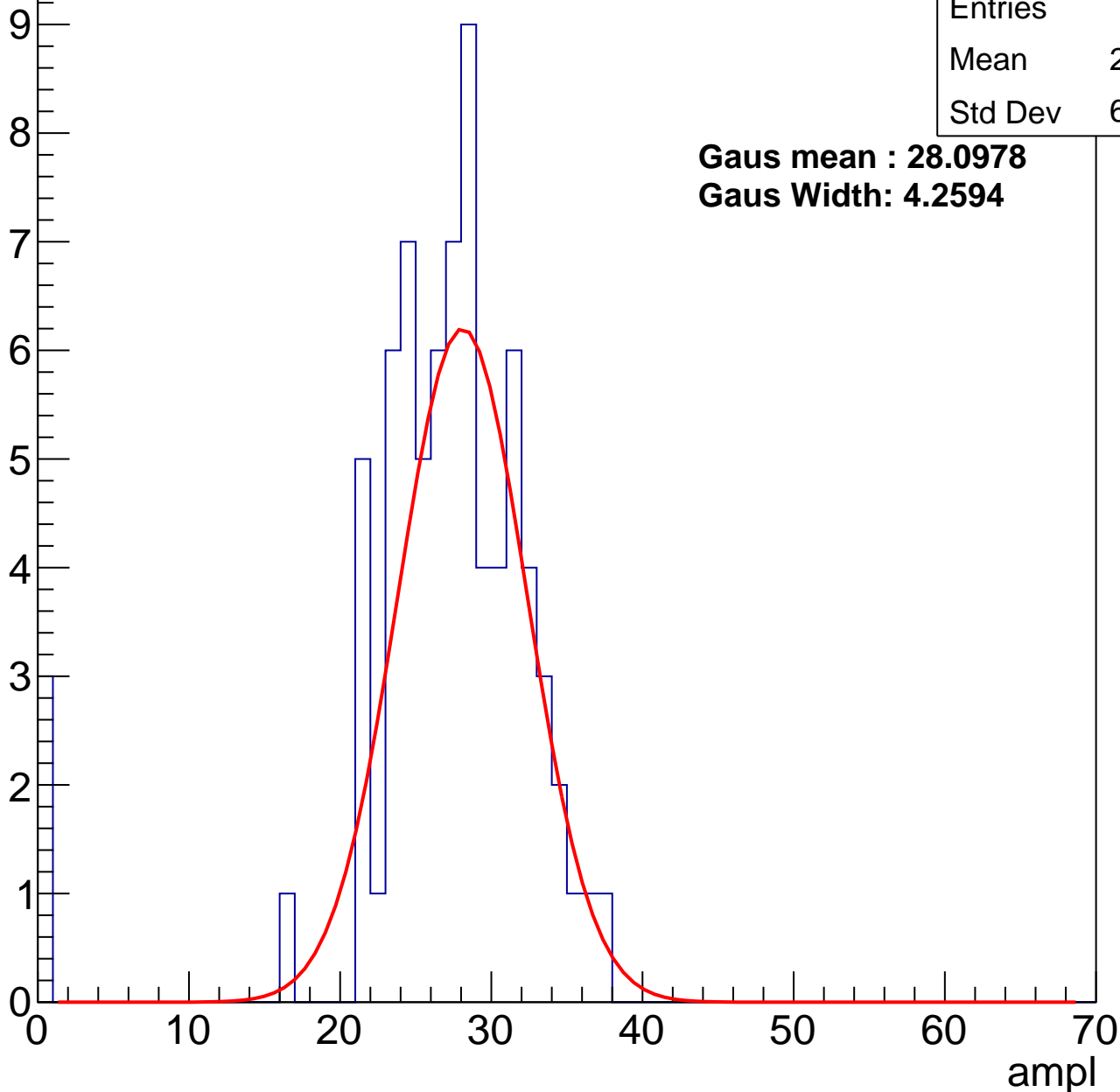
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	26.26
Std Dev	6.654

Gaus mean : 28.0978

Gaus Width: 4.2594



B1L102S, U19-ch92, adc1

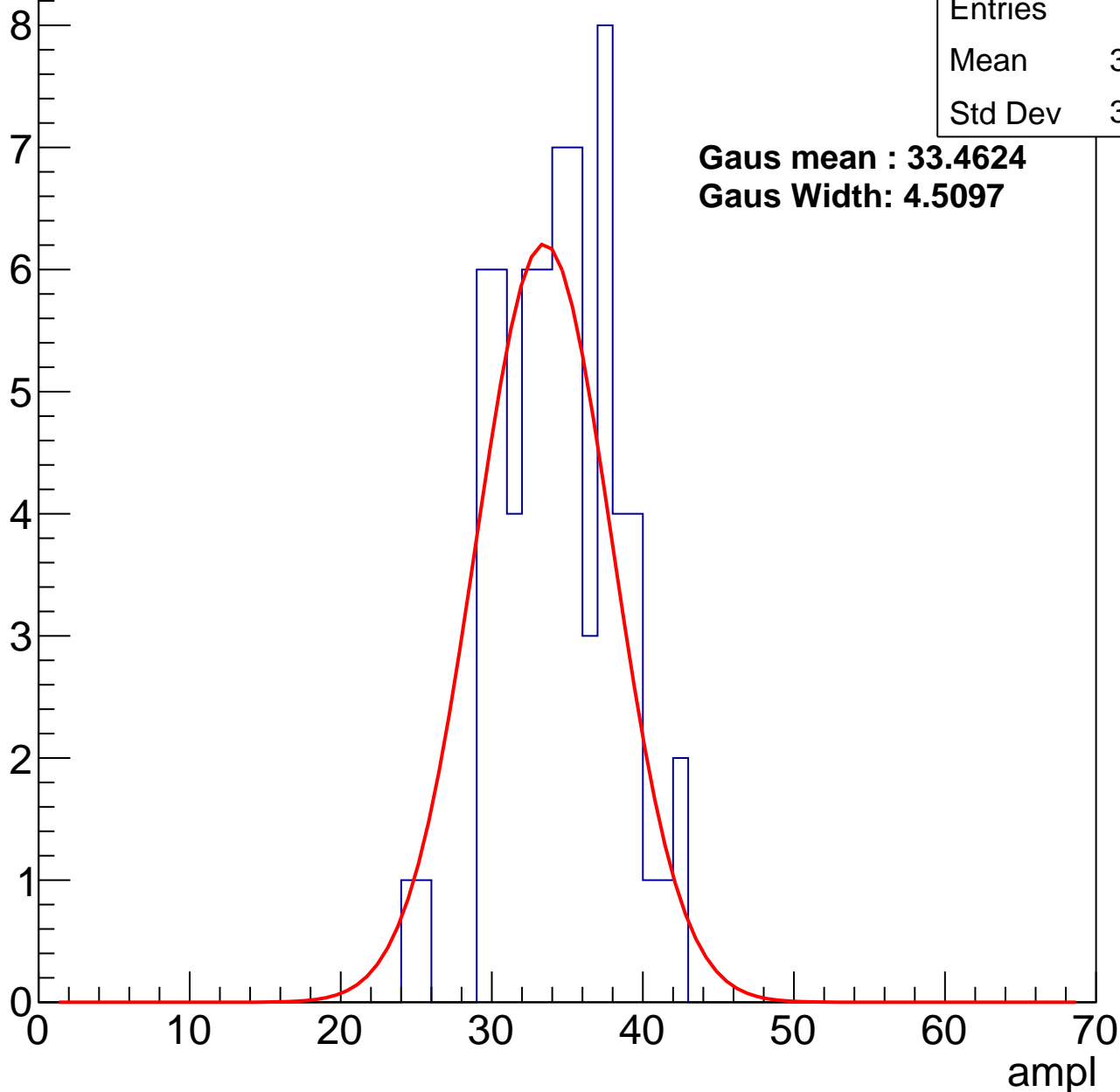
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.99
Std Dev	3.787

Gaus mean : 33.4624

Gaus Width: 4.5097



B1L102S, U19-ch92, adc2

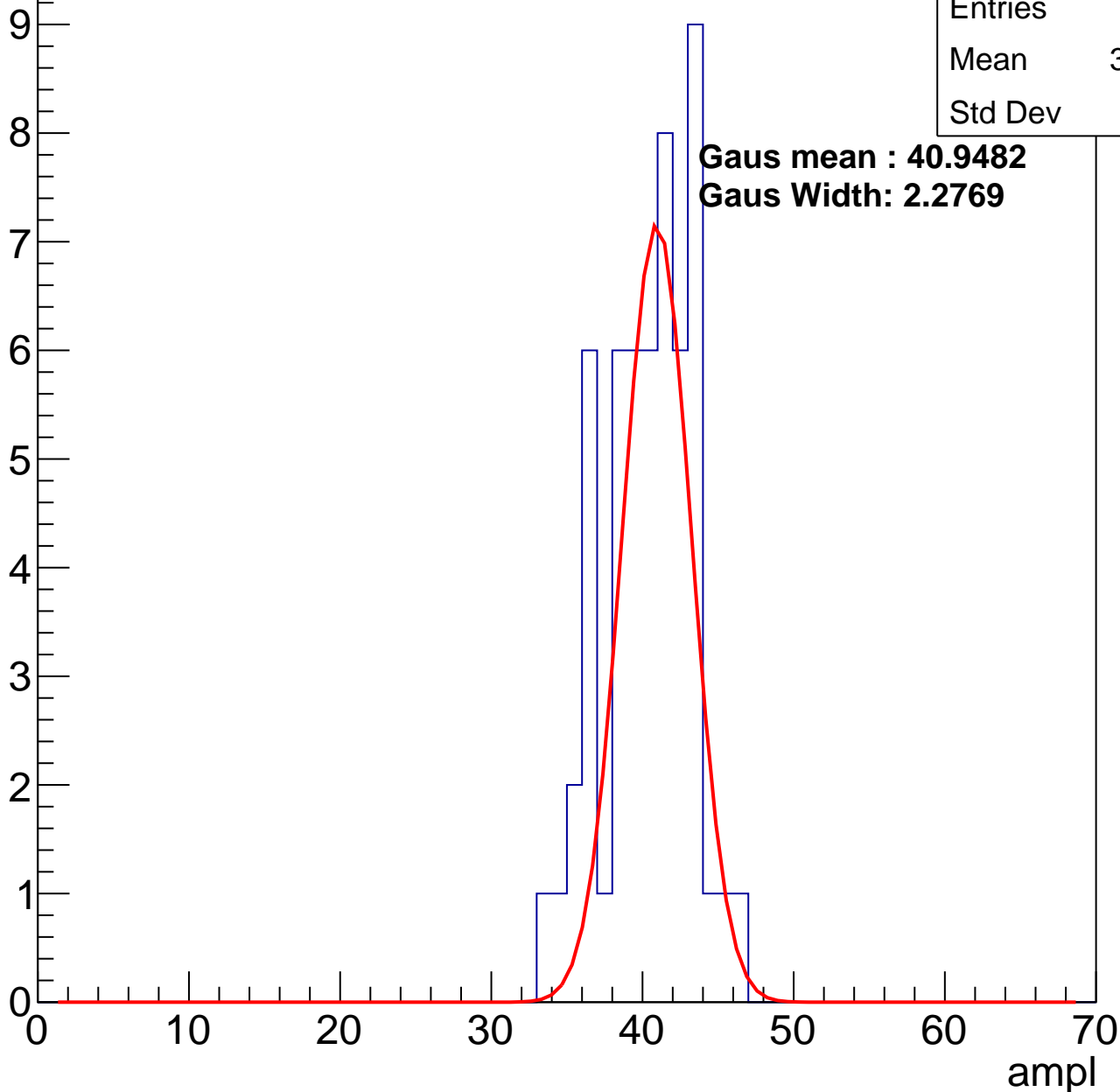
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	39.89
Std Dev	2.89

Gaus mean : 40.9482

Gaus Width: 2.2769

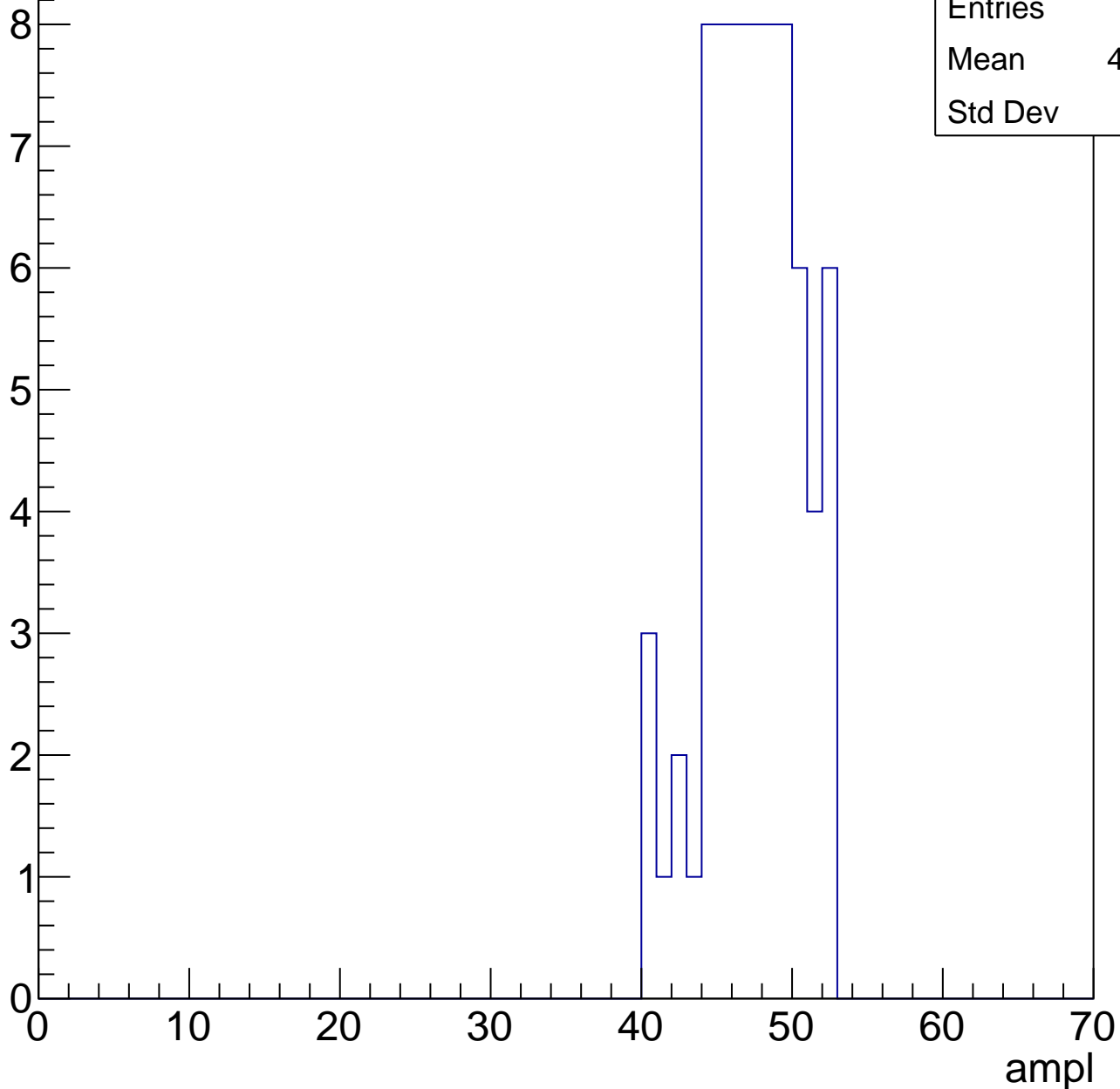


B1L102S, U19-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	46.99
Std Dev	3.07

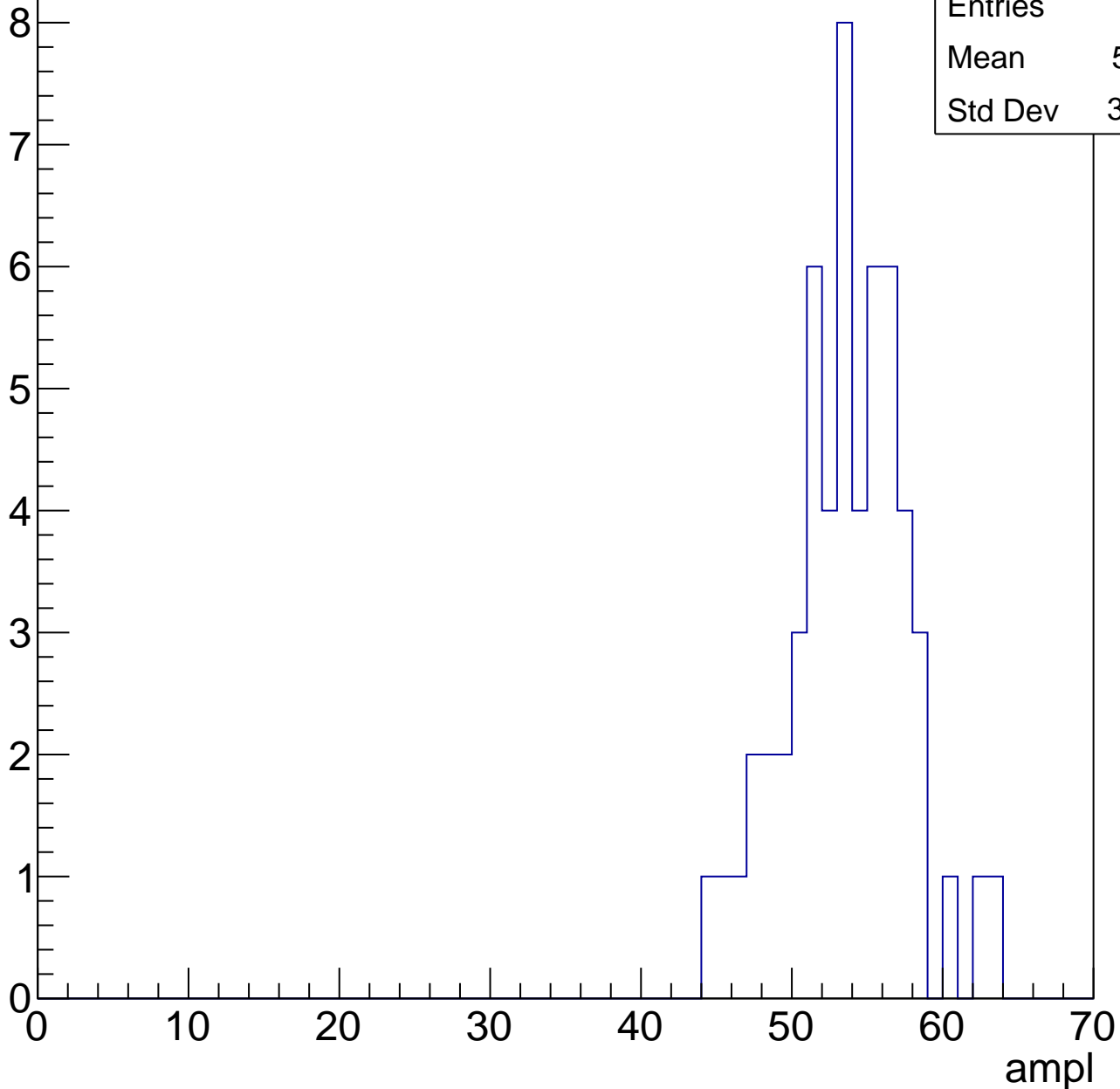


B1L102S, U19-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.21
Std Dev	3.899

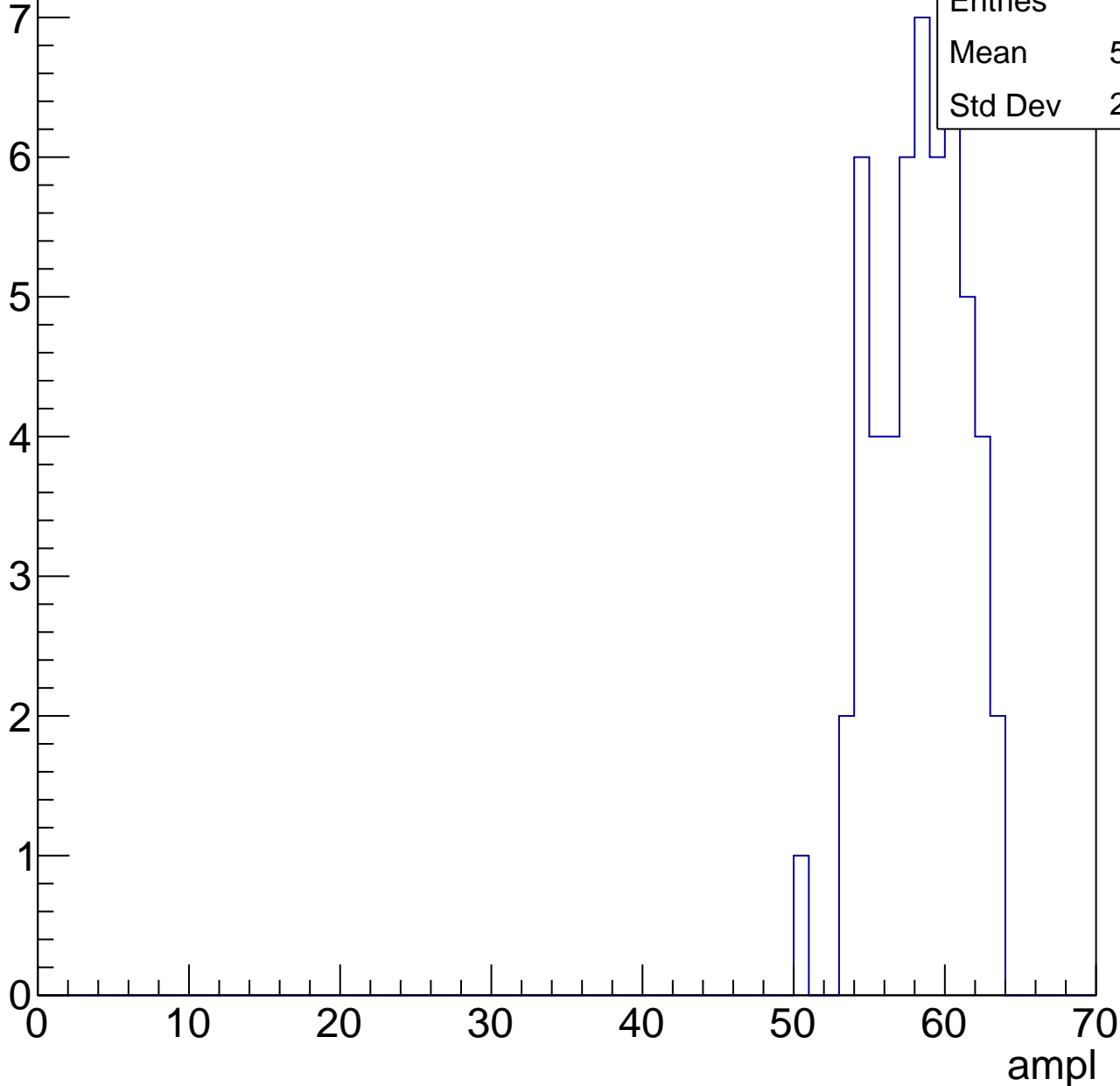


B1L102S, U19-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.87
Std Dev	2.919

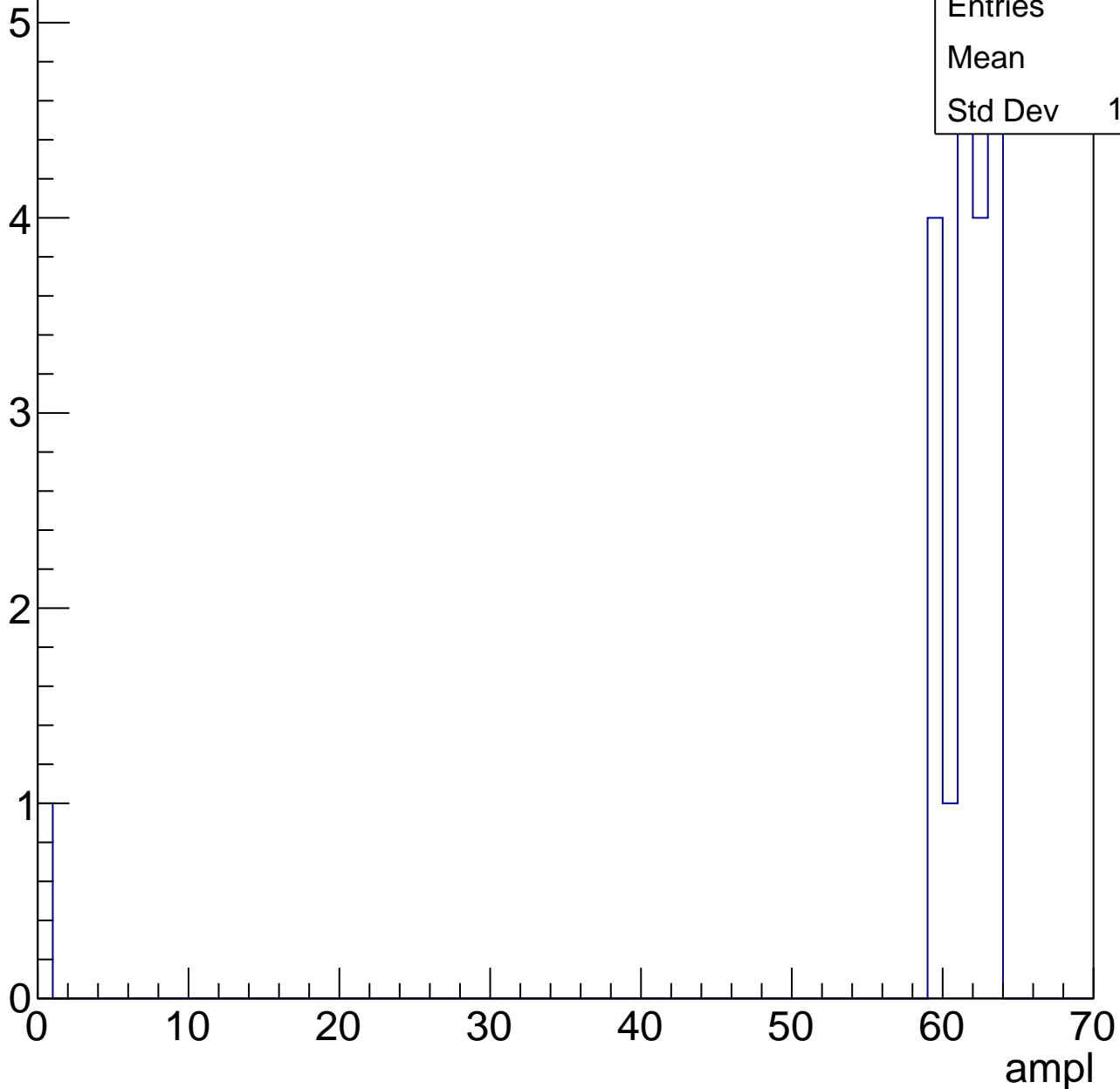


B1L102S, U19-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

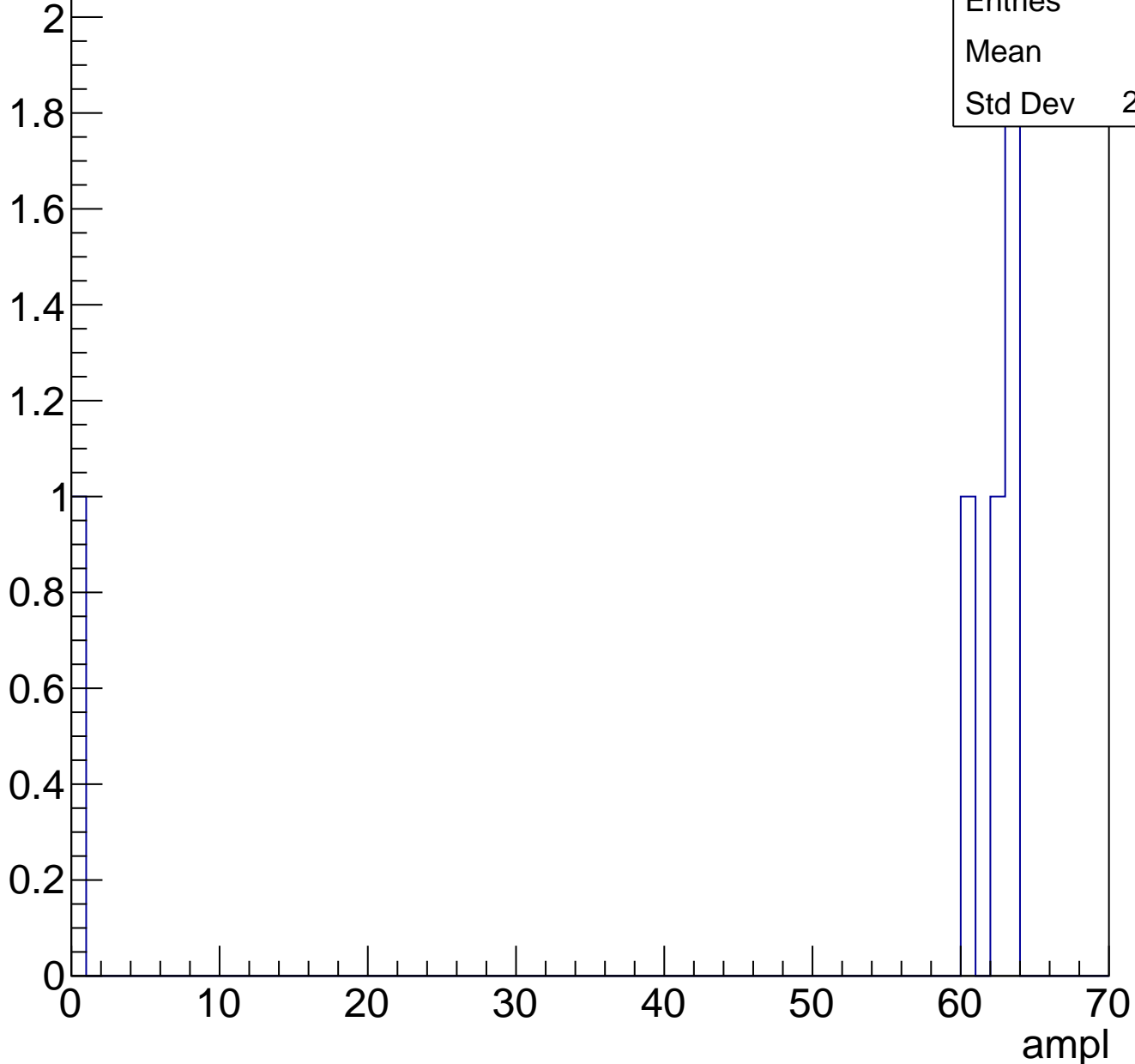
Entries	20
Mean	58.2
Std Dev	13.43



B1L102S, U19-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	49.6
Std Dev	24.82

B1L102S, U19-ch93, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	29.11
Std Dev	3.774

Gaus mean : 29.5427

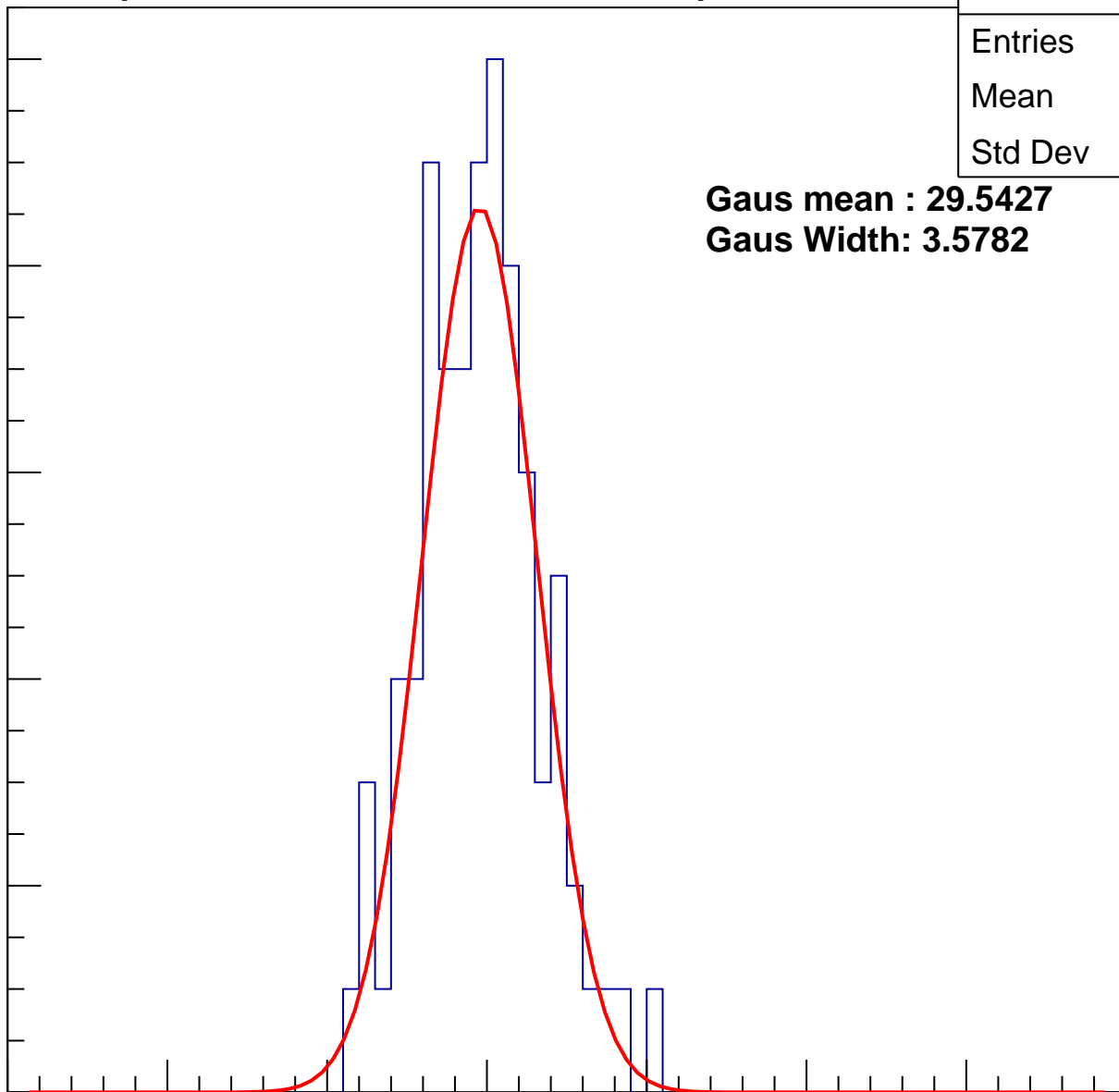
Gaus Width: 3.5782

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U19-ch93, adc1

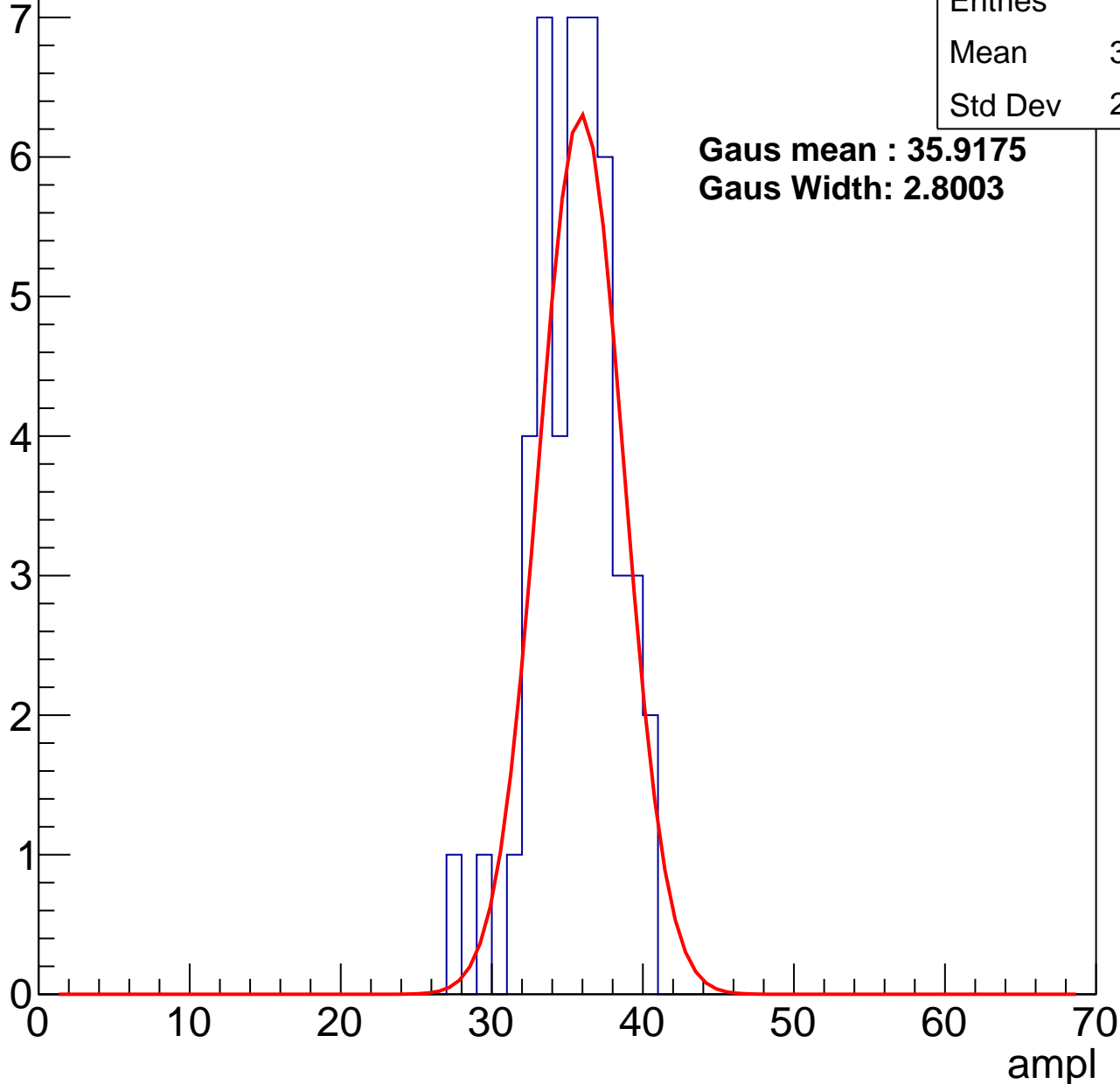
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	35.04
Std Dev	2.718

Gaus mean : 35.9175

Gaus Width: 2.8003



B1L102S, U19-ch93, adc2

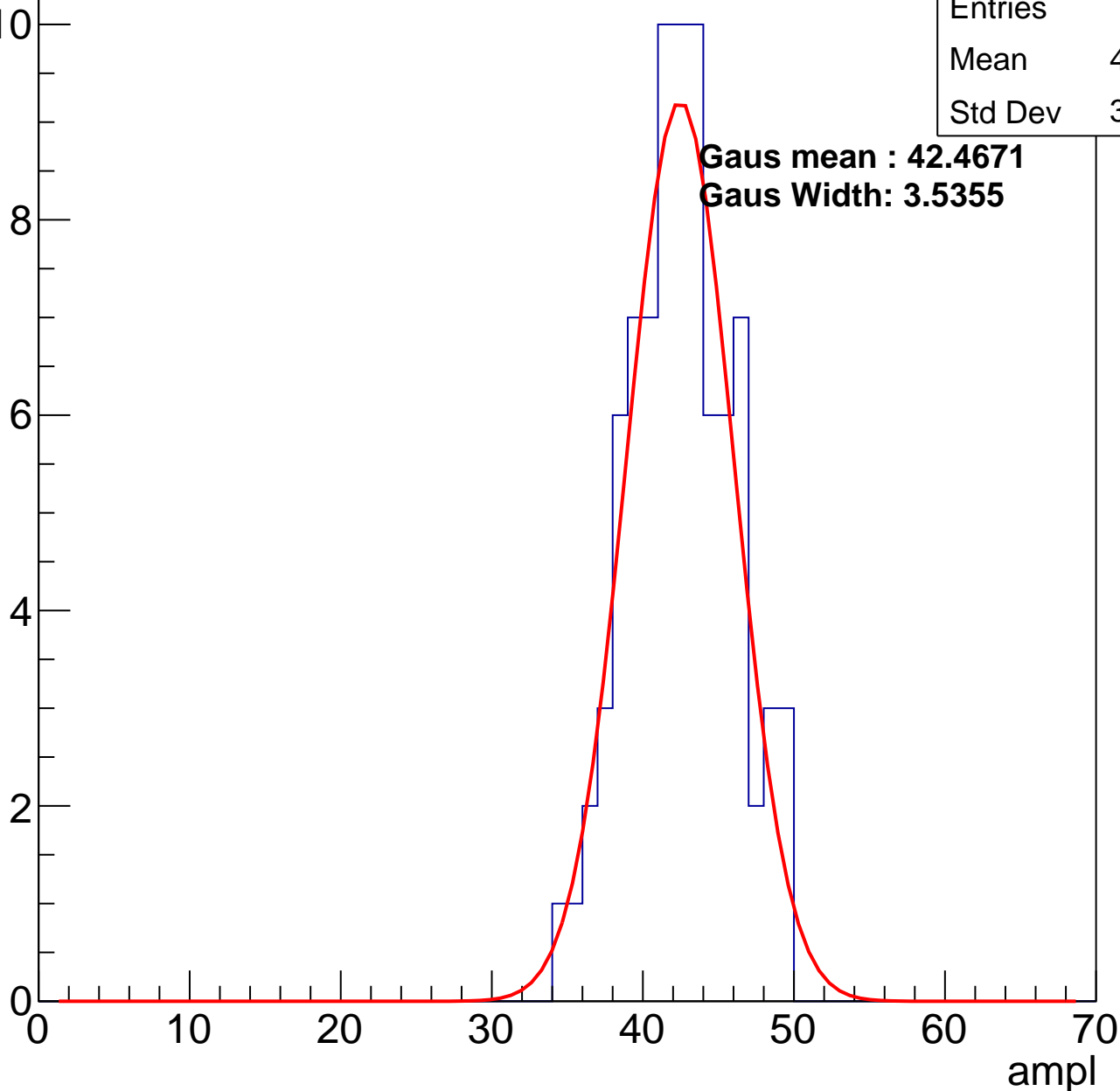
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	42.07
Std Dev	3.383

Gaus mean : 42.4671

Gaus Width: 3.5355

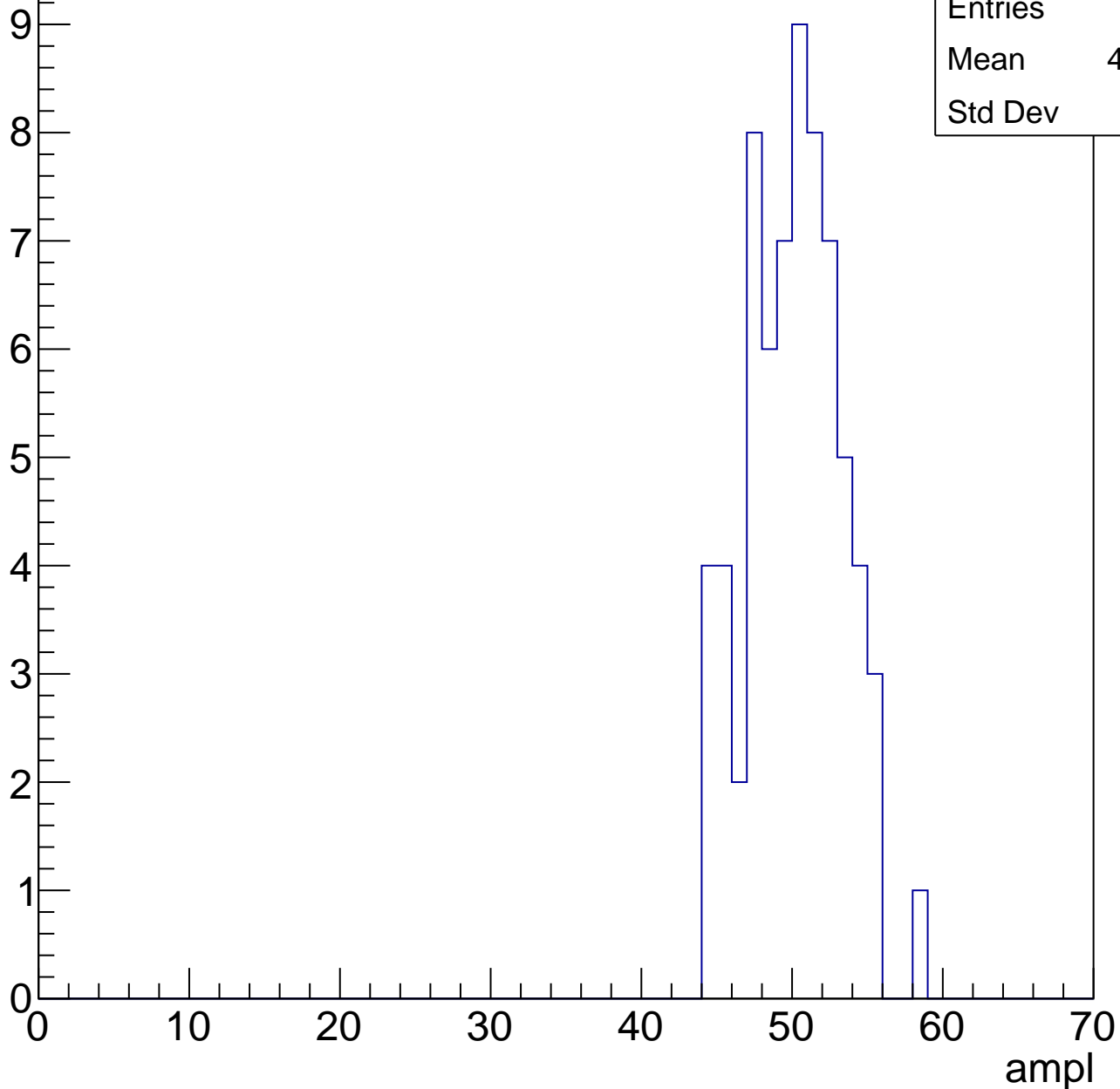


B1L102S, U19-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.72
Std Dev	3.11

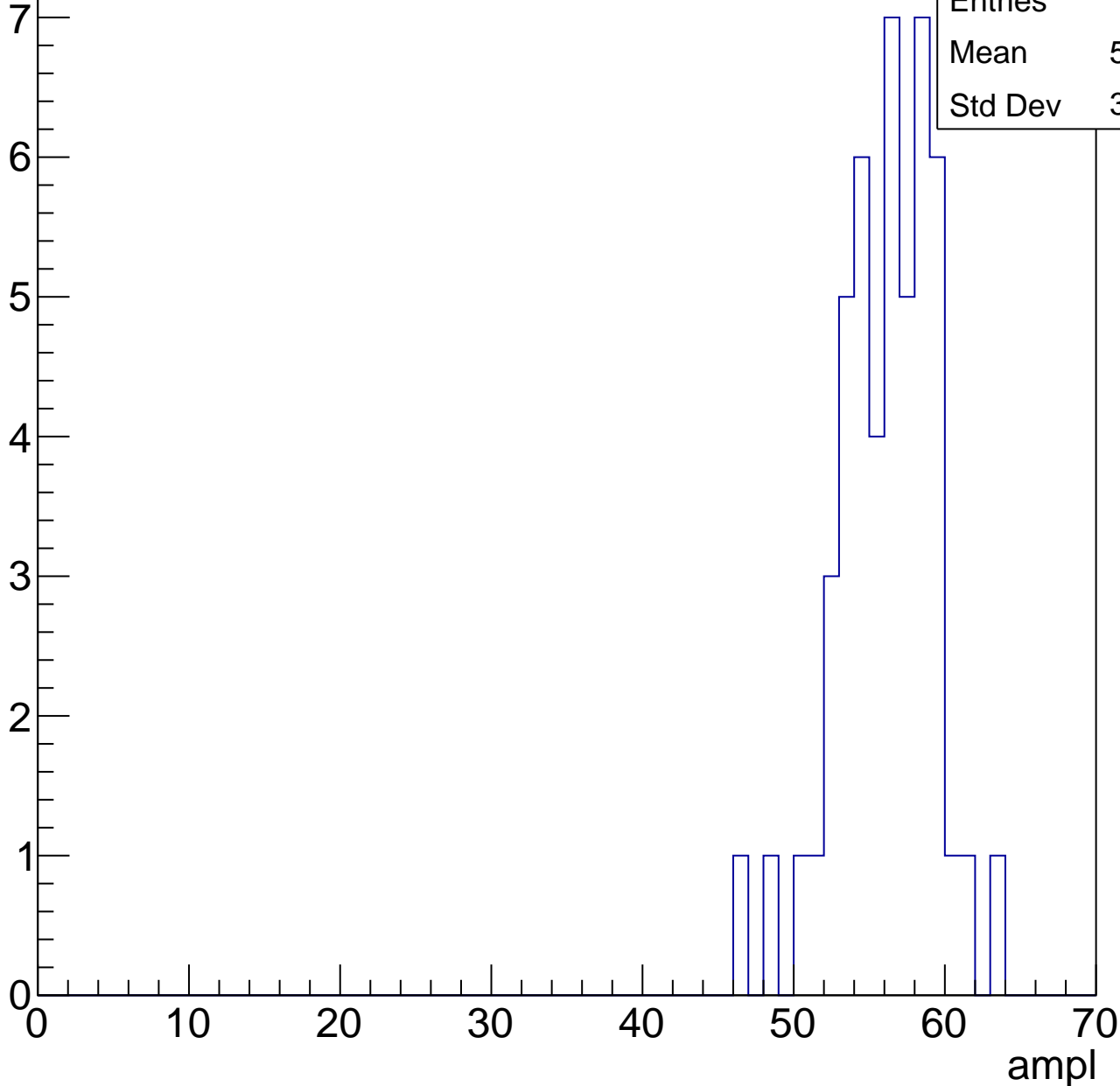


B1L102S, U19-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

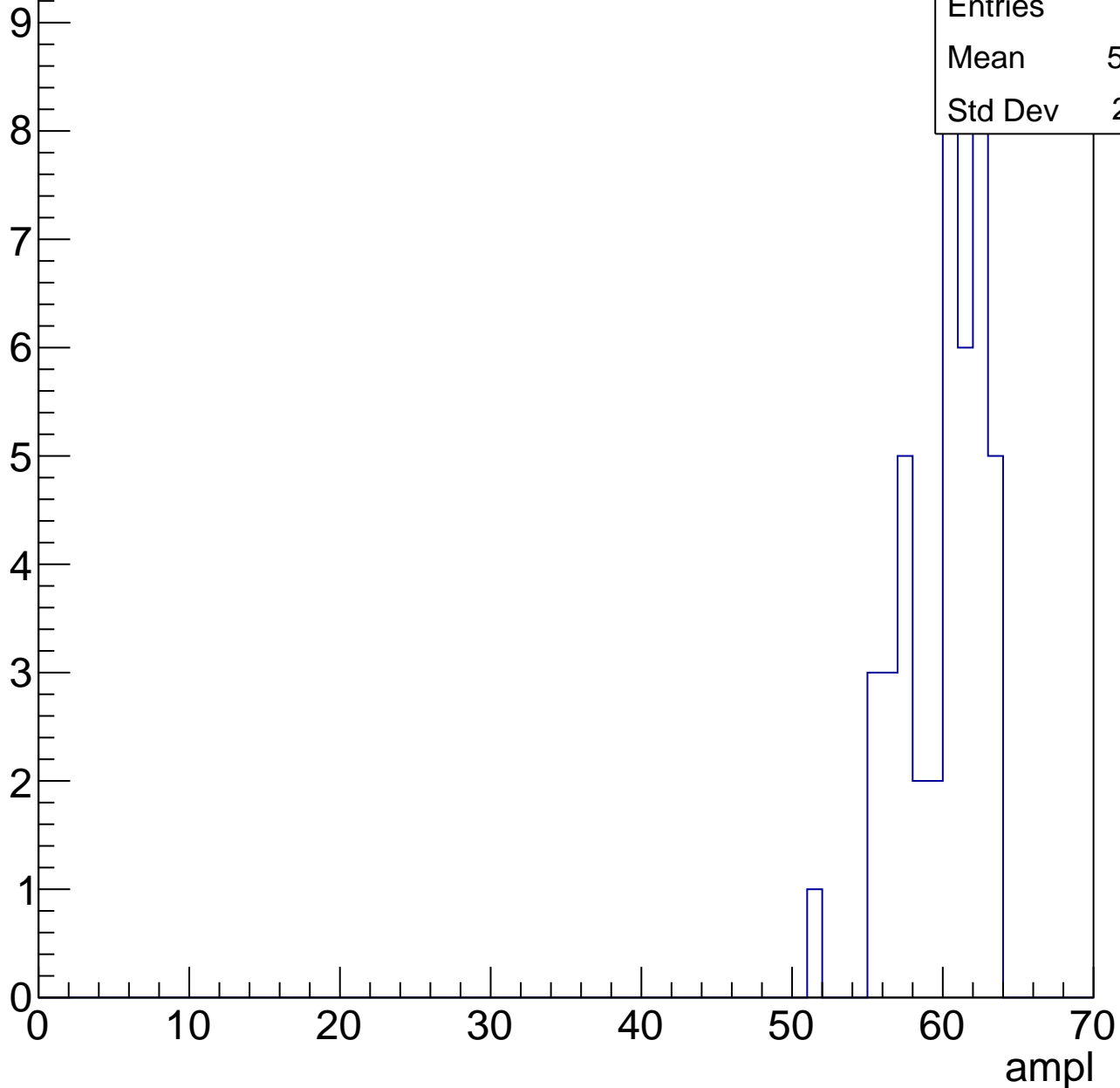
Entries	50
Mean	55.62
Std Dev	3.212



B1L102S, U19-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

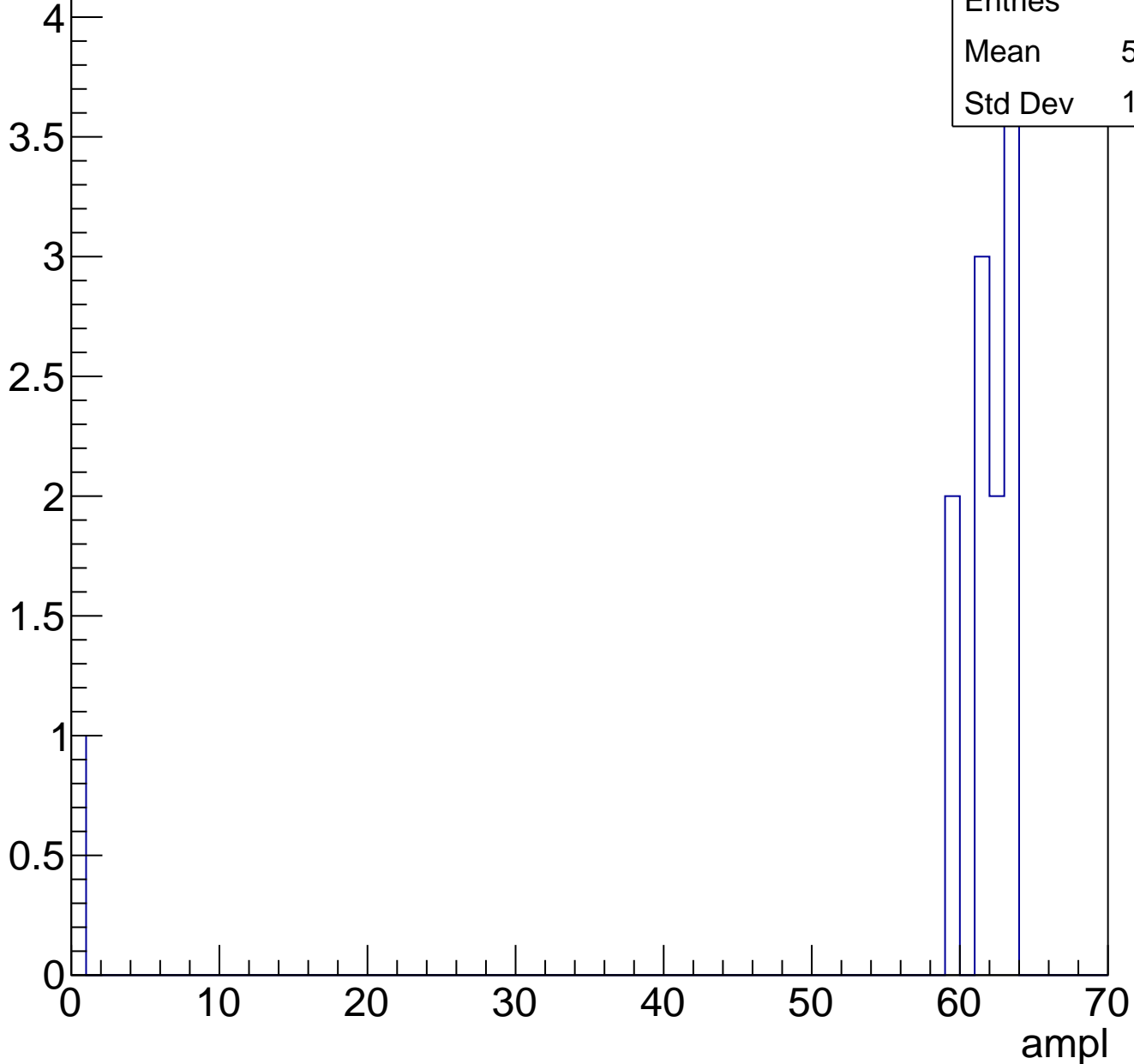
Entry



B1L102S, U19-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch94, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	61
Mean	27.84
Std Dev	3.163

Gaus mean : 28.7885

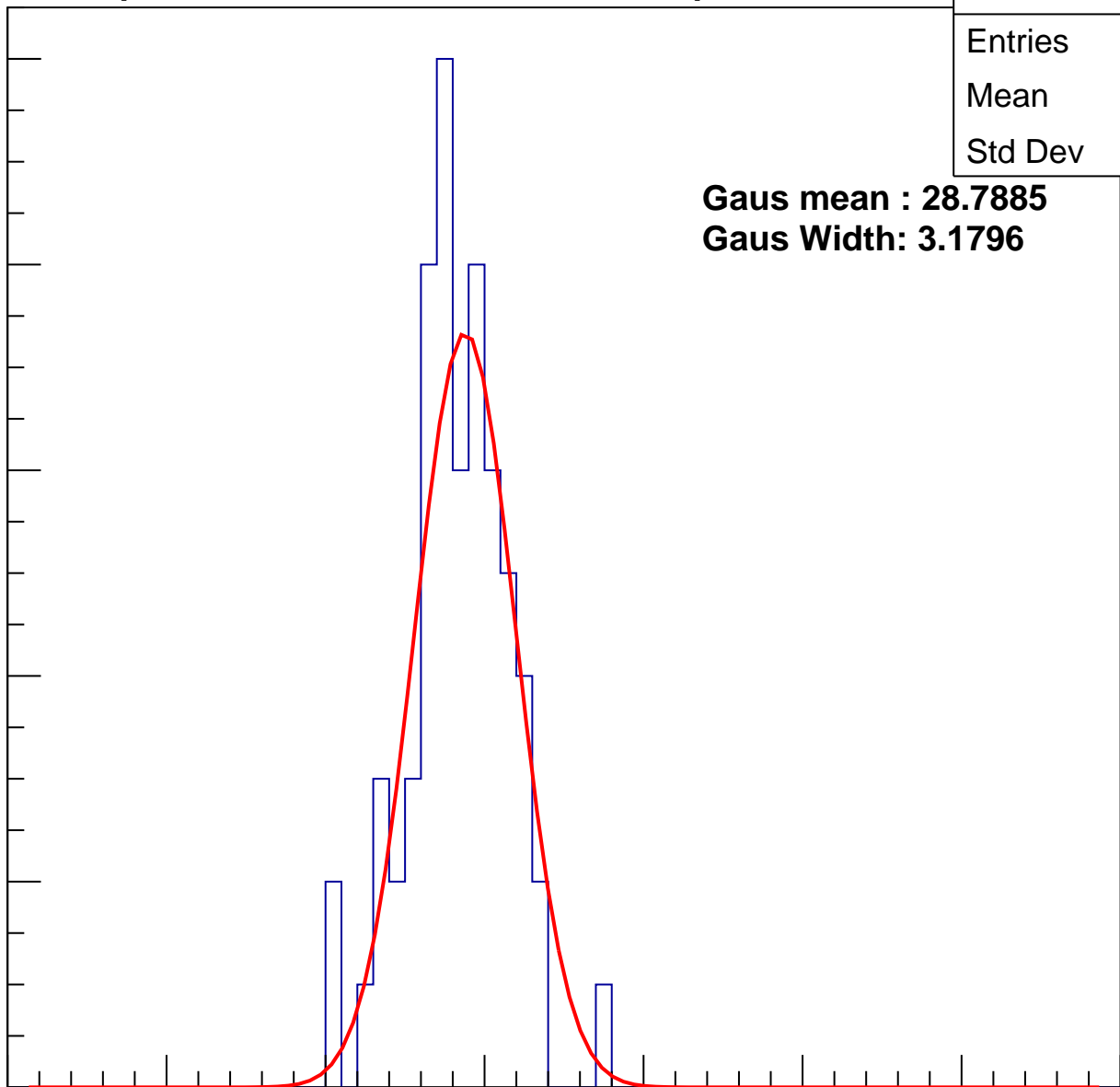
Gaus Width: 3.1796

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch94, adc1

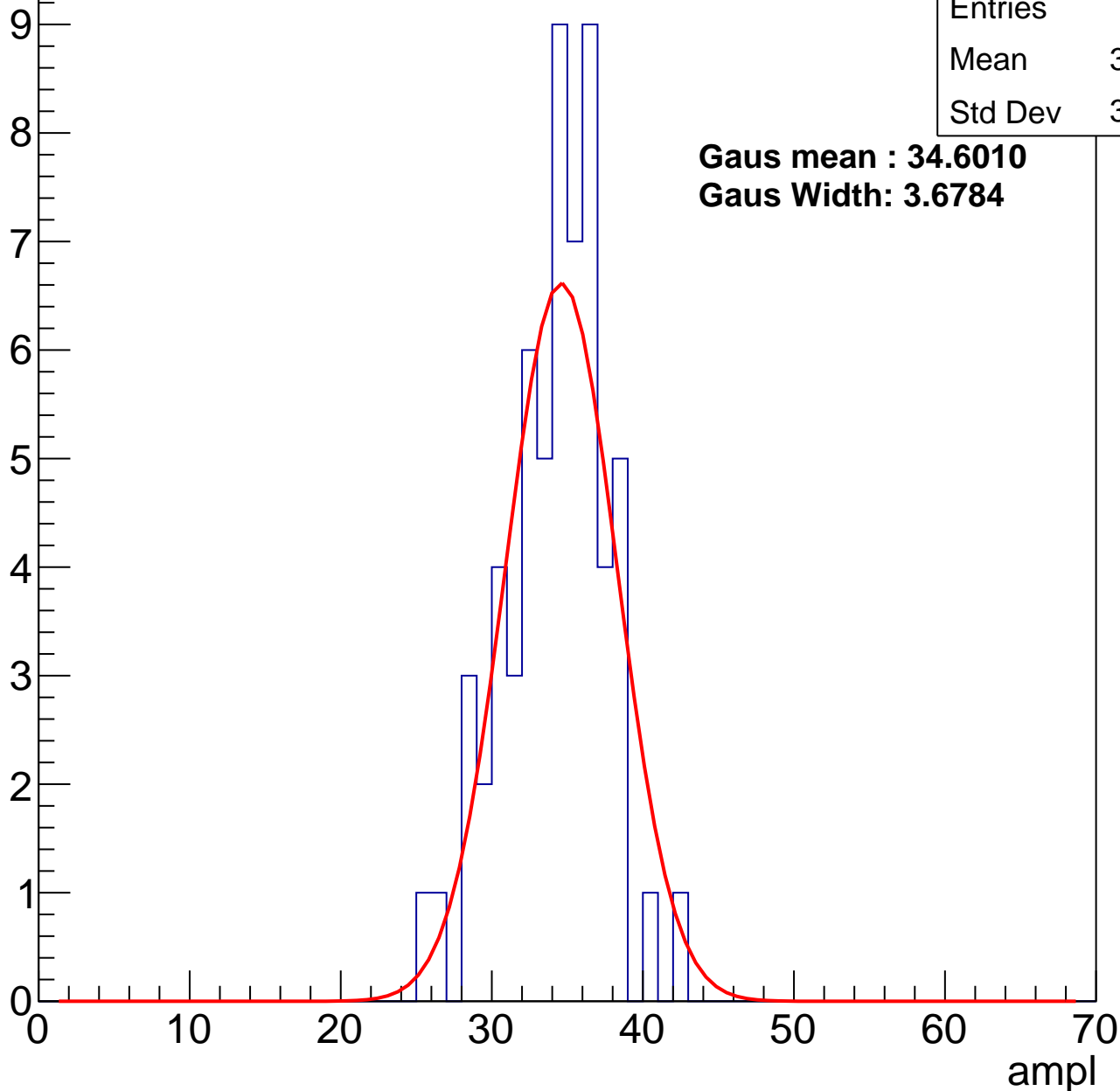
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	33.74
Std Dev	3.333

Gaus mean : 34.6010

Gaus Width: 3.6784



B1L102S, U19-ch94, adc2

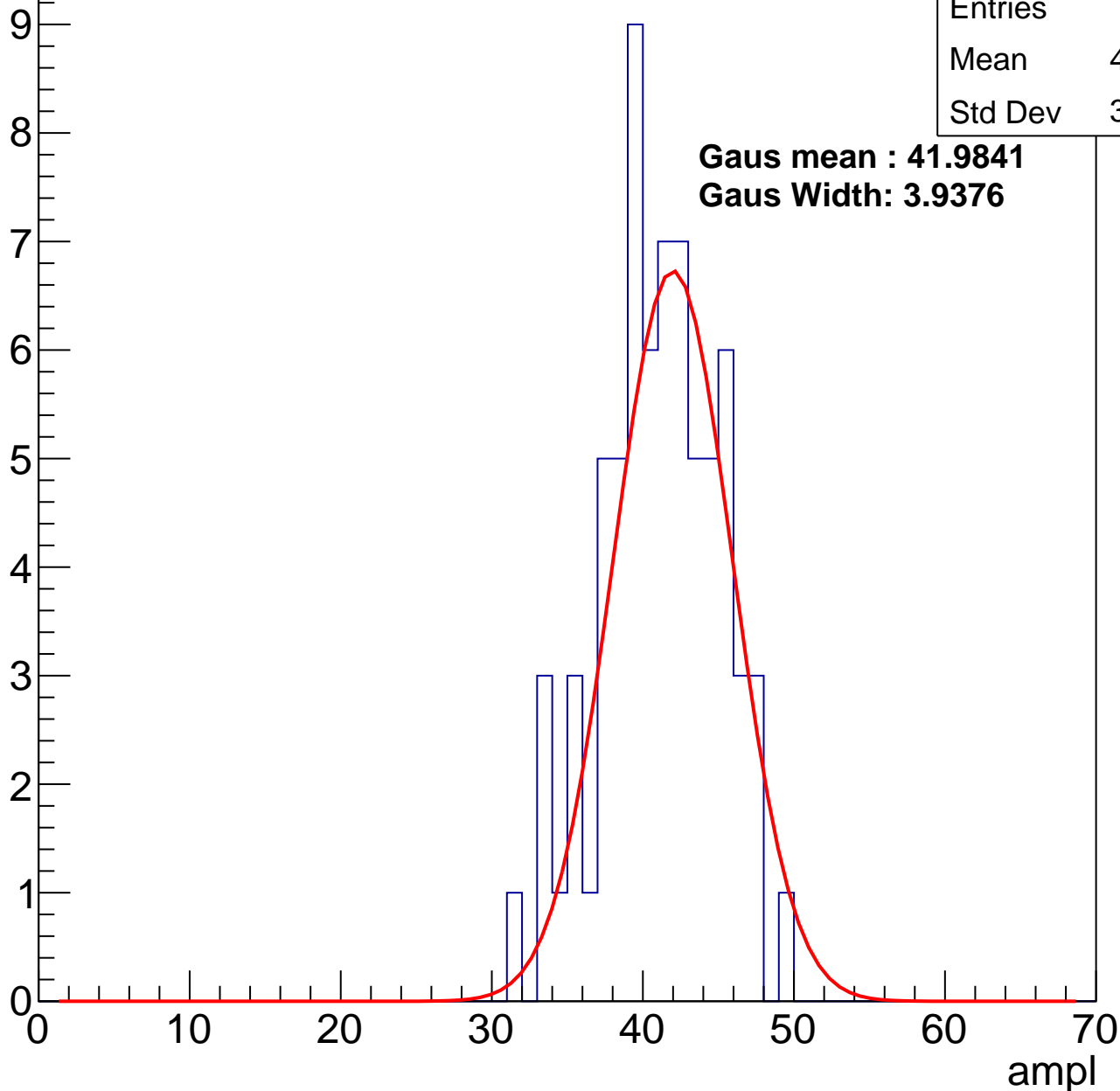
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	40.63
Std Dev	3.839

Gaus mean : 41.9841

Gaus Width: 3.9376

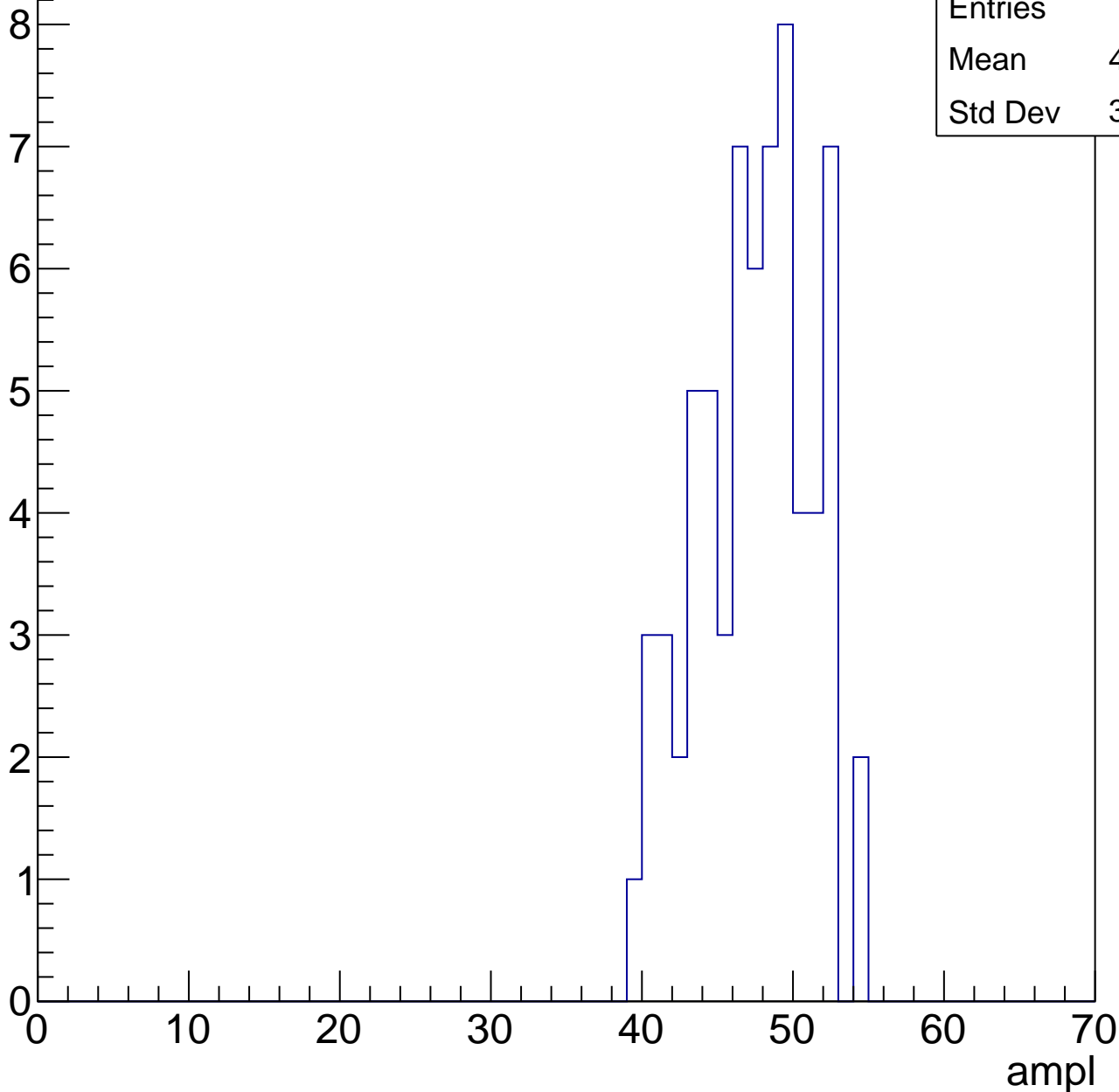


B1L102S, U19-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

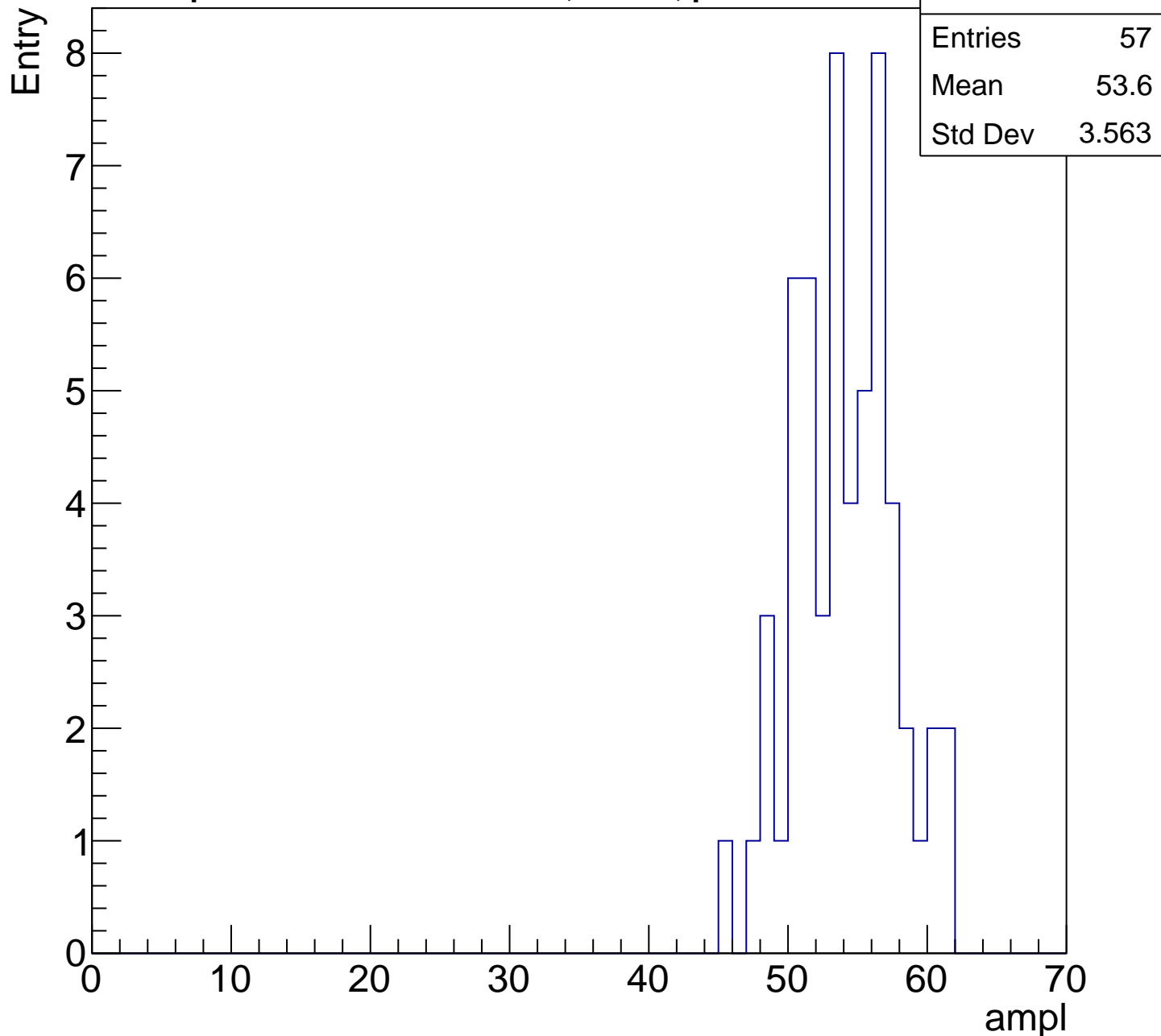
Entry

Entries	67
Mean	46.93
Std Dev	3.715



B1L102S, U19-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

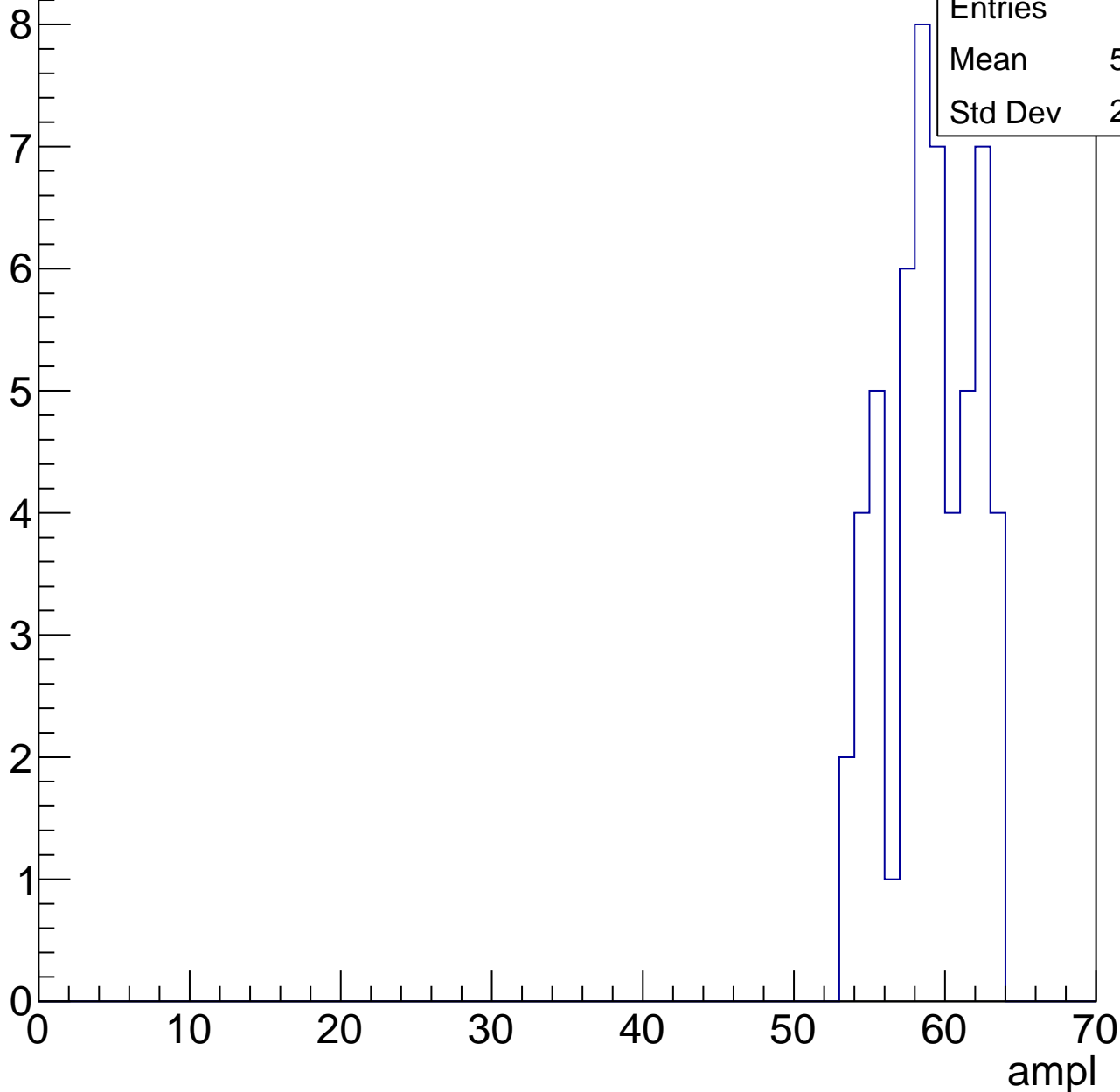


B1L102S, U19-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.55
Std Dev	2.859

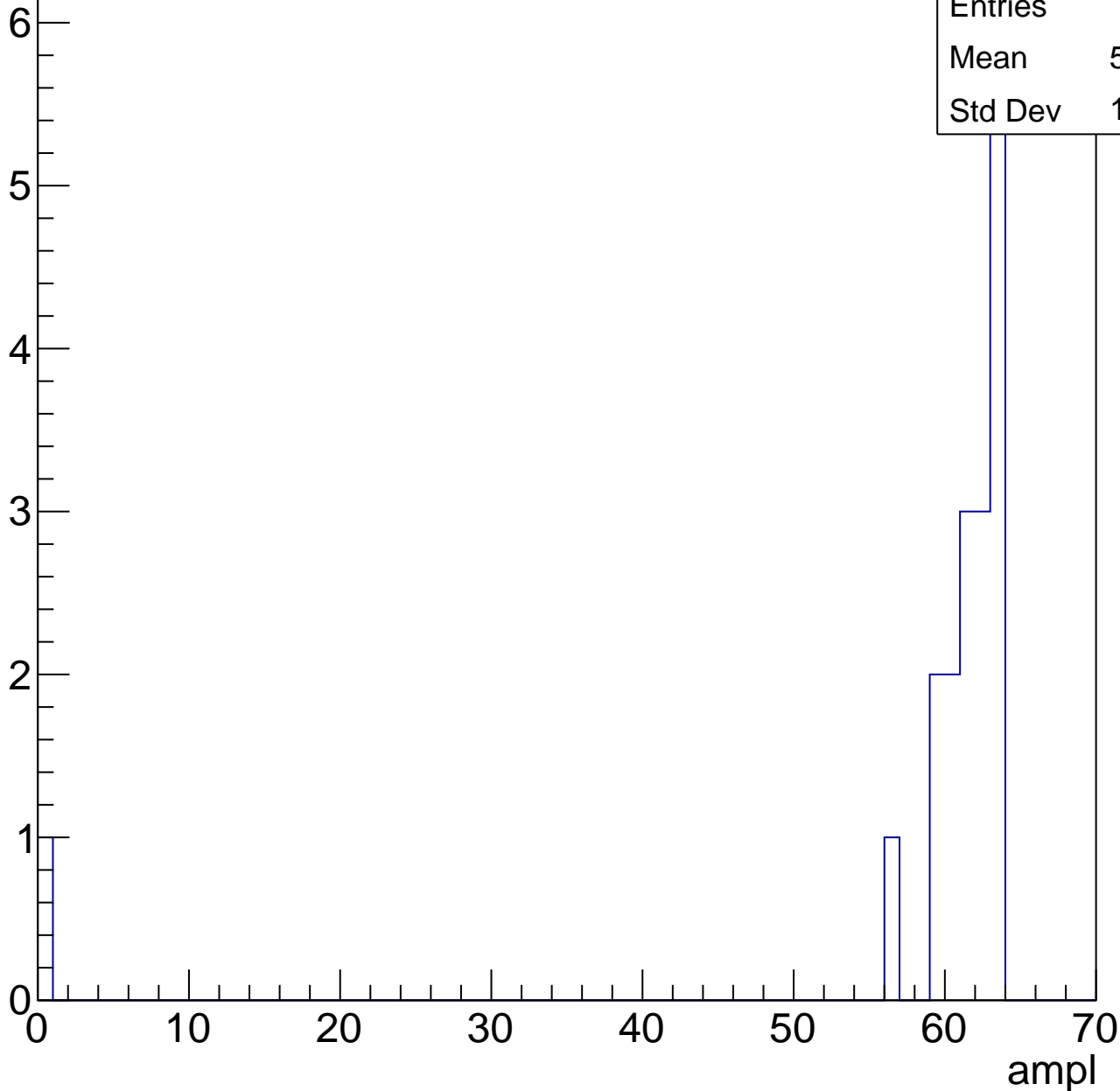


B1L102S, U19-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

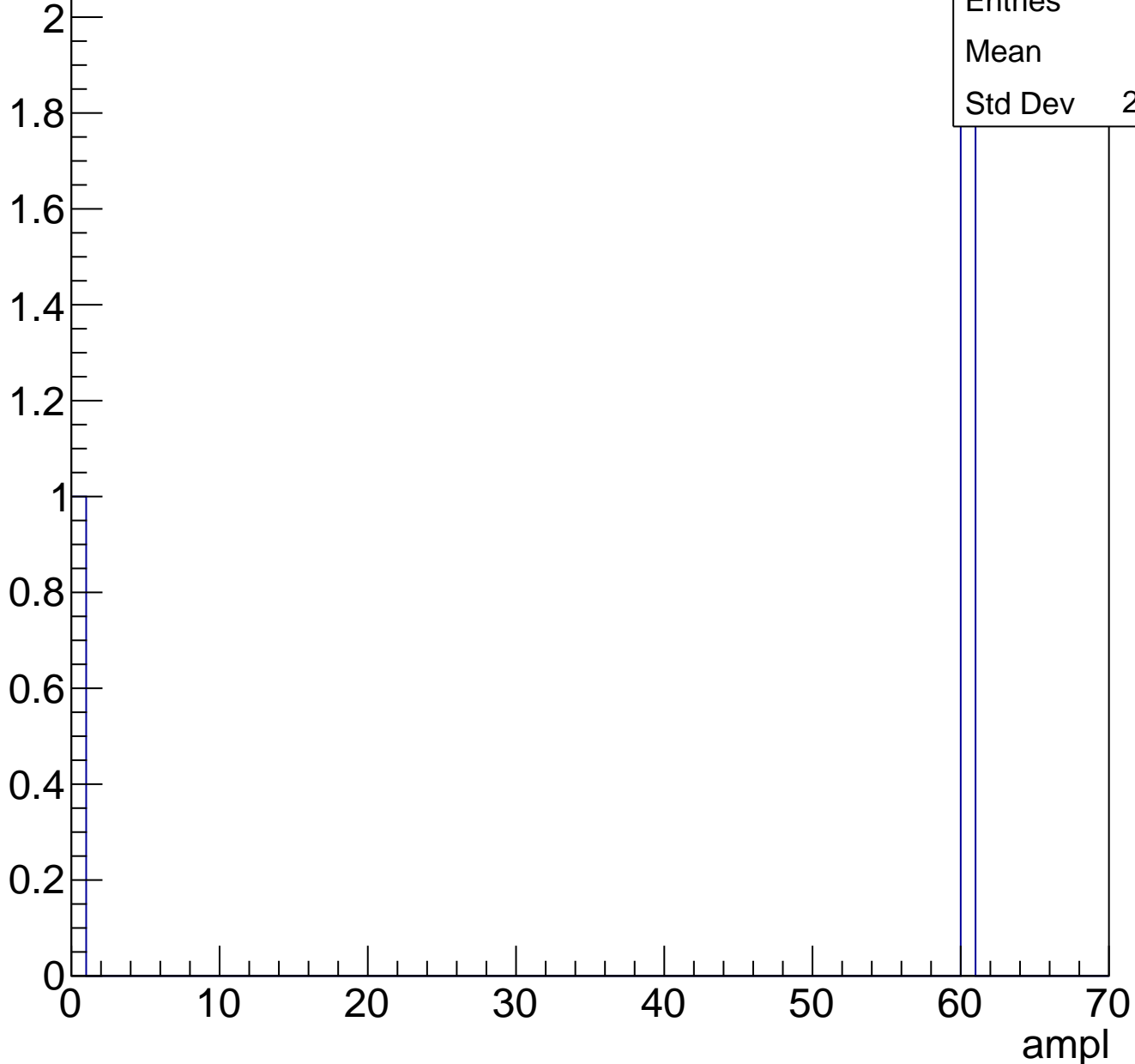
Entries	18
Mean	57.83
Std Dev	14.15



B1L102S, U19-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch95, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	26.61
Std Dev	4.529

Gaus mean : 27.4627

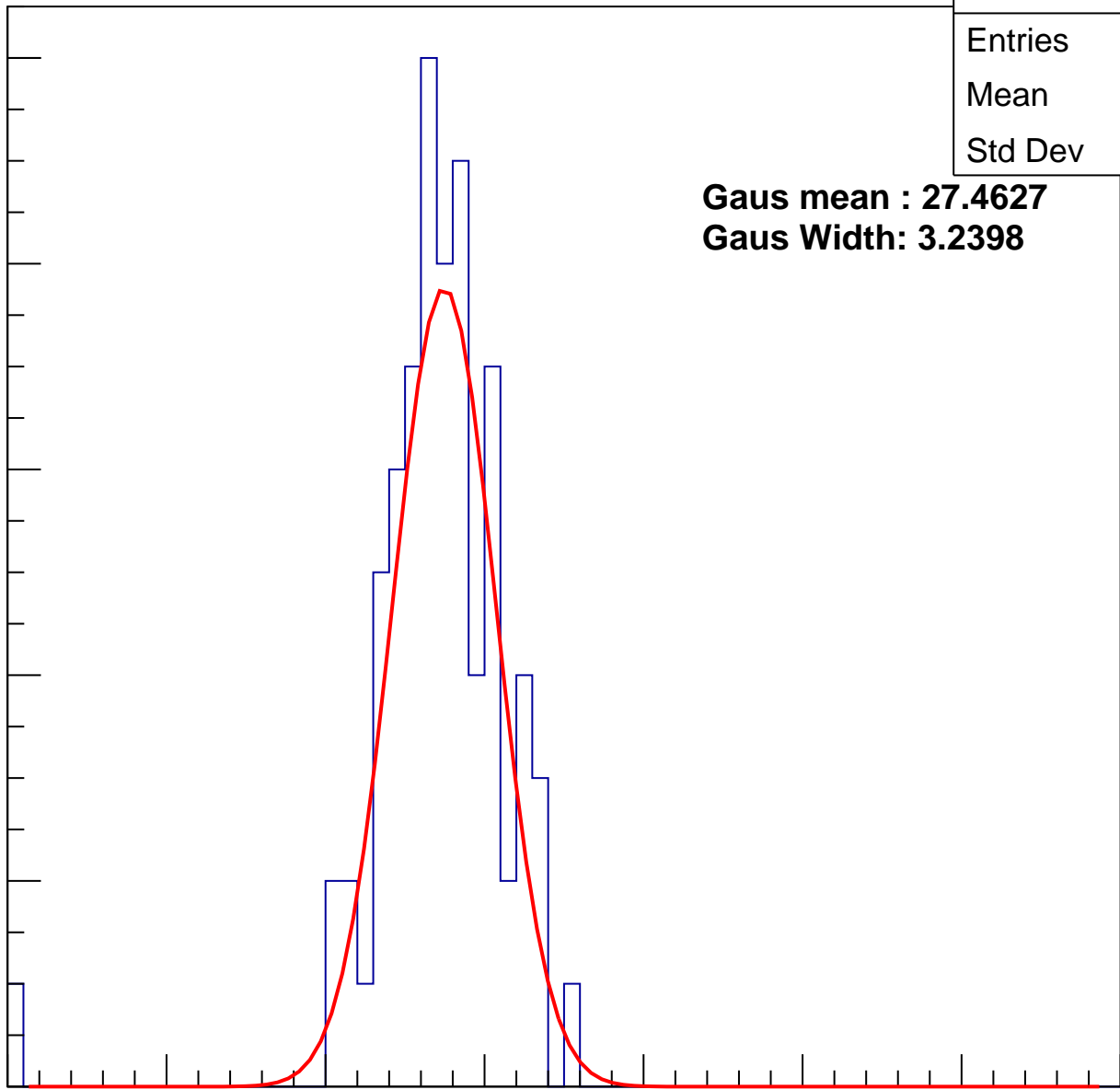
Gaus Width: 3.2398

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch95, adc1

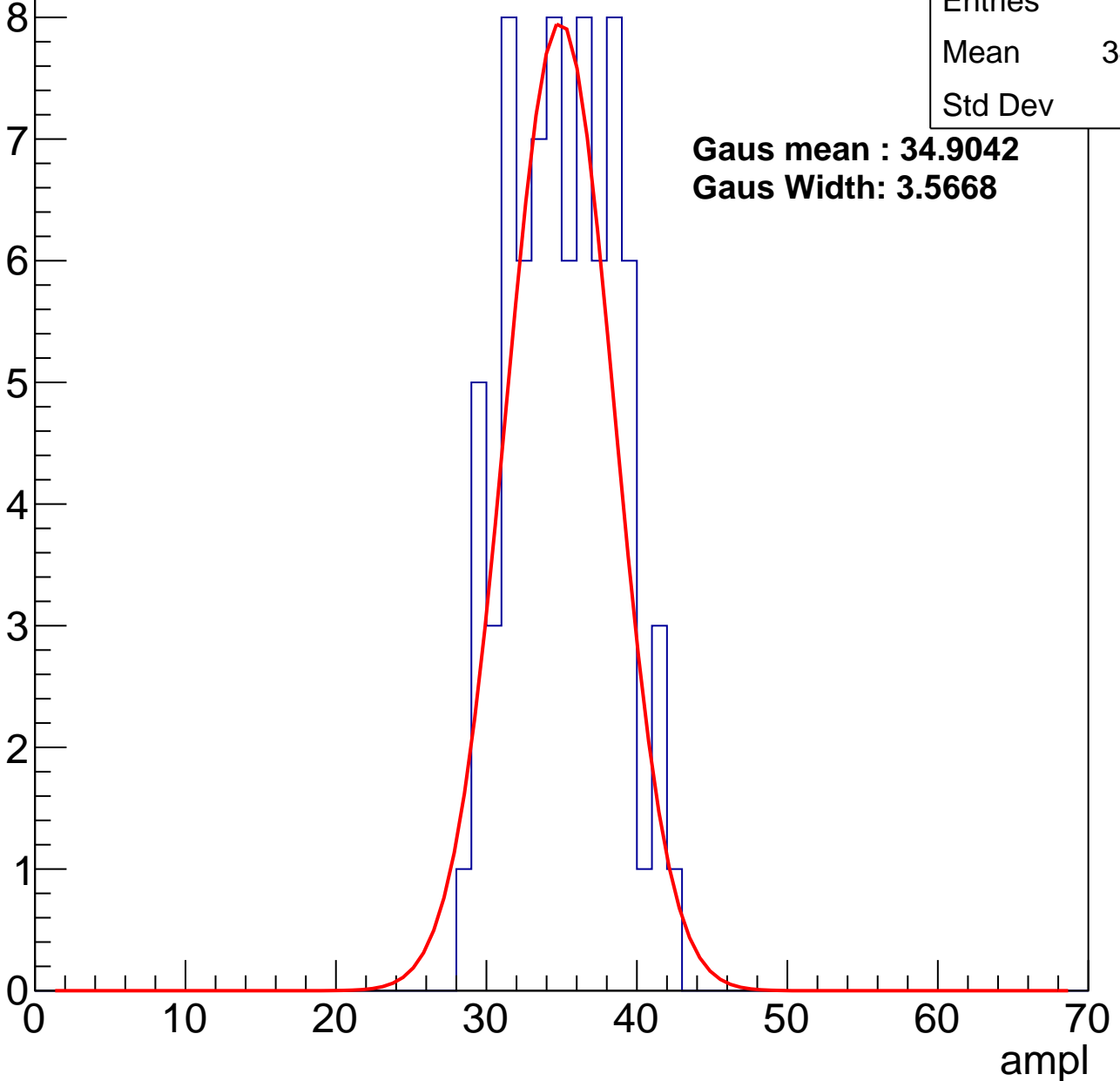
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.66
Std Dev	3.41

Gaus mean : 34.9042

Gaus Width: 3.5668



B1L102S, U19-ch95, adc2

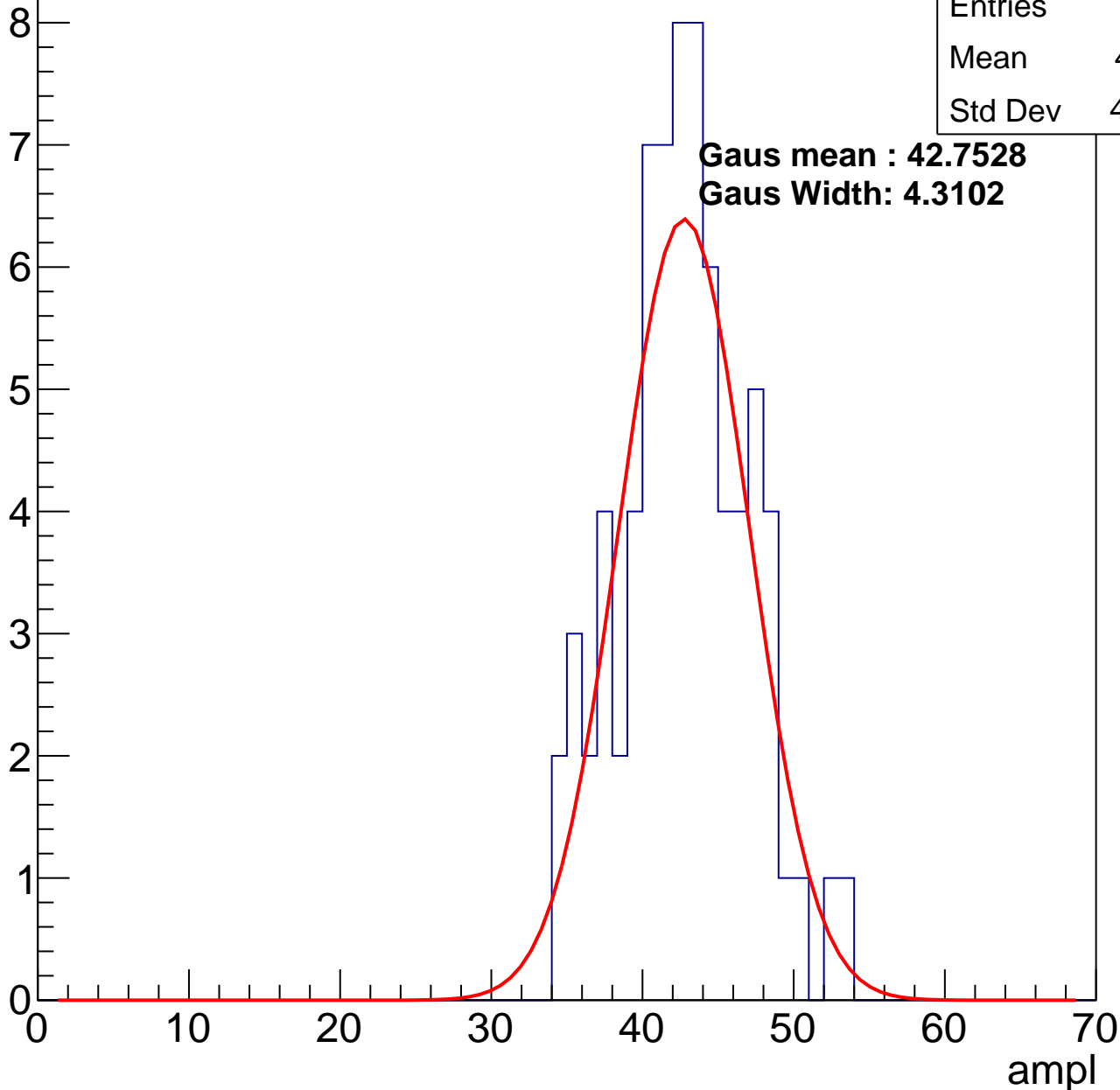
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.31
Std Dev	4.156

Gaus mean : 42.7528

Gaus Width: 4.3102

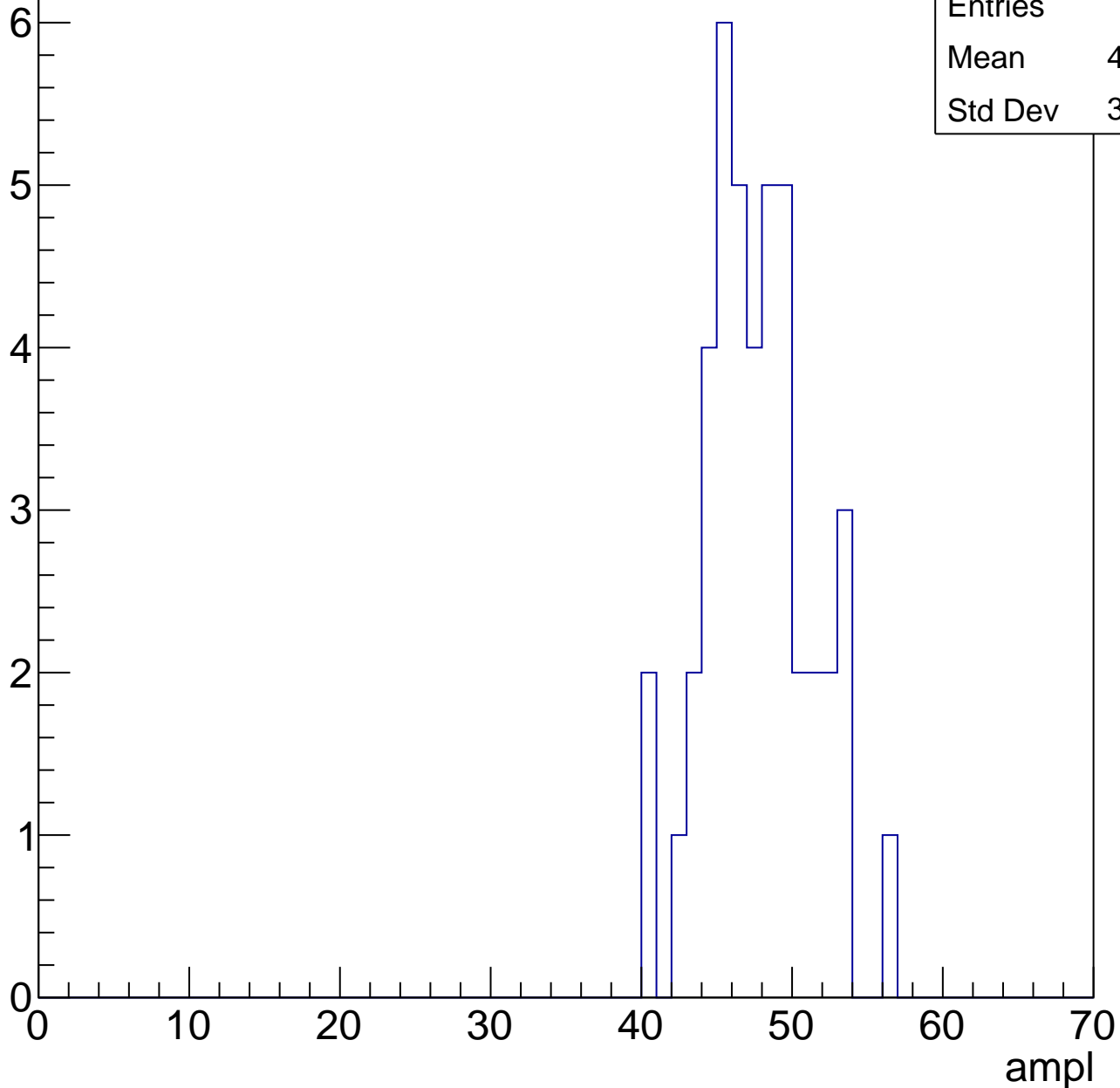


B1L102S, U19-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	47.23
Std Dev	3.476

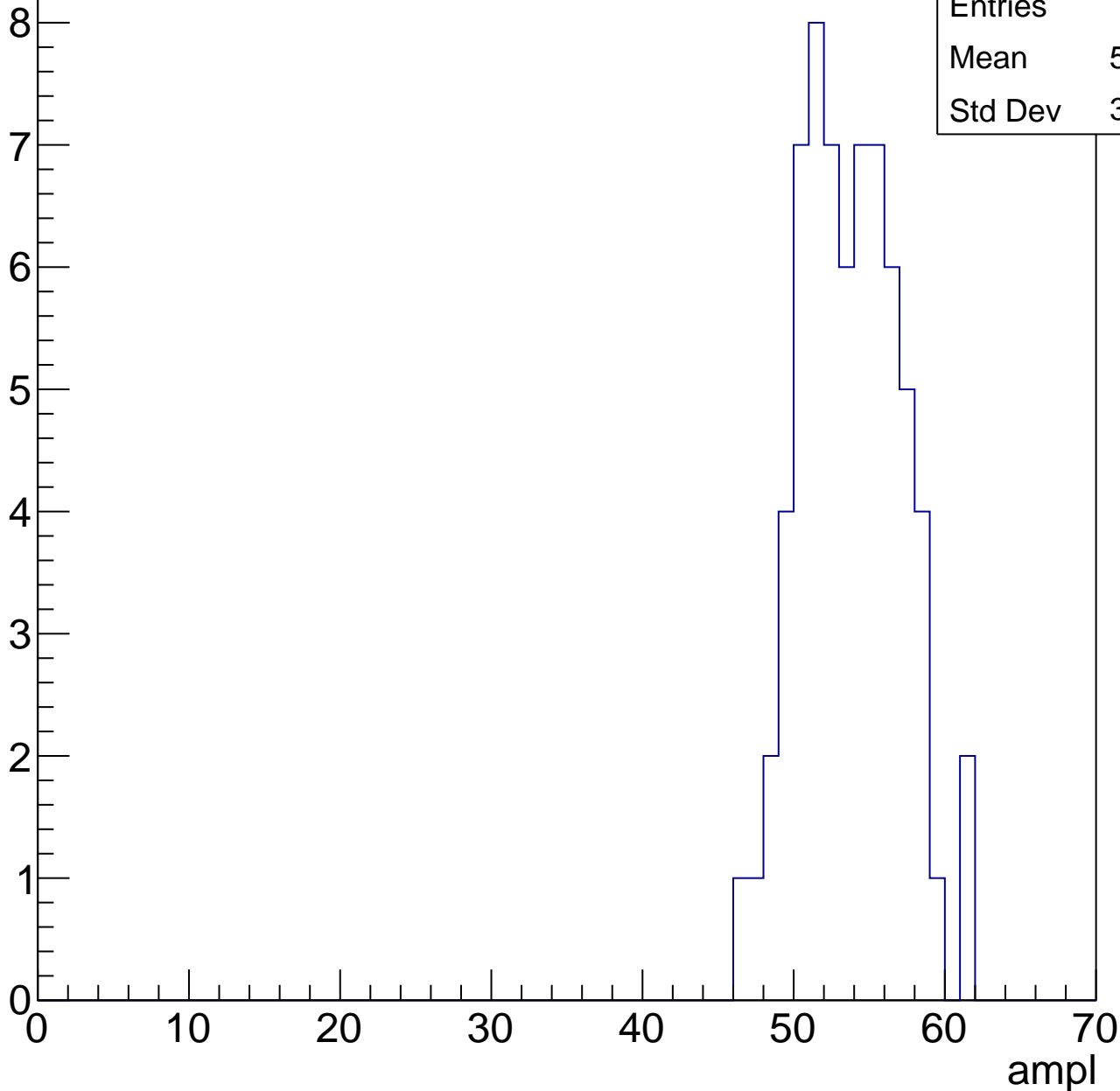


B1L102S, U19-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

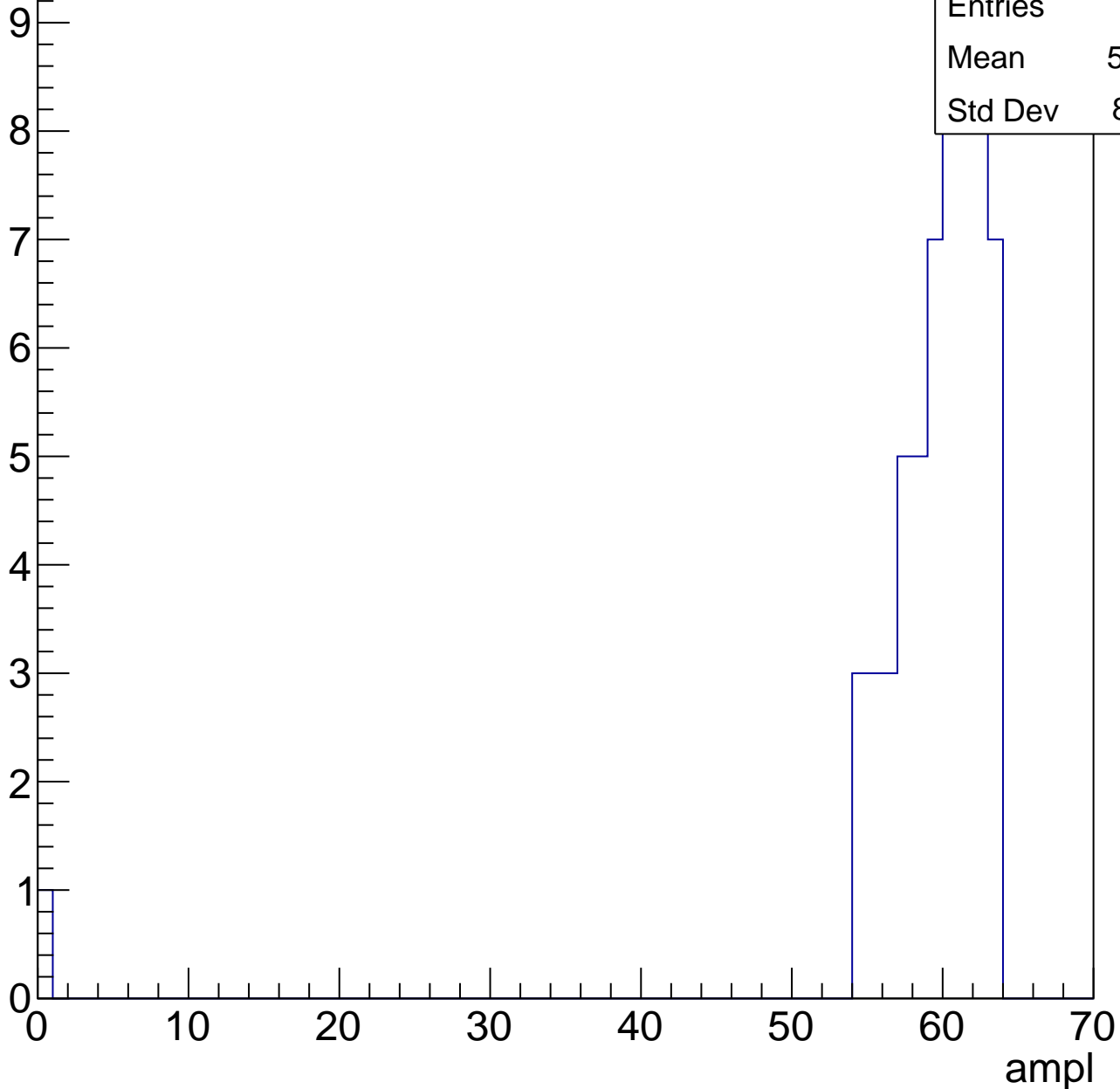
Entries	68
Mean	53.26
Std Dev	3.275



B1L102S, U19-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

60.88

Std Dev

1.965

3

2.5

2

1.5

1

0.5

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

60.88

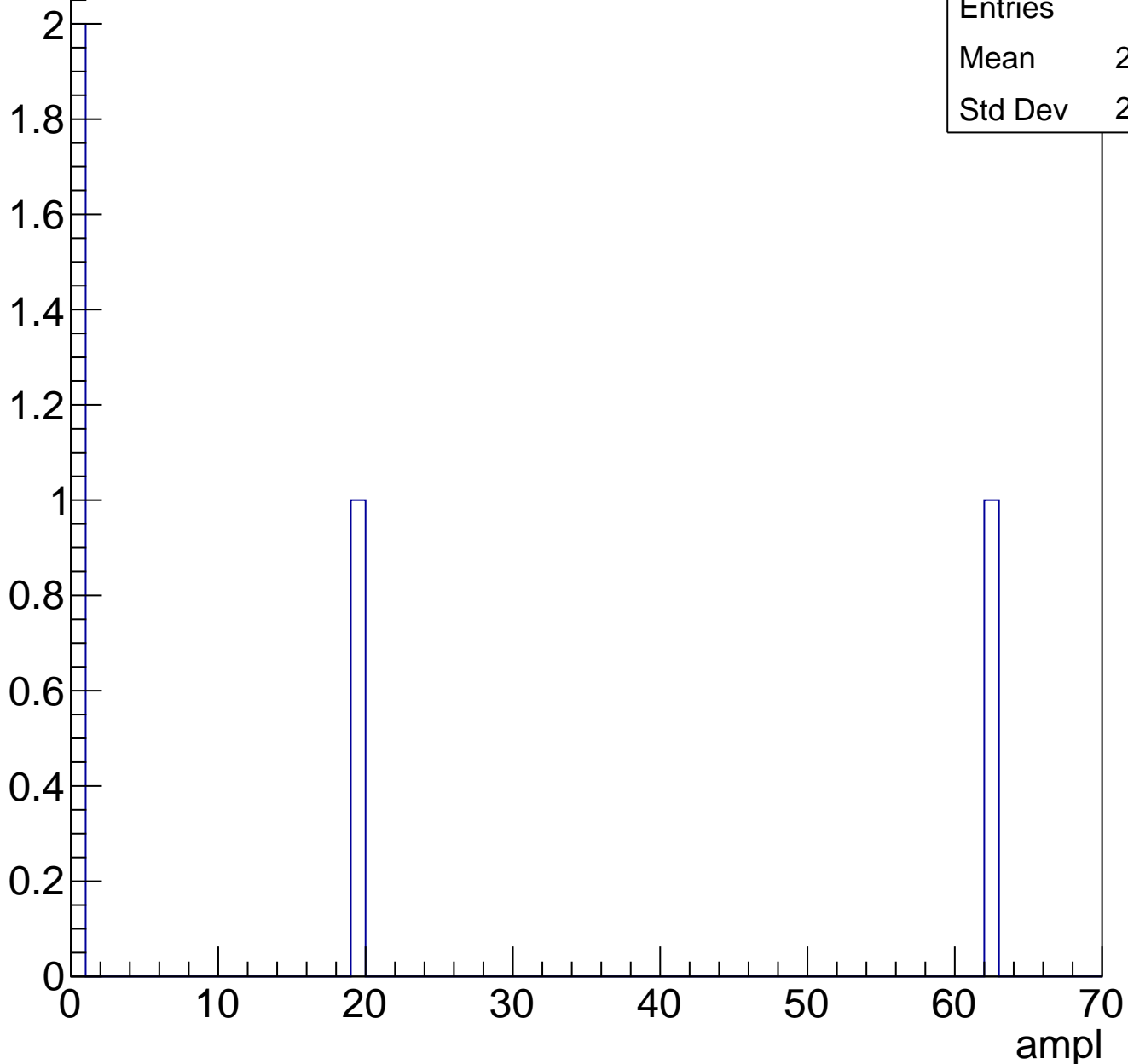
Std Dev

1.965

B1L102S, U19-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20.25
Std Dev	25.32

B1L102S, U19-ch96, adc0

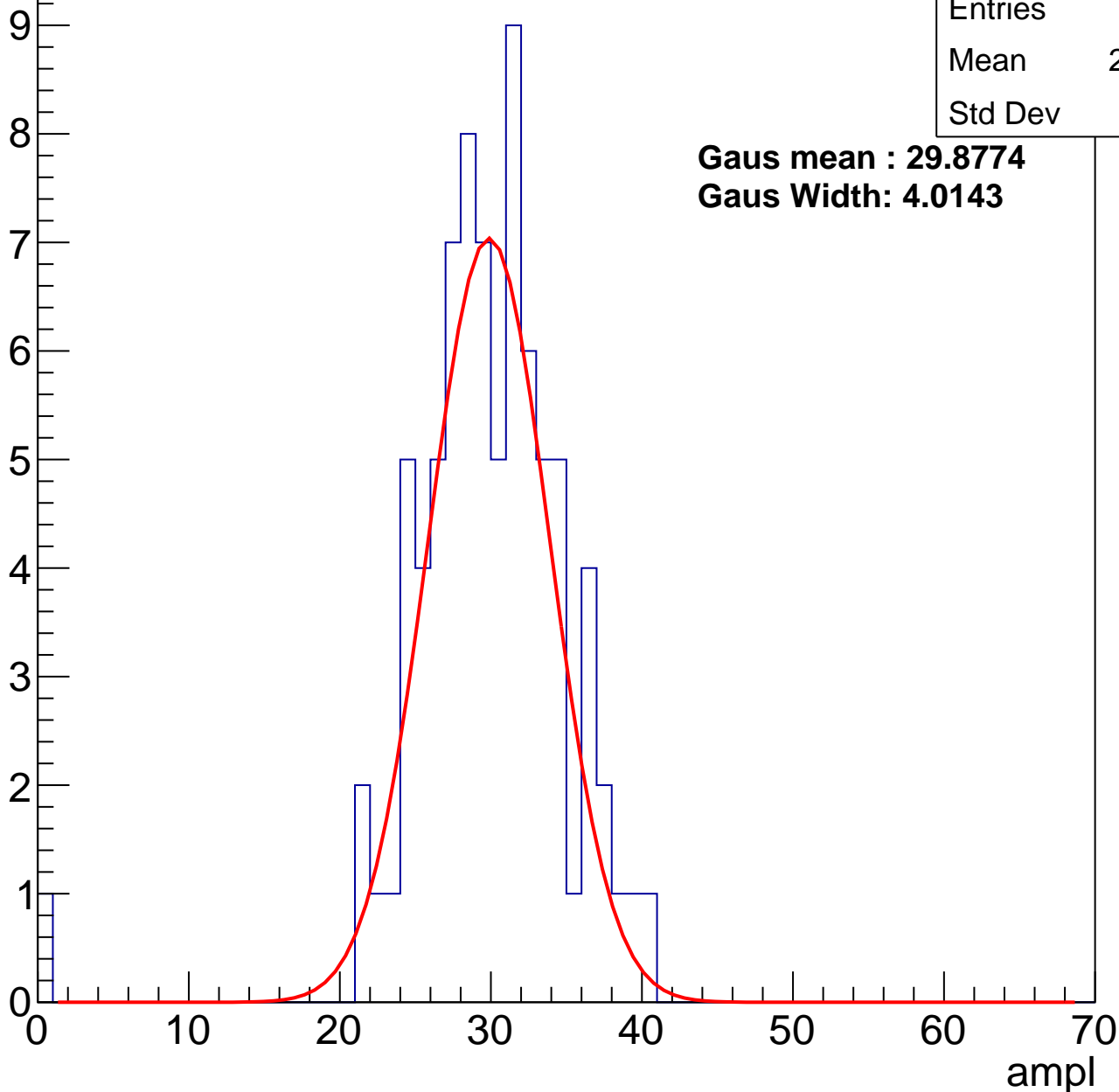
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.37
Std Dev	5.3

Gaus mean : 29.8774

Gaus Width: 4.0143



B1L102S, U19-ch96, adc1

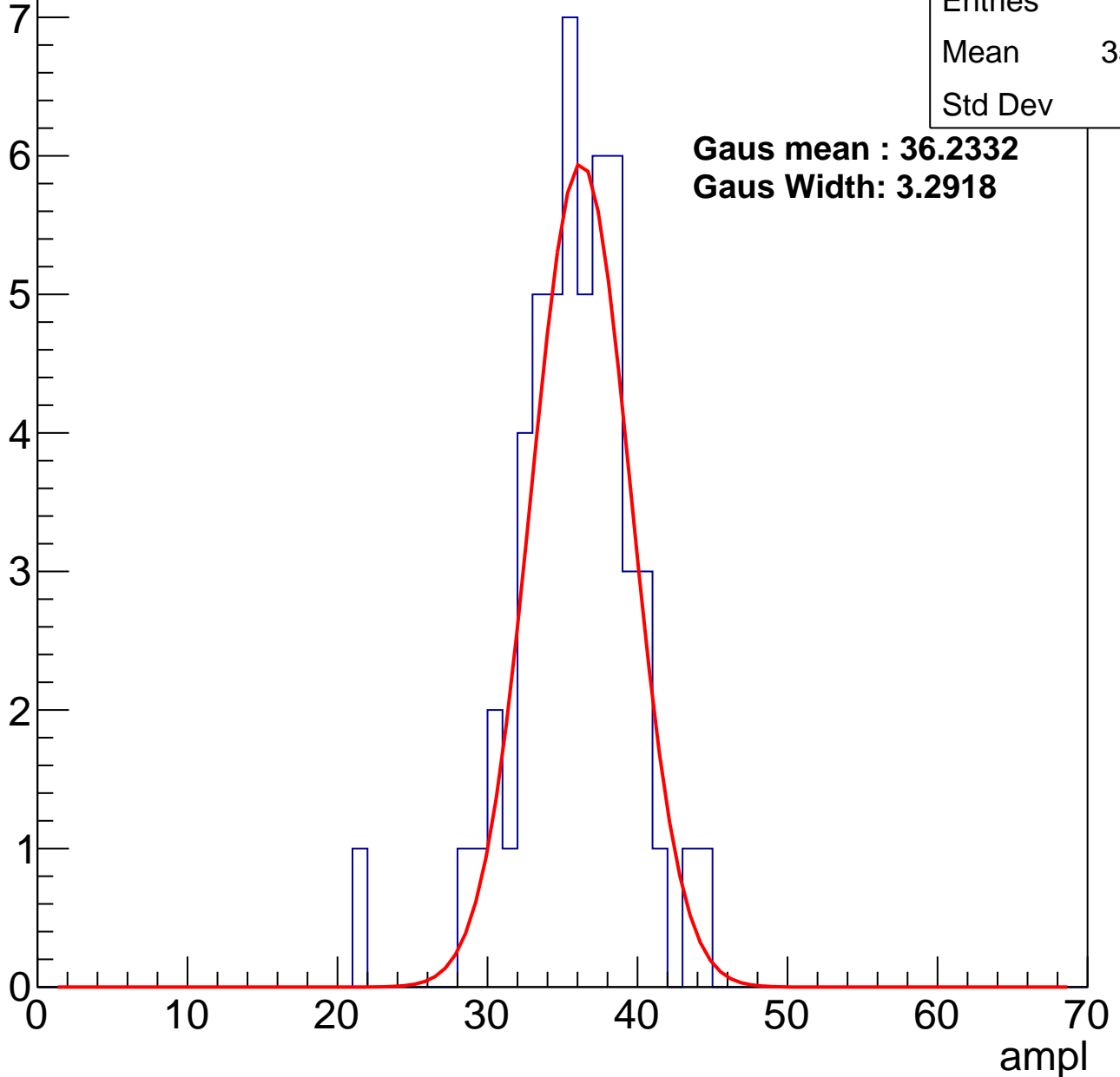
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	35.32
Std Dev	3.86

Gaus mean : 36.2332

Gaus Width: 3.2918



B1L102S, U19-ch96, adc2

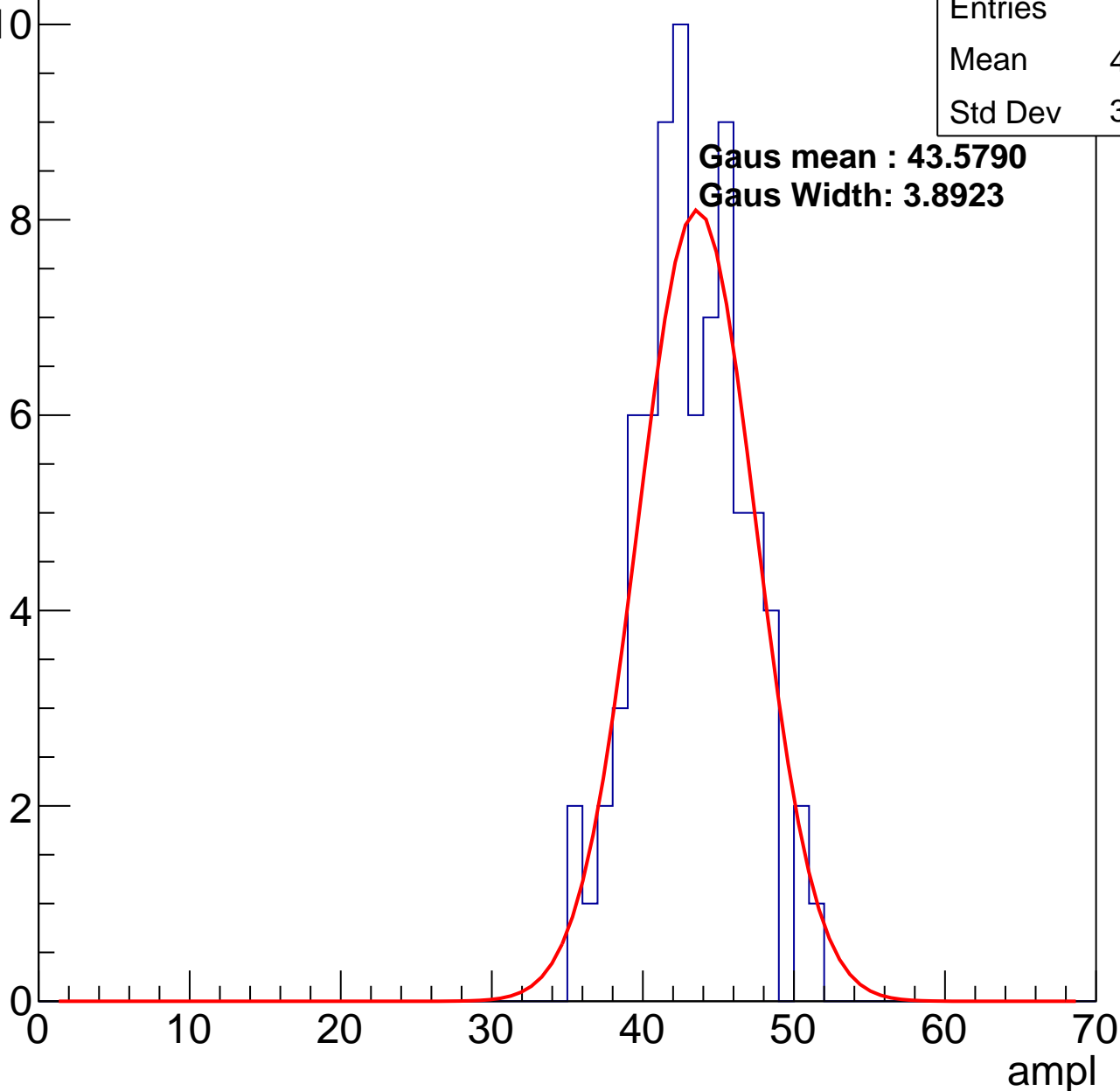
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.77
Std Dev	3.482

Gaus mean : 43.5790

Gaus Width: 3.8923

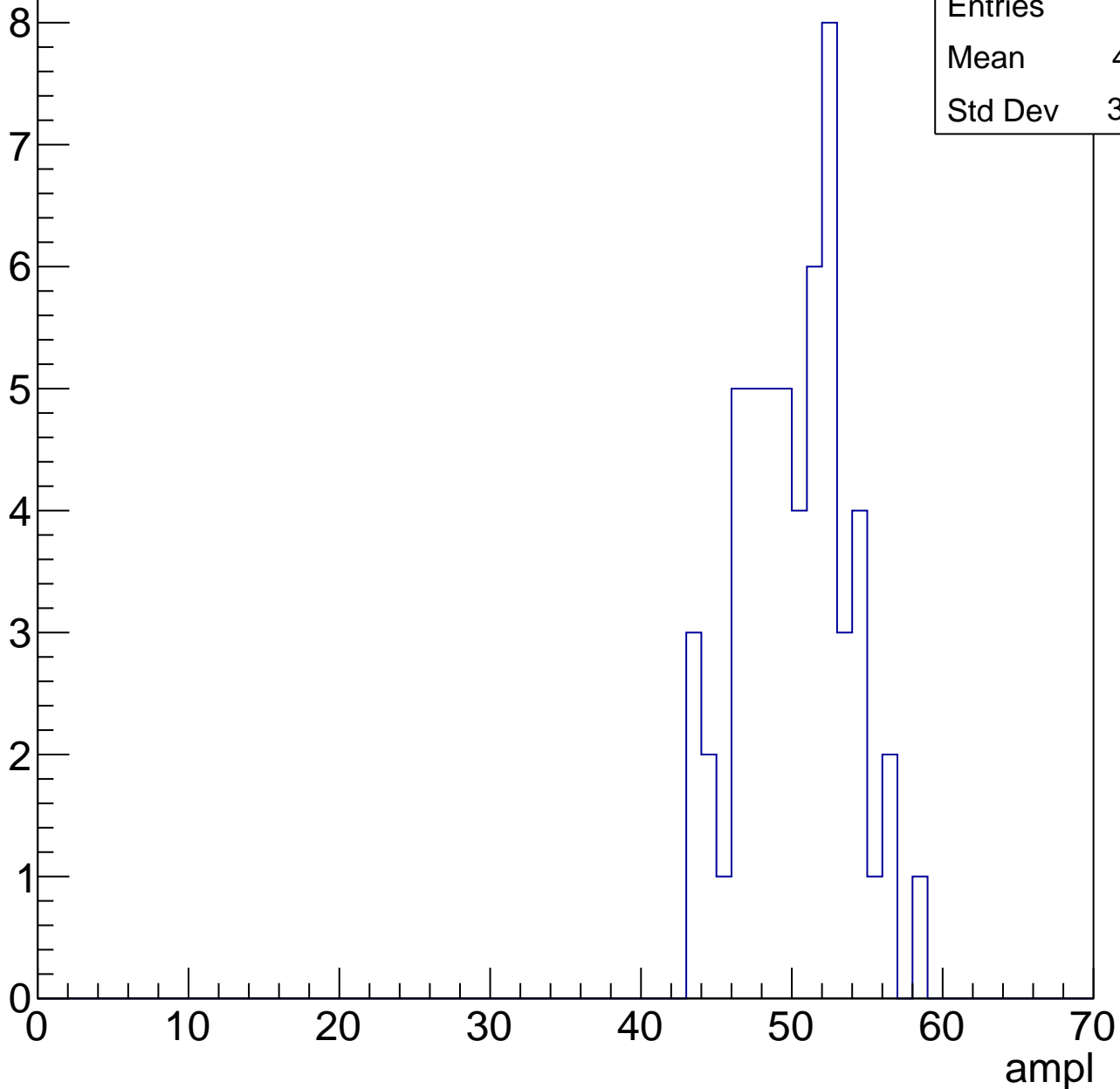


B1L102S, U19-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.71
Std Dev	3.515

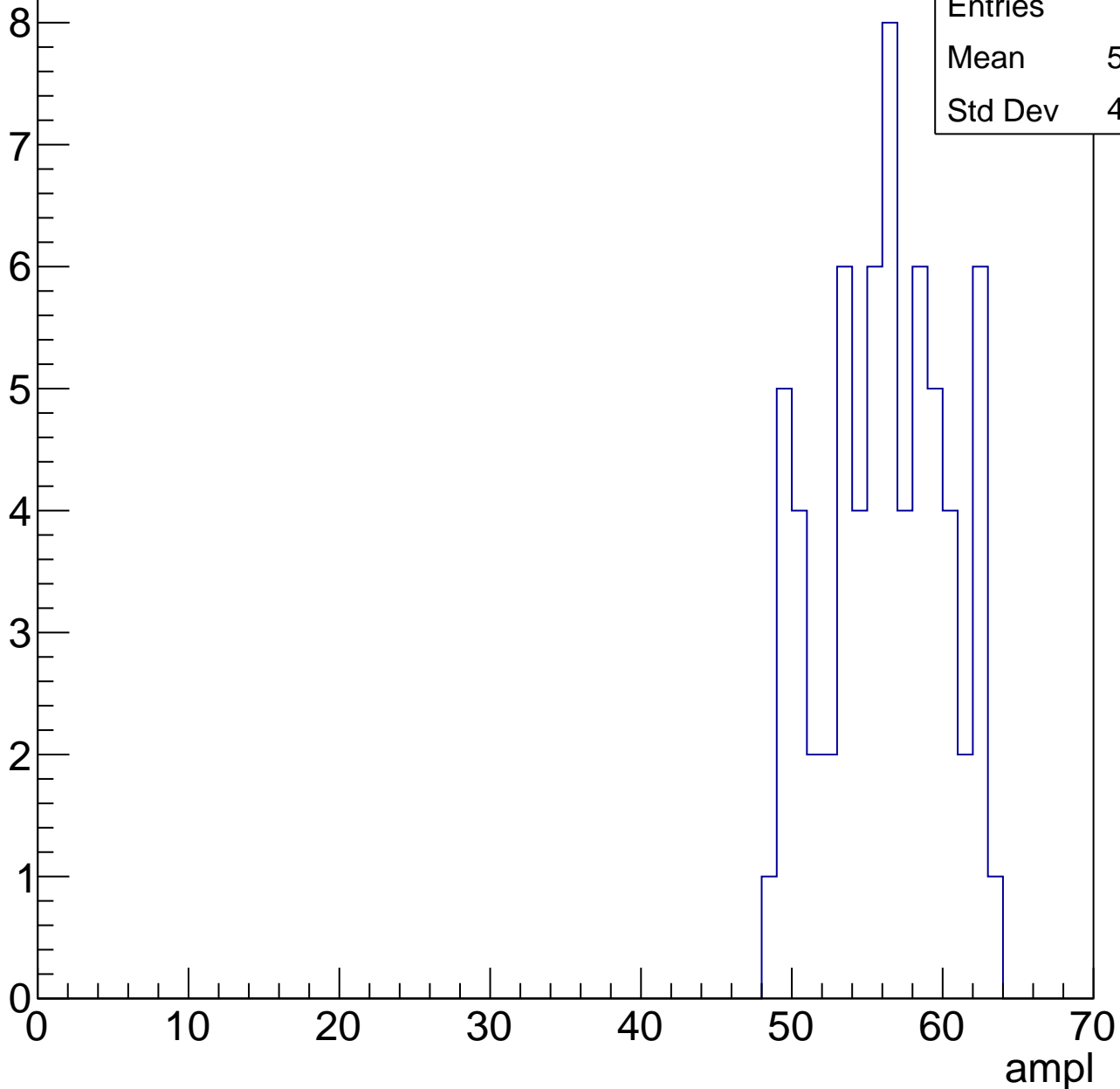


B1L102S, U19-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.74
Std Dev	4.005

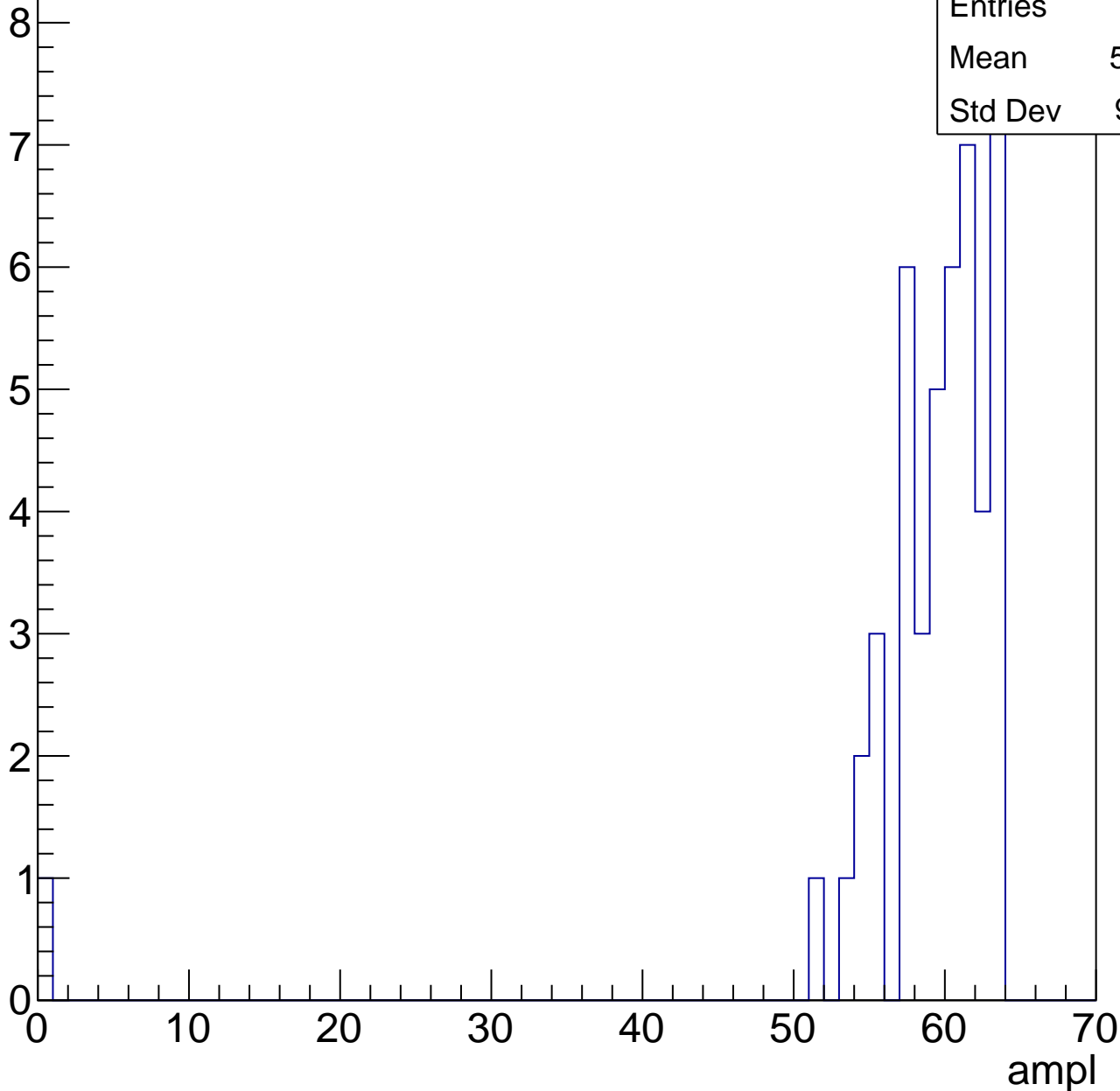


B1L102S, U19-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.02
Std Dev	9.061



B1L102S, U19-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch97, adc0

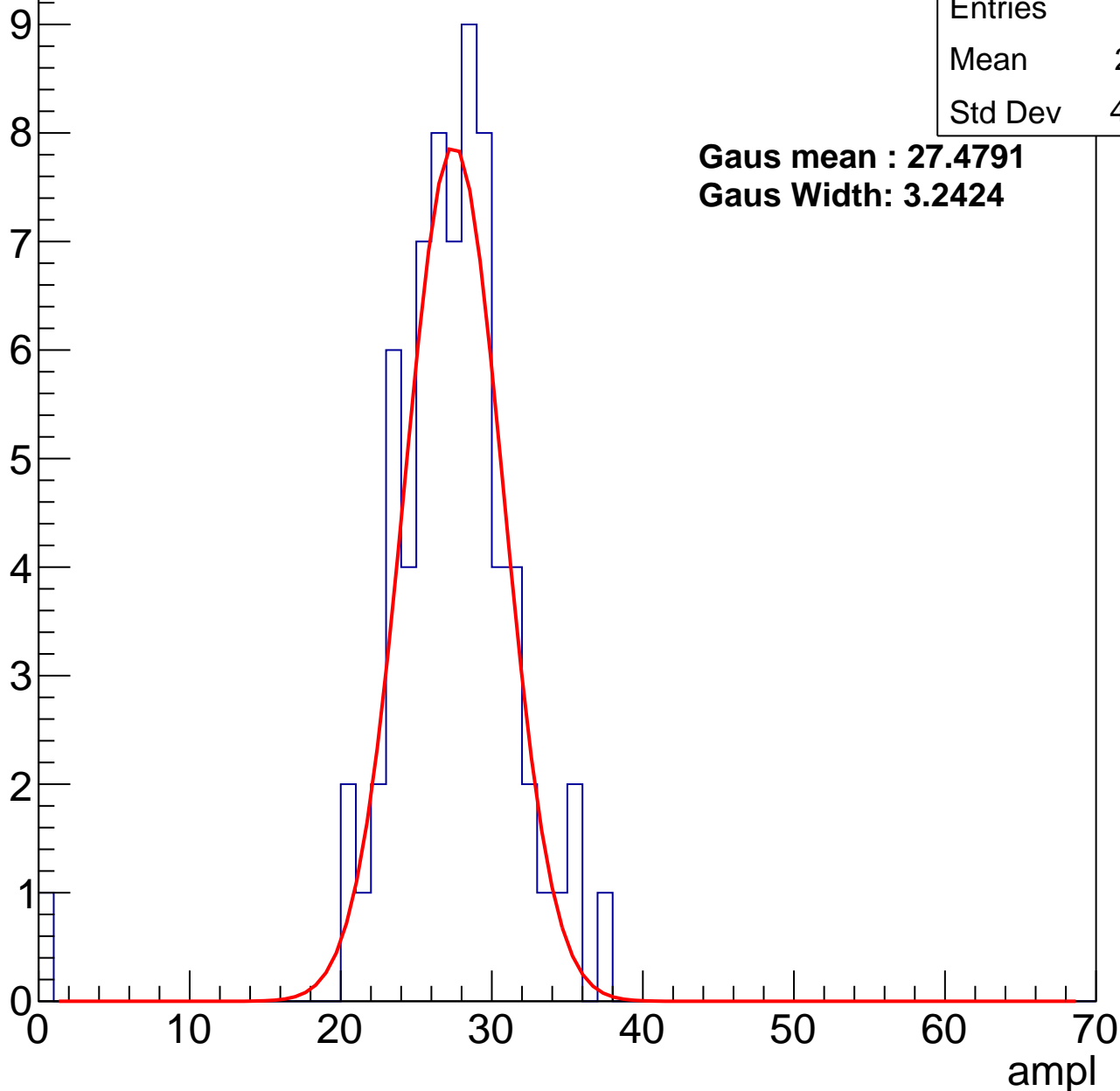
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	26.81
Std Dev	4.755

Gaus mean : 27.4791

Gaus Width: 3.2424



B1L102S, U19-ch97, adc1

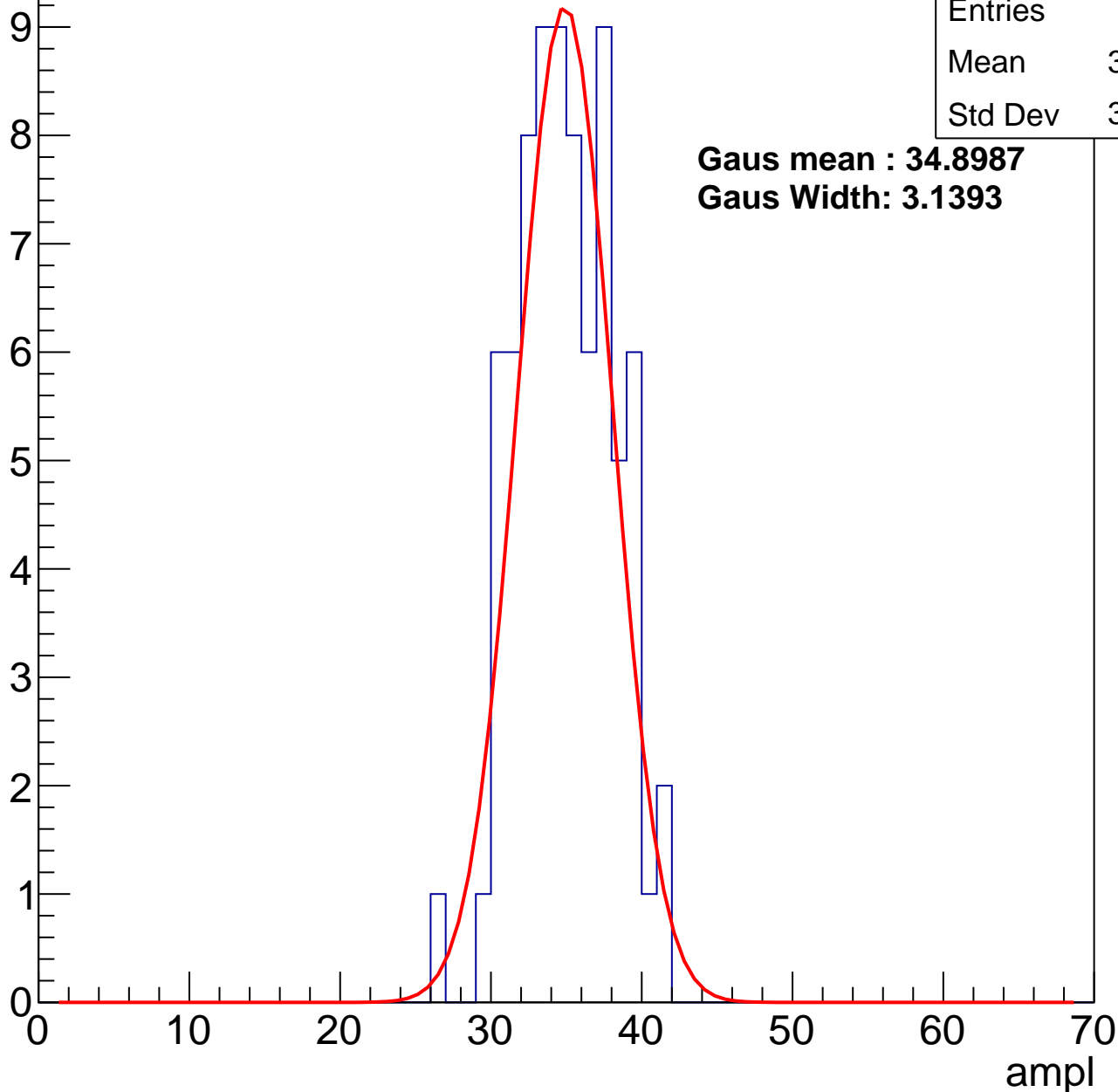
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.48
Std Dev	3.098

Gaus mean : 34.8987

Gaus Width: 3.1393



B1L102S, U19-ch97, adc2

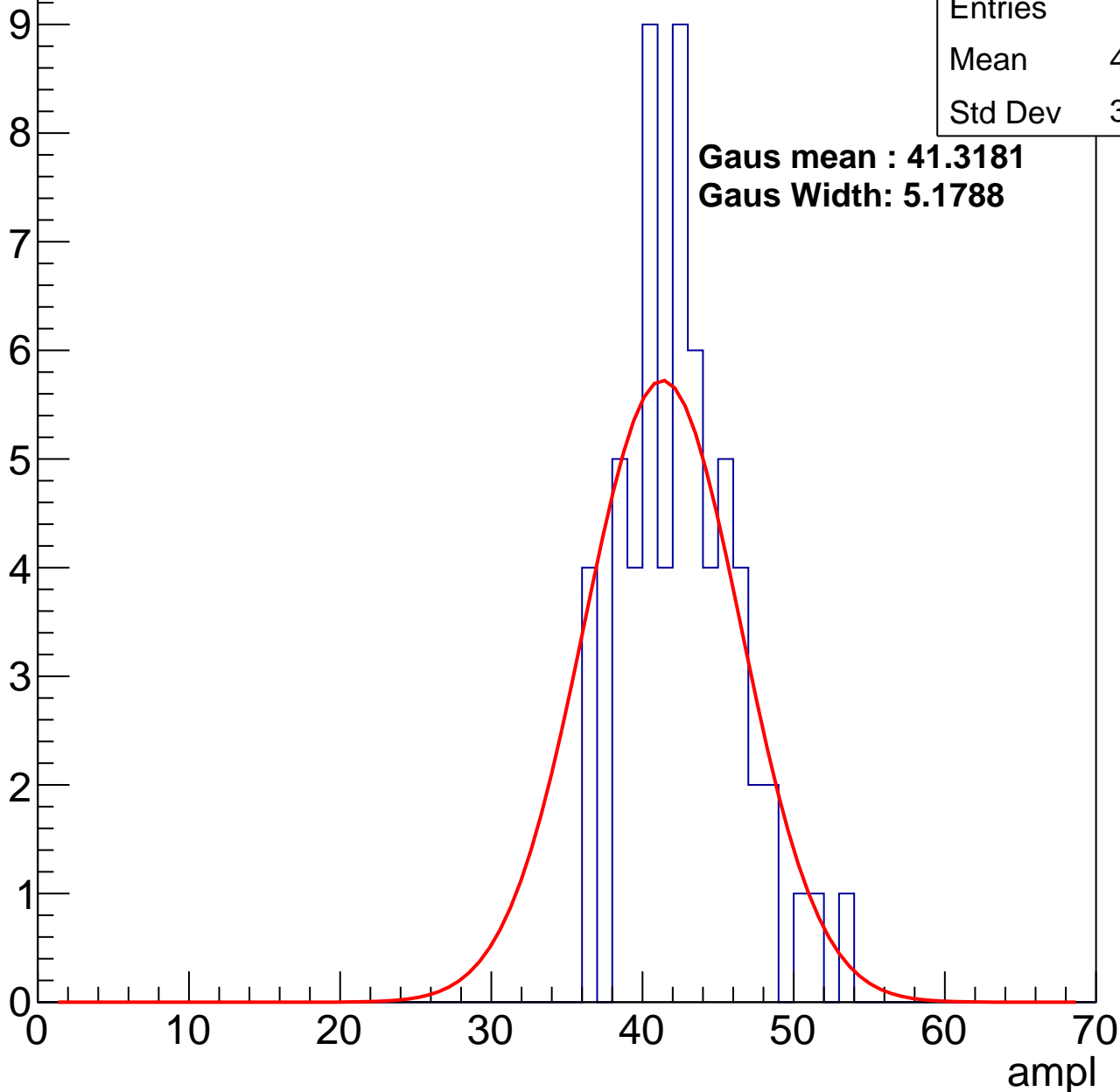
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.28
Std Dev	3.654

Gaus mean : 41.3181

Gaus Width: 5.1788

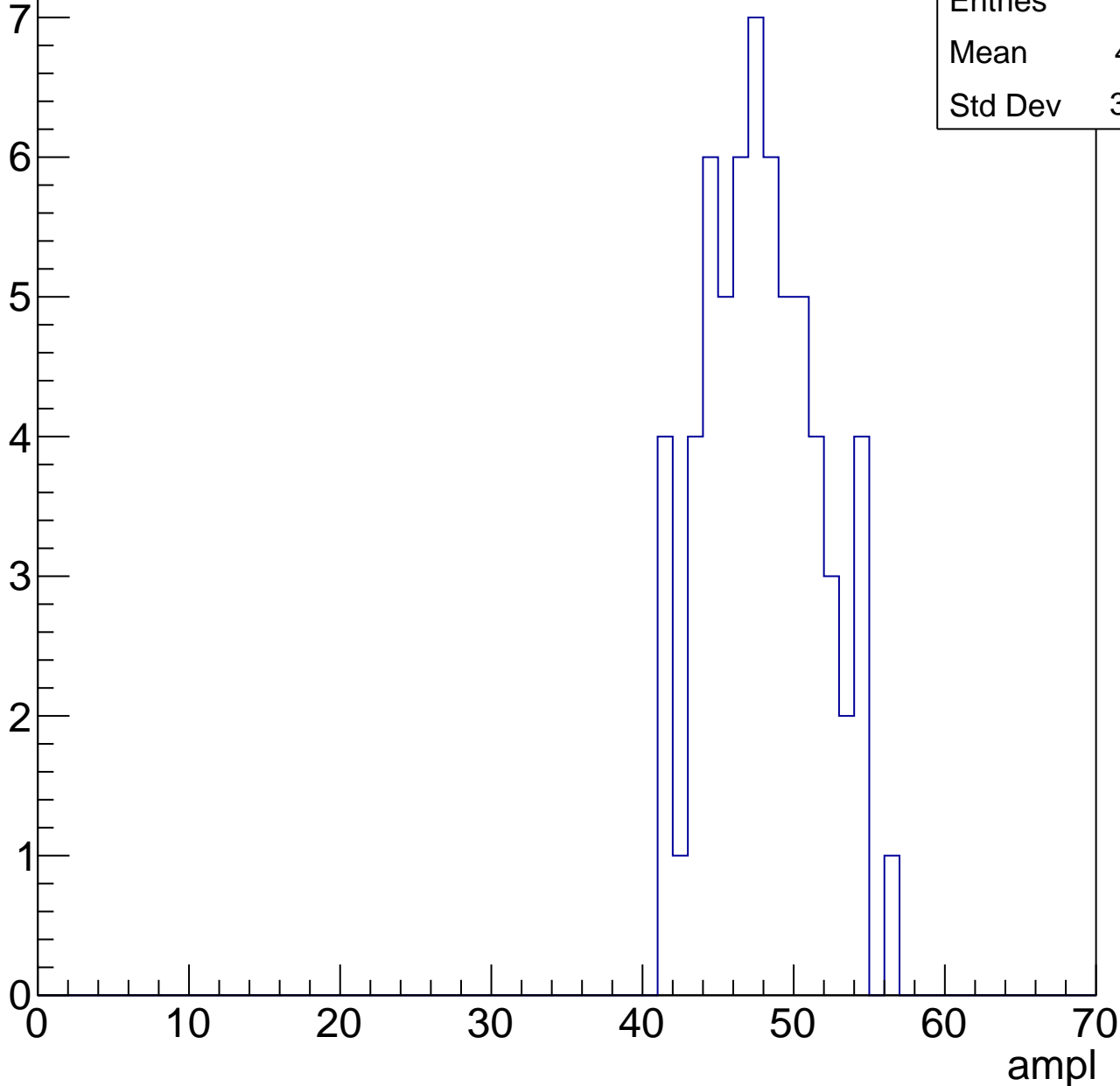


B1L102S, U19-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.51
Std Dev	3.686

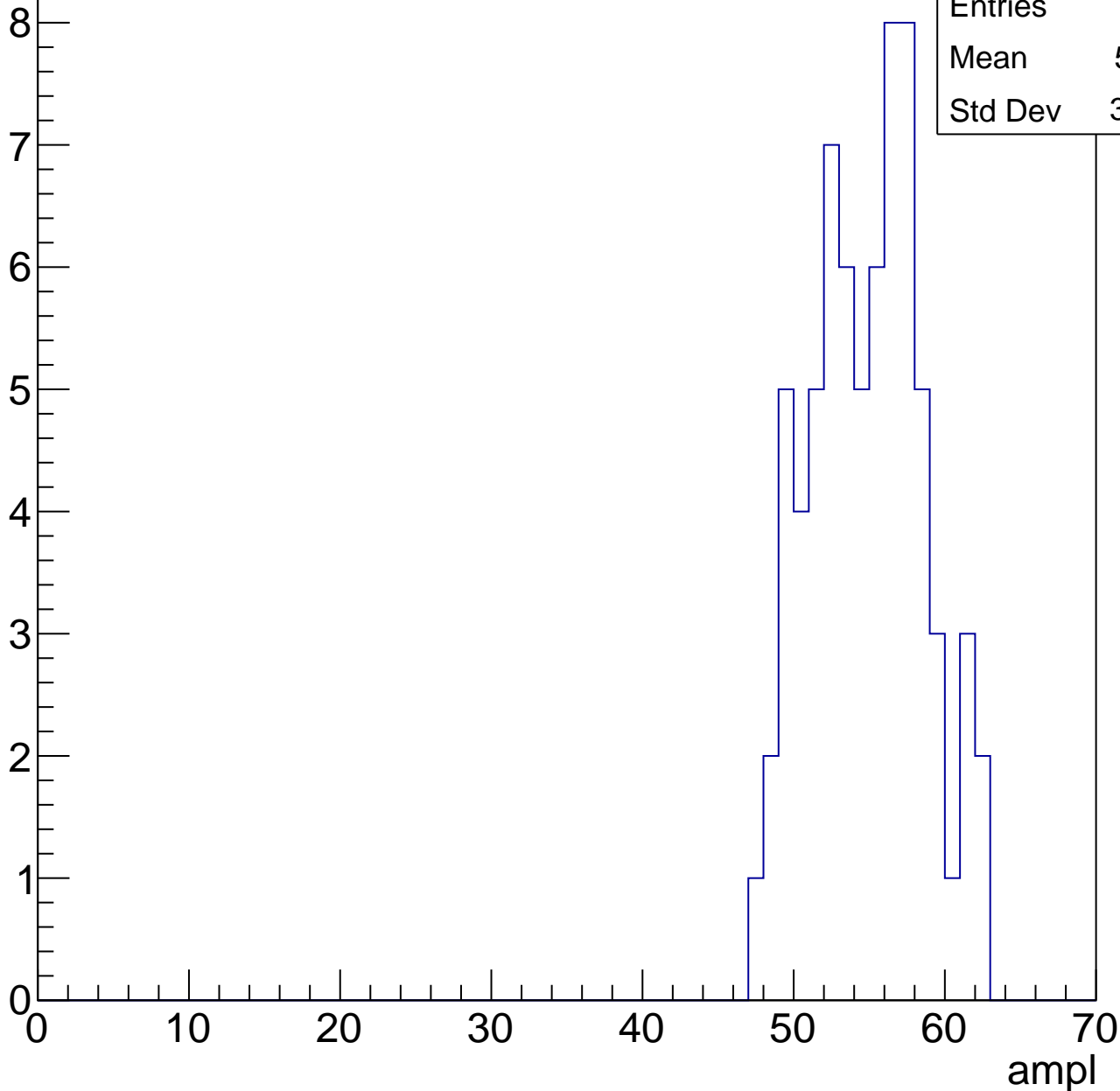


B1L102S, U19-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.41
Std Dev	3.664

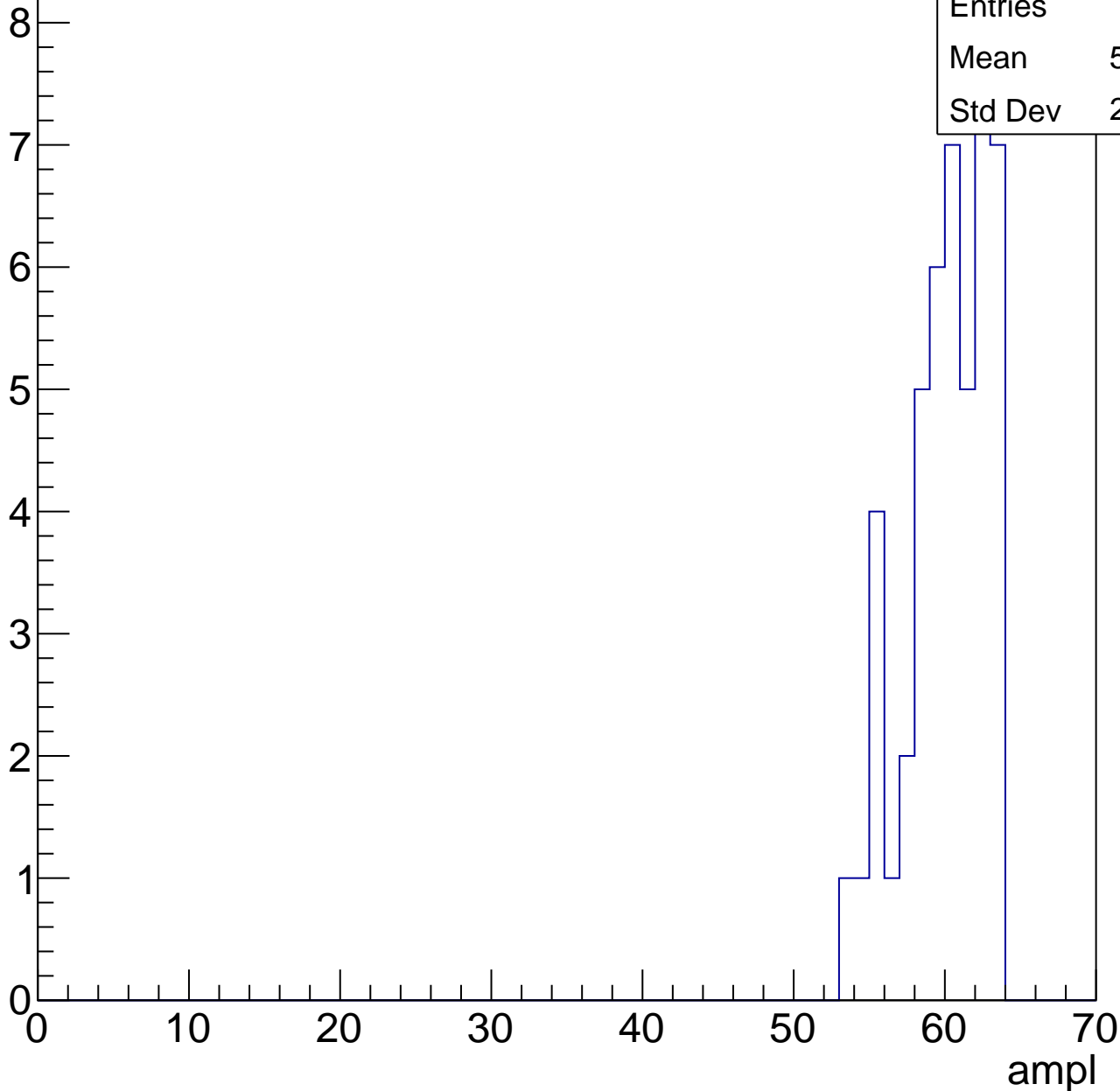


B1L102S, U19-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

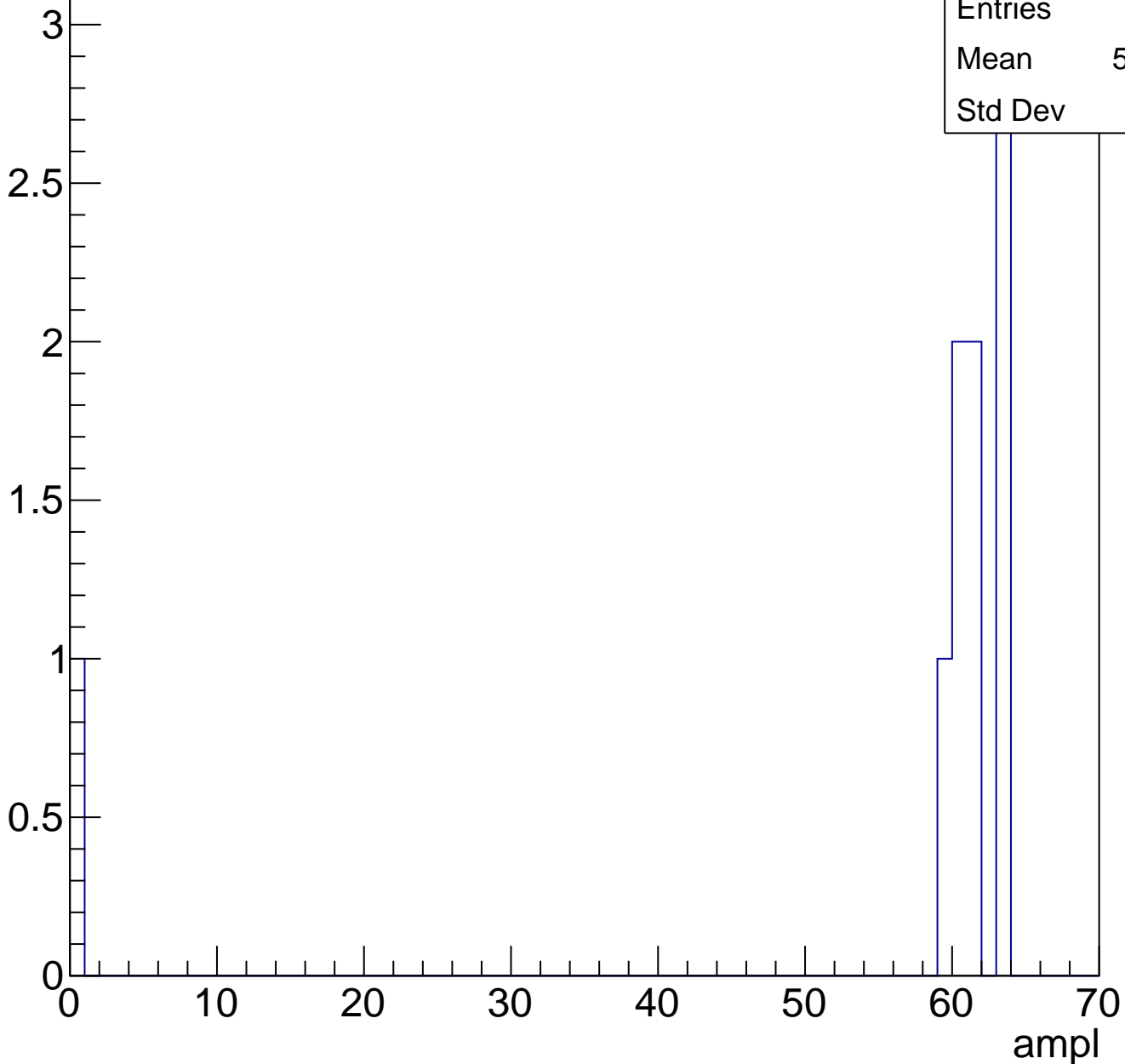
Entries	47
Mean	59.64
Std Dev	2.685



B1L102S, U19-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U19-ch98, adc0

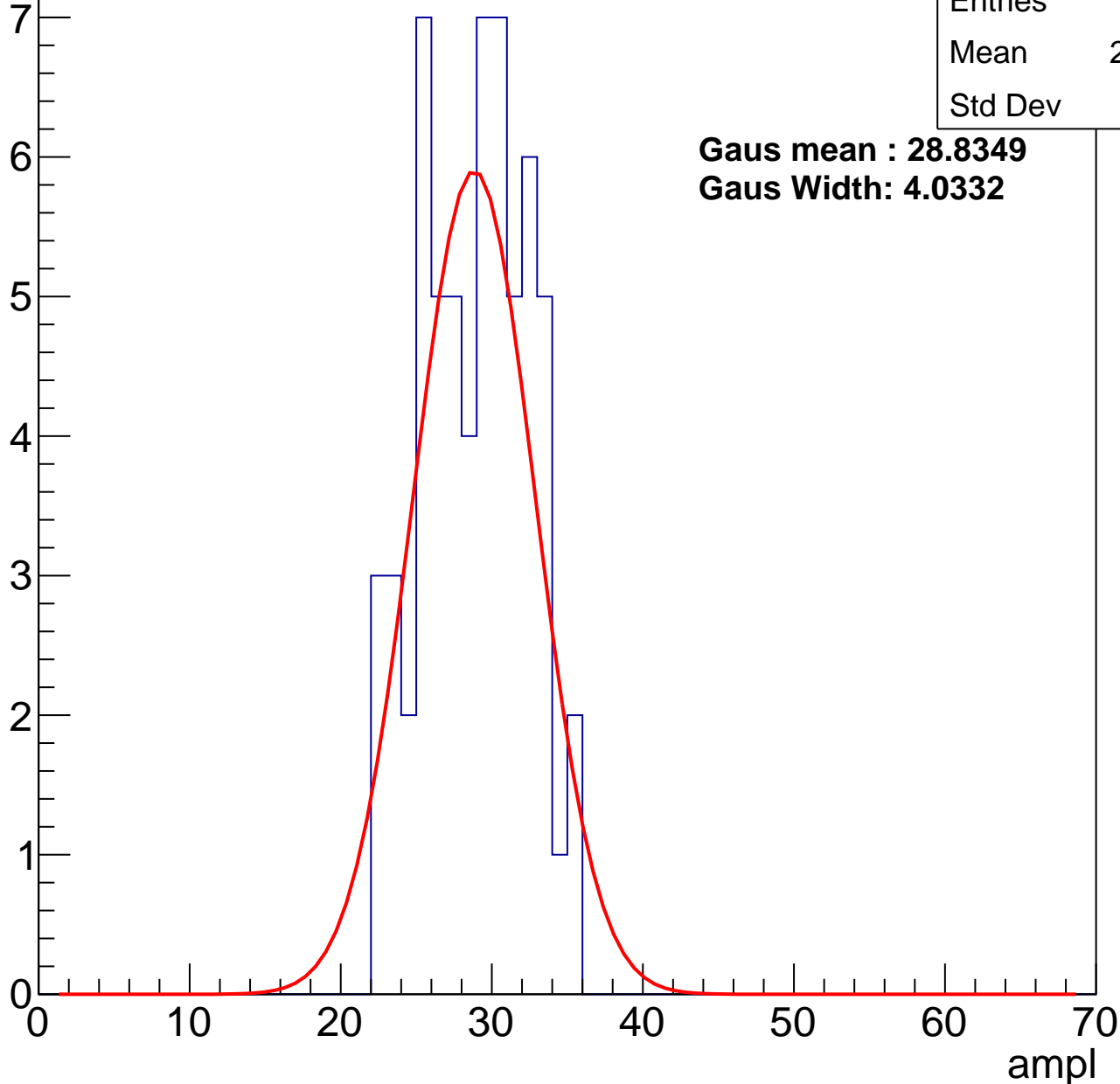
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.45
Std Dev	3.42

Gaus mean : 28.8349

Gaus Width: 4.0332



B1L102S, U19-ch98, adc1

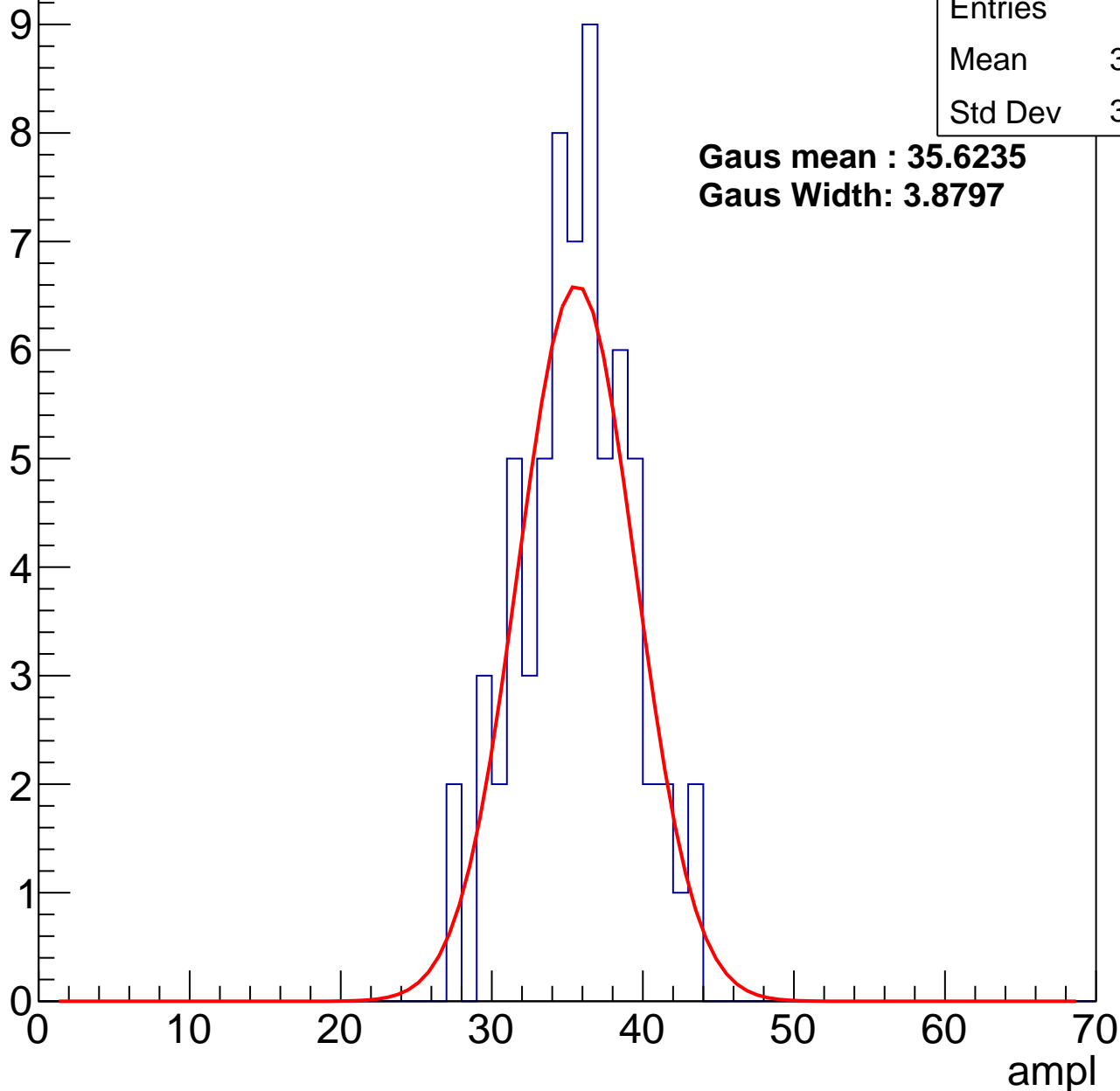
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.16
Std Dev	3.627

Gaus mean : 35.6235

Gaus Width: 3.8797



B1L102S, U19-ch98, adc2

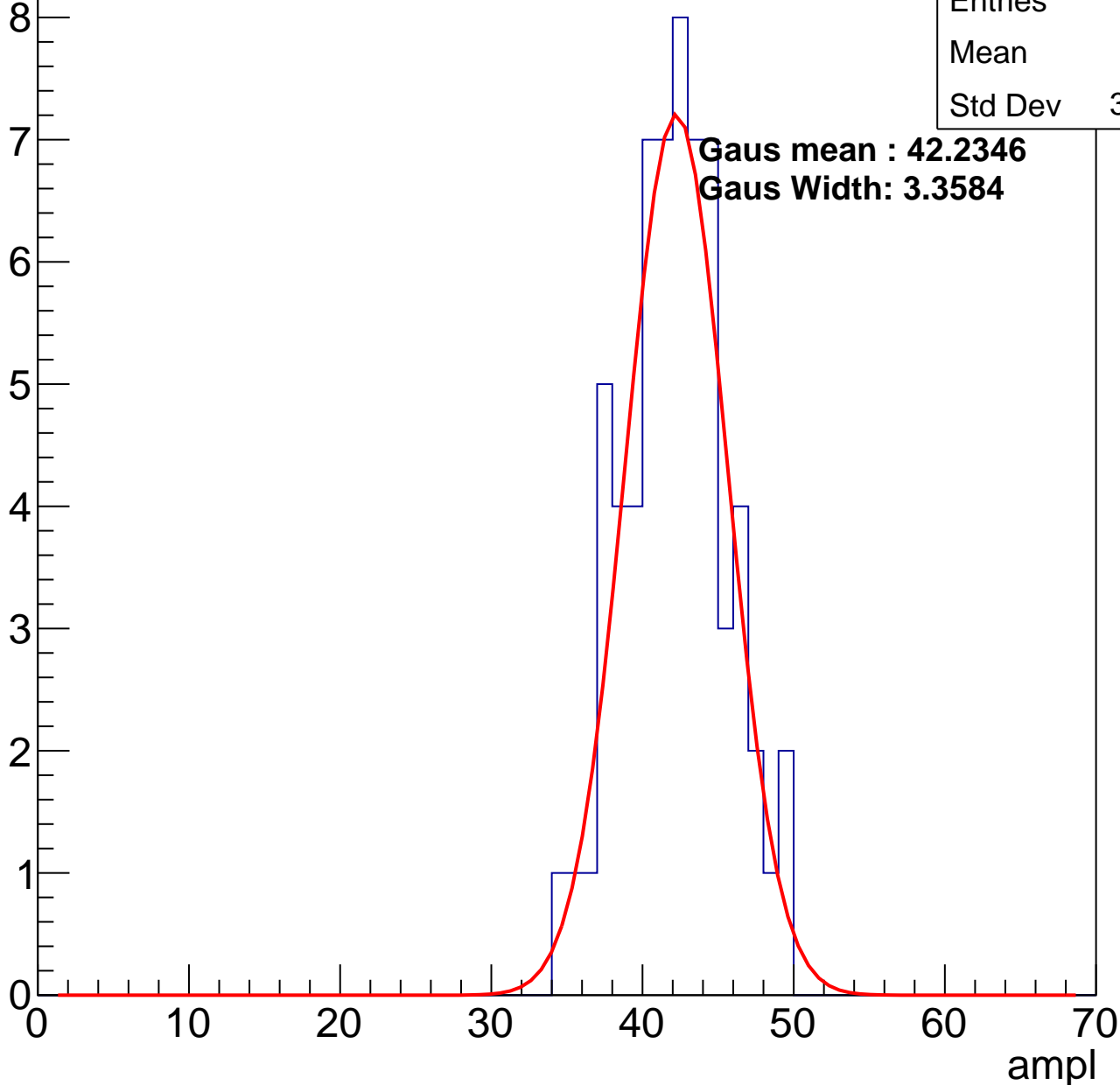
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.7
Std Dev	3.339

Gaus mean : 42.2346

Gaus Width: 3.3584

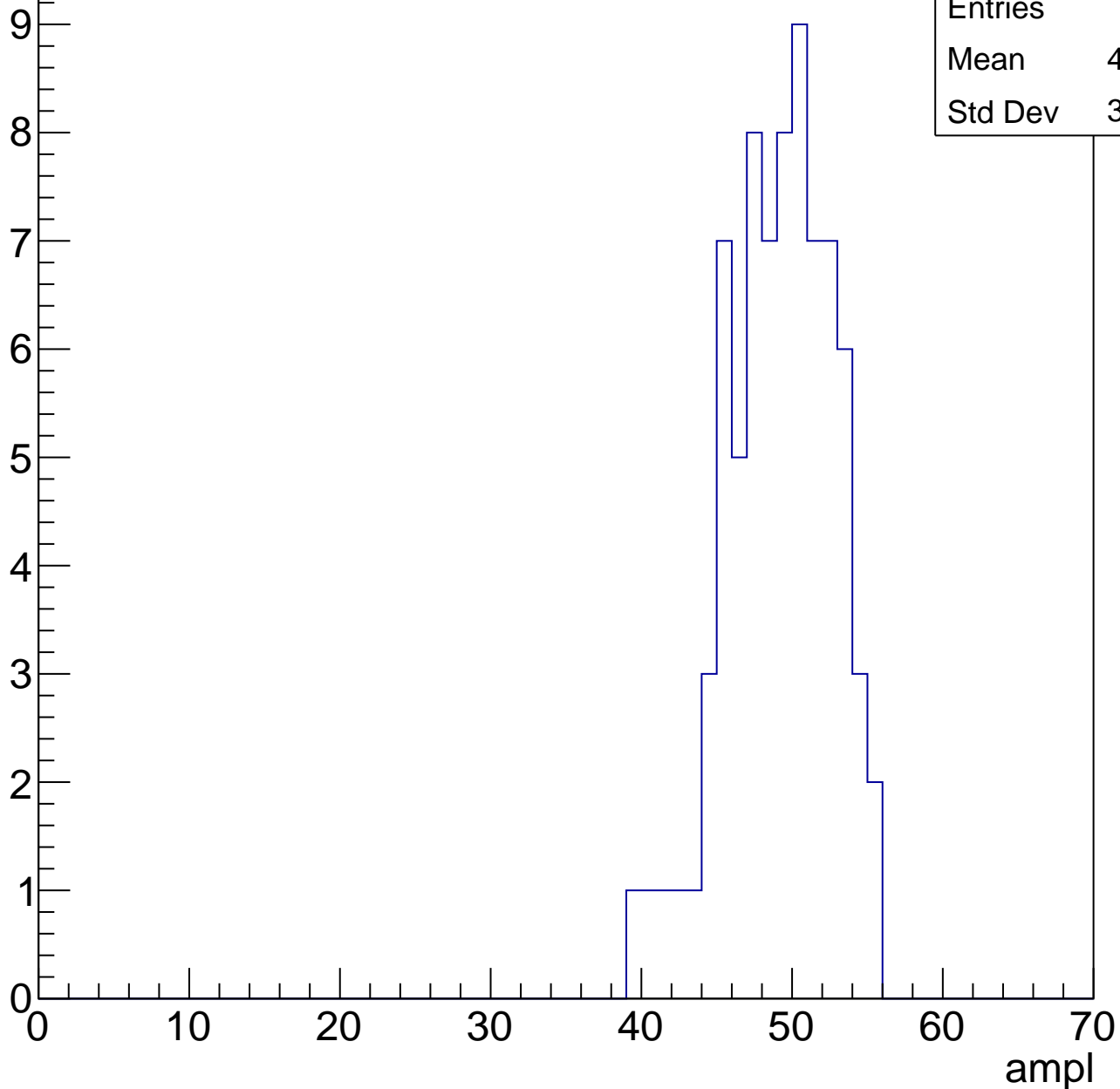


B1L102S, U19-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.66
Std Dev	3.485

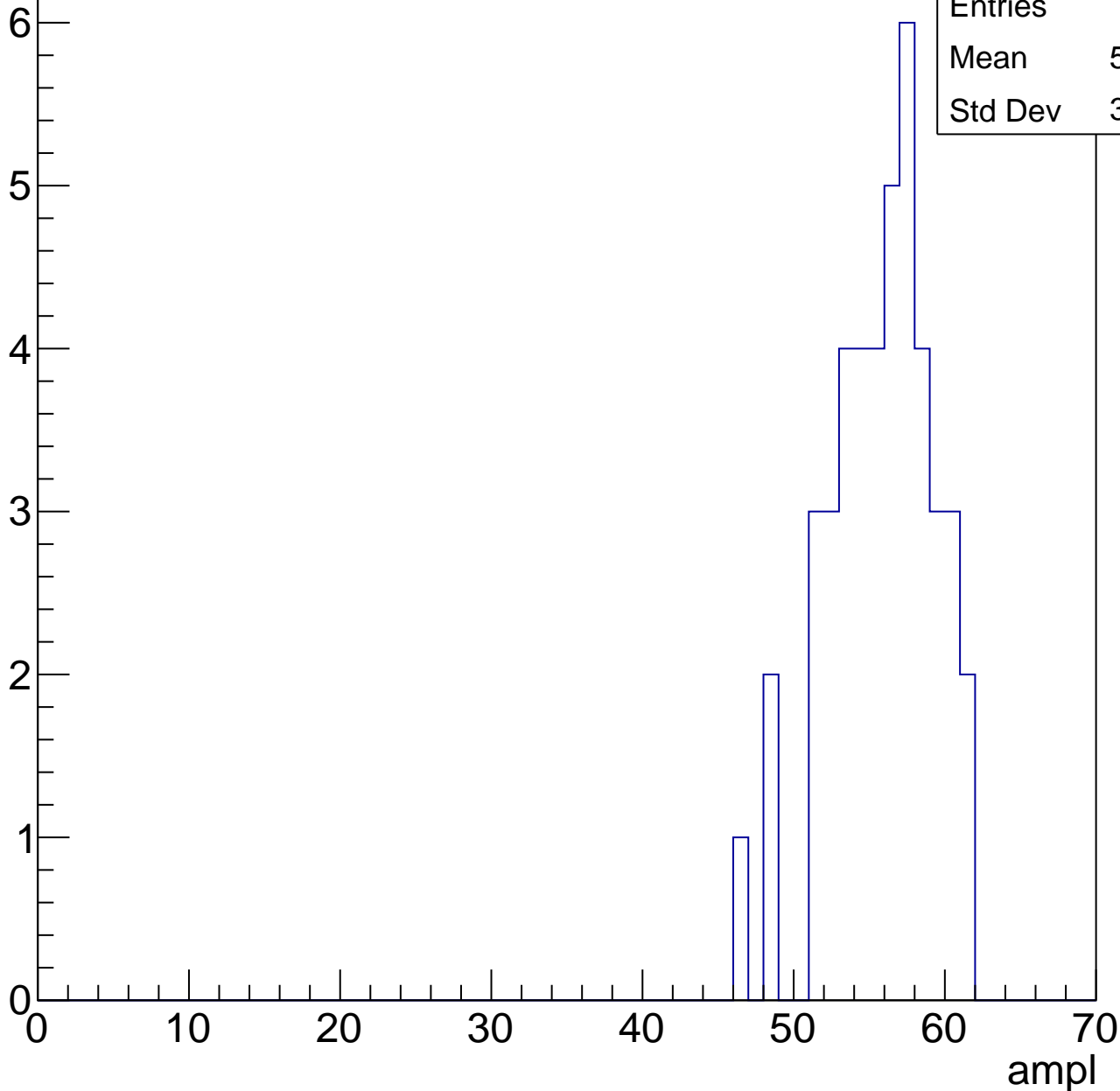


B1L102S, U19-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	55.27
Std Dev	3.473

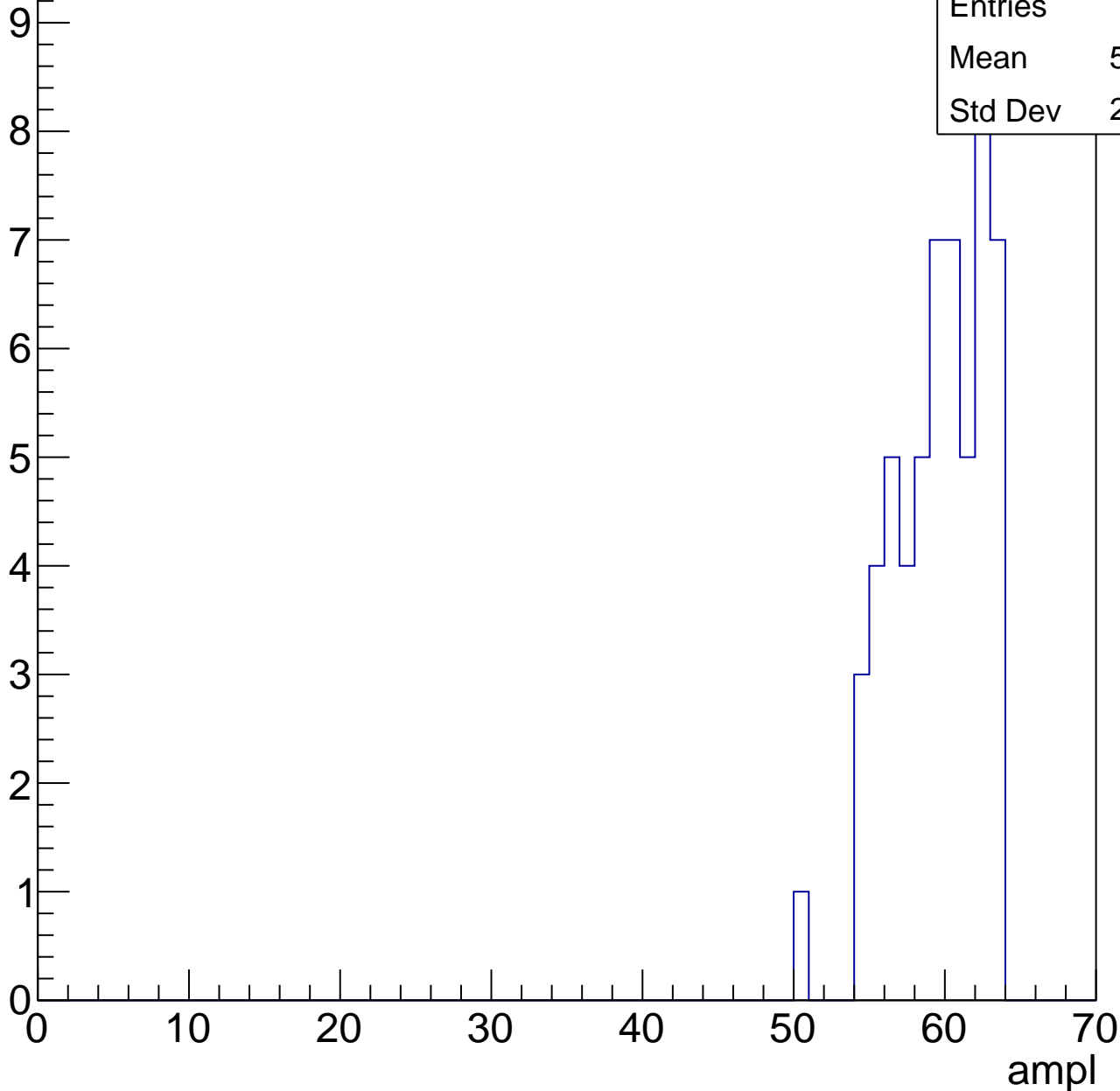


B1L102S, U19-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	59.07
Std Dev	2.979



B1L102S, U19-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

ampl

Entries	9
Mean	54.67
Std Dev	19.38

0 10 20 30 40 50 60 70

B1L102S, U19-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch99, adc0

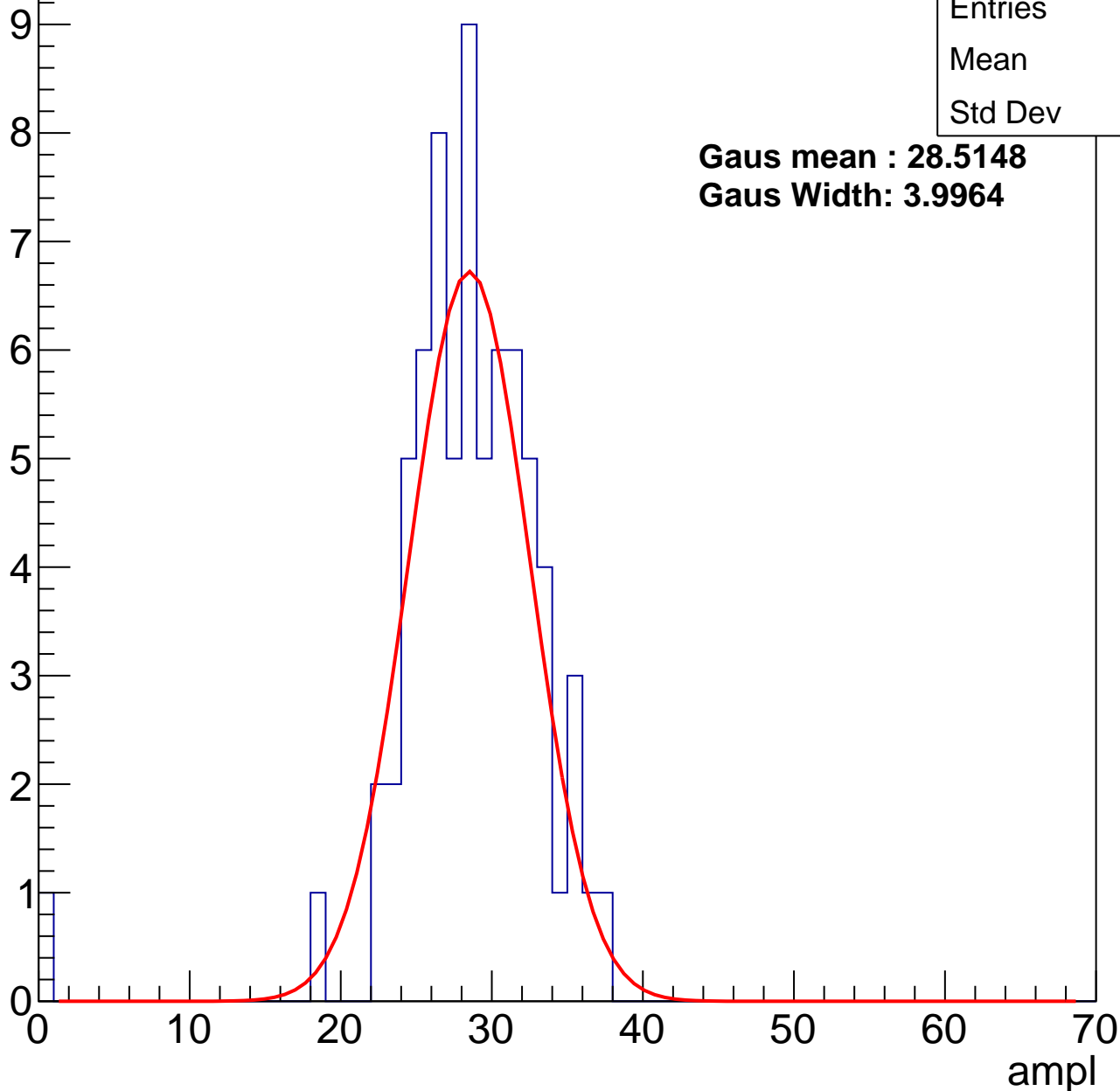
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28
Std Dev	4.99

Gaus mean : 28.5148

Gaus Width: 3.9964



B1L102S, U19-ch99, adc1

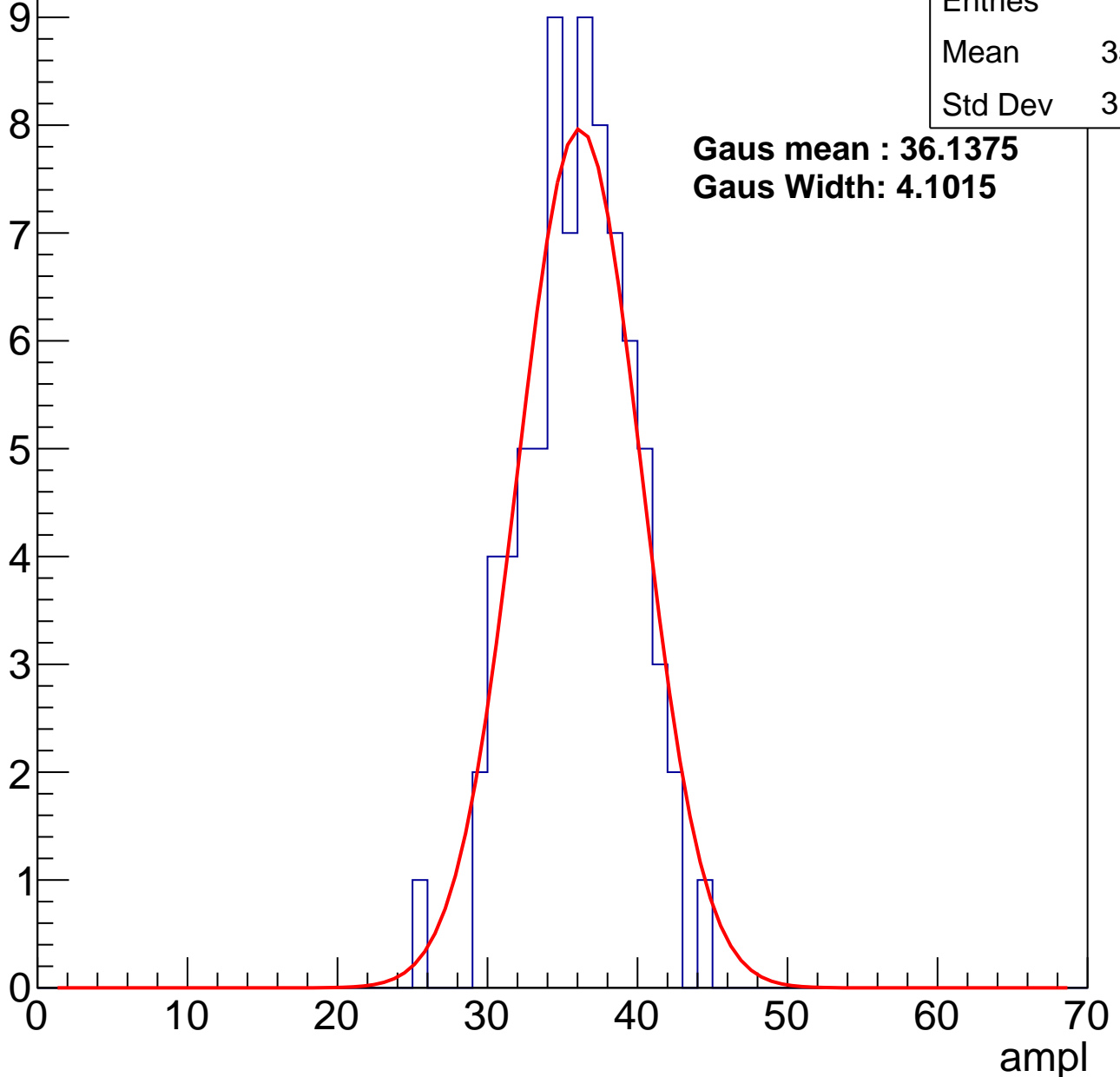
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.56
Std Dev	3.576

Gaus mean : 36.1375

Gaus Width: 4.1015



B1L102S, U19-ch99, adc2

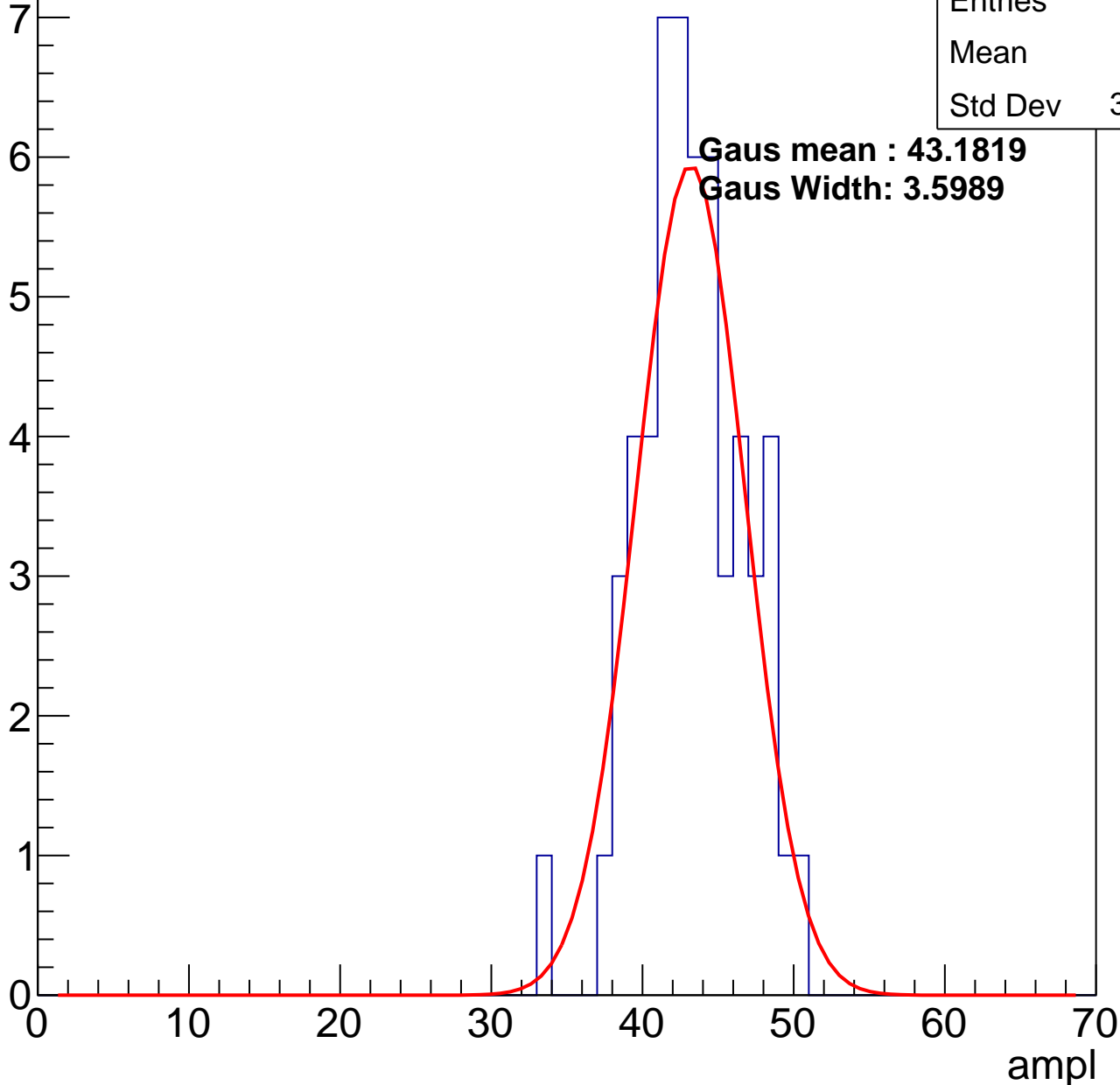
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.8
Std Dev	3.387

Gaus mean : 43.1819

Gaus Width: 3.5989

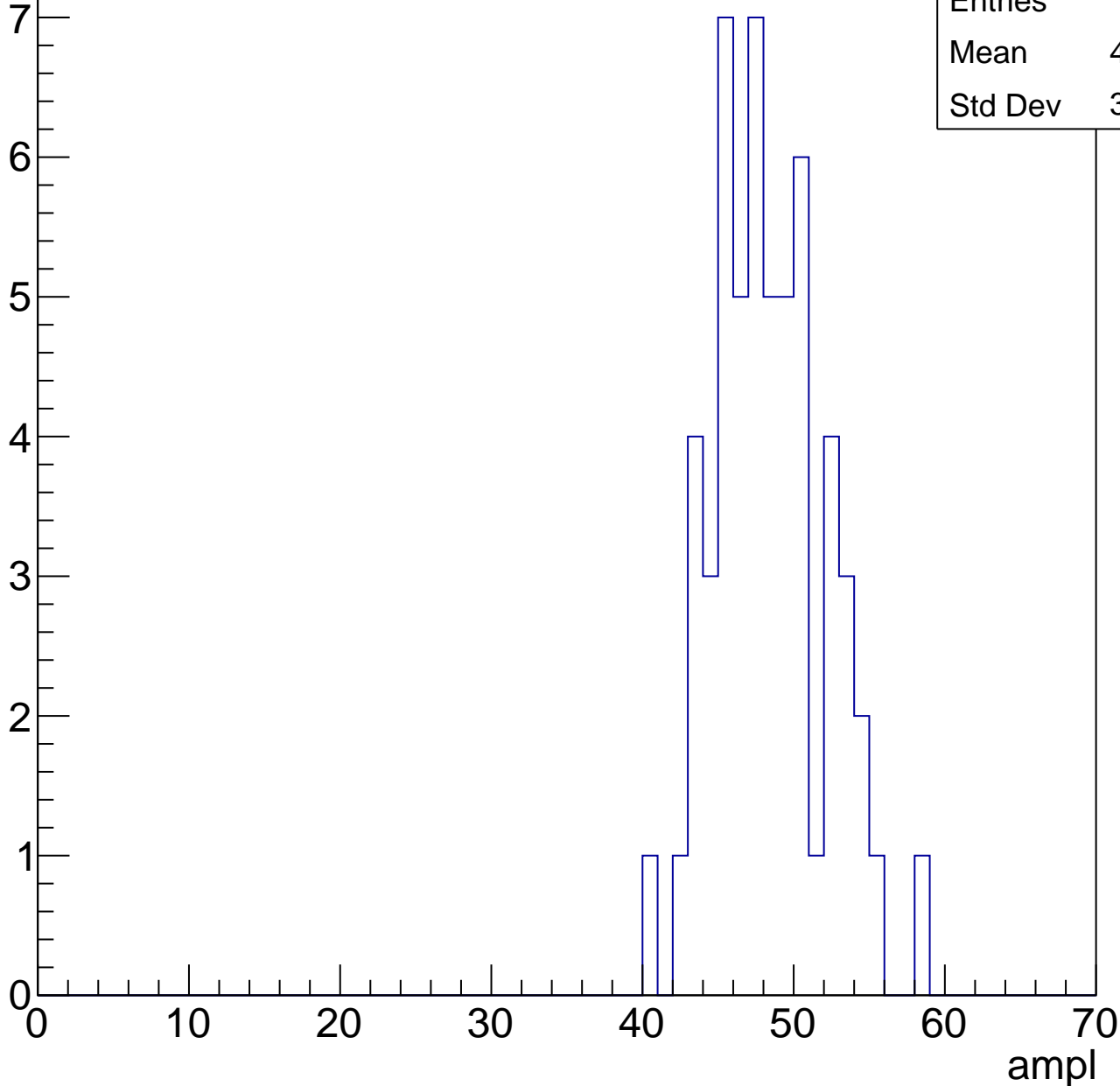


B1L102S, U19-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.93
Std Dev	3.625

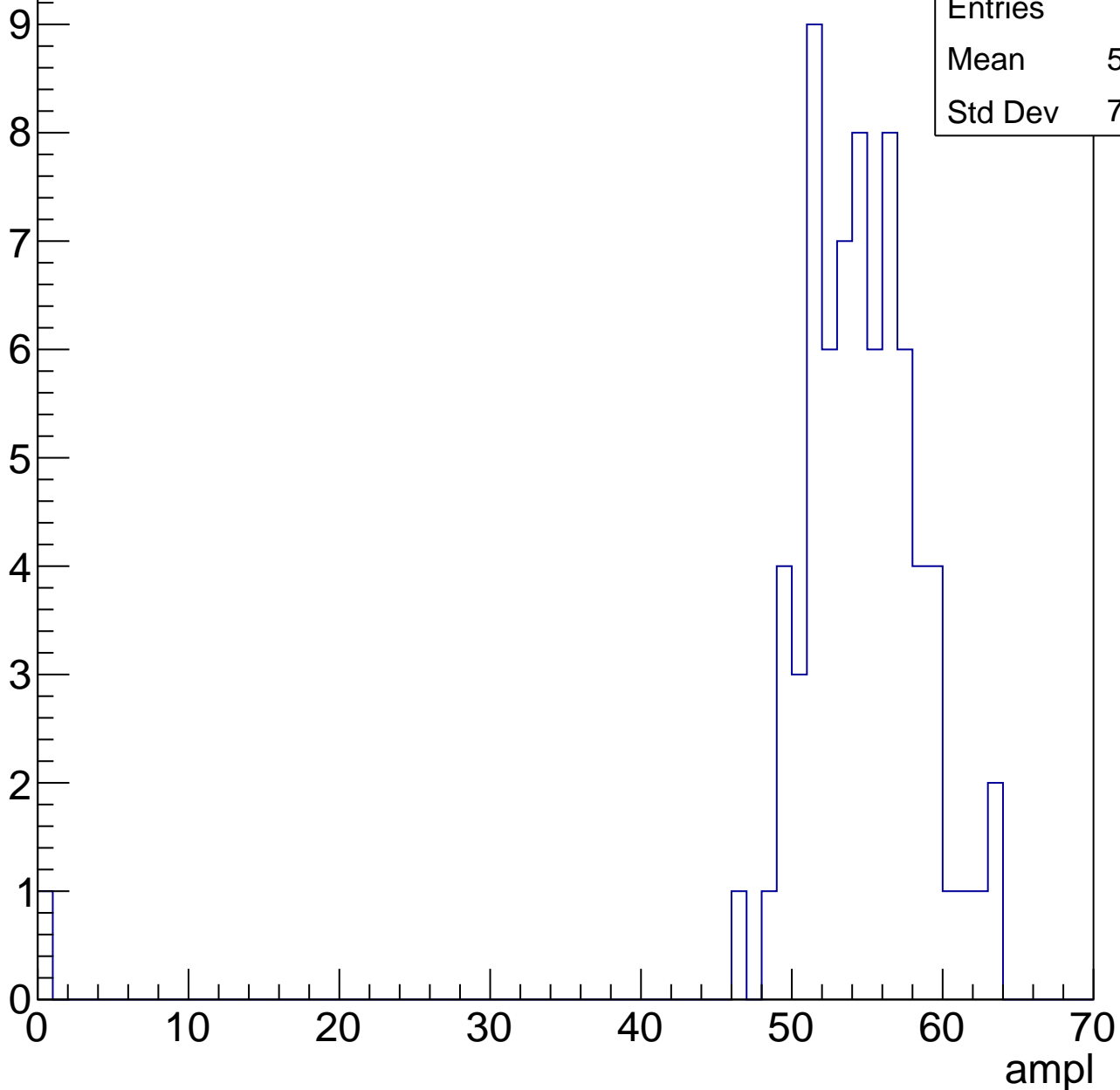


B1L102S, U19-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	53.58
Std Dev	7.243

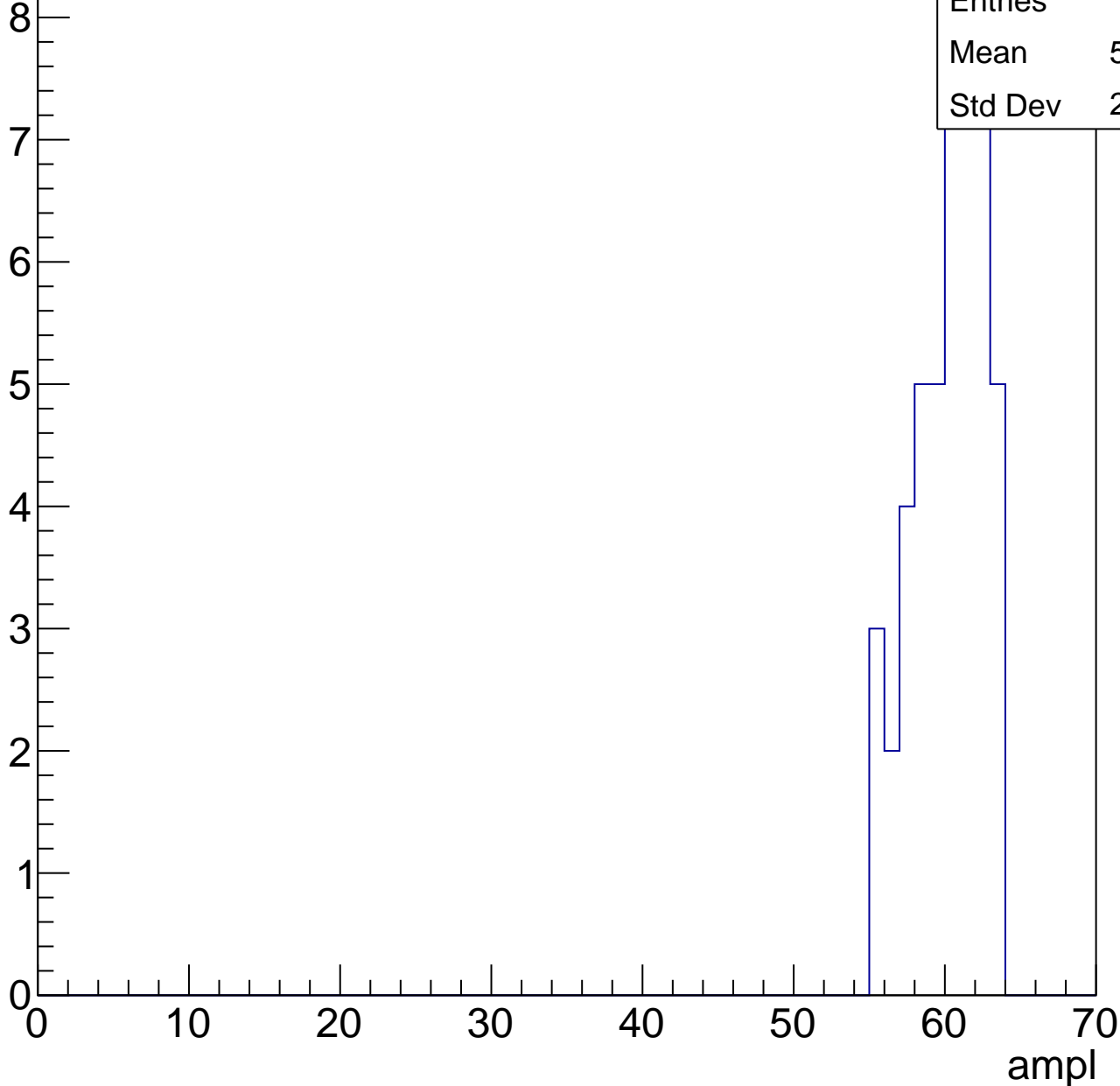


B1L102S, U19-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

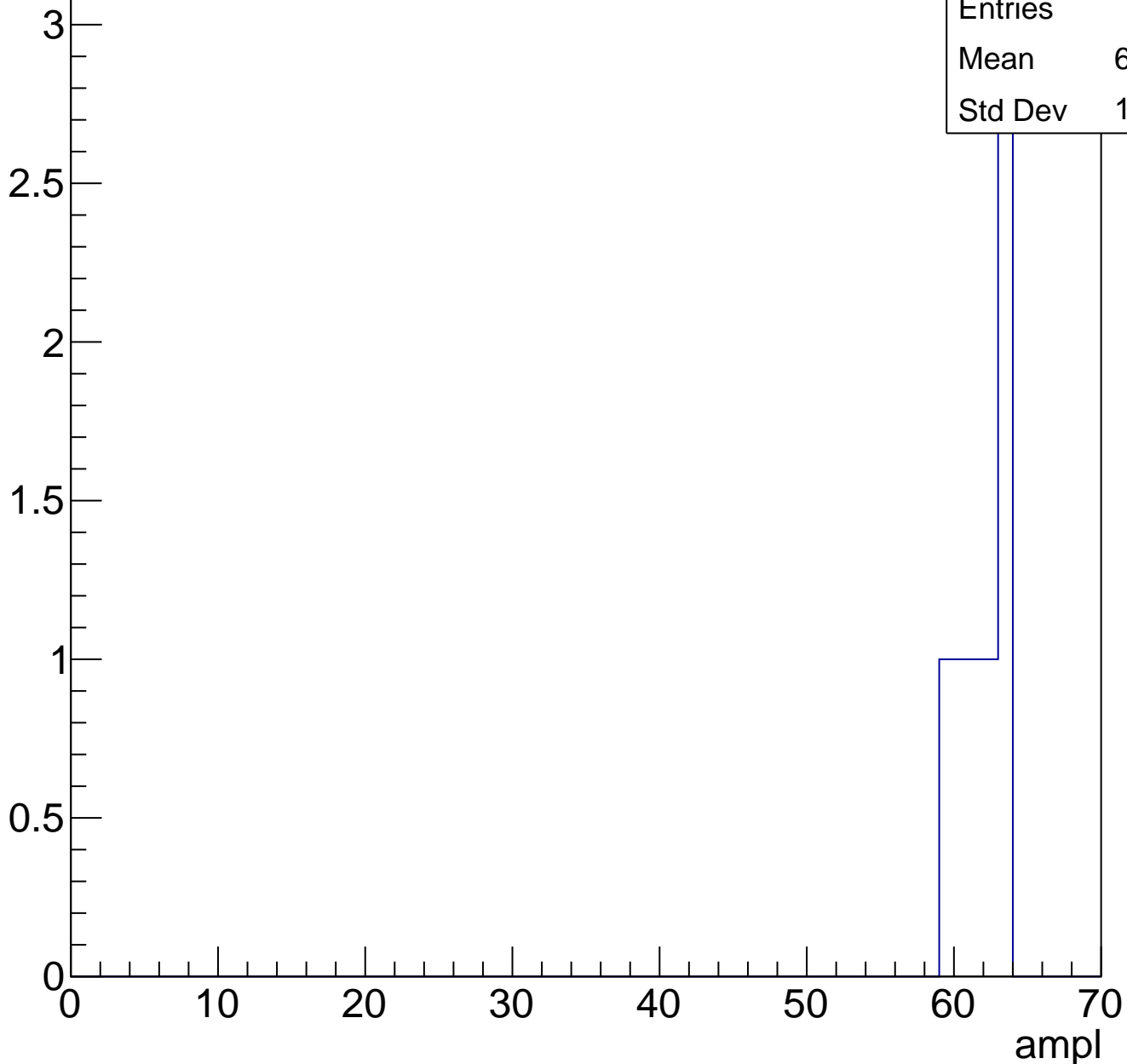
Entries	48
Mean	59.77
Std Dev	2.284



B1L102S, U19-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch100, adc0

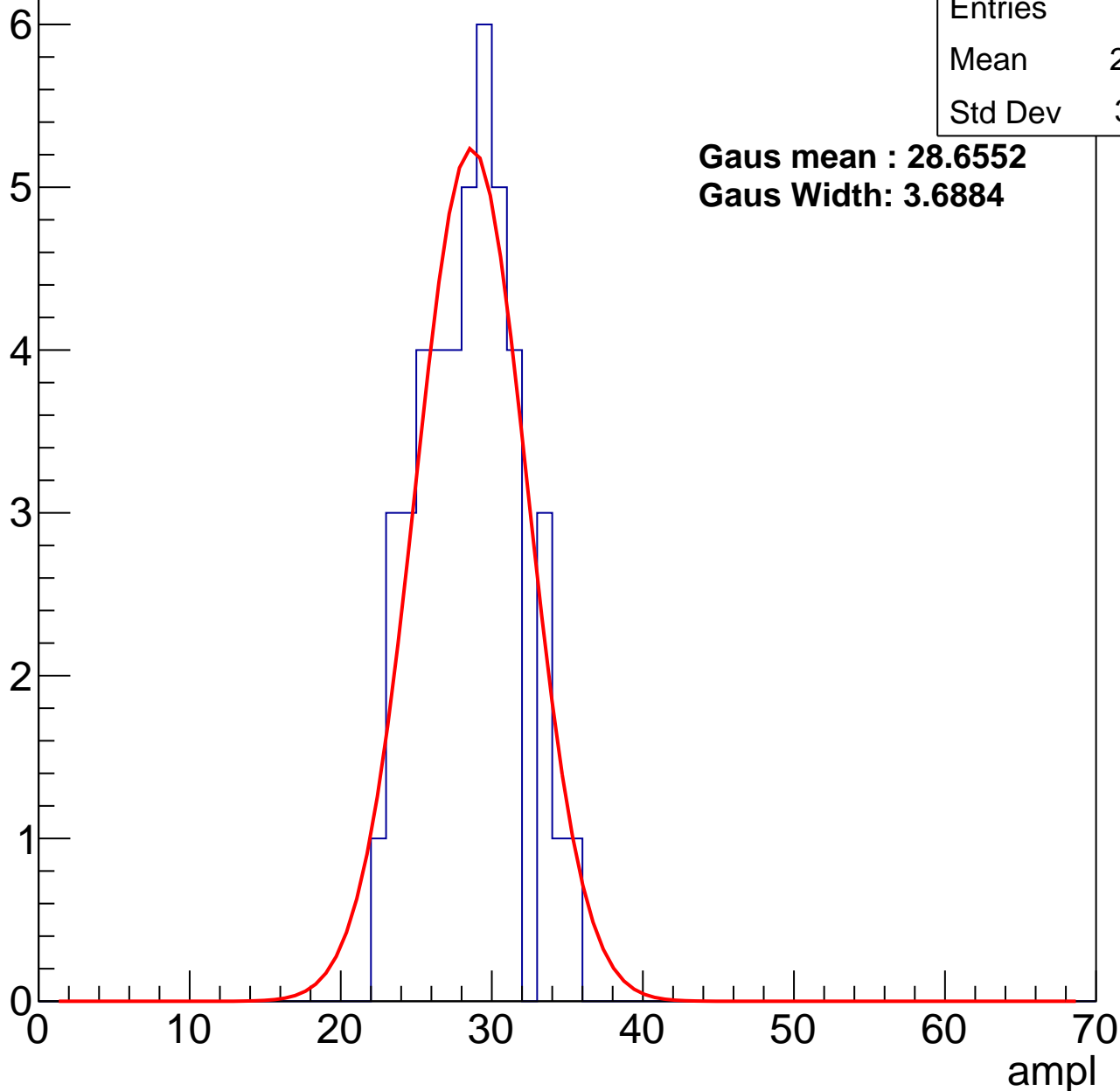
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	27.98
Std Dev	3.151

Gaus mean : 28.6552

Gaus Width: 3.6884



B1L102S, U19-ch100, adc1

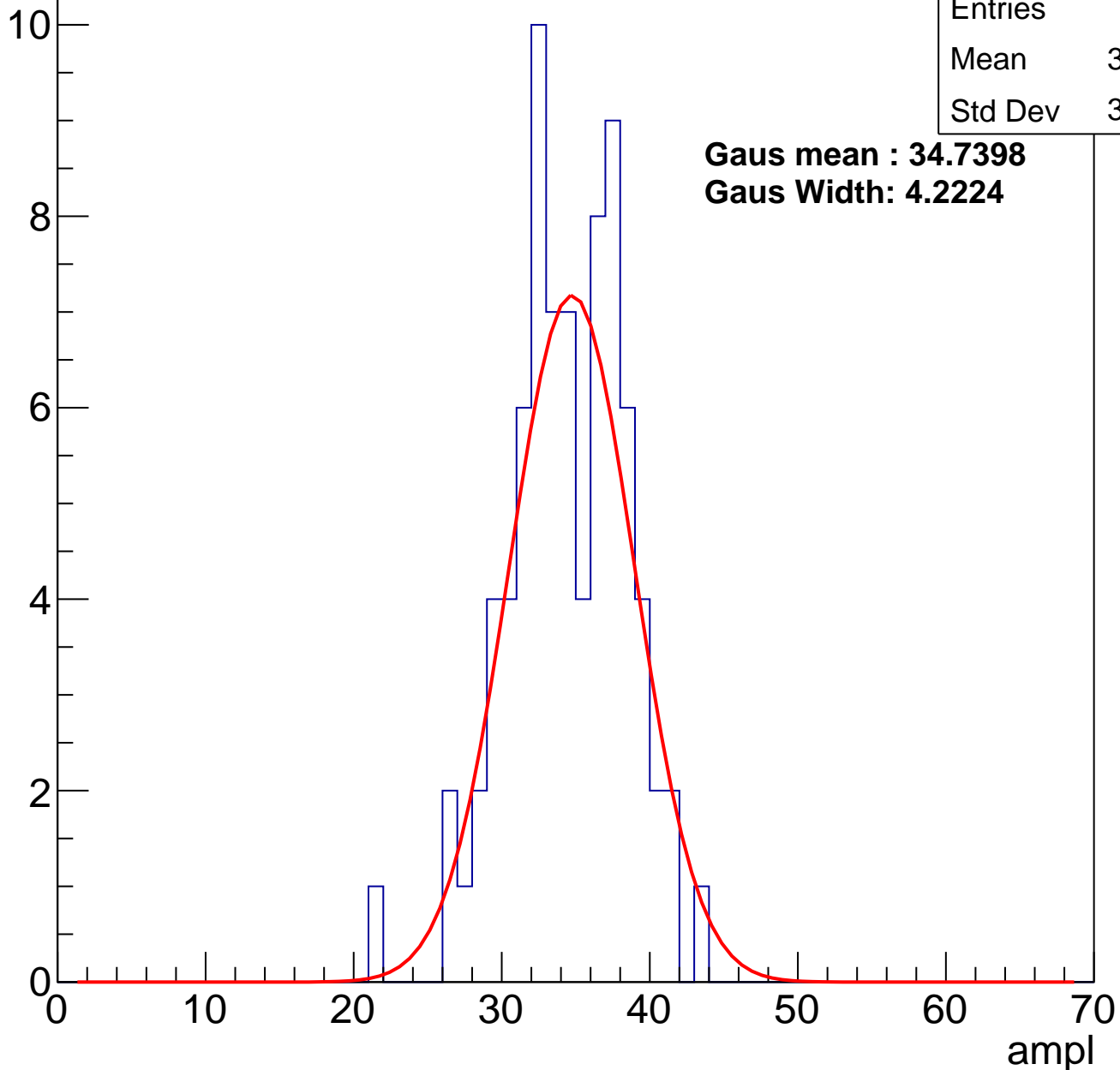
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	33.96
Std Dev	3.948

Gaus mean : 34.7398

Gaus Width: 4.2224

Entry



B1L102S, U19-ch100, adc2

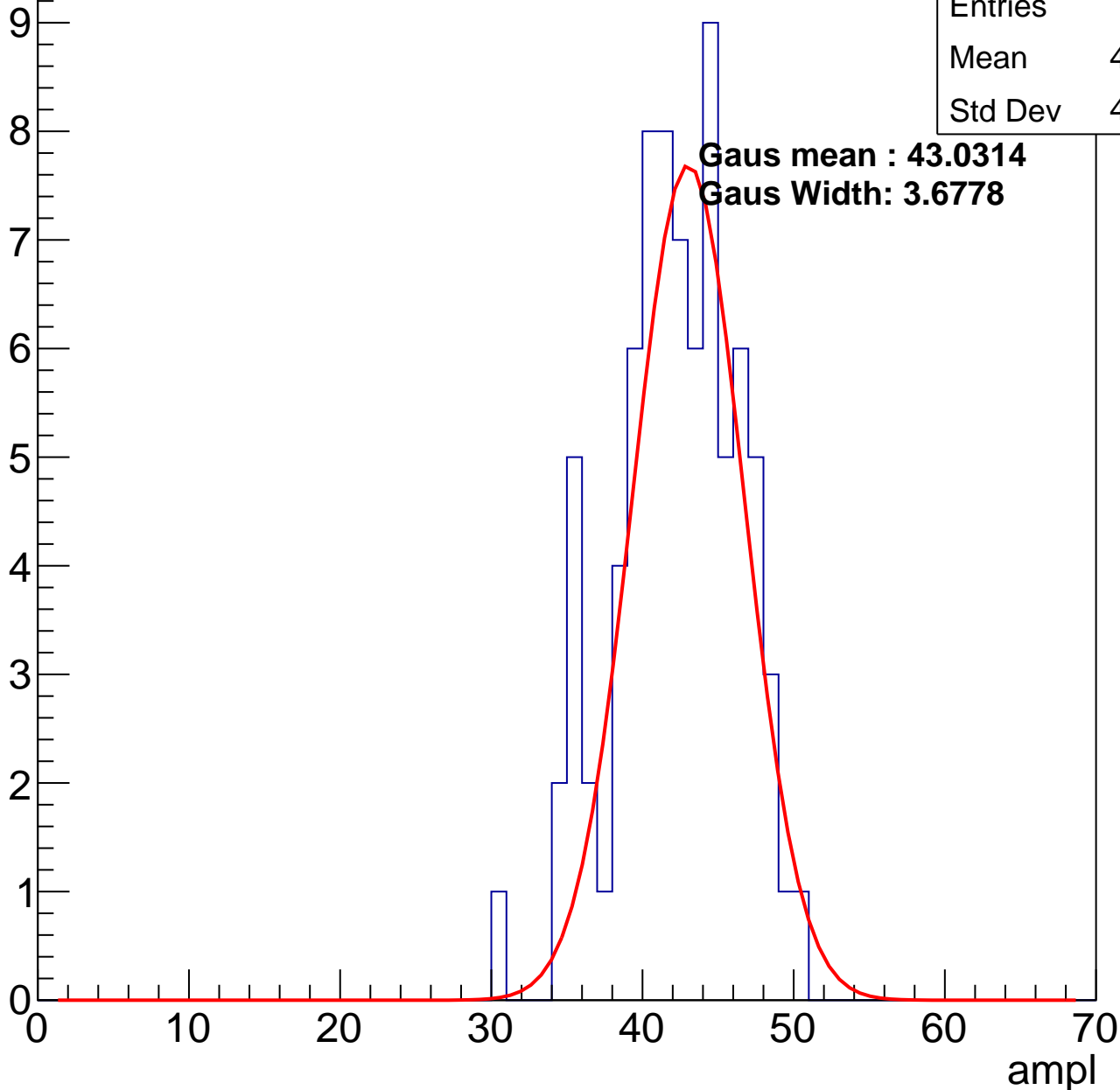
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.79
Std Dev	4.027

Gaus mean : 43.0314

Gaus Width: 3.6778

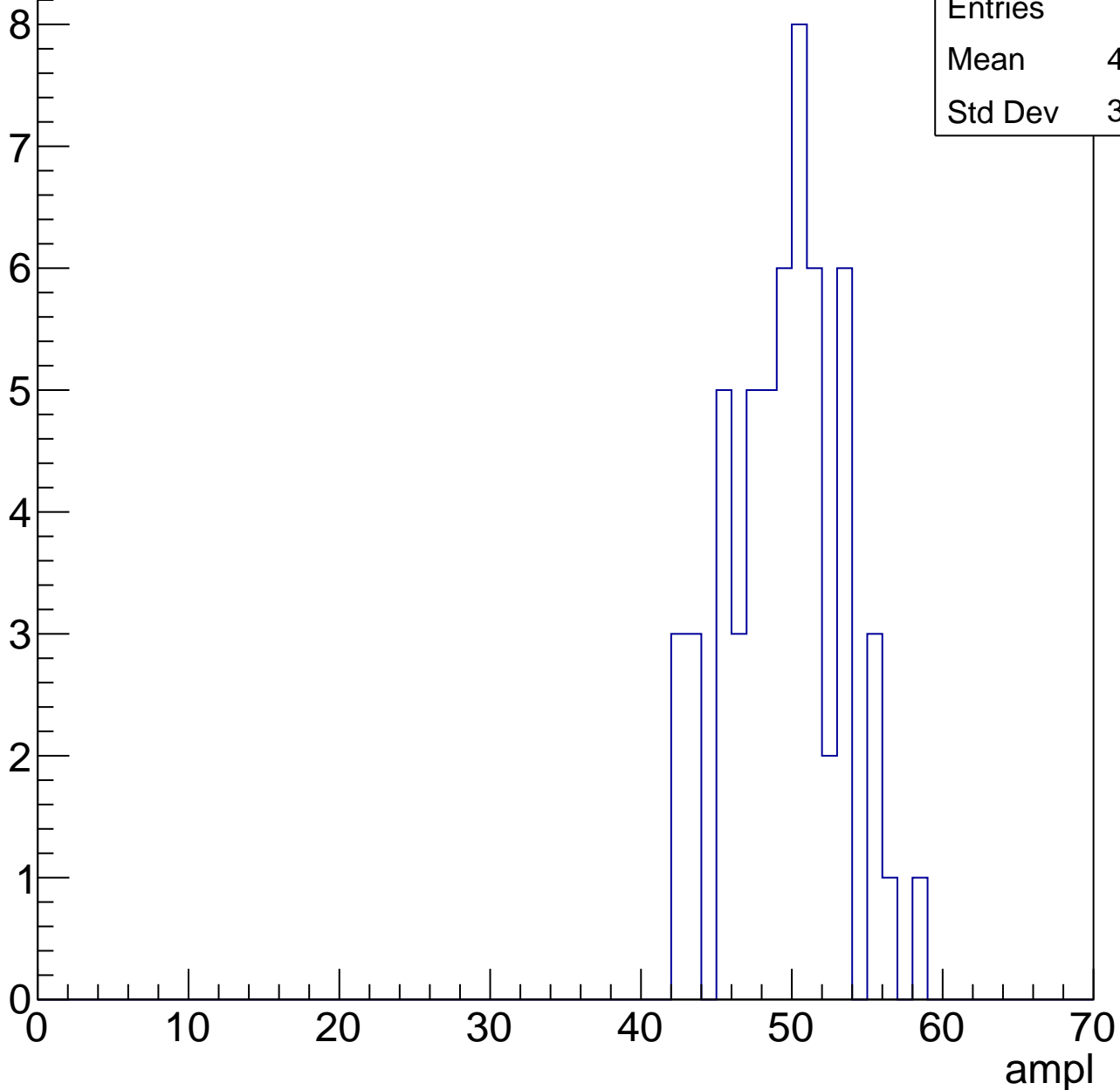


B1L102S, U19-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.02
Std Dev	3.678

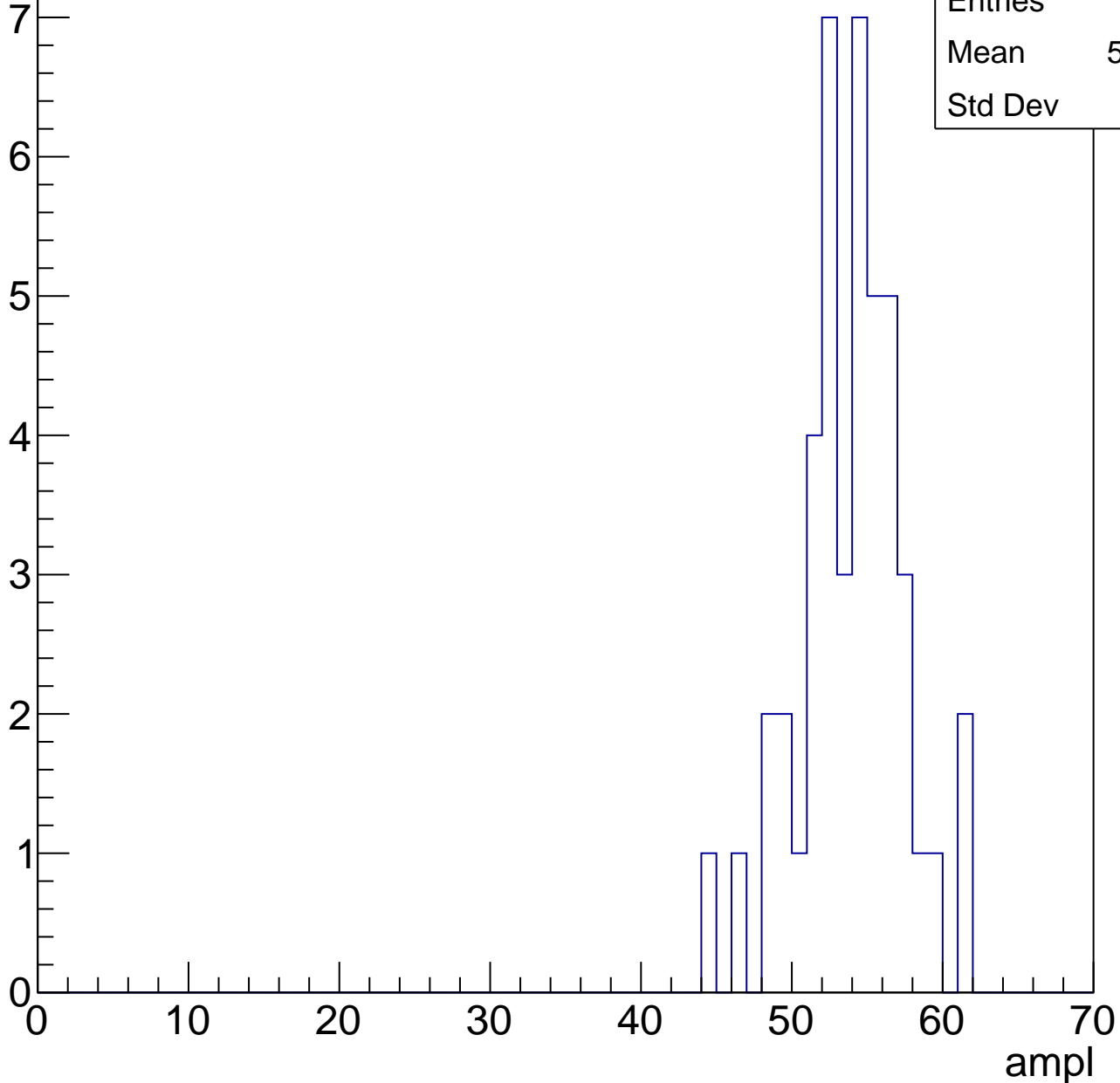


B1L102S, U19-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	53.42
Std Dev	3.48

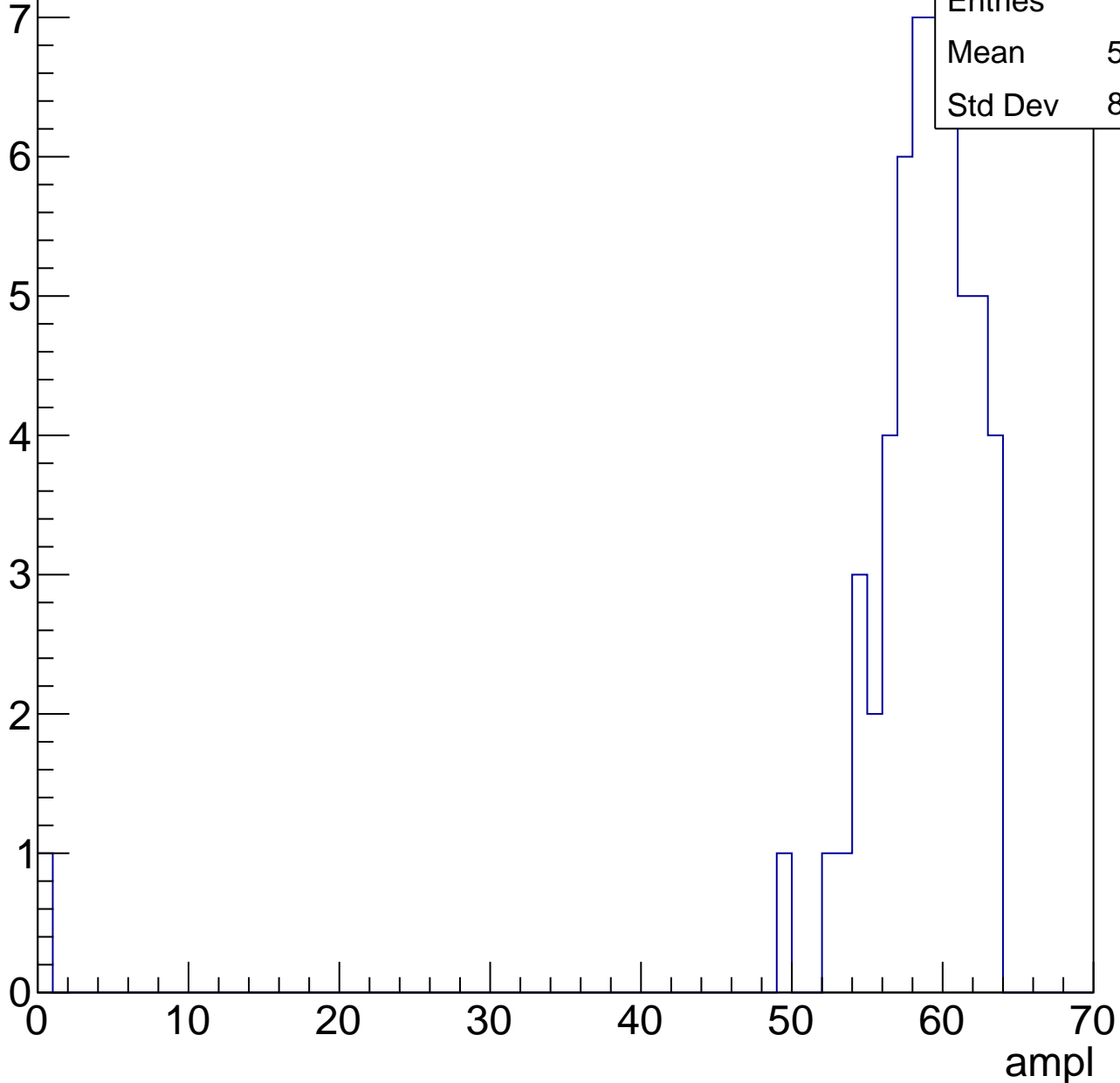


B1L102S, U19-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.37
Std Dev	8.425

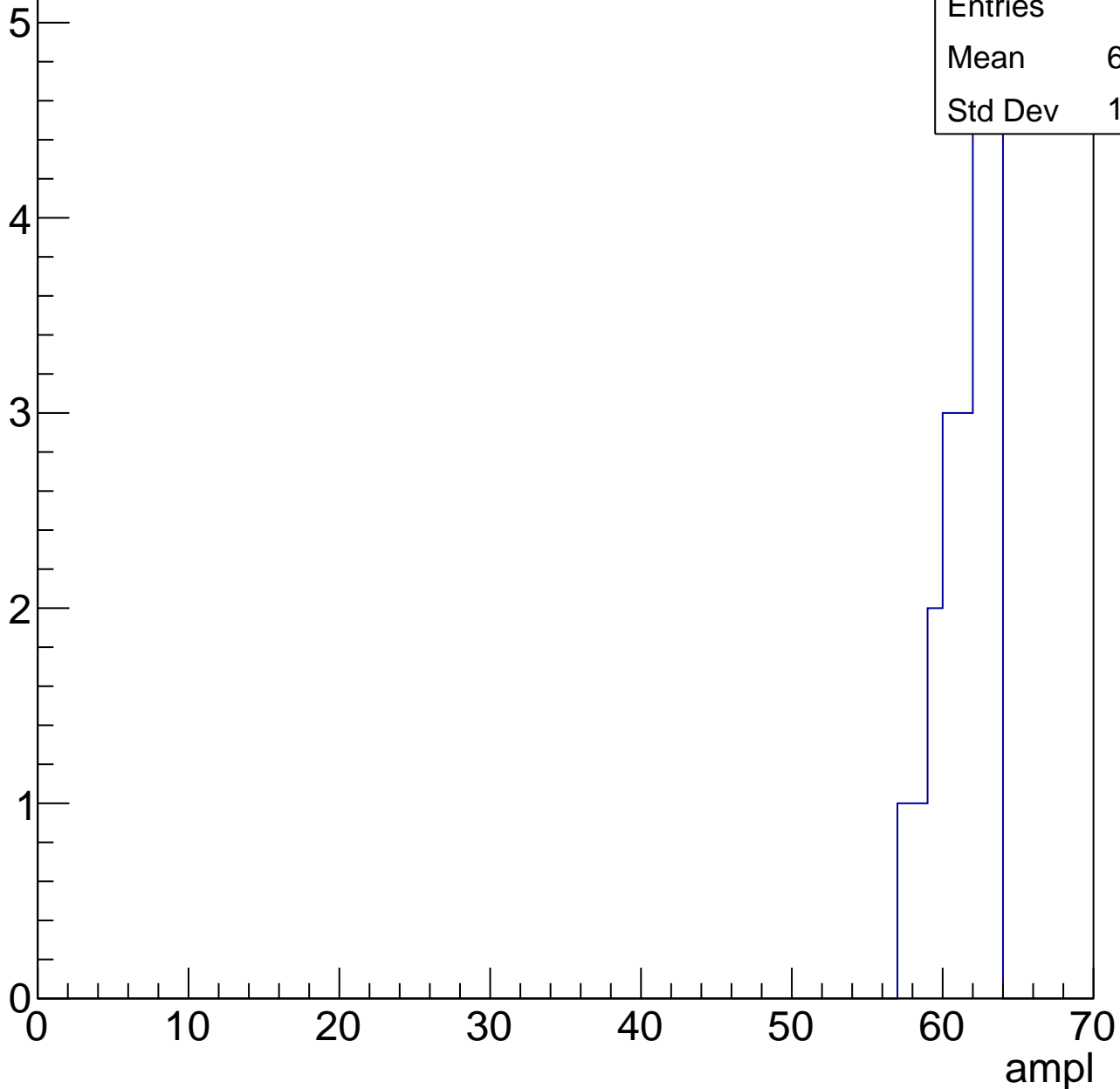


B1L102S, U19-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.05
Std Dev	1.746



B1L102S, U19-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U19-ch101, adc0

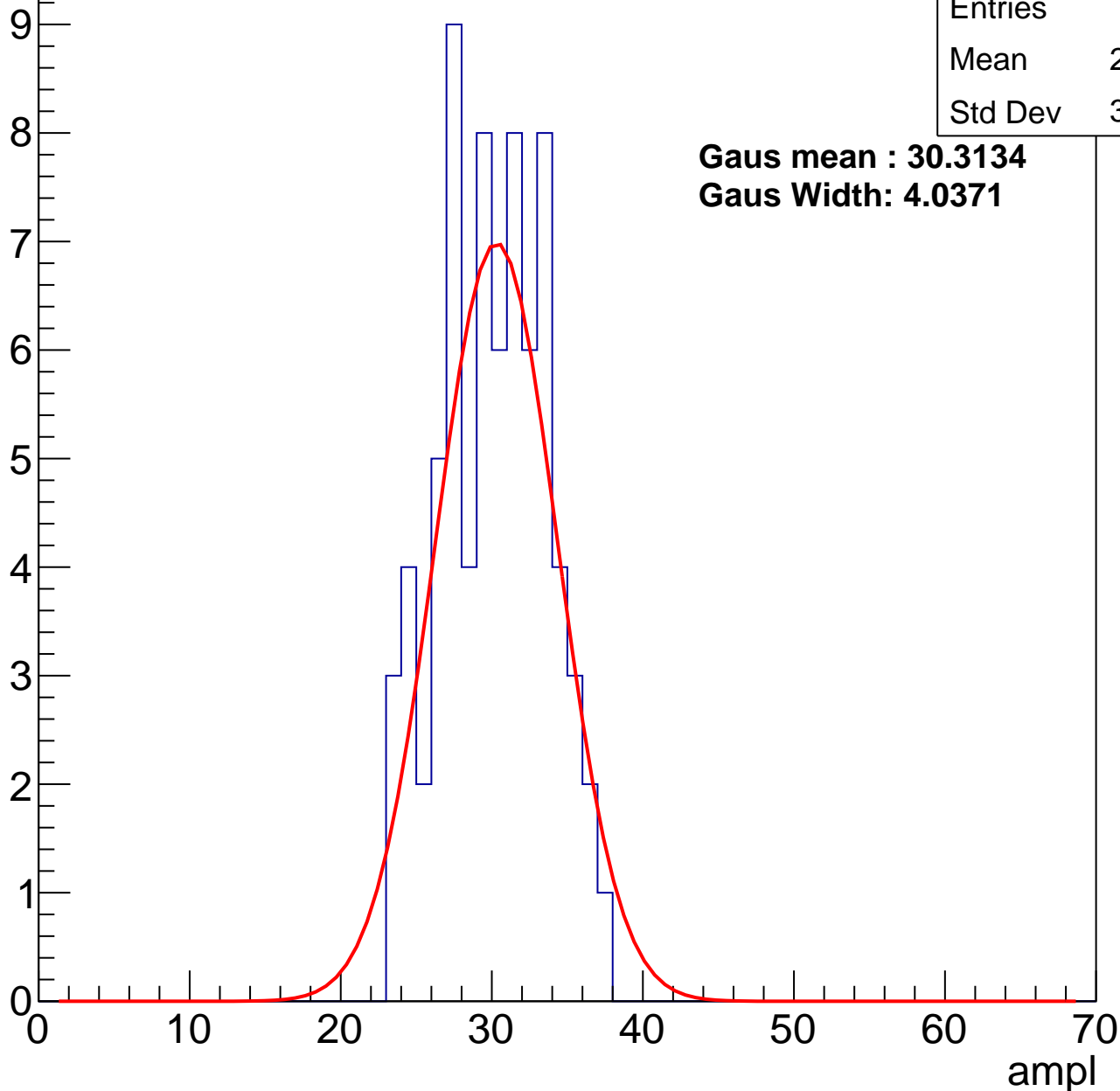
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.67
Std Dev	3.476

Gaus mean : 30.3134

Gaus Width: 4.0371



B1L102S, U19-ch101, adc1

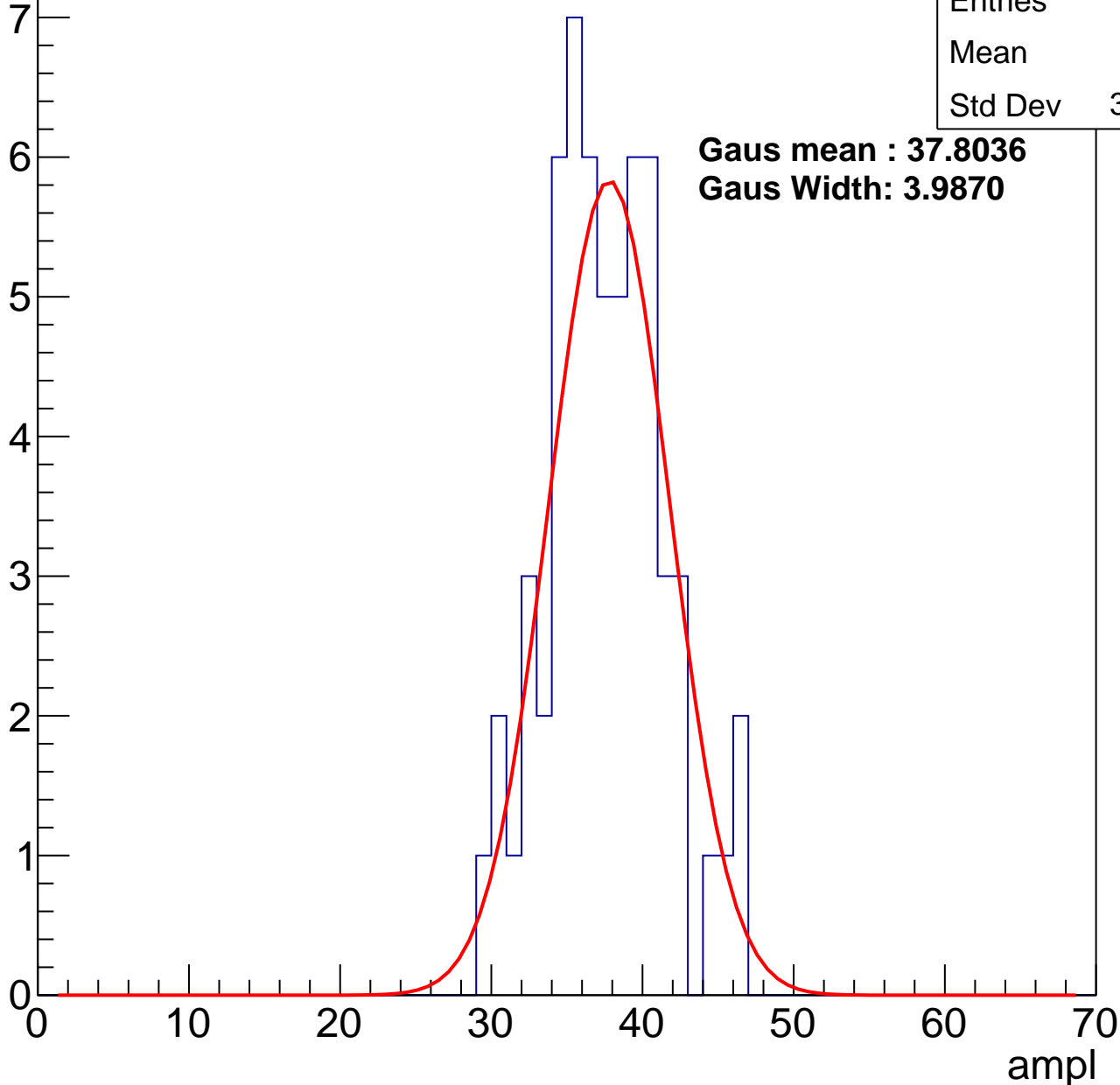
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	37.1
Std Dev	3.815

Gaus mean : 37.8036

Gaus Width: 3.9870



B1L102S, U19-ch101, adc2

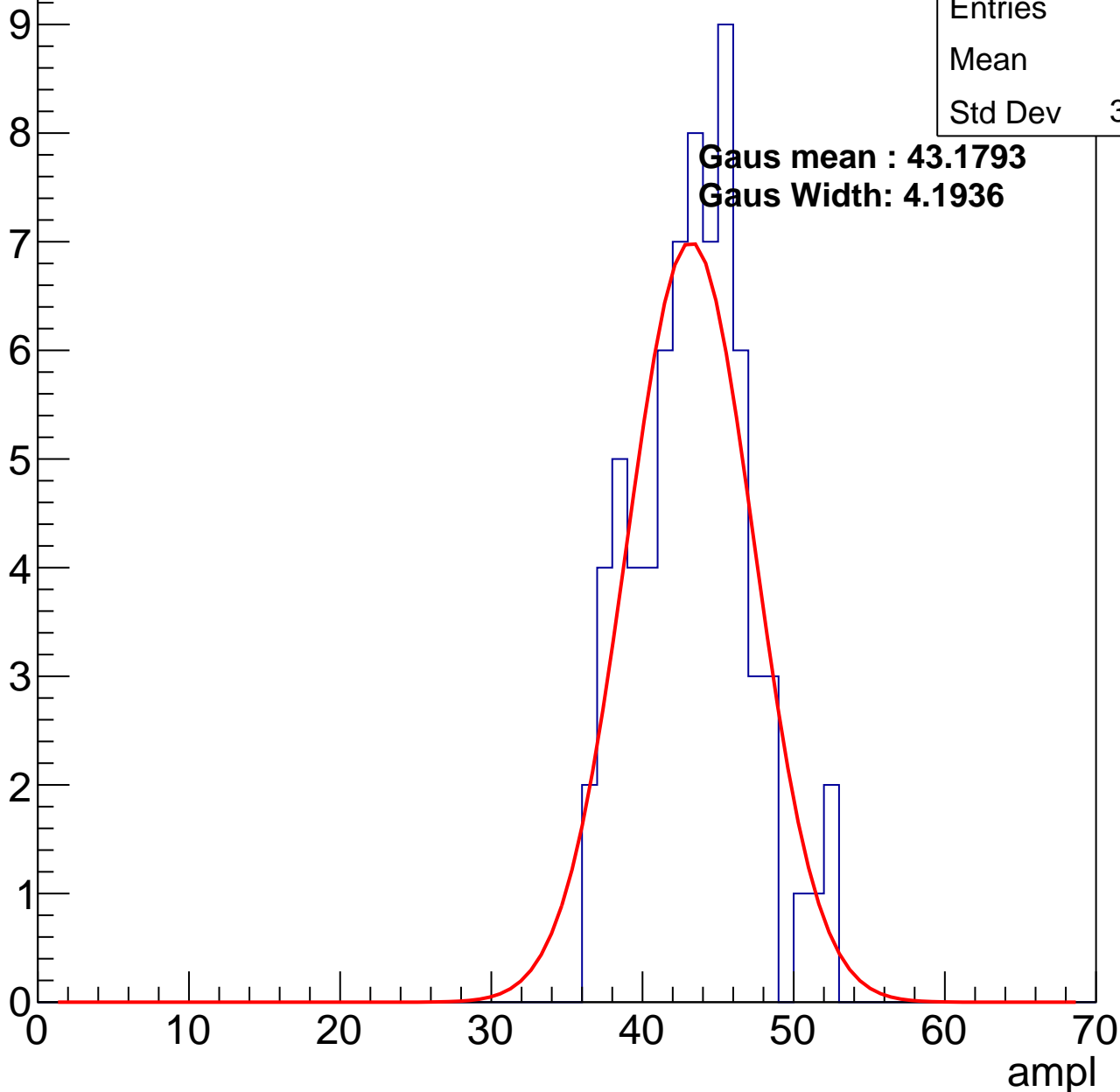
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.9
Std Dev	3.712

Gaus mean : 43.1793

Gaus Width: 4.1936

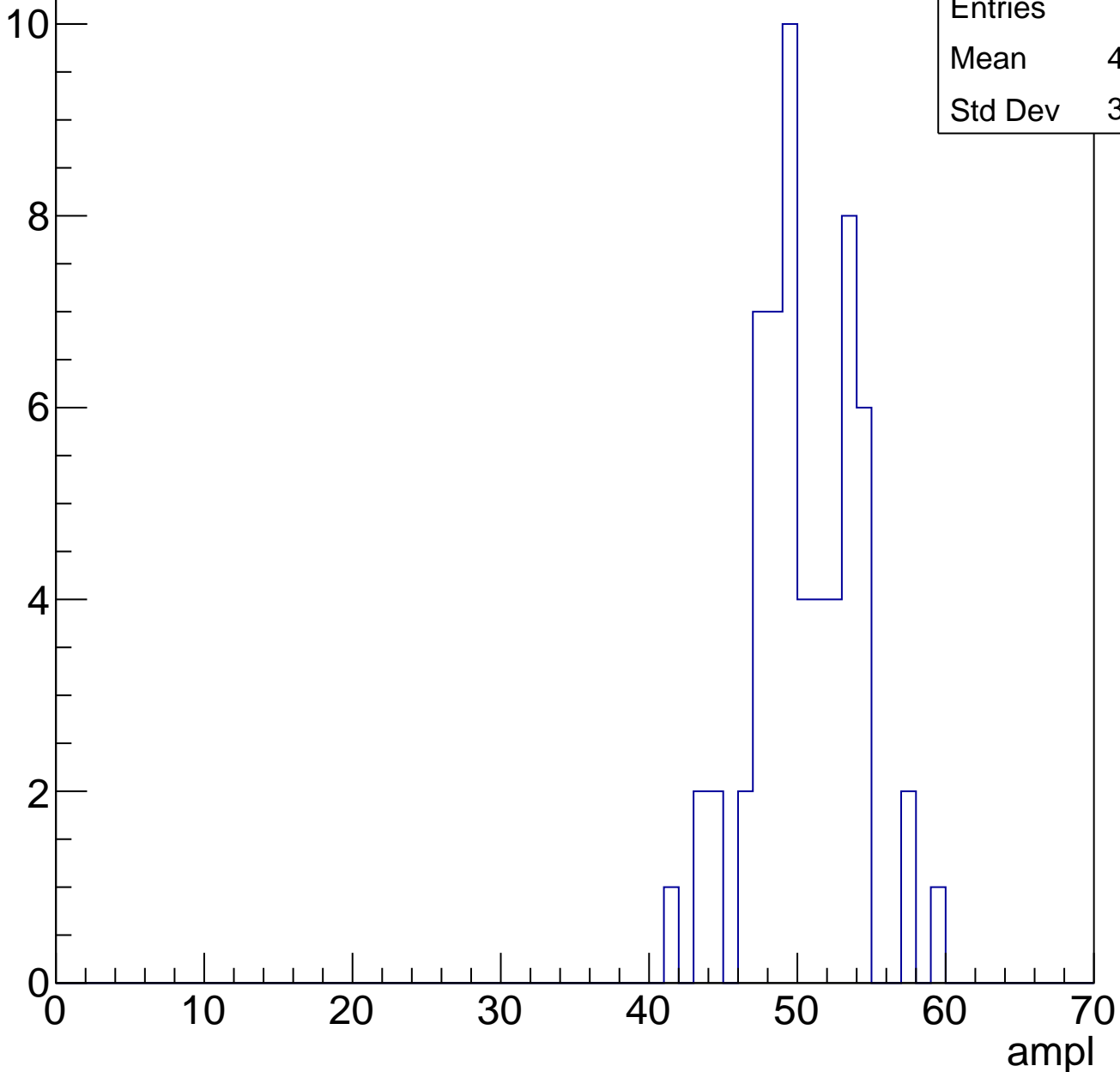


B1L102S, U19-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	49.92
Std Dev	3.537

Entry

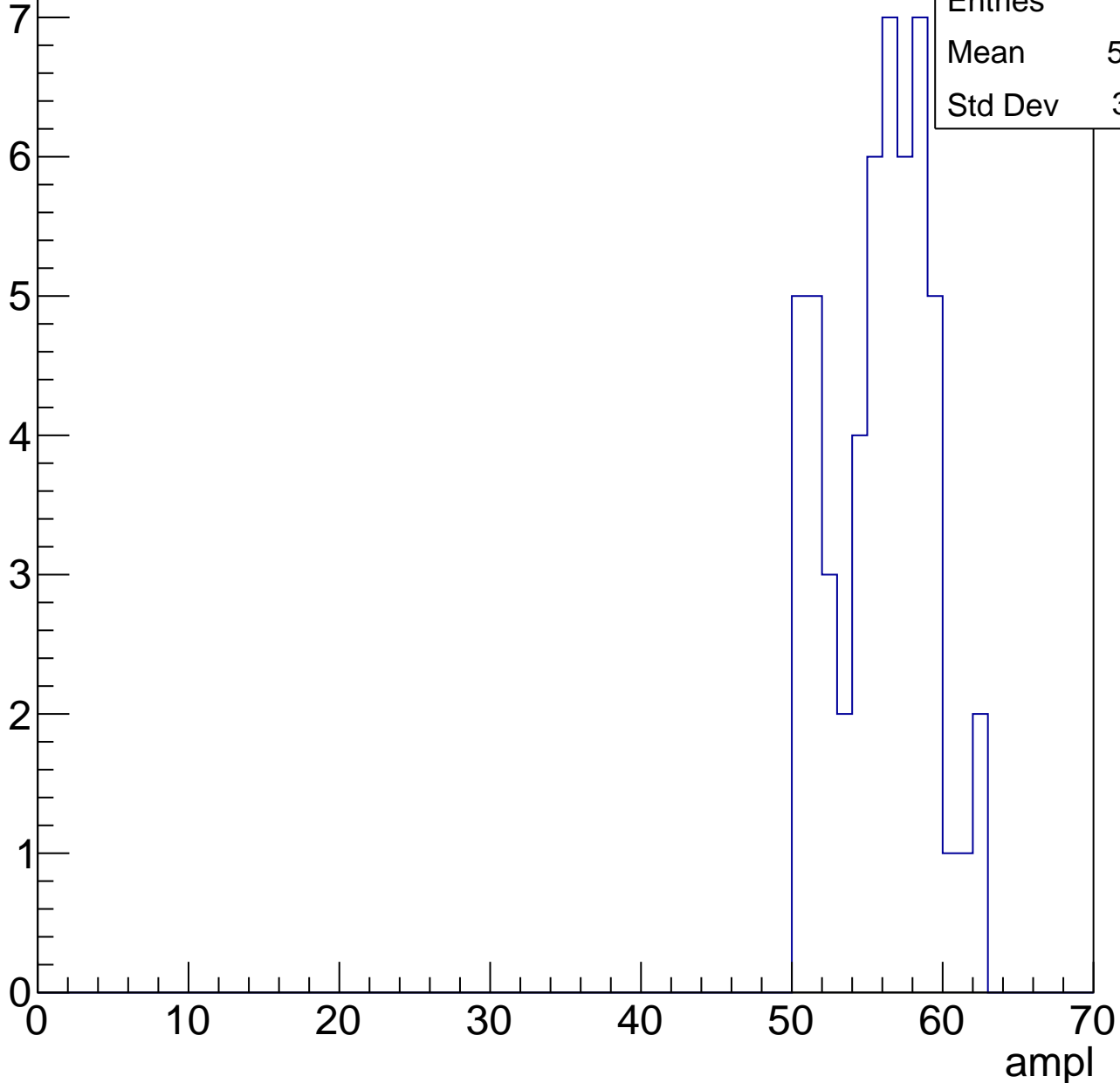


B1L102S, U19-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.43
Std Dev	3.241

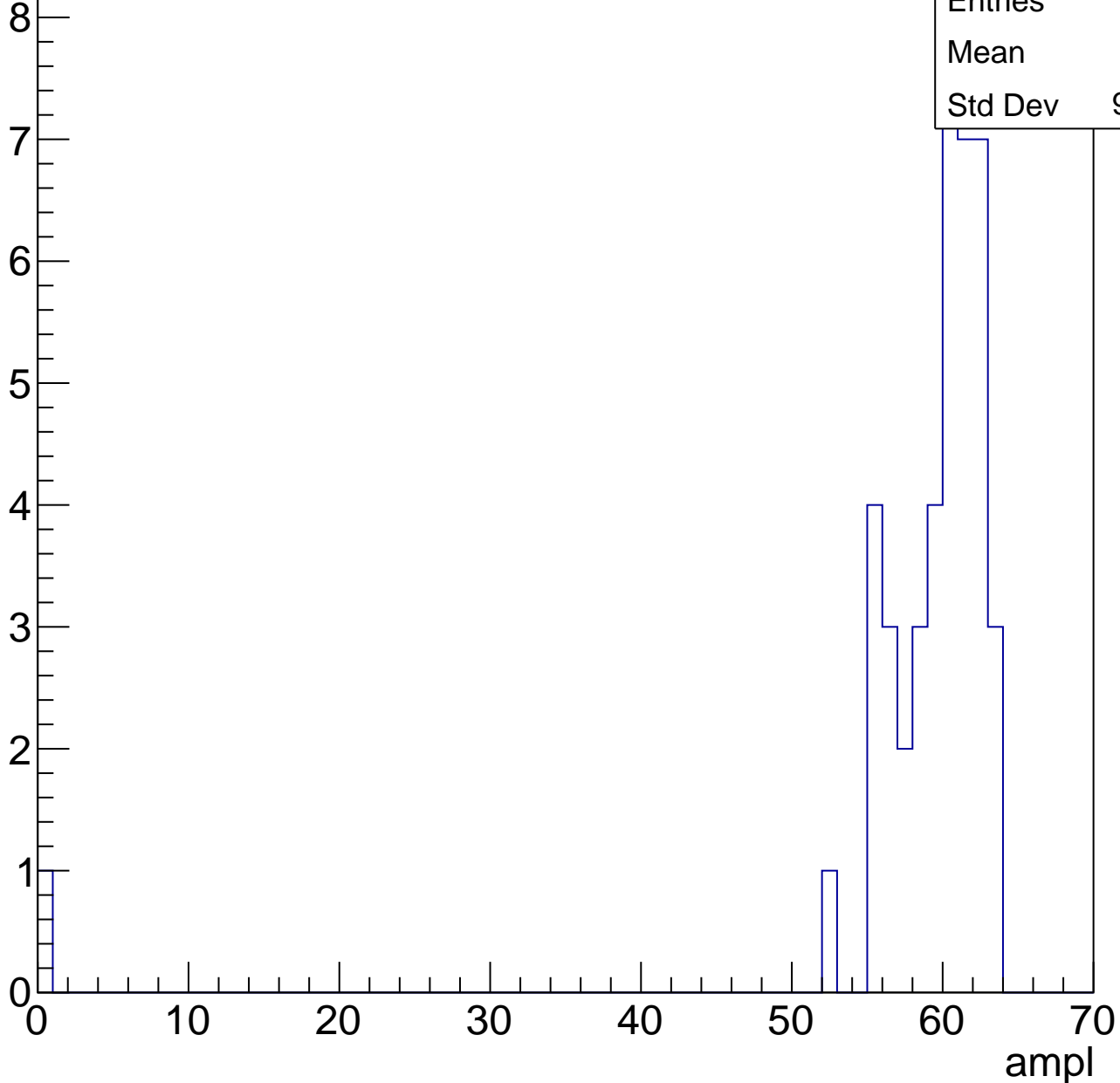


B1L102S, U19-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58
Std Dev	9.321

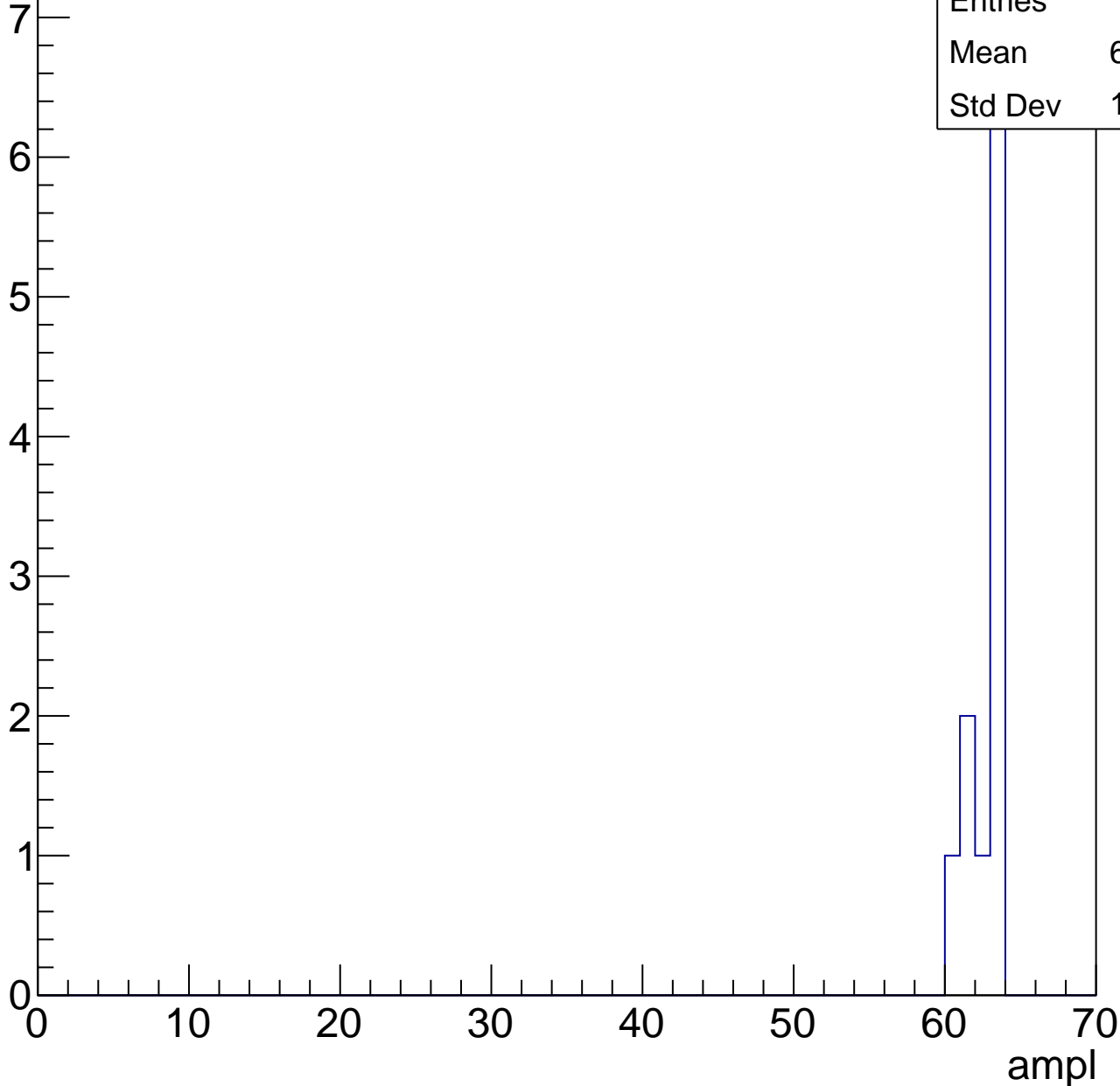


B1L102S, U19-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62.27
Std Dev	1.052



B1L102S, U19-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch102, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	28.7
Std Dev	3.574

Gaus mean : 29.2703

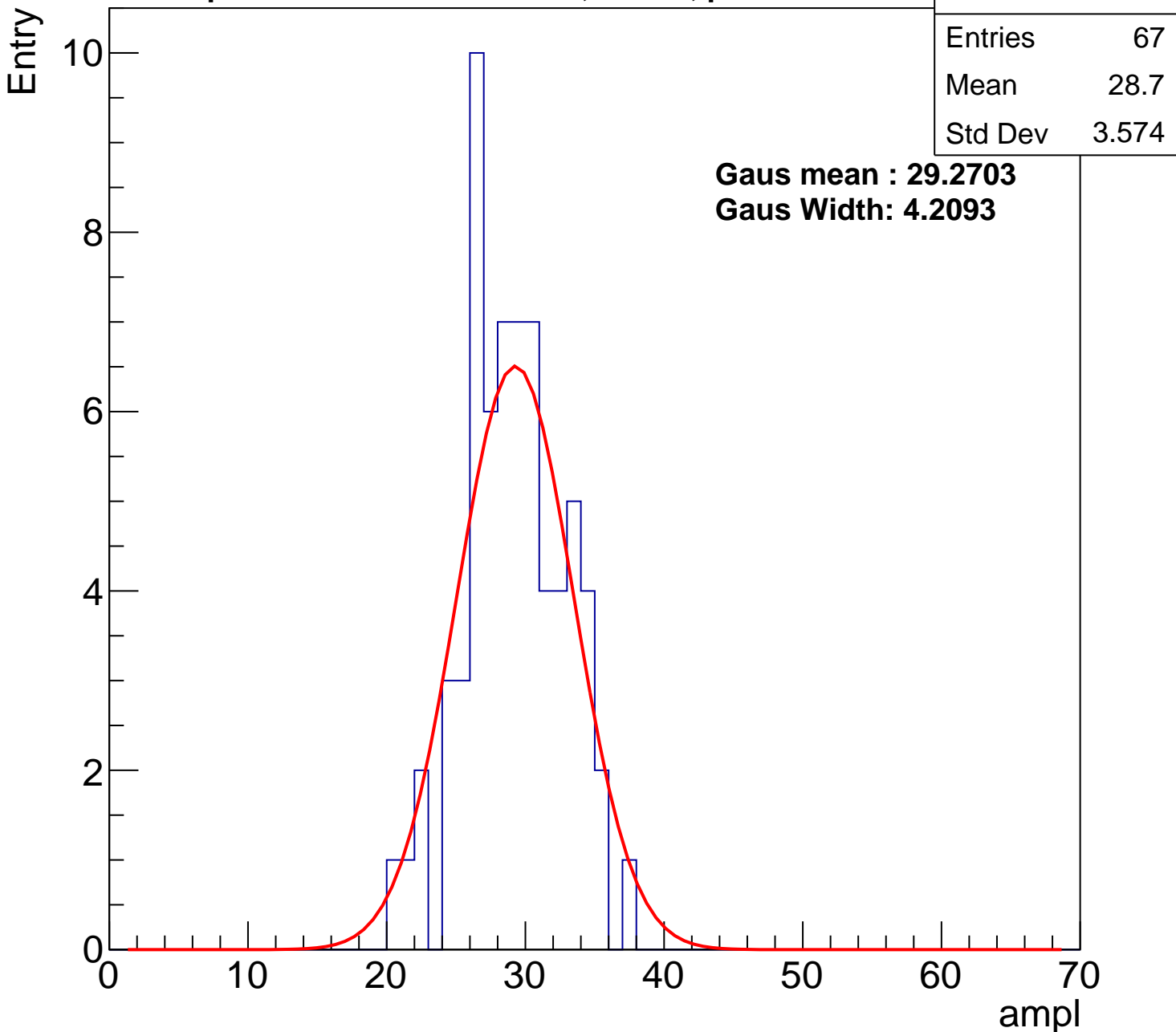
Gaus Width: 4.2093

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch102, adc1

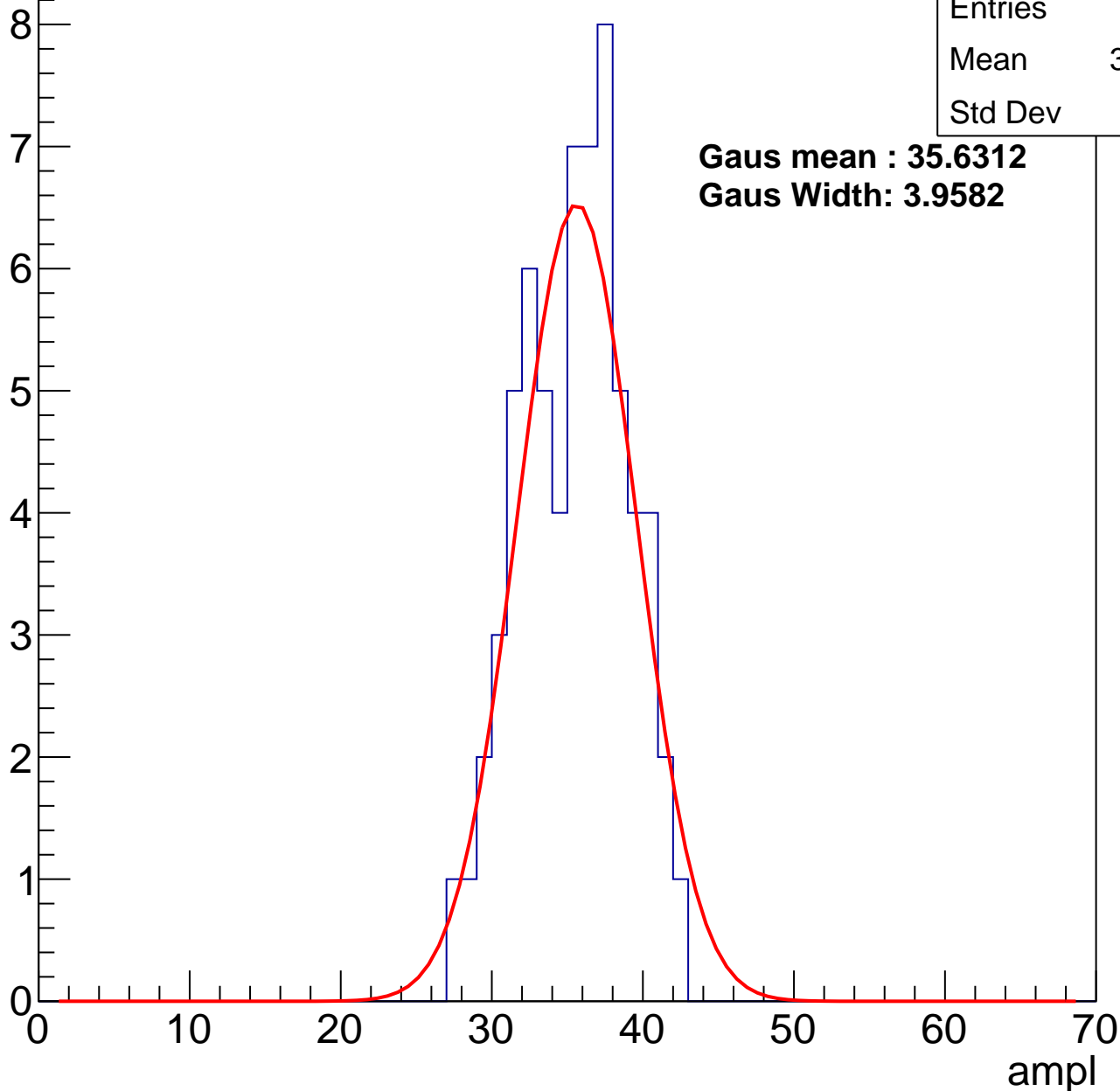
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.98
Std Dev	3.48

Gaus mean : 35.6312

Gaus Width: 3.9582



B1L102S, U19-ch102, adc2

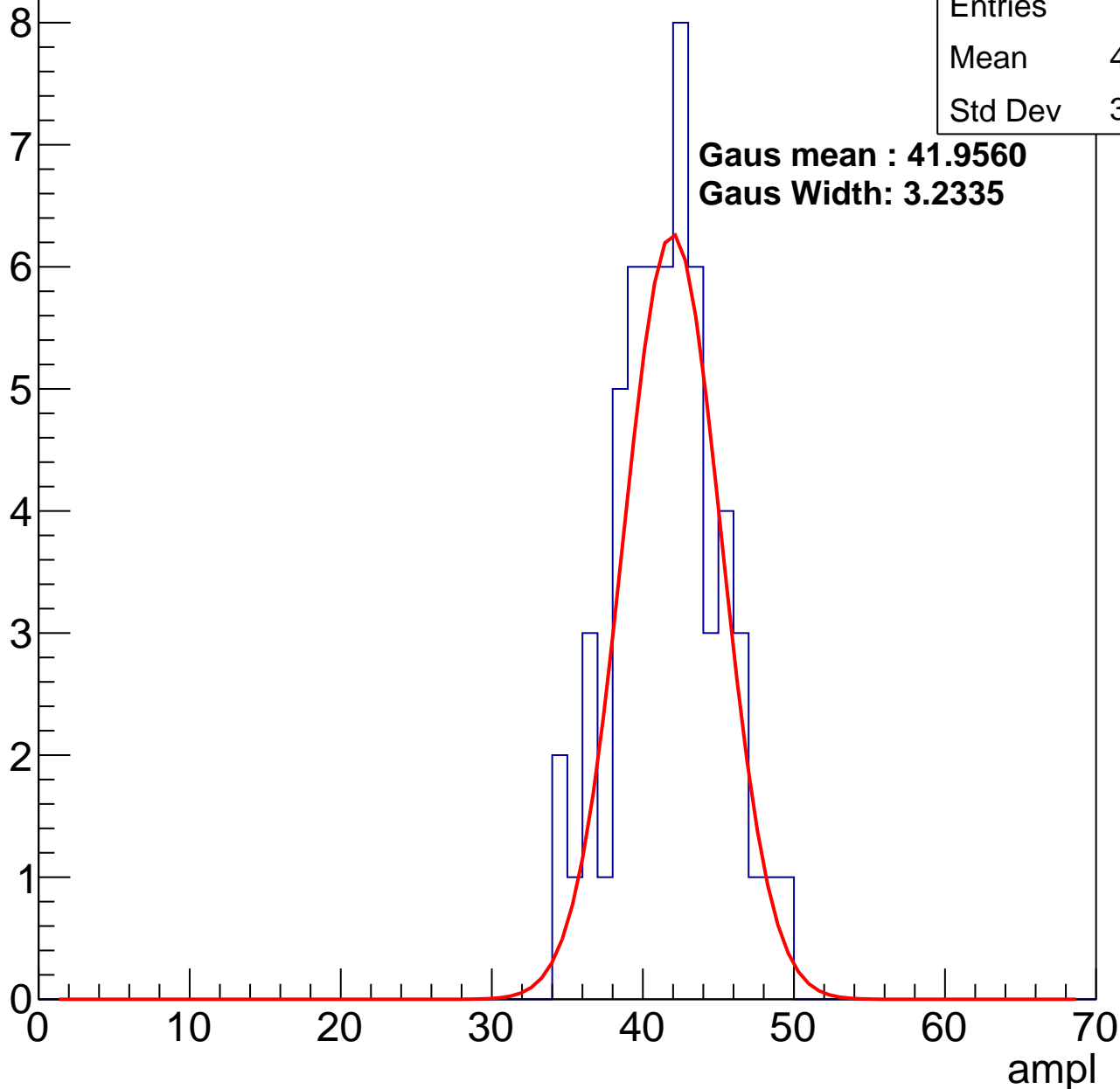
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.16
Std Dev	3.365

Gaus mean : 41.9560

Gaus Width: 3.2335

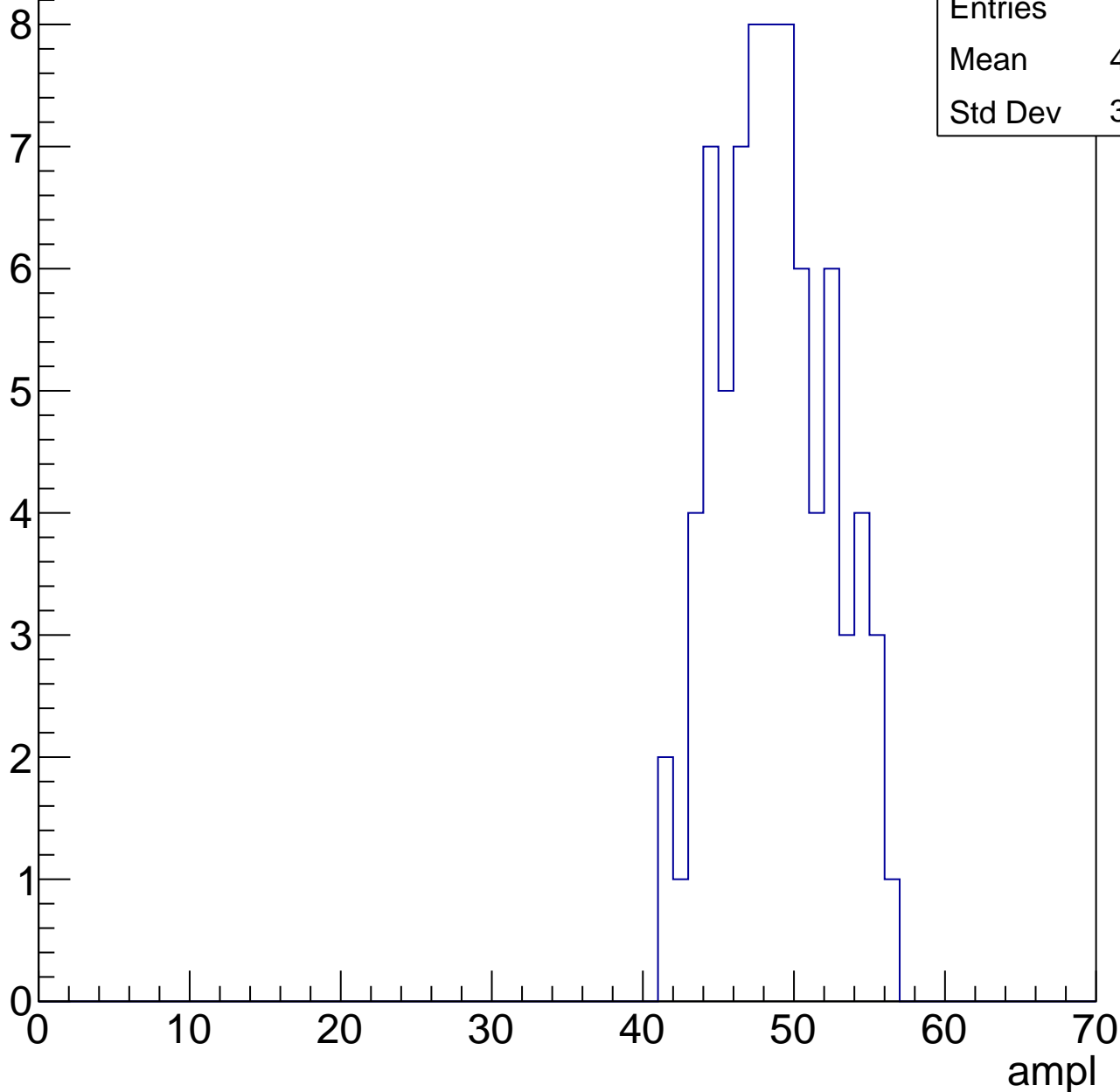


B1L102S, U19-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.25
Std Dev	3.633

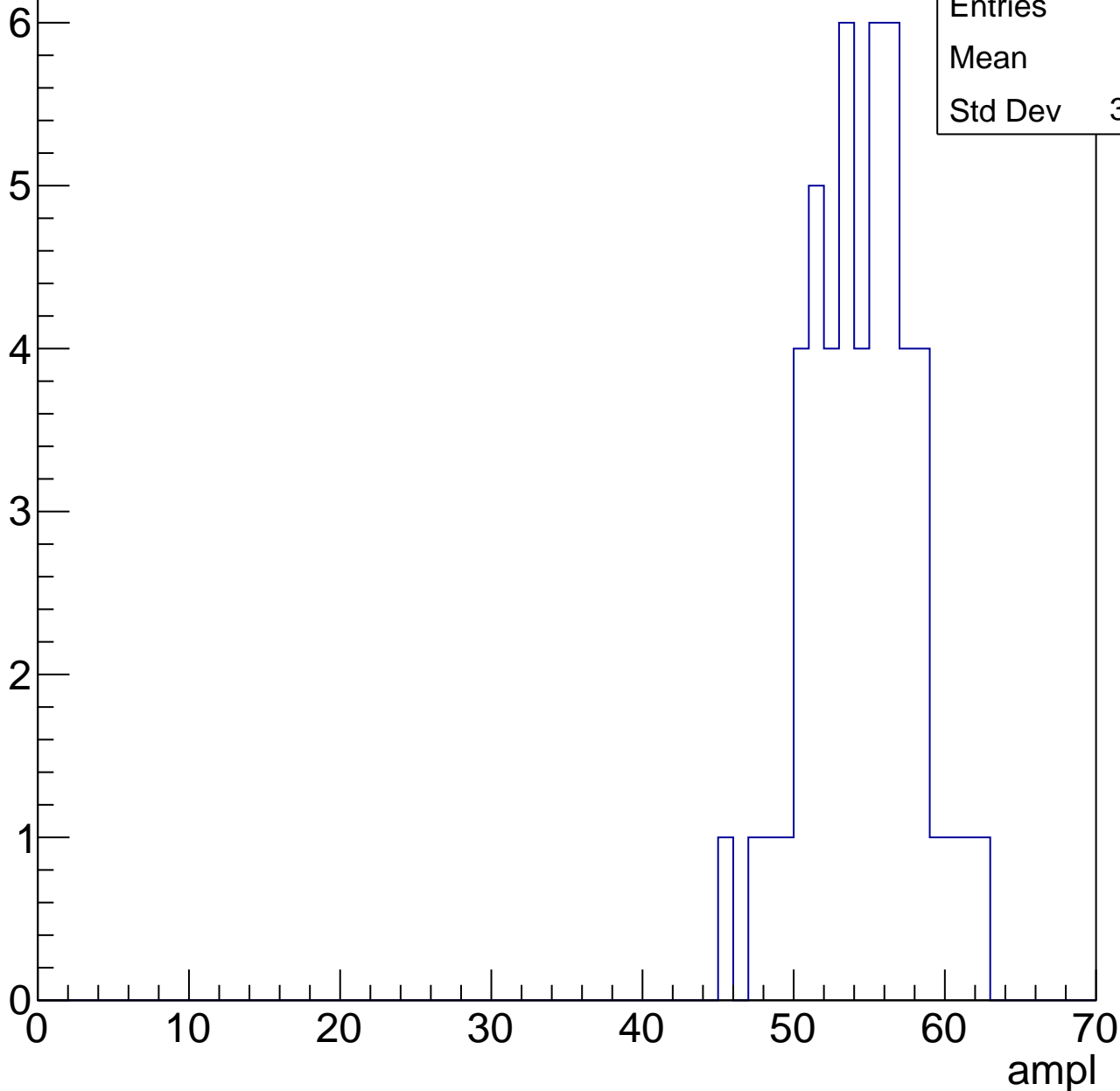


B1L102S, U19-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54
Std Dev	3.503

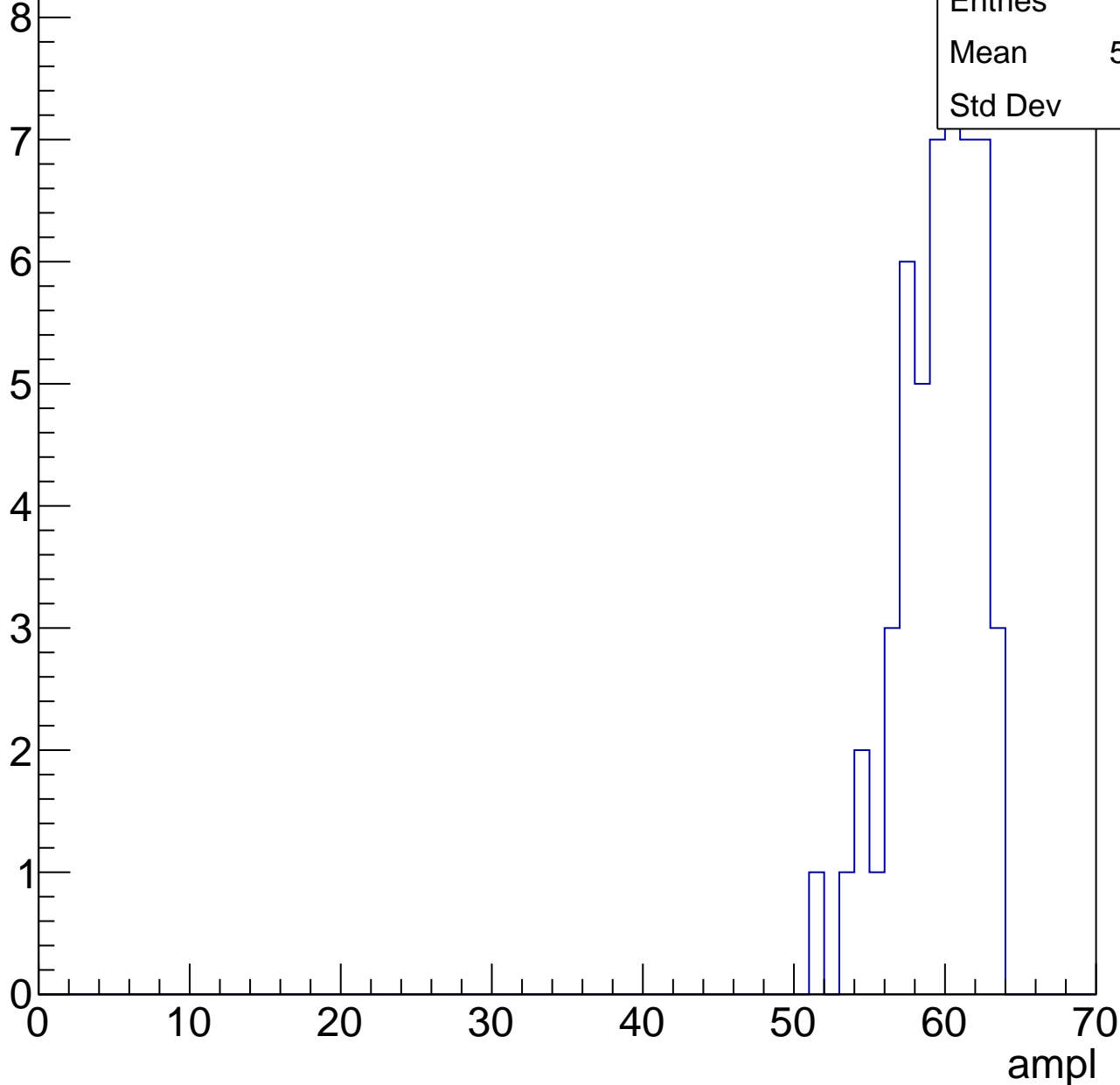


B1L102S, U19-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.02
Std Dev	2.69

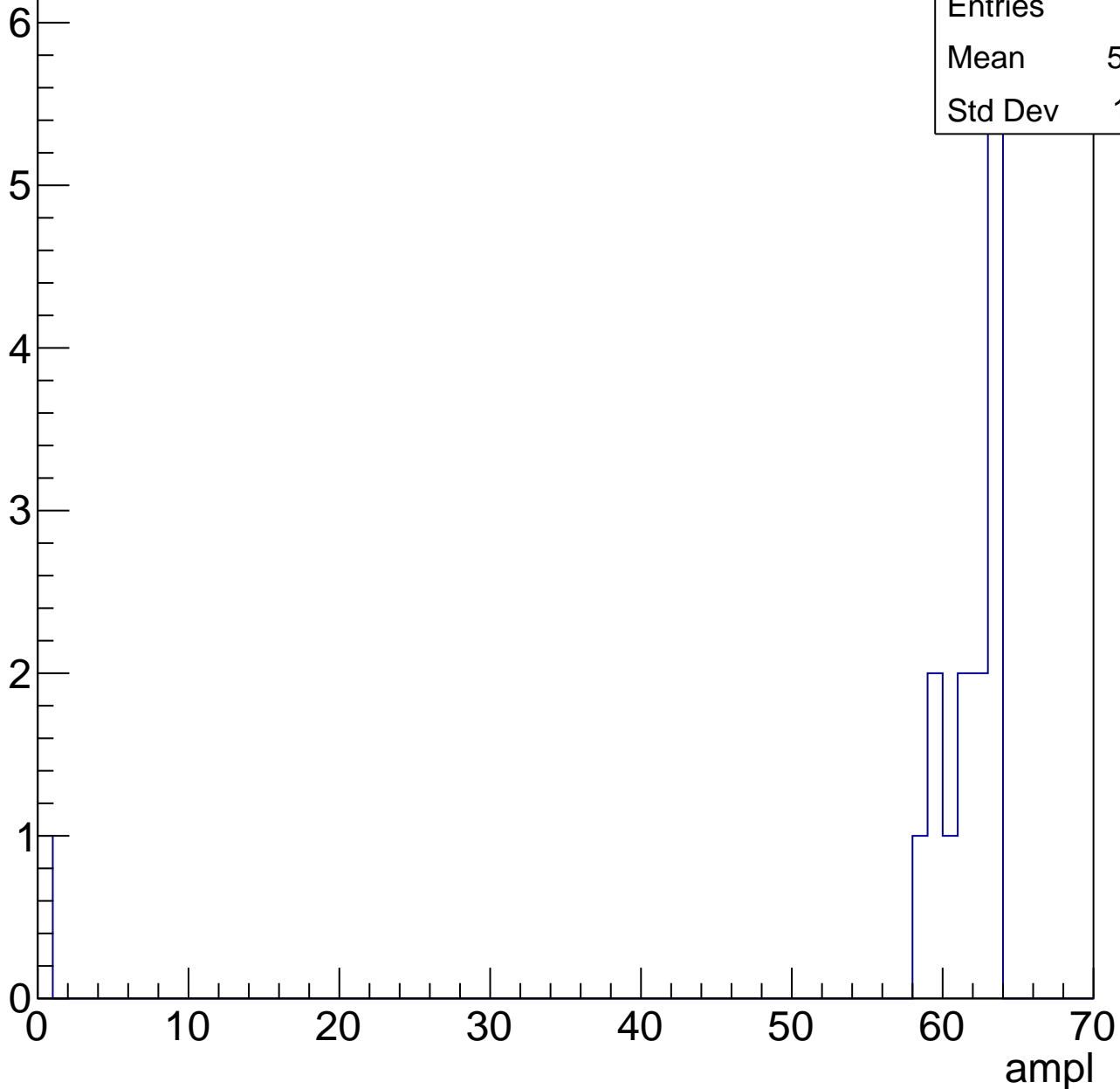


B1L102S, U19-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.33
Std Dev	15.41



B1L102S, U19-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch103, adc0

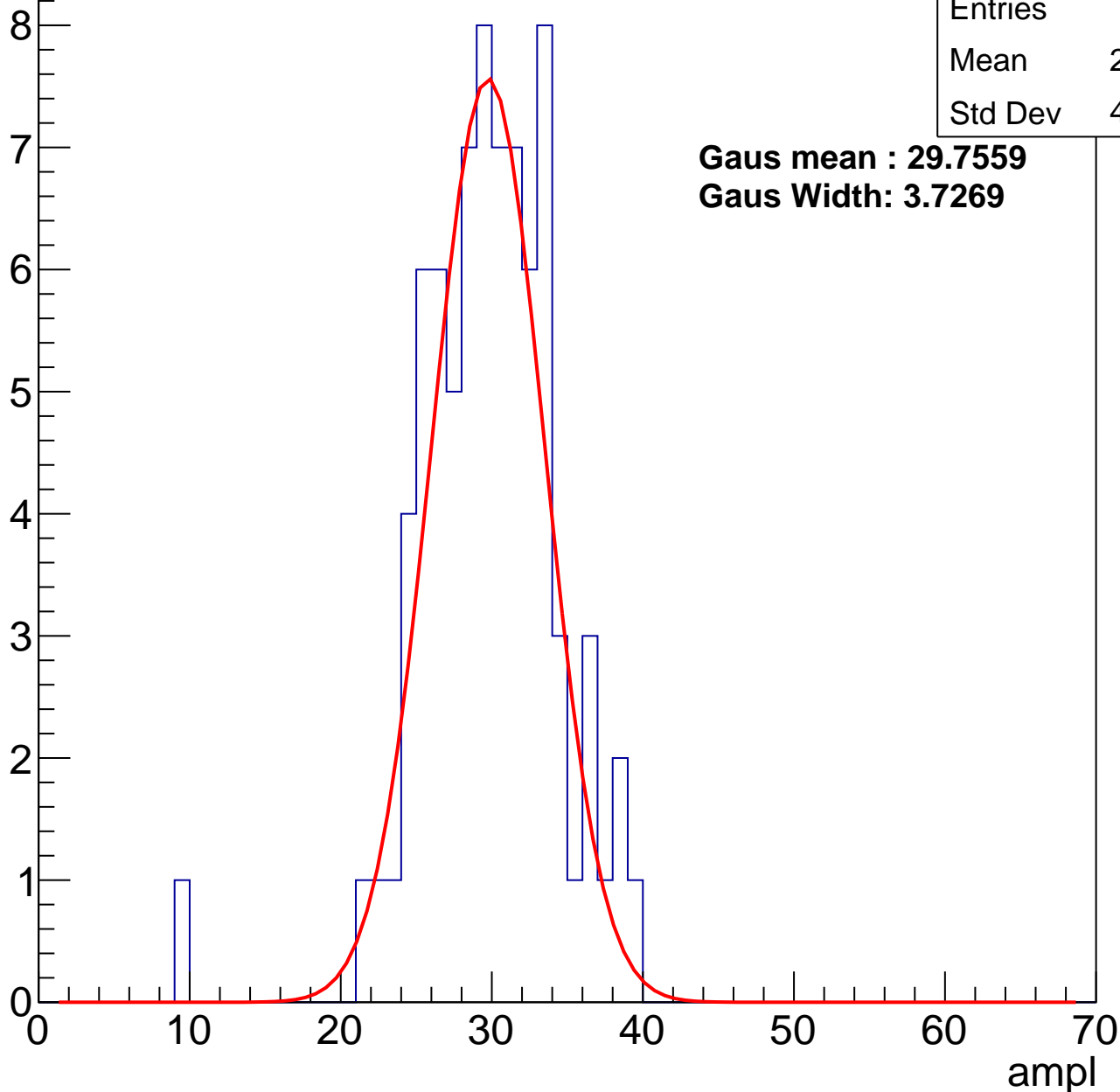
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.37
Std Dev	4.523

Gaus mean : 29.7559

Gaus Width: 3.7269



B1L102S, U19-ch103, adc1

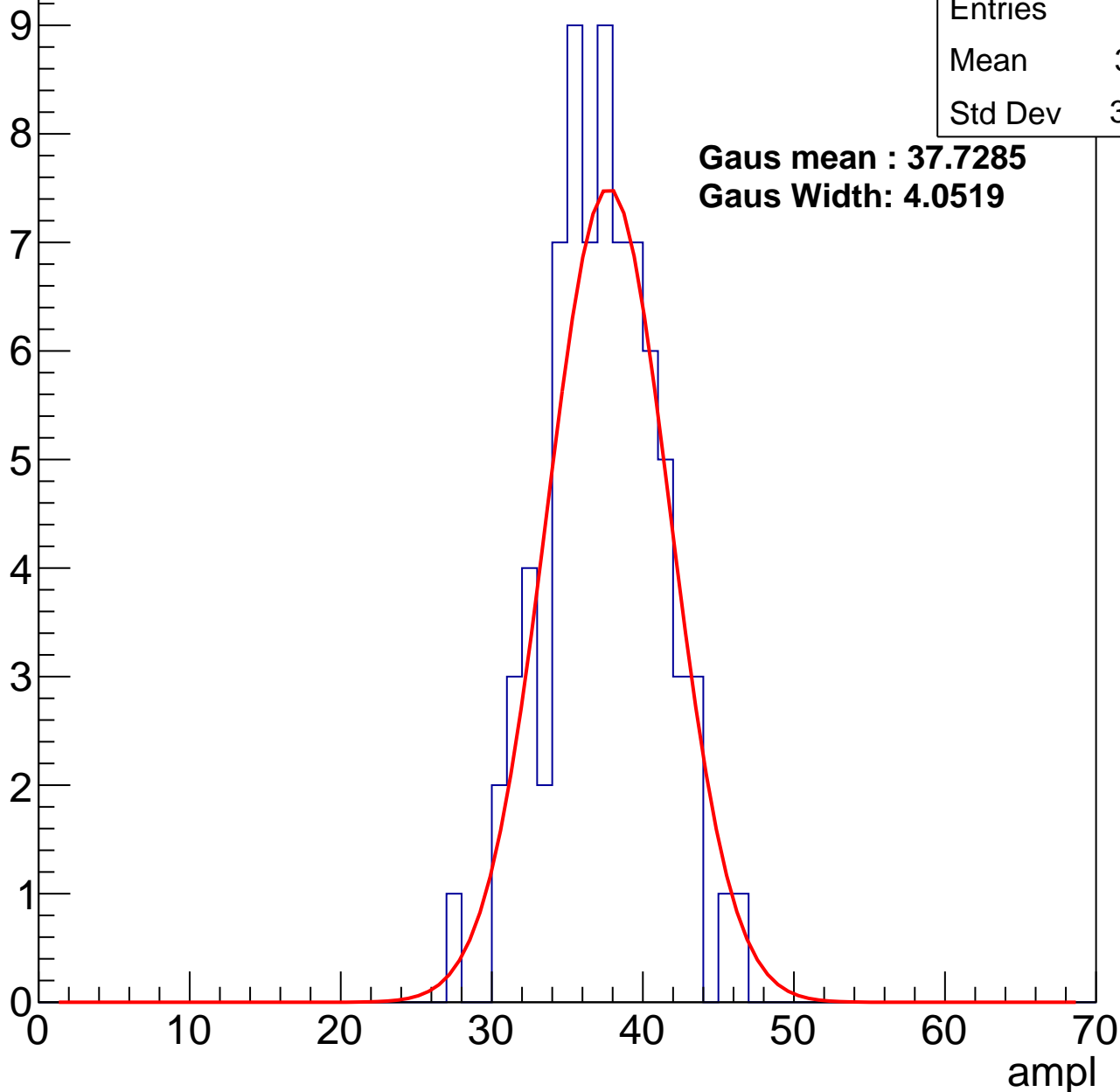
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.91
Std Dev	3.676

Gaus mean : 37.7285

Gaus Width: 4.0519



B1L102S, U19-ch103, adc2

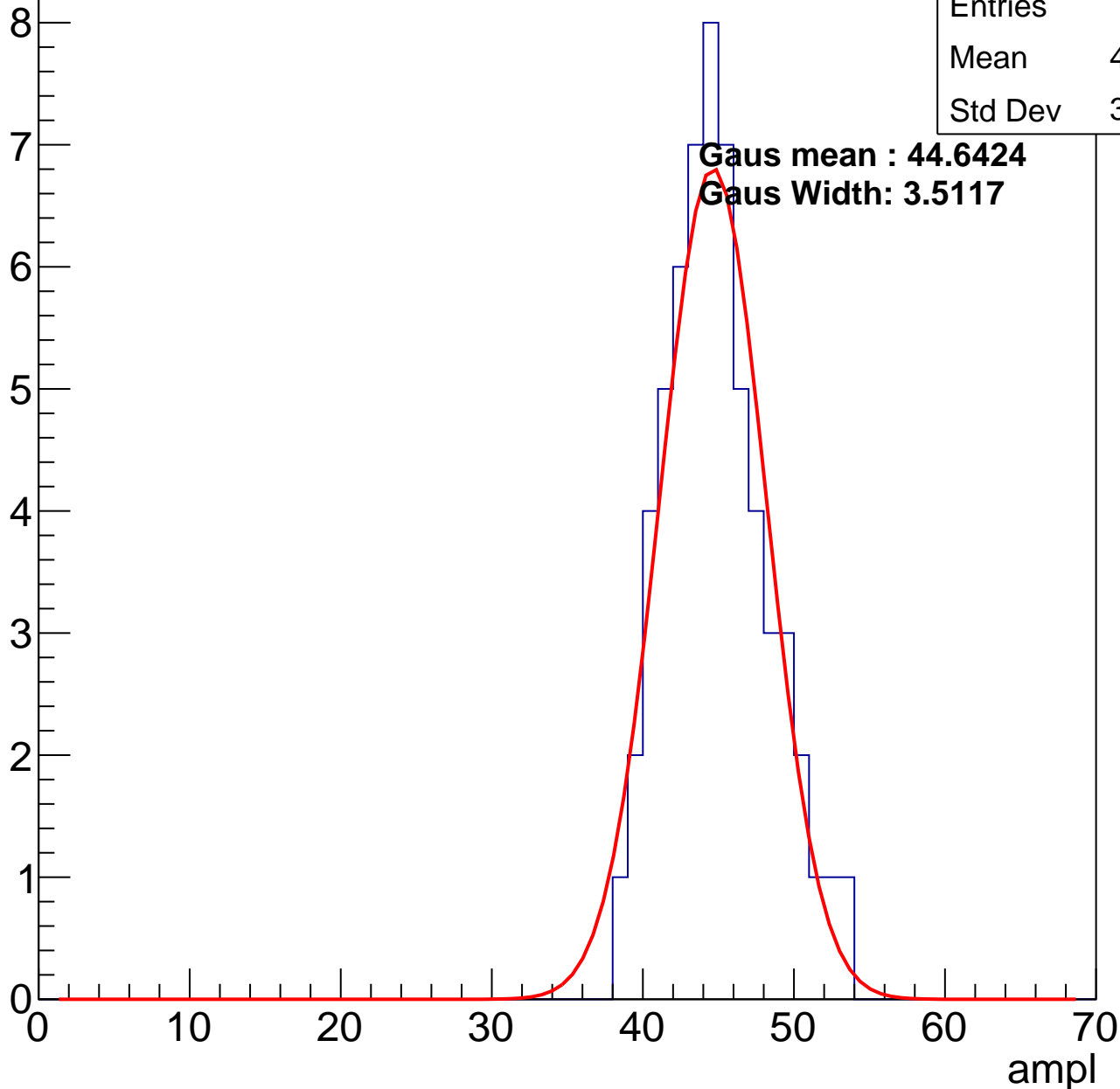
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	44.43
Std Dev	3.333

Gaus mean : 44.6424

Gaus Width: 3.5117

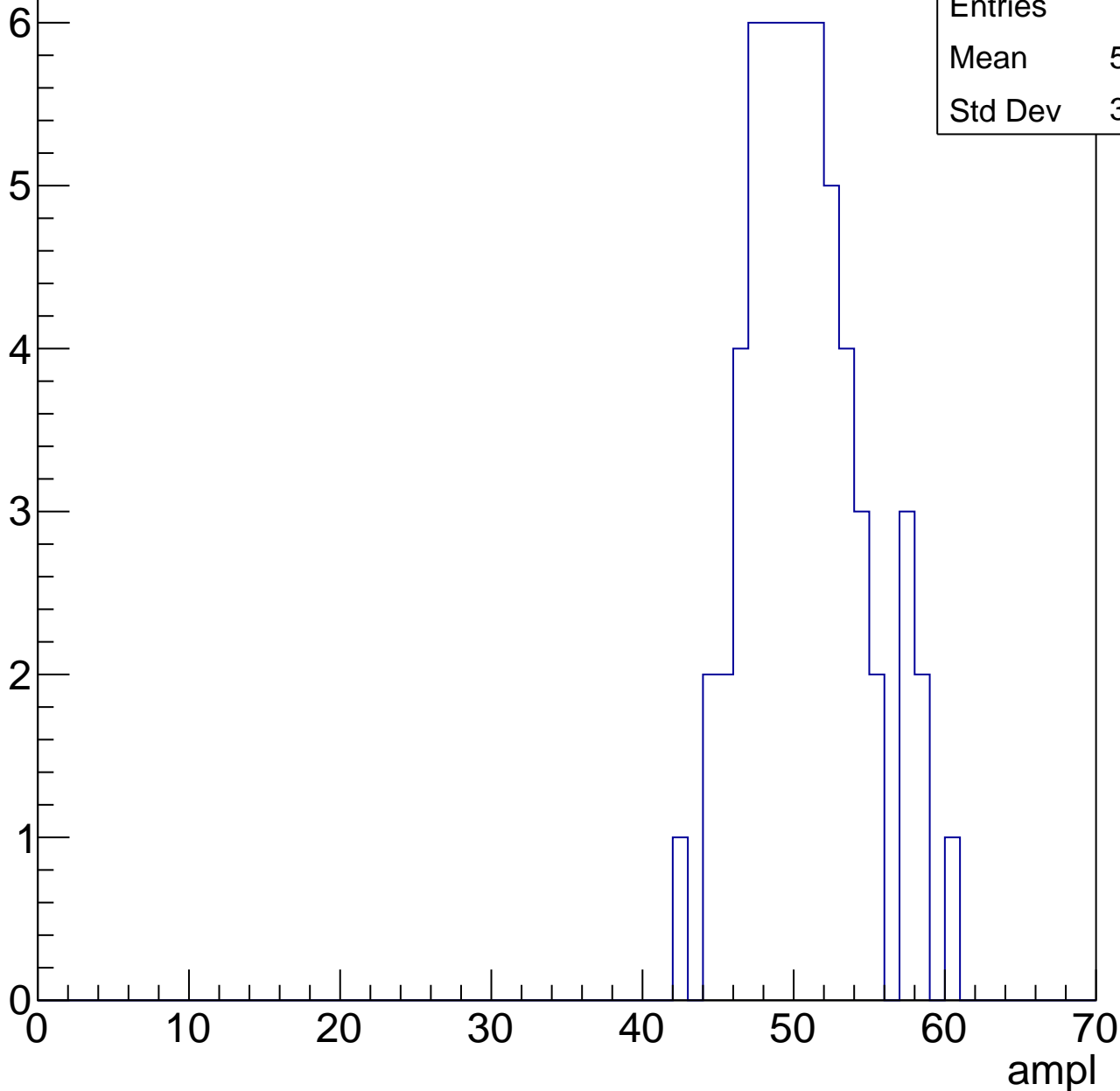


B1L102S, U19-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.25
Std Dev	3.829

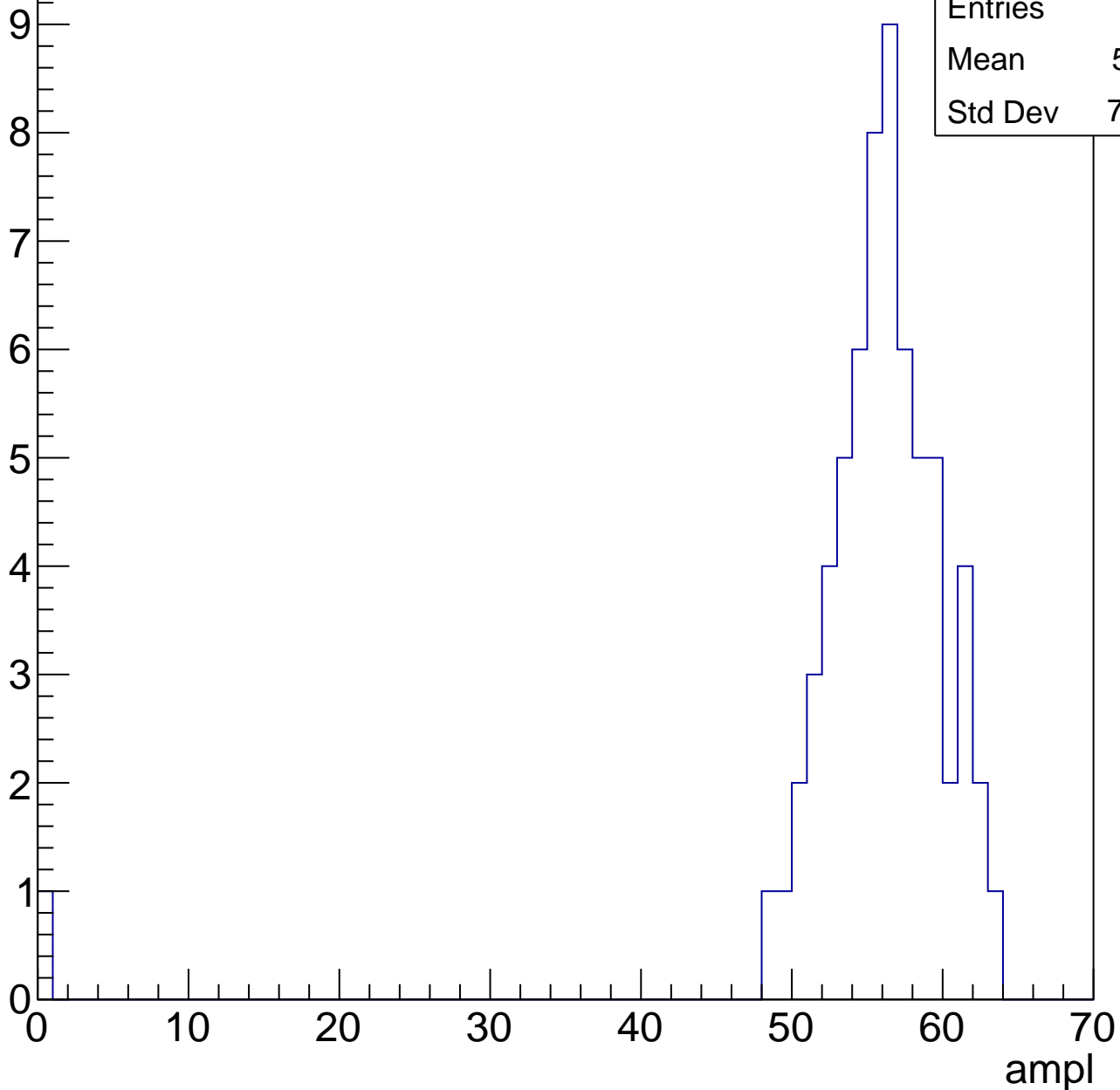


B1L102S, U19-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.91
Std Dev	7.629

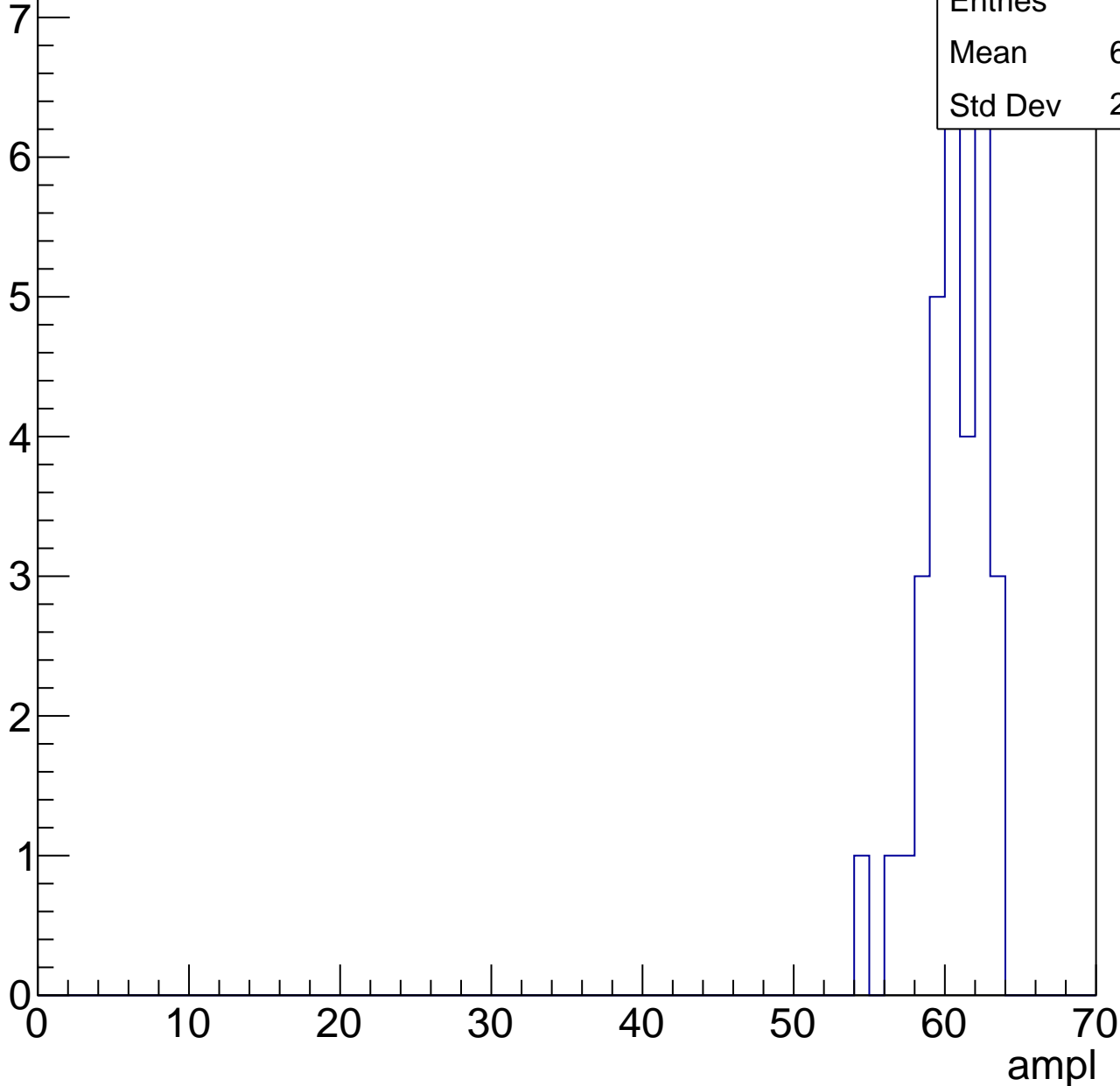


B1L102S, U19-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	60.09
Std Dev	2.067

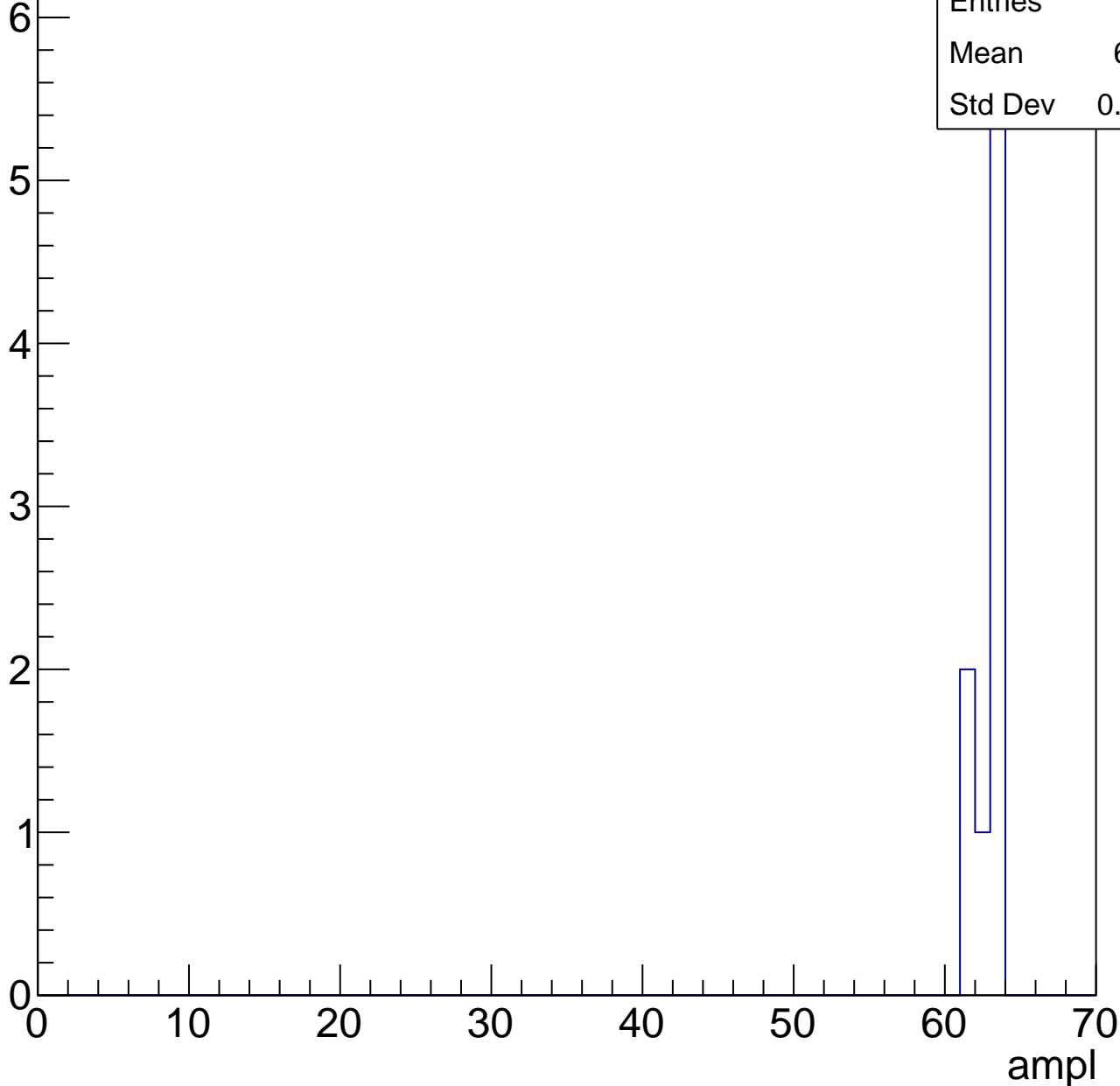


B1L102S, U19-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.44
Std Dev	0.8315



B1L102S, U19-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L102S, U19-ch104, adc0

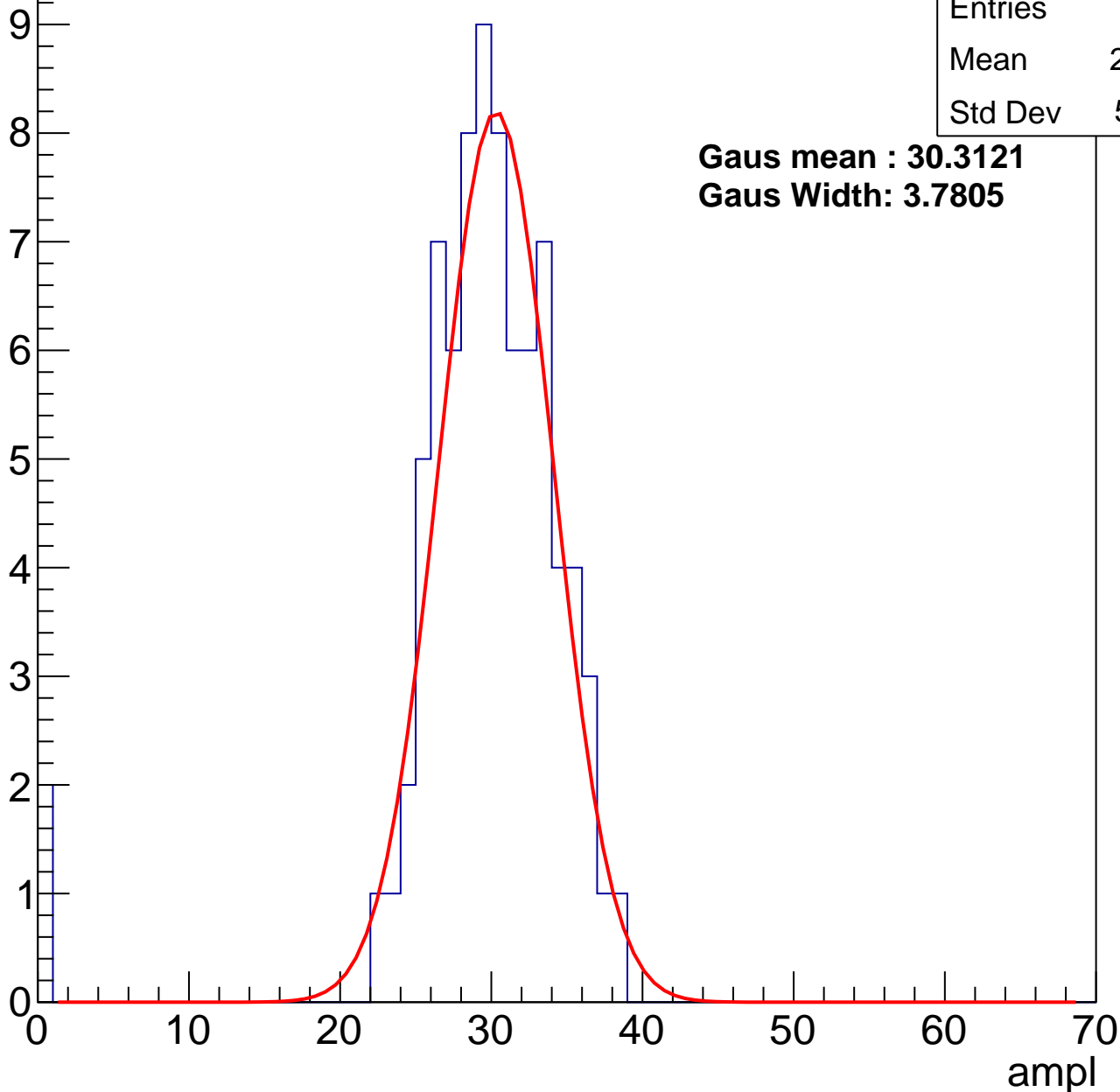
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.07
Std Dev	5.801

Gaus mean : 30.3121

Gaus Width: 3.7805



B1L102S, U19-ch104, adc1

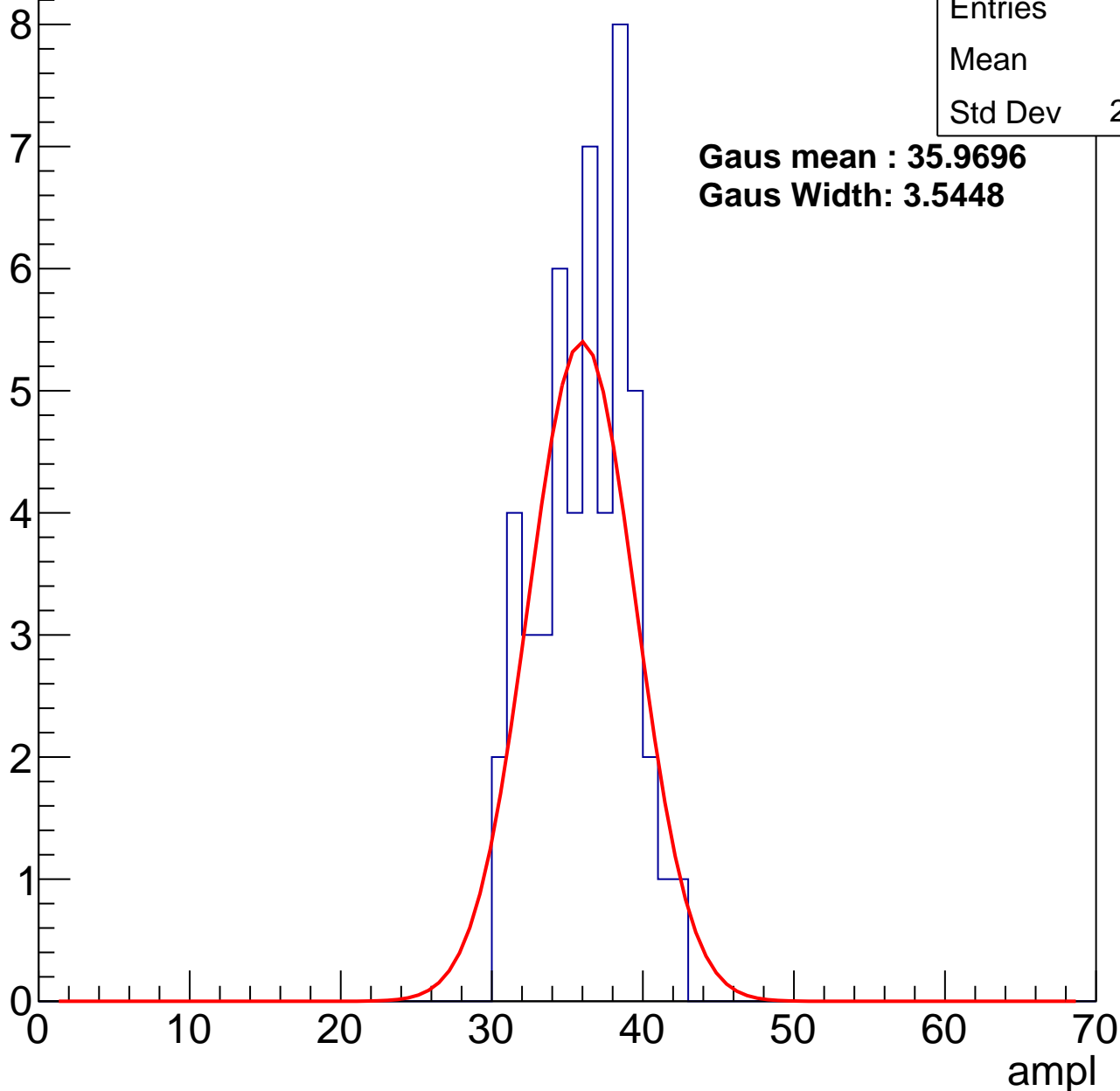
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	35.7
Std Dev	2.982

Gaus mean : 35.9696

Gaus Width: 3.5448



B1L102S, U19-ch104, adc2

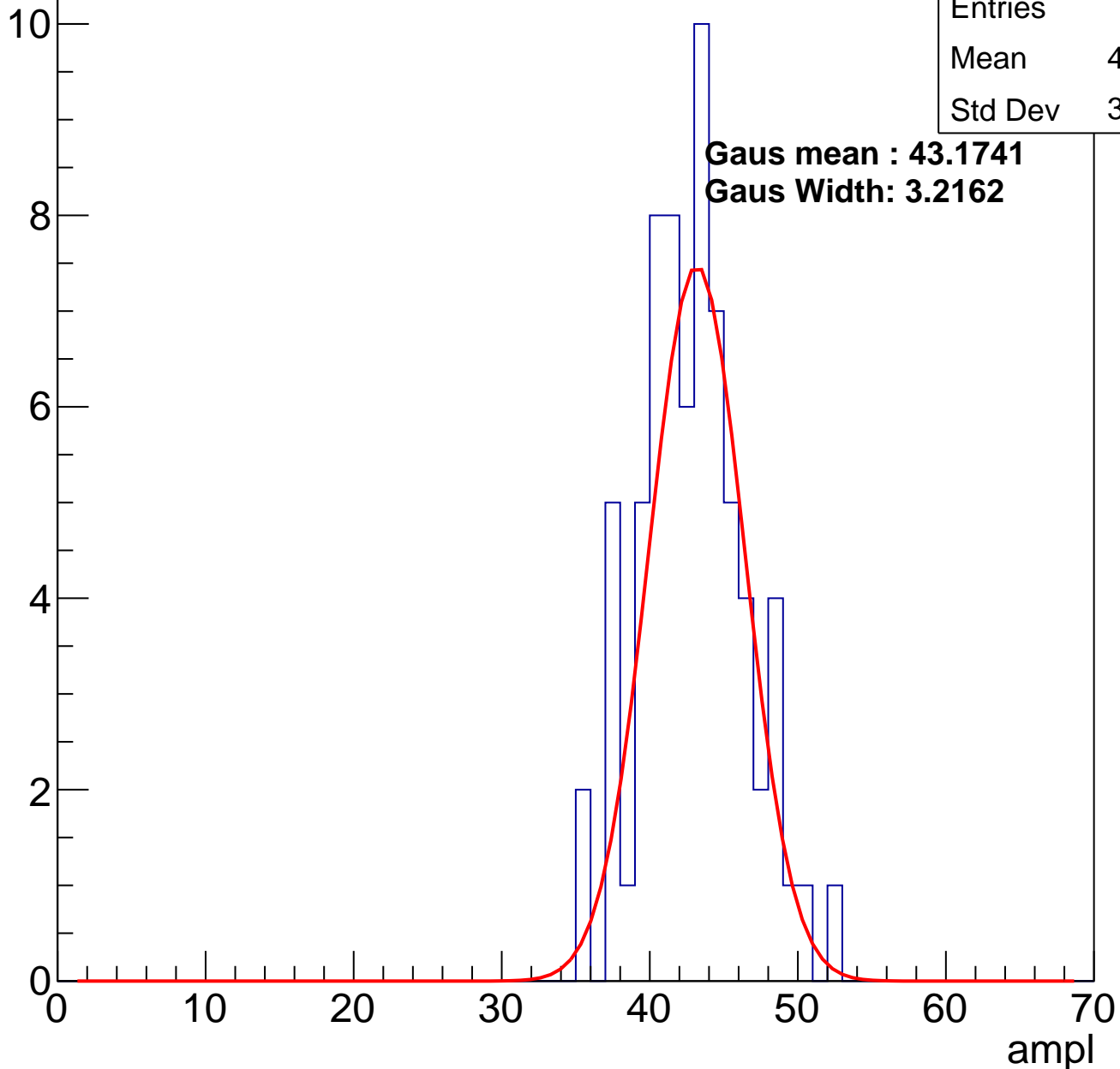
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	42.46
Std Dev	3.512

Gaus mean : 43.1741

Gaus Width: 3.2162

Entry

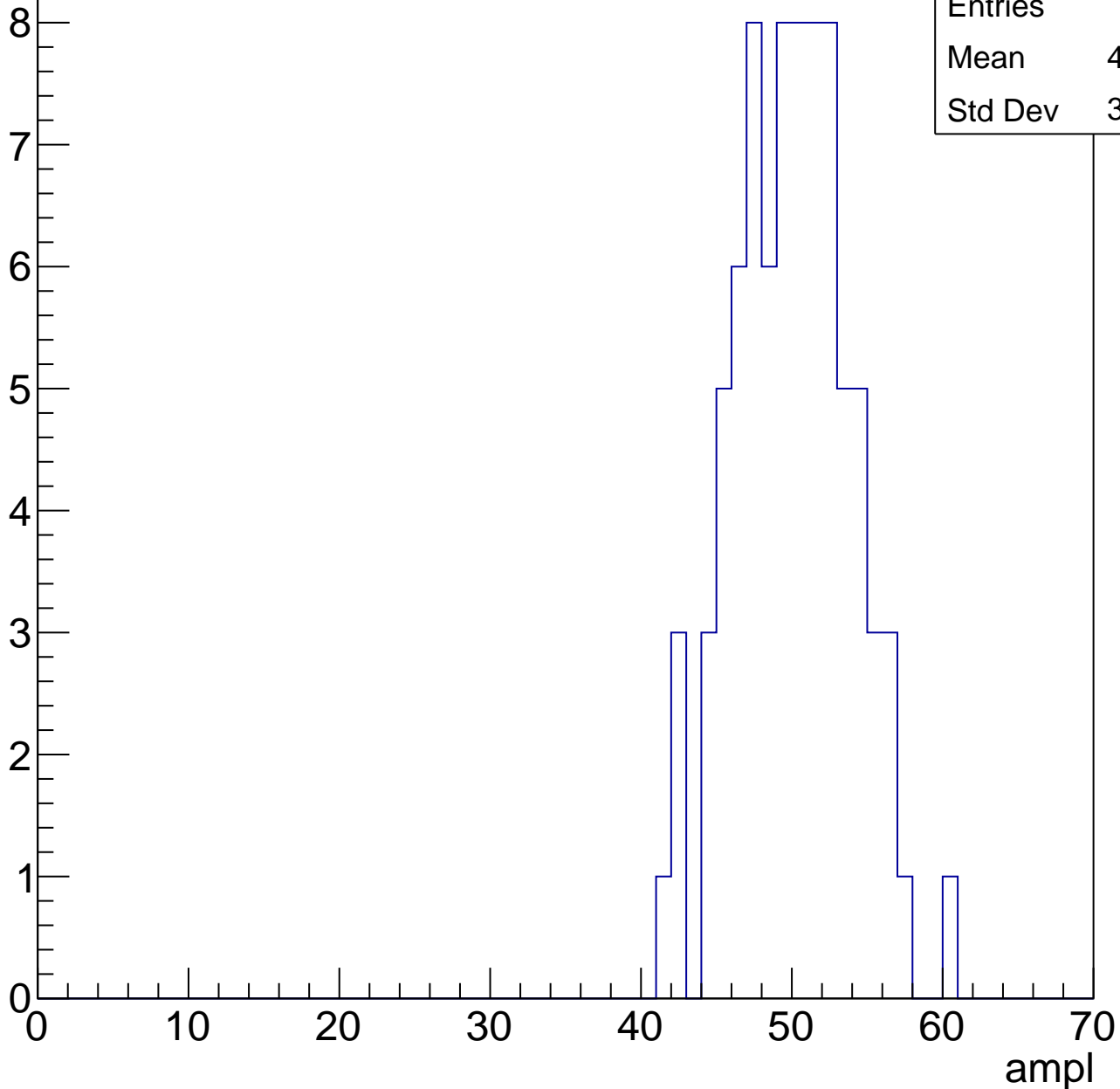


B1L102S, U19-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	49.57
Std Dev	3.806

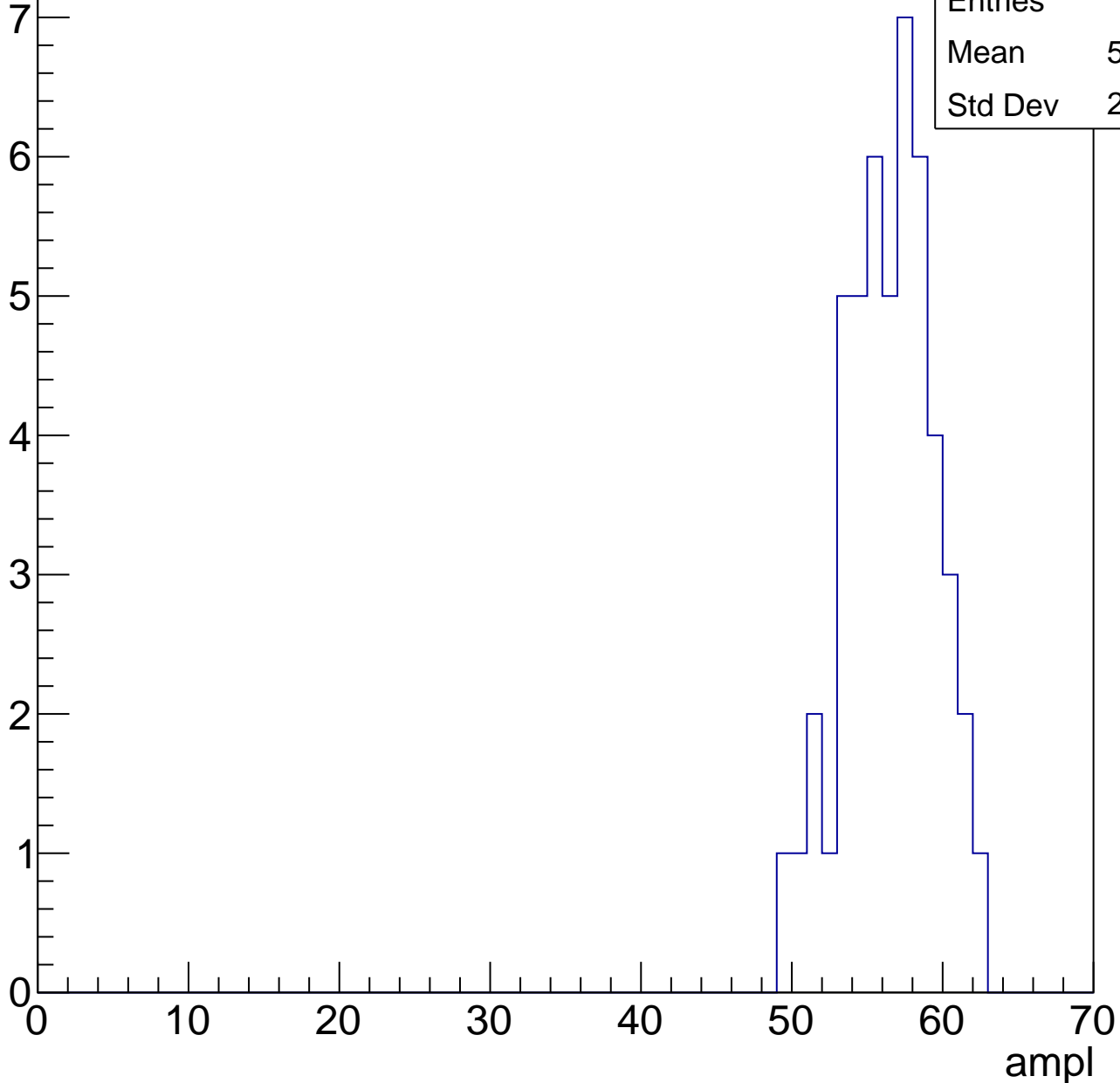


B1L102S, U19-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.02
Std Dev	2.938

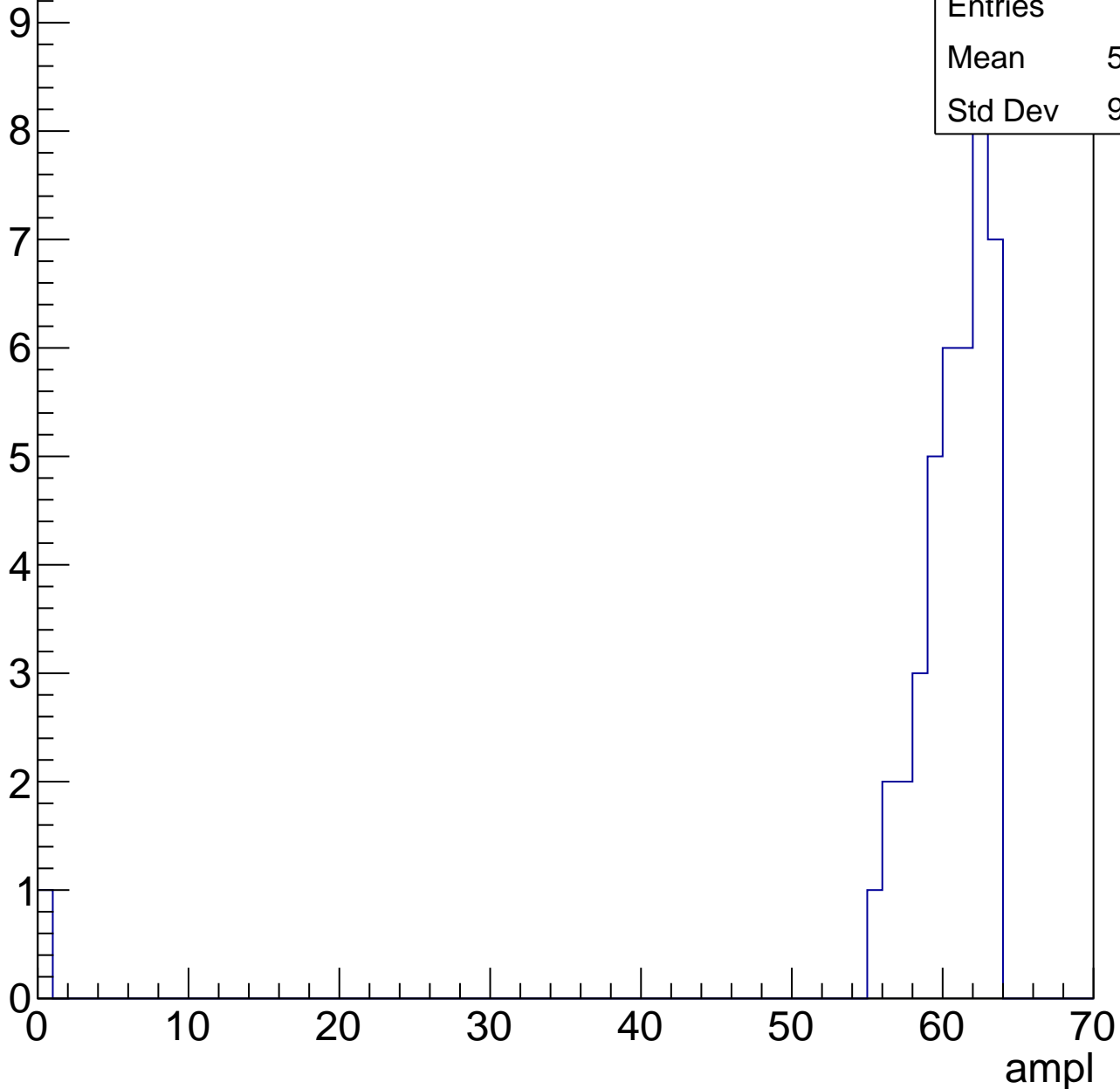


B1L102S, U19-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

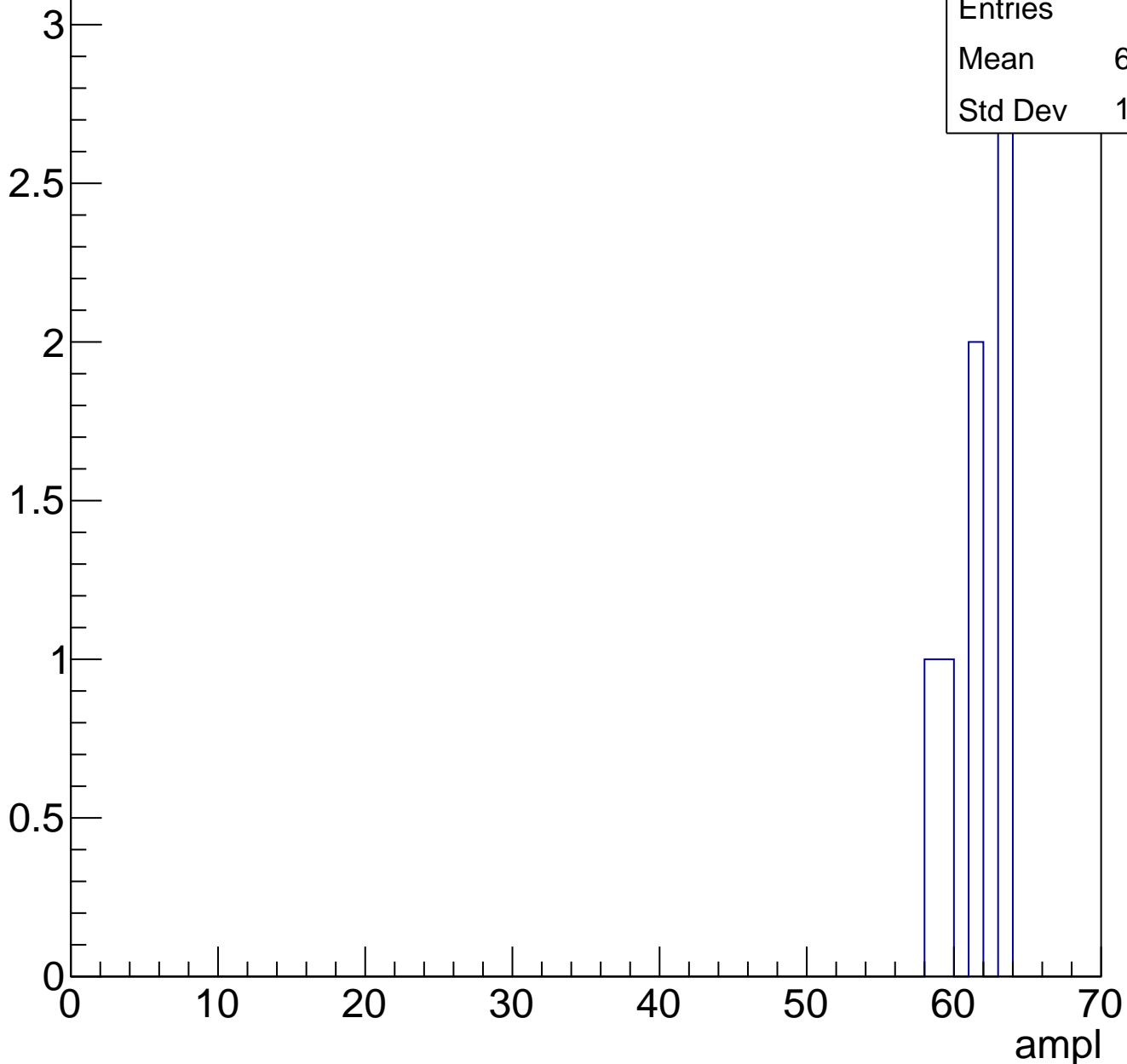
Entries	42
Mean	58.93
Std Dev	9.448



B1L102S, U19-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch105, adc0

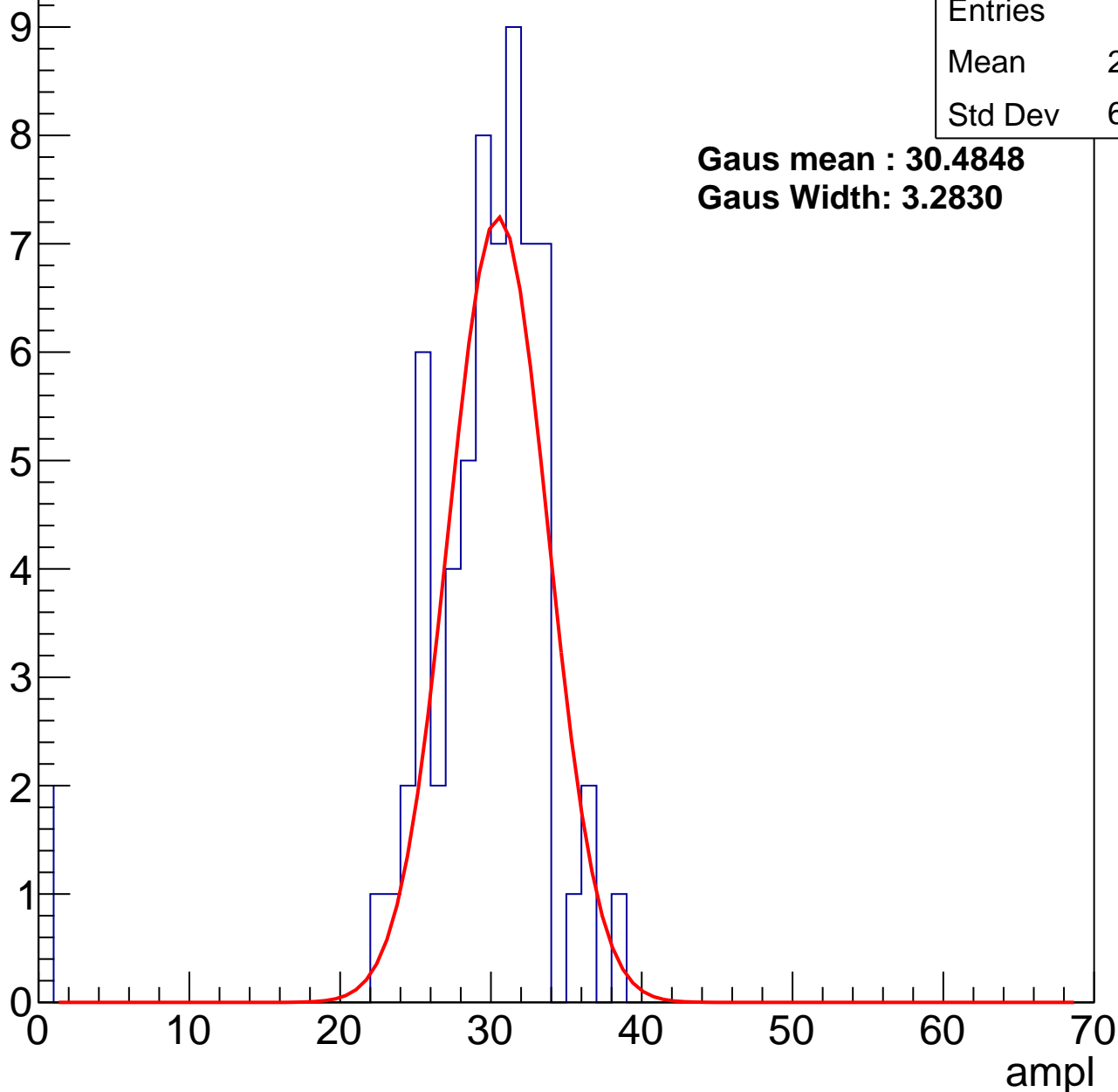
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.68
Std Dev	6.049

Gaus mean : 30.4848

Gaus Width: 3.2830



B1L102S, U19-ch105, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	37.44
Std Dev	3.57

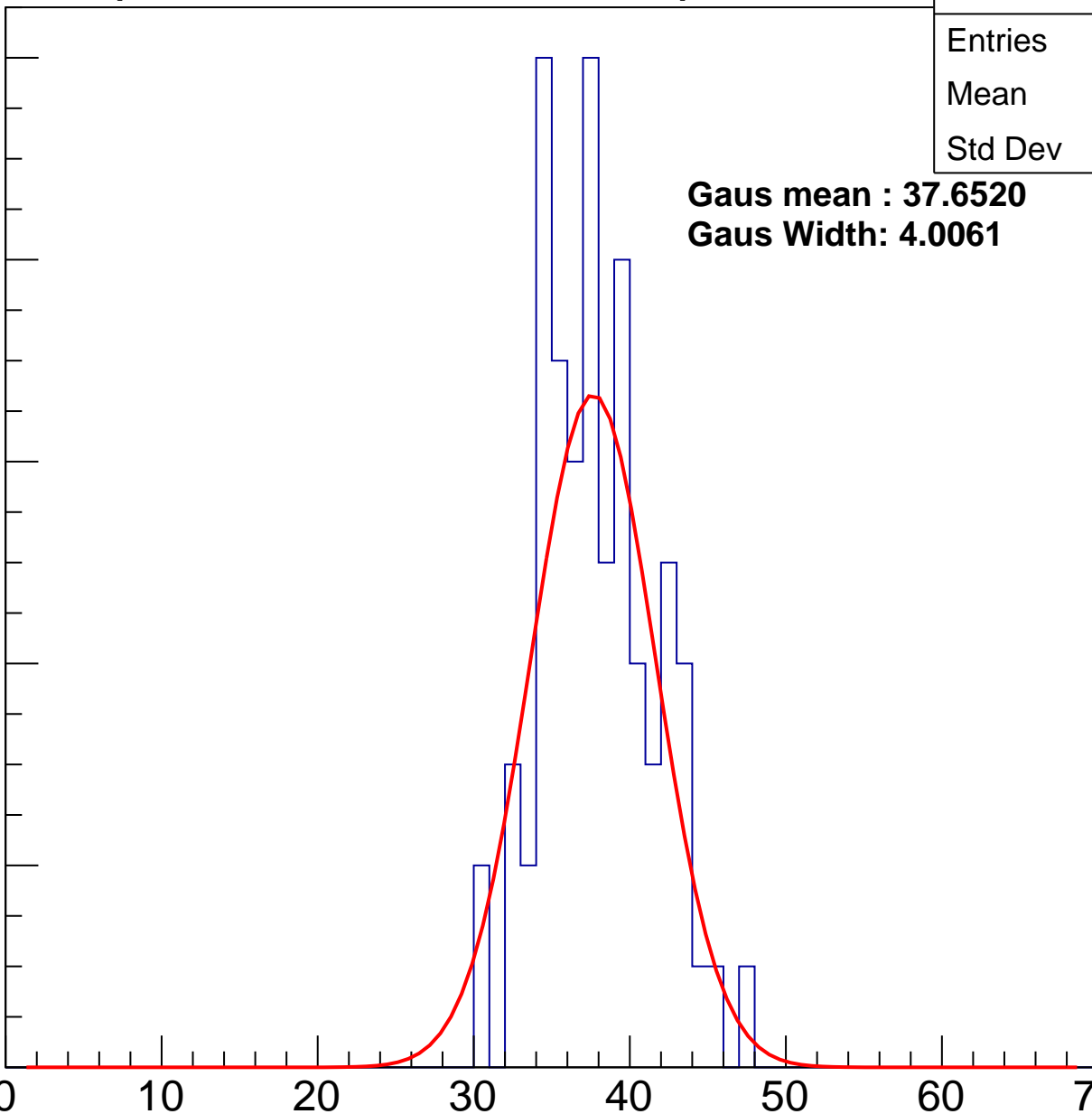
Gaus mean : 37.6520

Gaus Width: 4.0061

Entry

10
8
6
4
2
0

ampl



B1L102S, U19-ch105, adc2

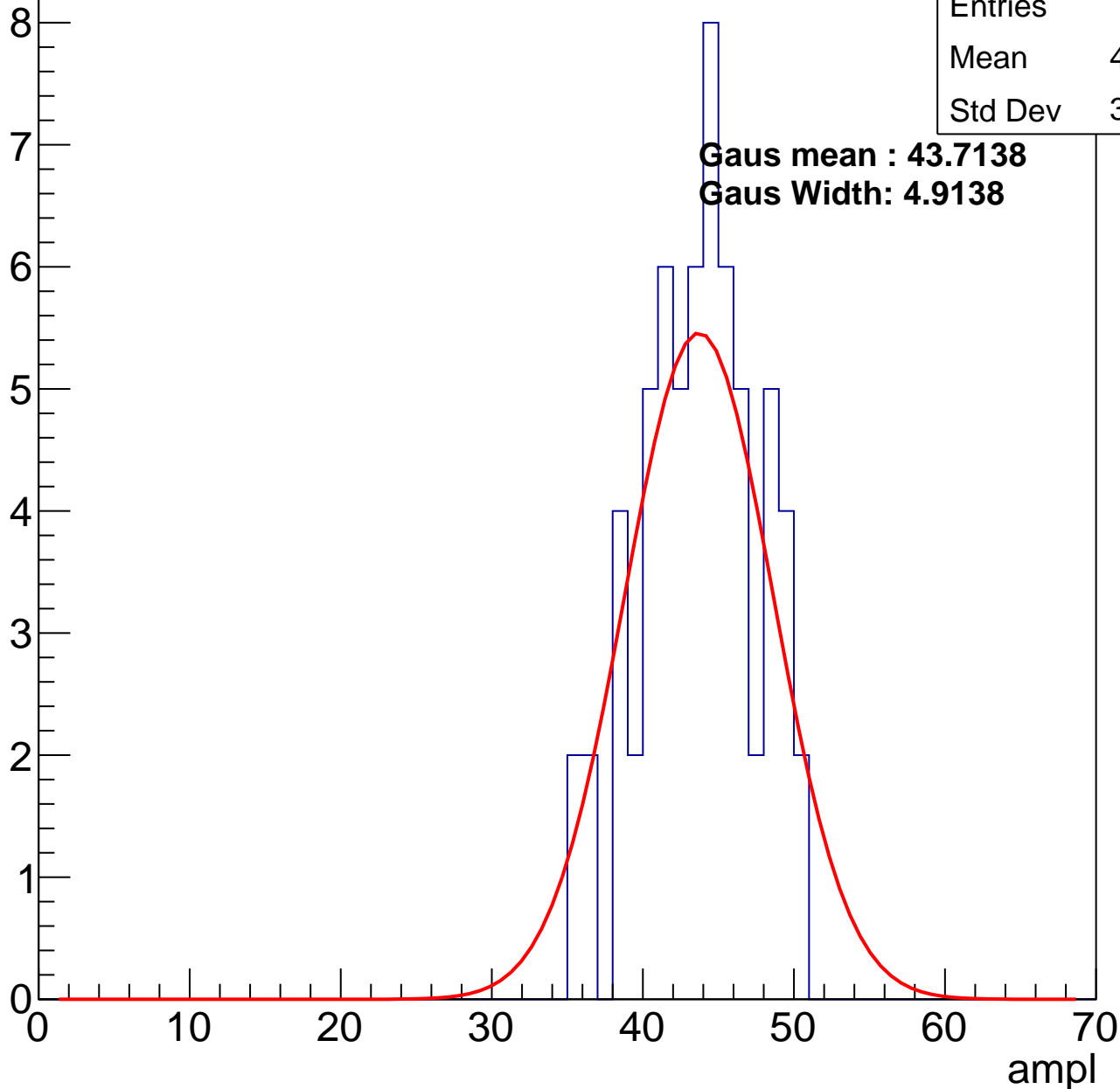
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.25
Std Dev	3.758

Gaus mean : 43.7138

Gaus Width: 4.9138

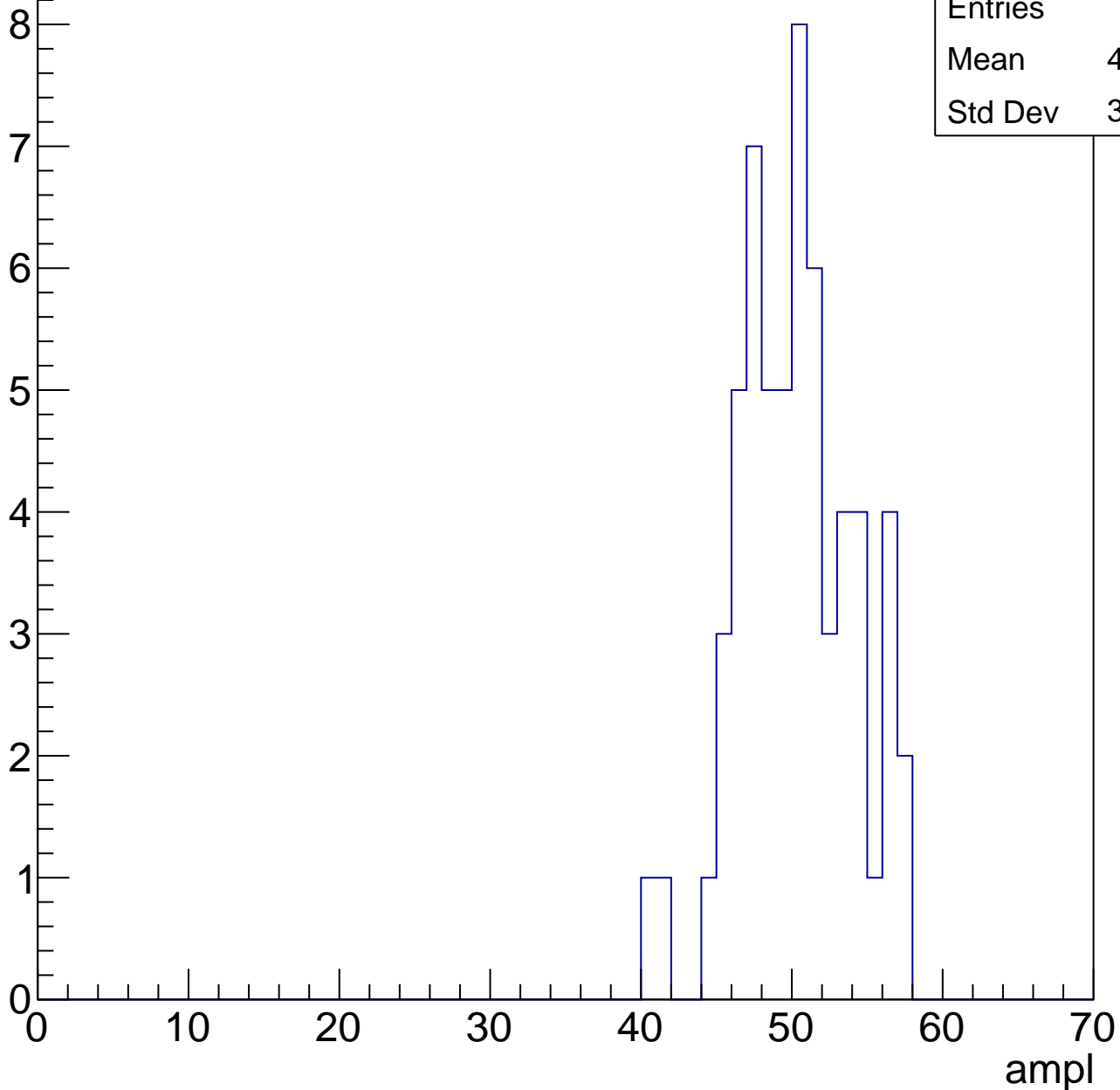


B1L102S, U19-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.78
Std Dev	3.747

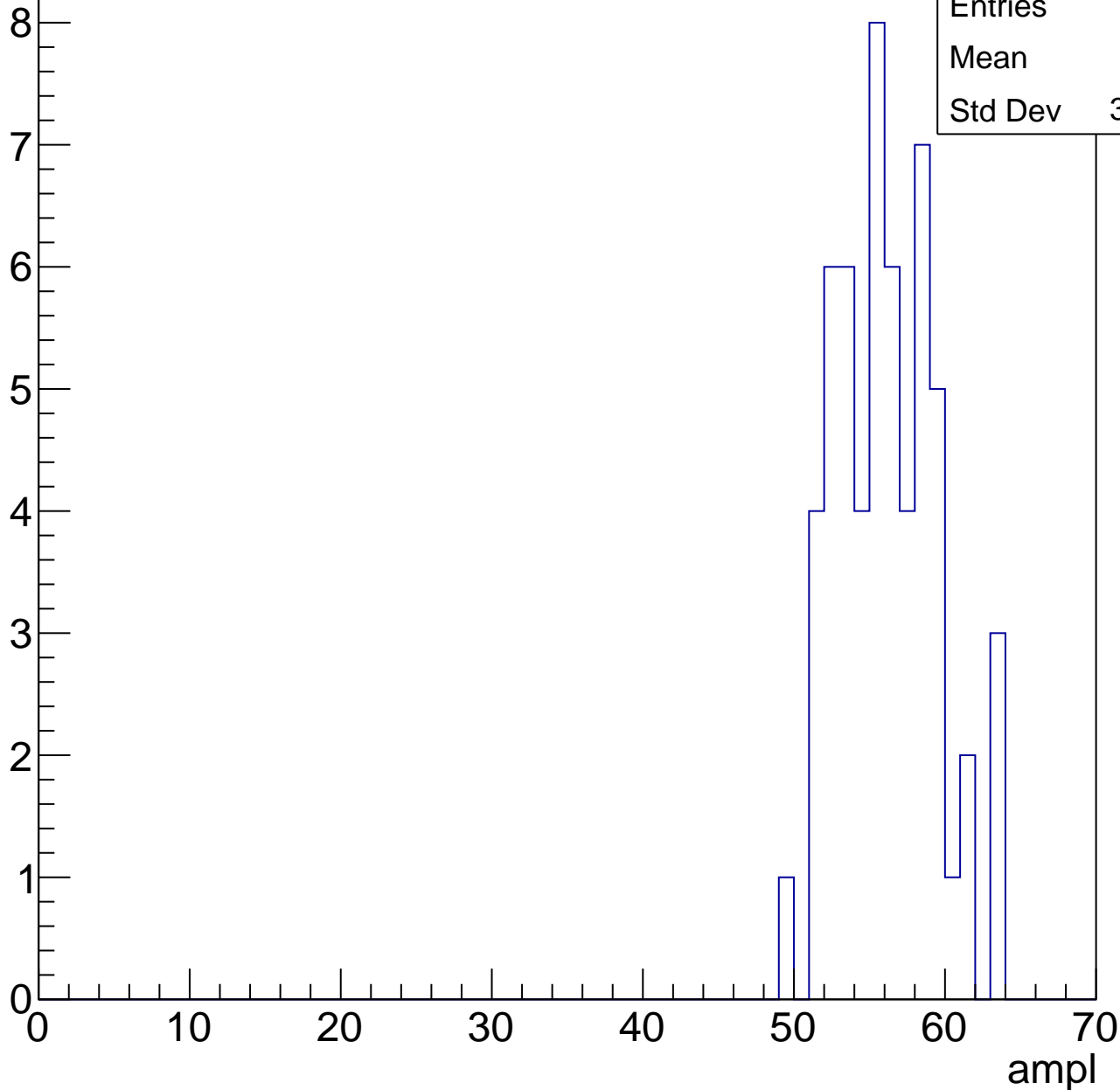


B1L102S, U19-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.7
Std Dev	3.266

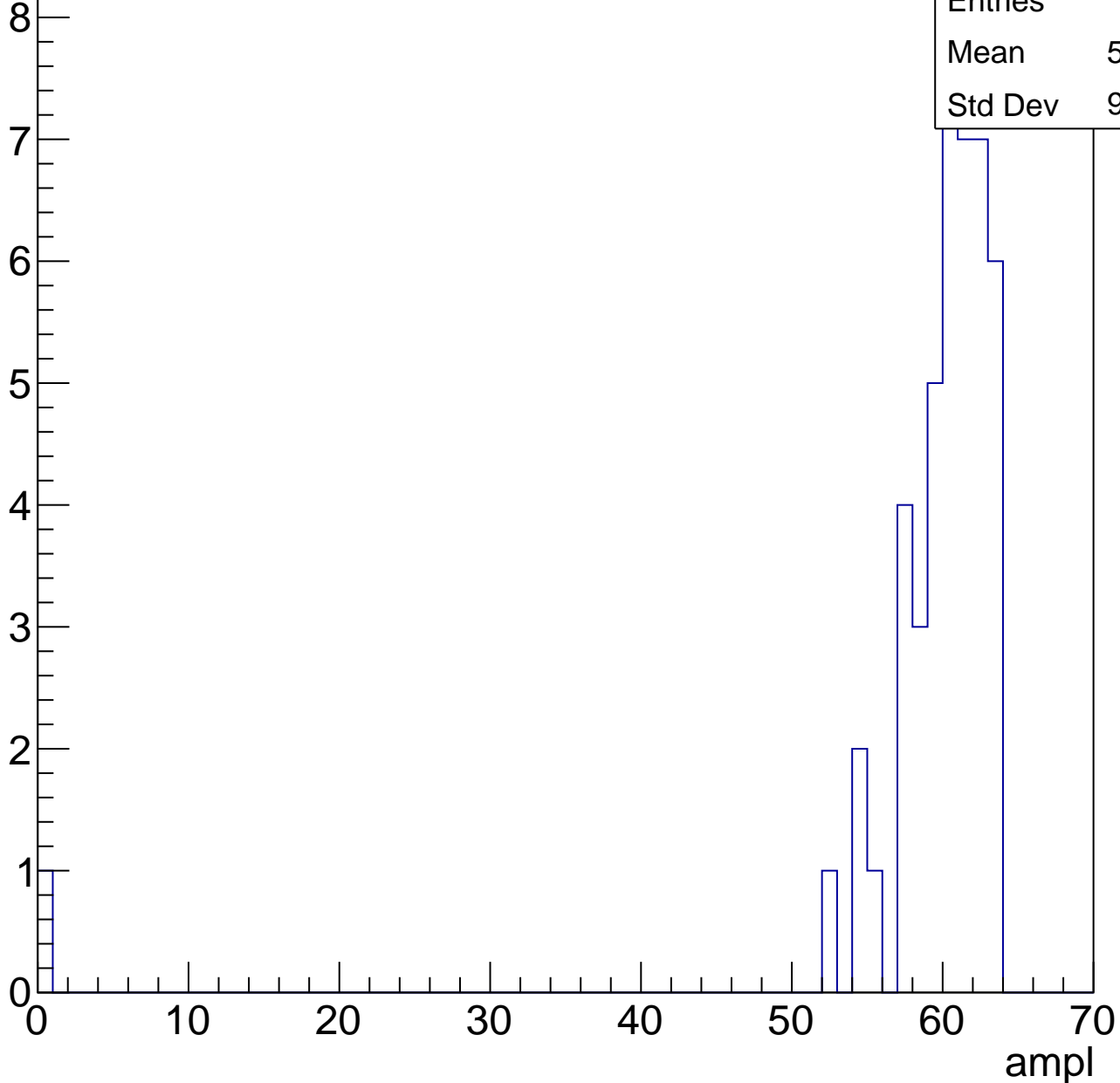


B1L102S, U19-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

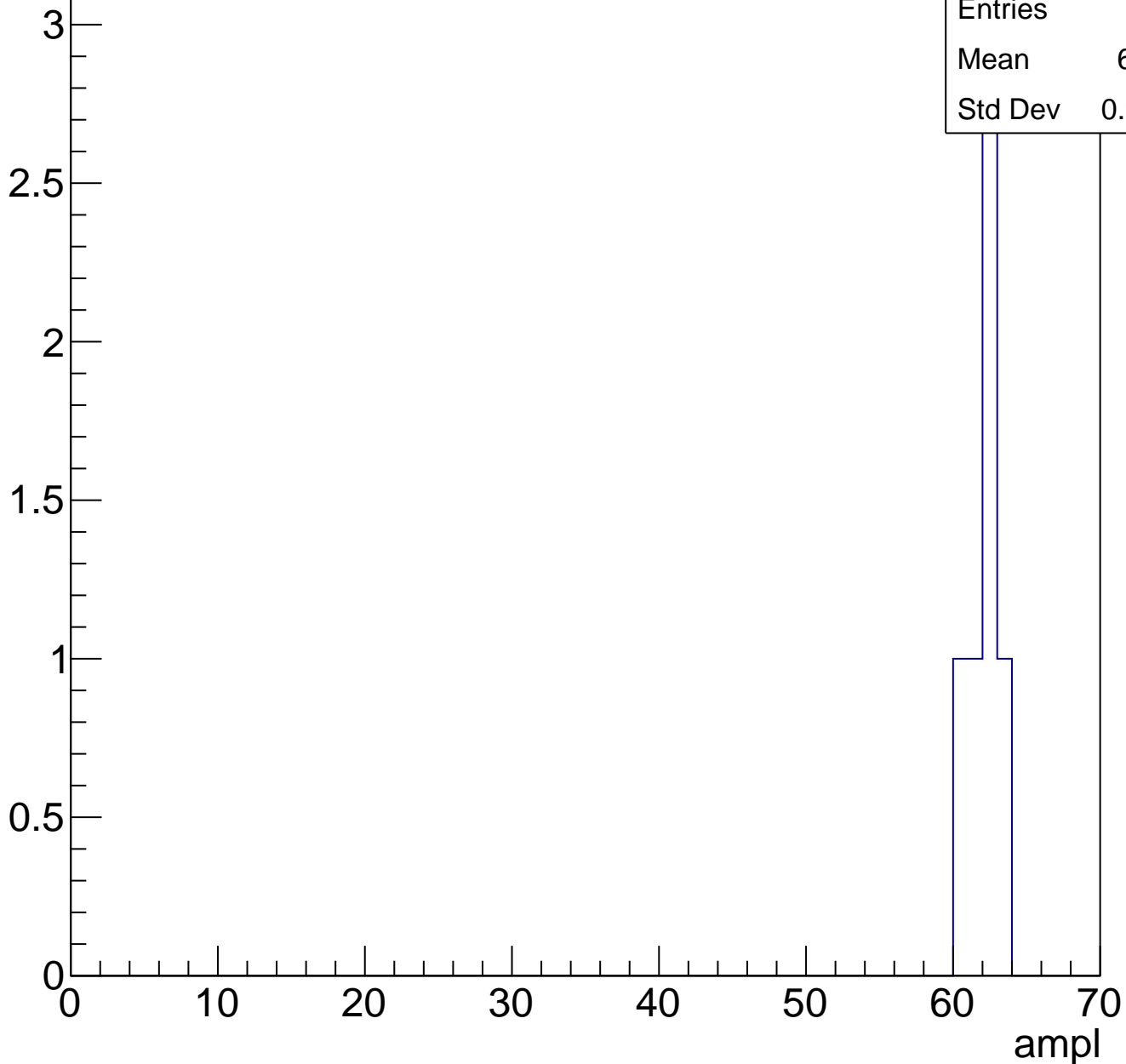
Entries	45
Mean	58.47
Std Dev	9.186



B1L102S, U19-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch106, adc0

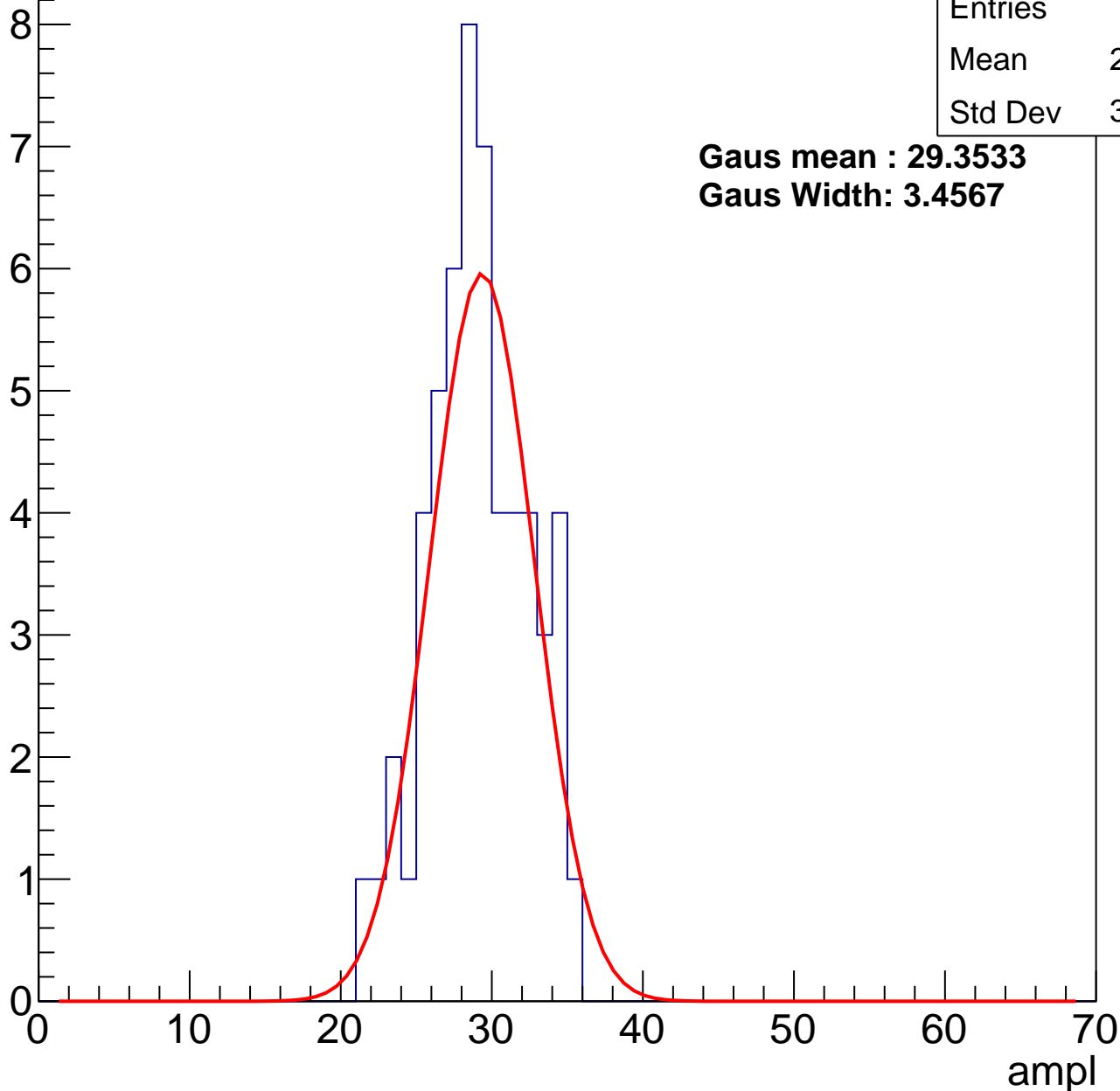
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.62
Std Dev	3.256

Gaus mean : 29.3533

Gaus Width: 3.4567



B1L102S, U19-ch106, adc1

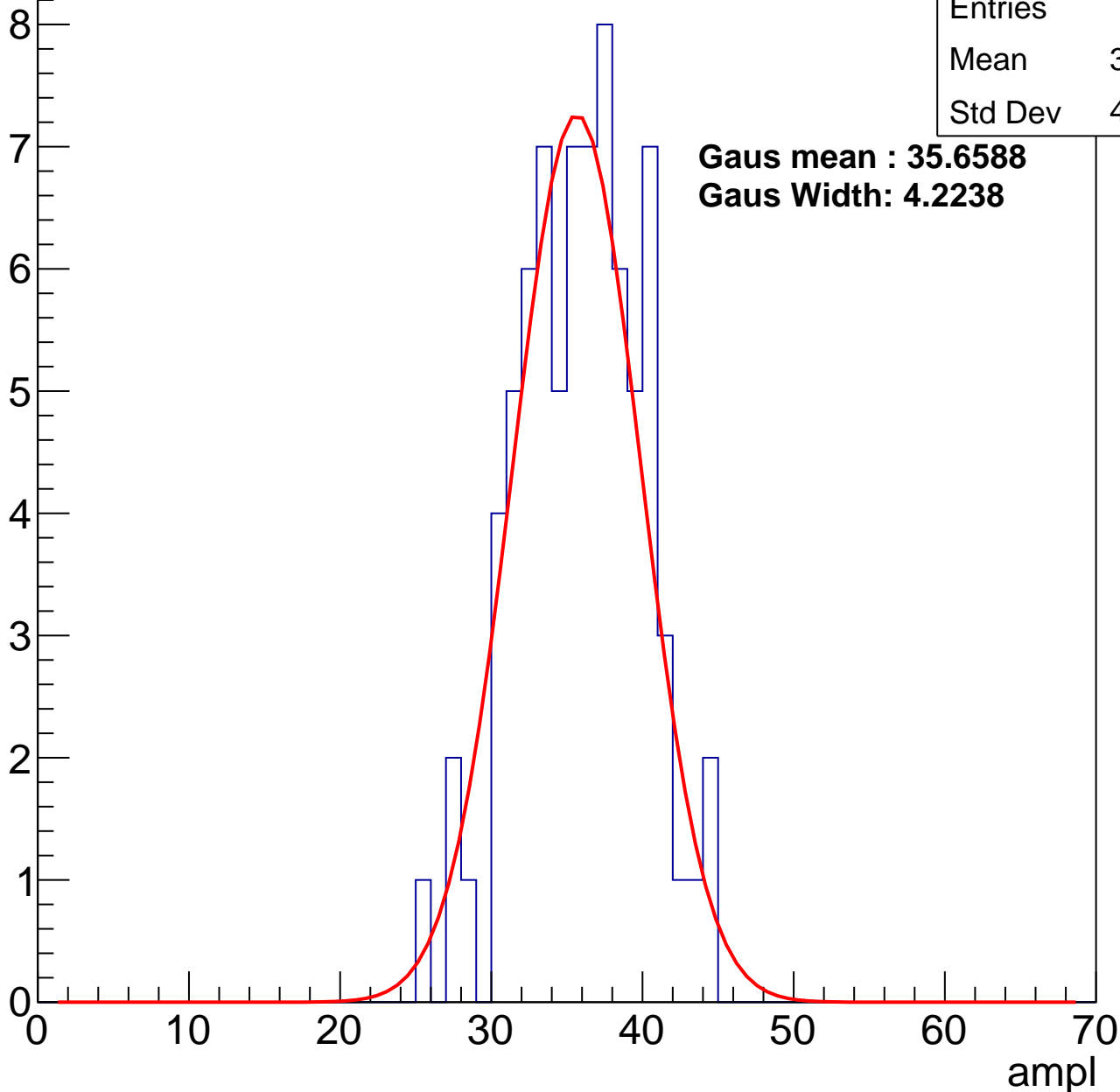
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.47
Std Dev	4.015

Gaus mean : 35.6588

Gaus Width: 4.2238



B1L102S, U19-ch106, adc2

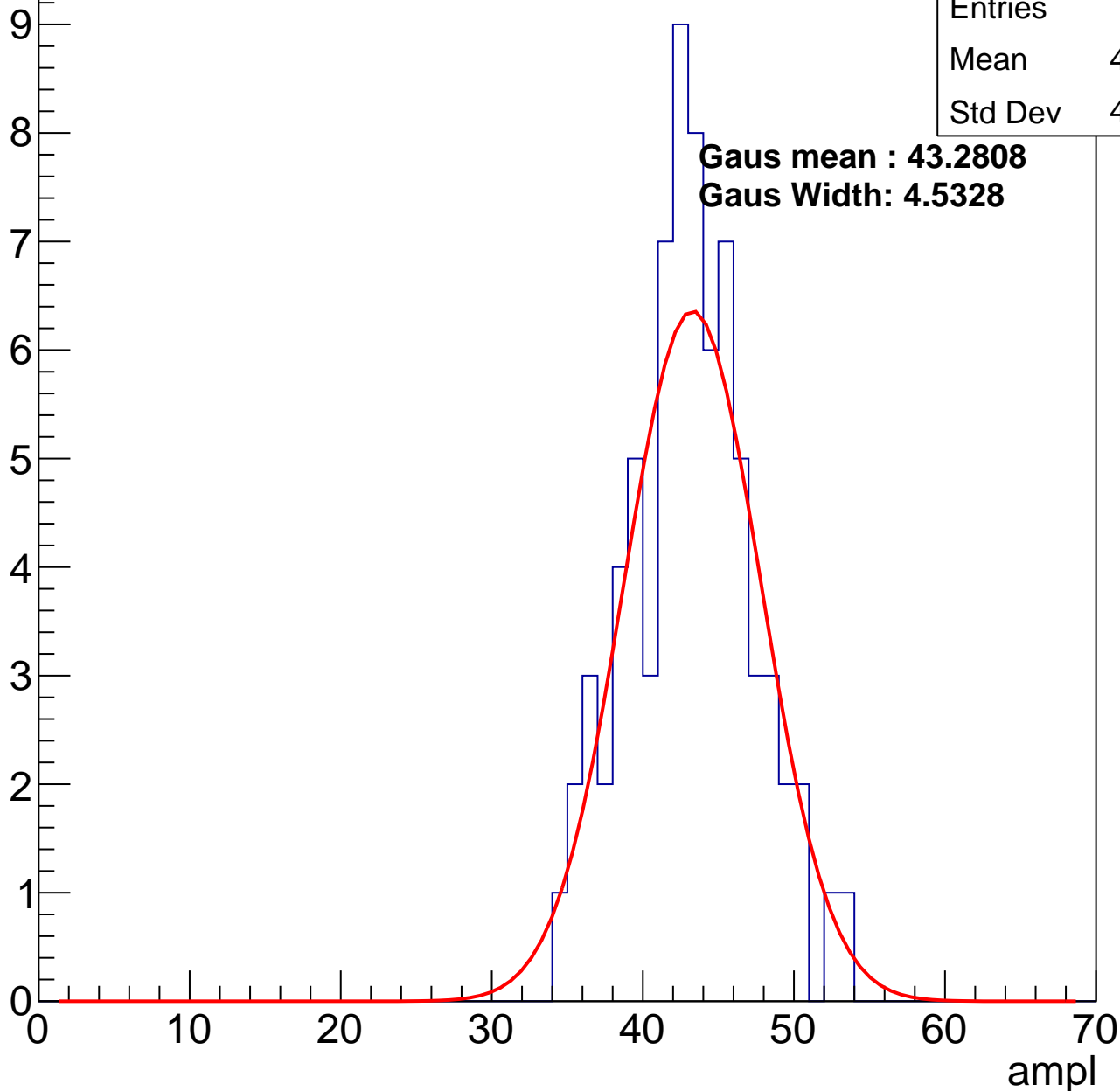
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.69
Std Dev	4.057

Gaus mean : 43.2808

Gaus Width: 4.5328

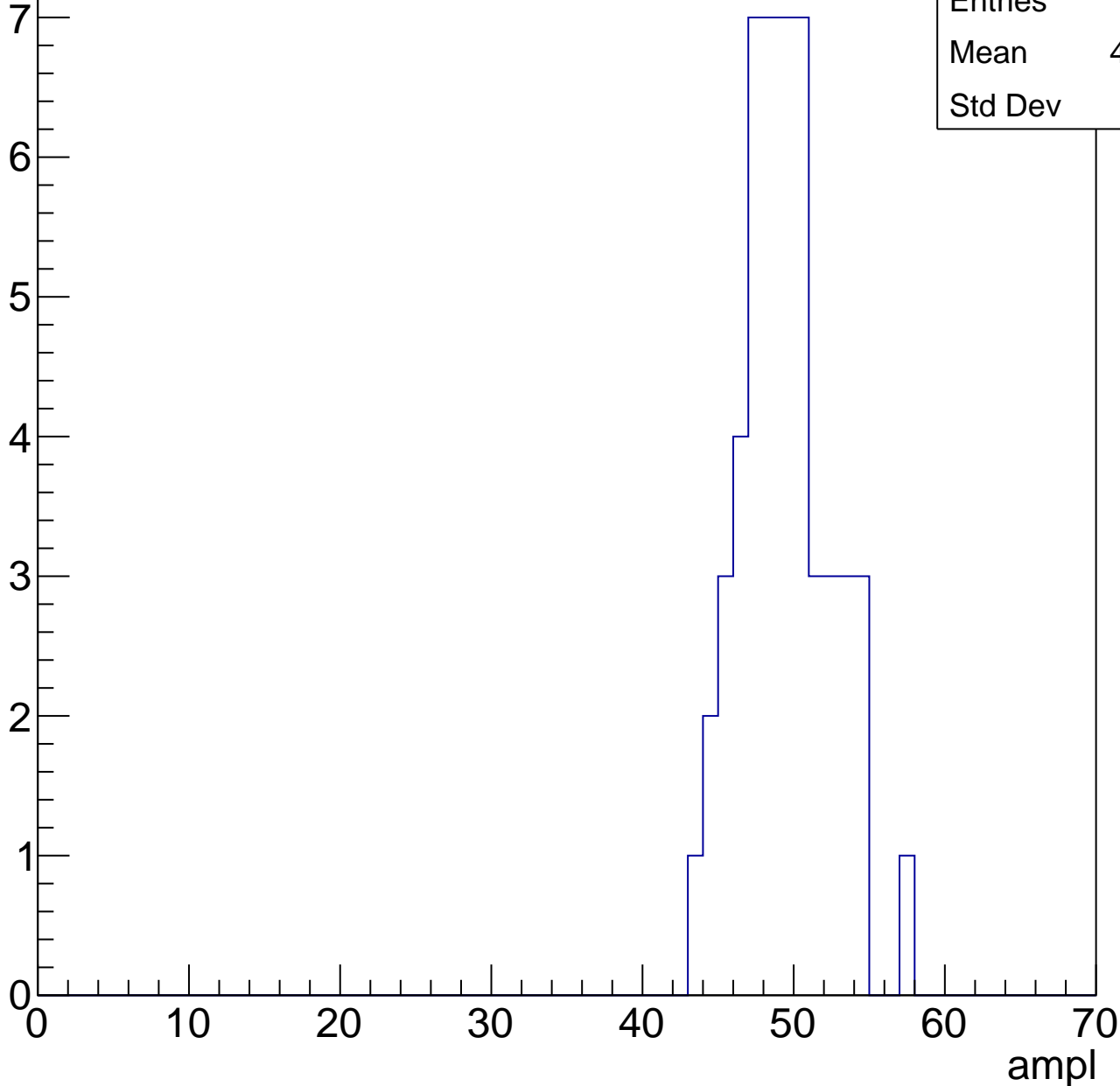


B1L102S, U19-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	48.92
Std Dev	2.93

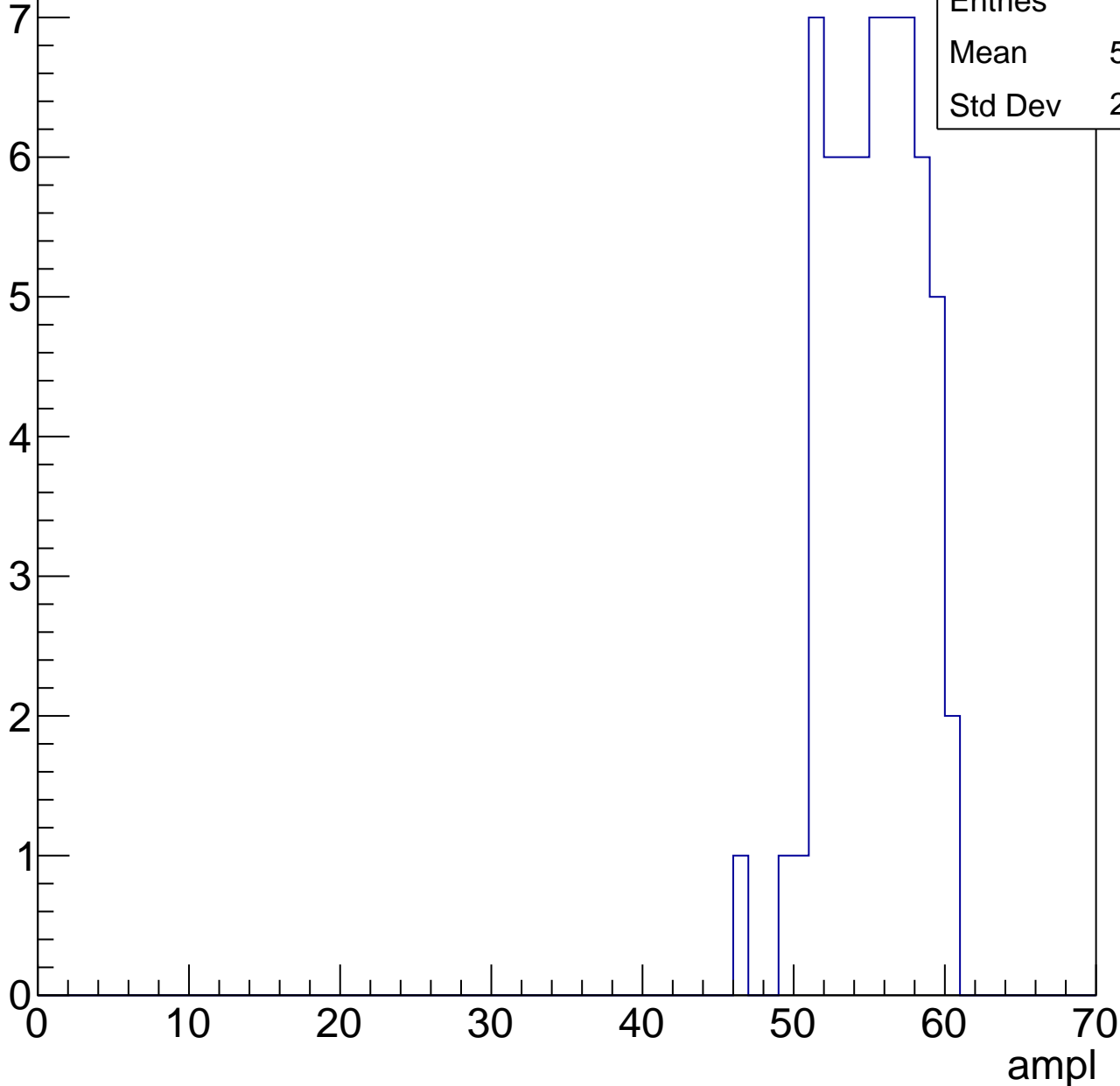


B1L102S, U19-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.76
Std Dev	2.988

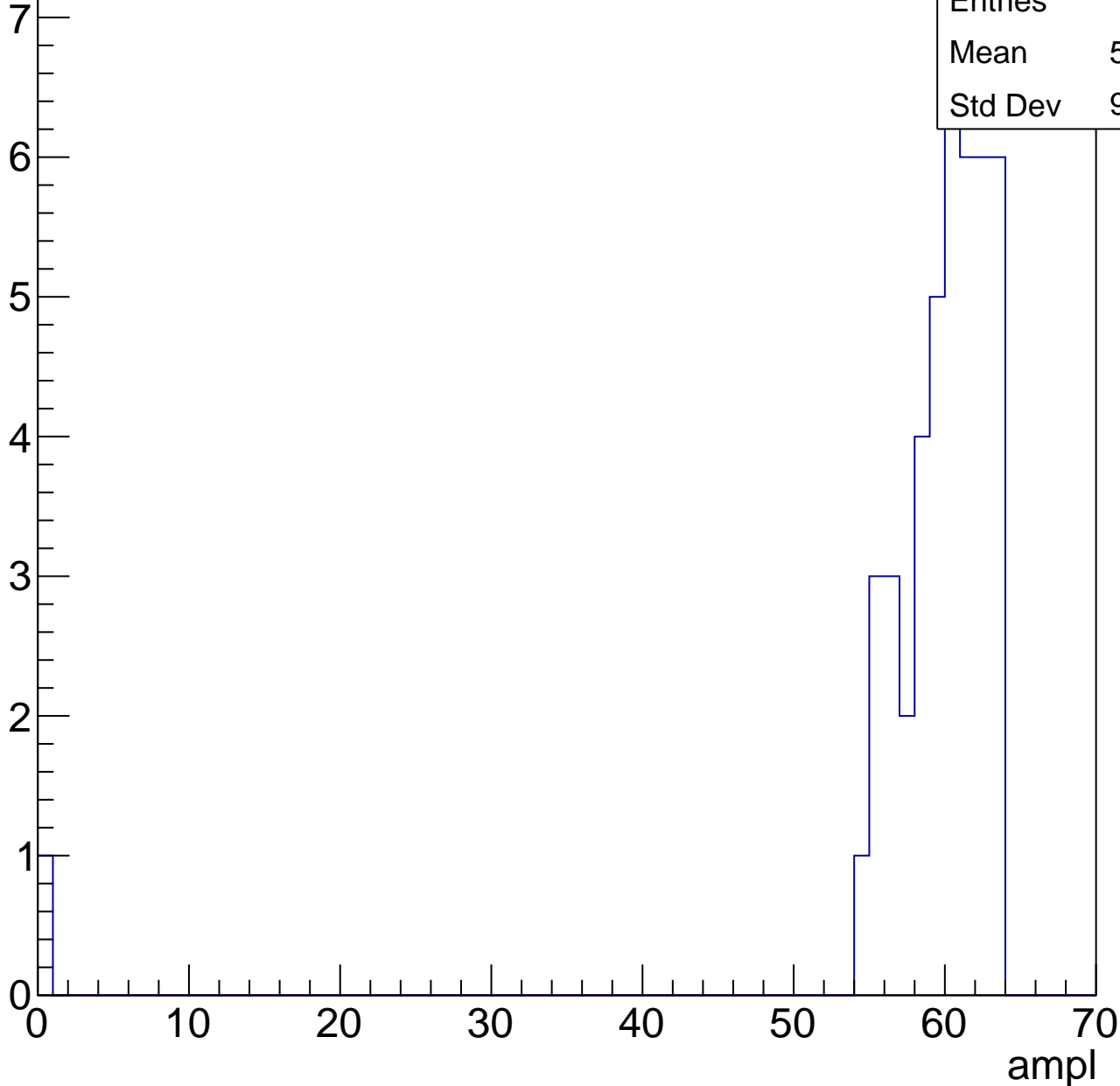


B1L102S, U19-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

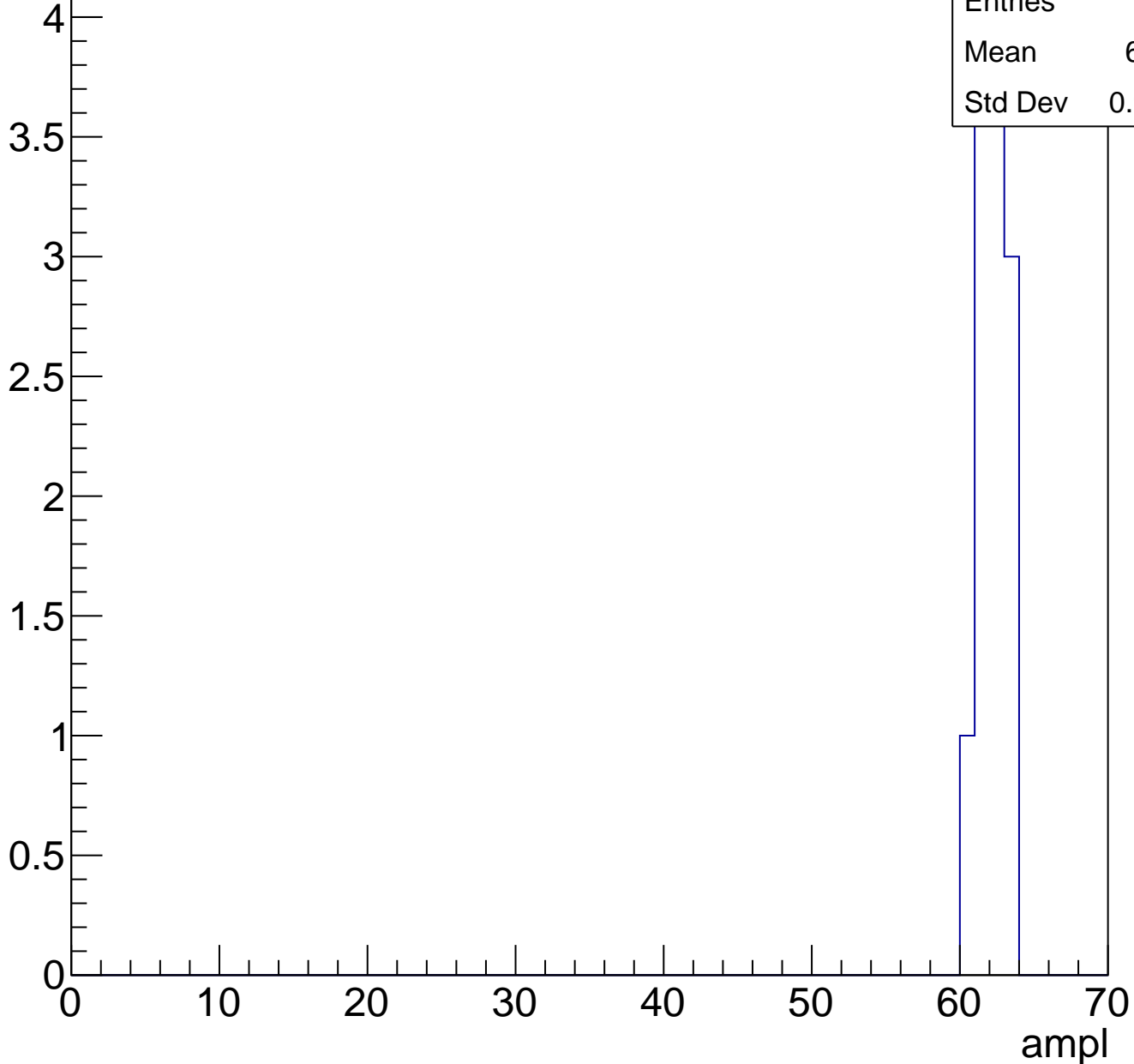
Entries	44
Mean	58.27
Std Dev	9.233



B1L102S, U19-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch107, adc0

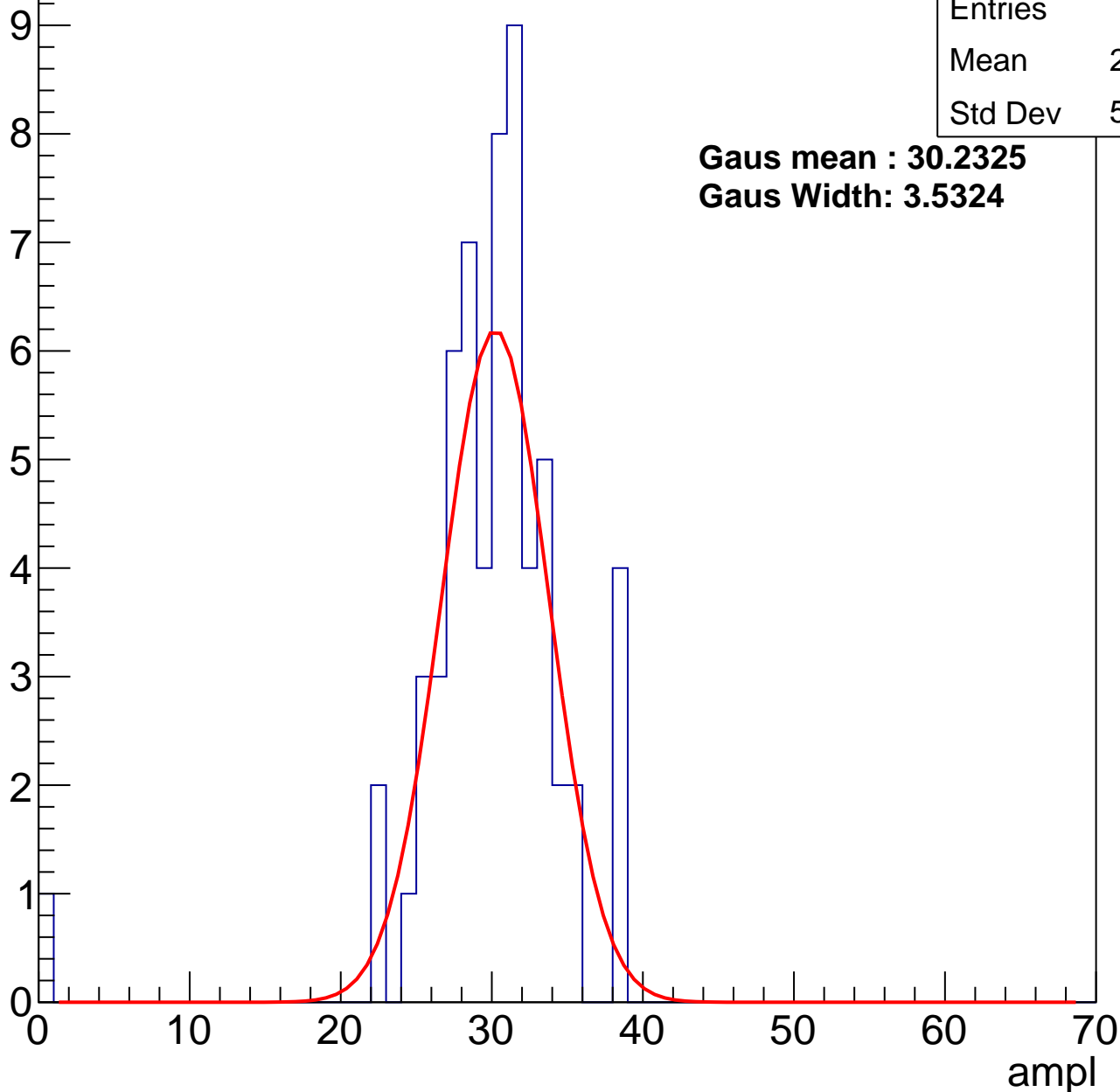
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.46
Std Dev	5.222

Gaus mean : 30.2325

Gaus Width: 3.5324



B1L102S, U19-ch107, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	36.49
Std Dev	3.582

Gaus mean : 36.7230

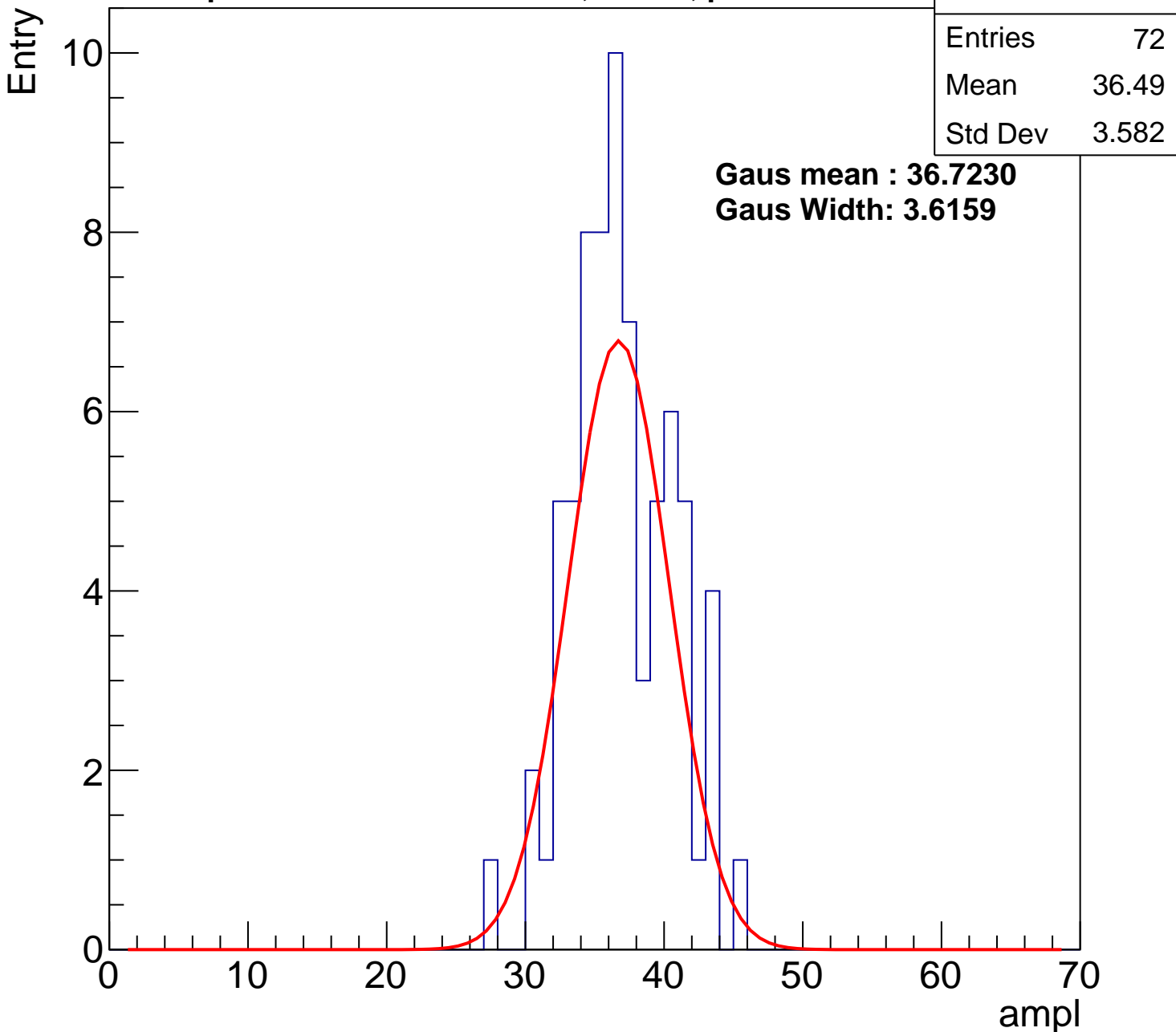
Gaus Width: 3.6159

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch107, adc2

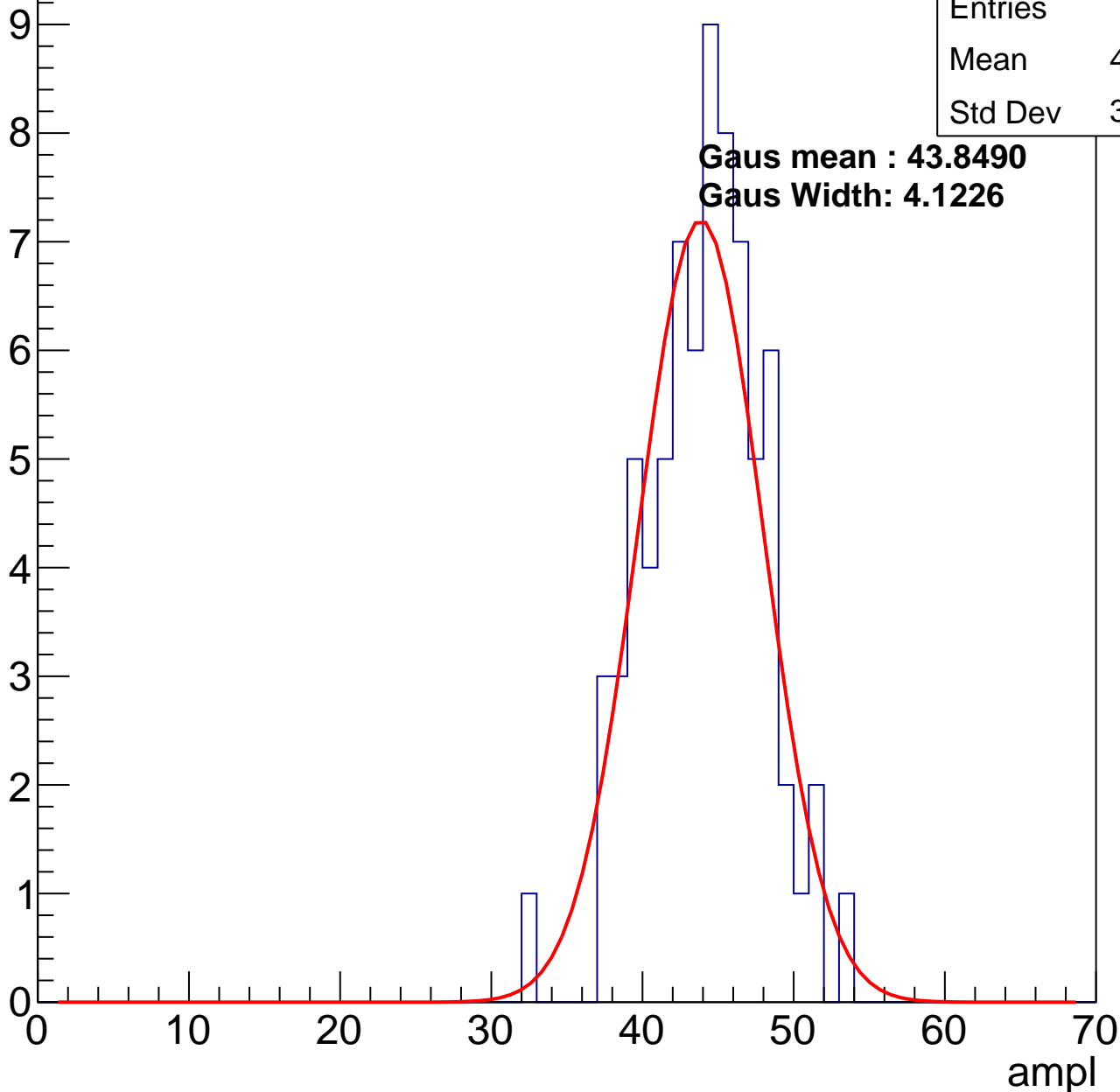
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.64
Std Dev	3.828

Gaus mean : 43.8490

Gaus Width: 4.1226

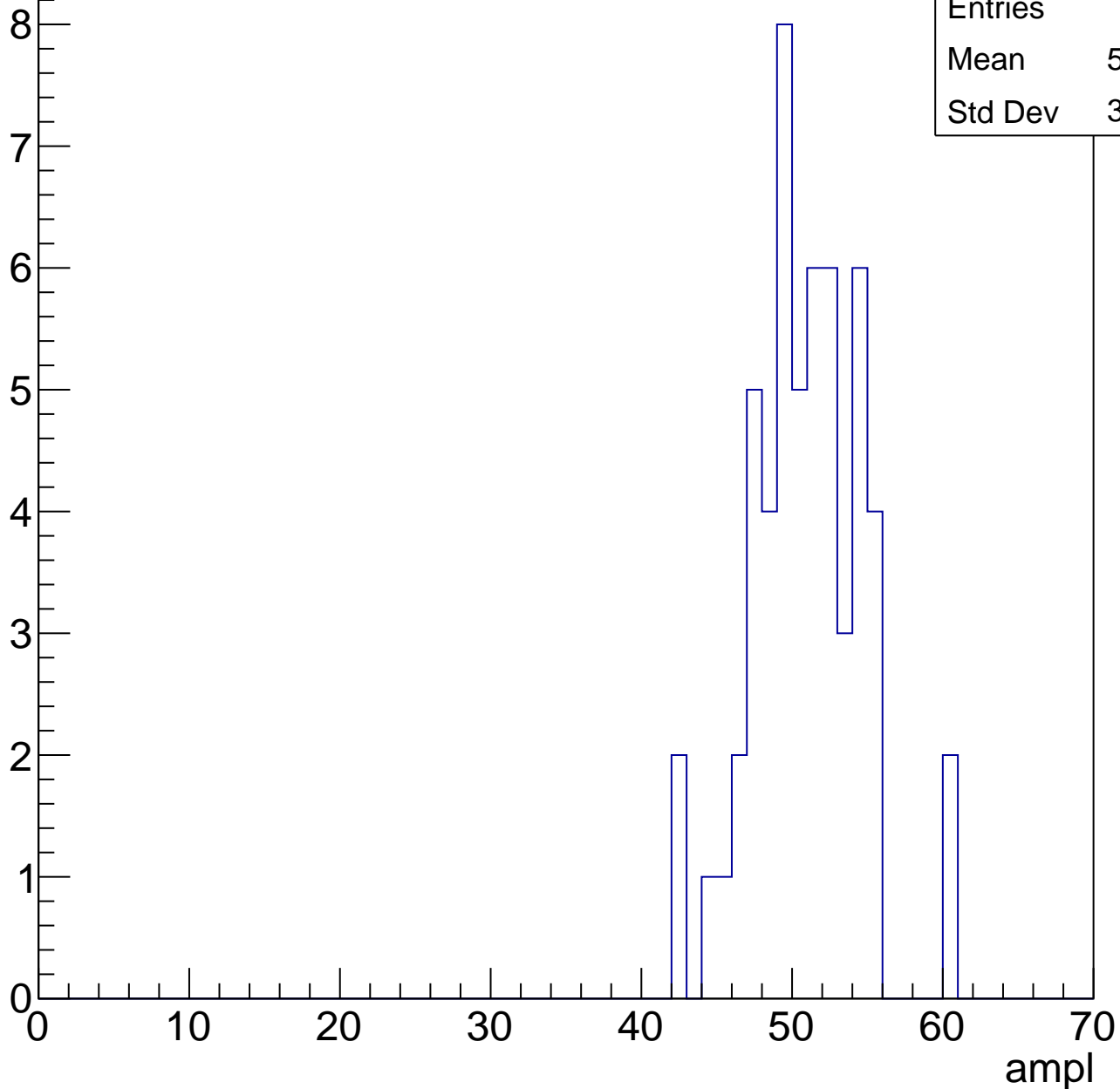


B1L102S, U19-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.45
Std Dev	3.647

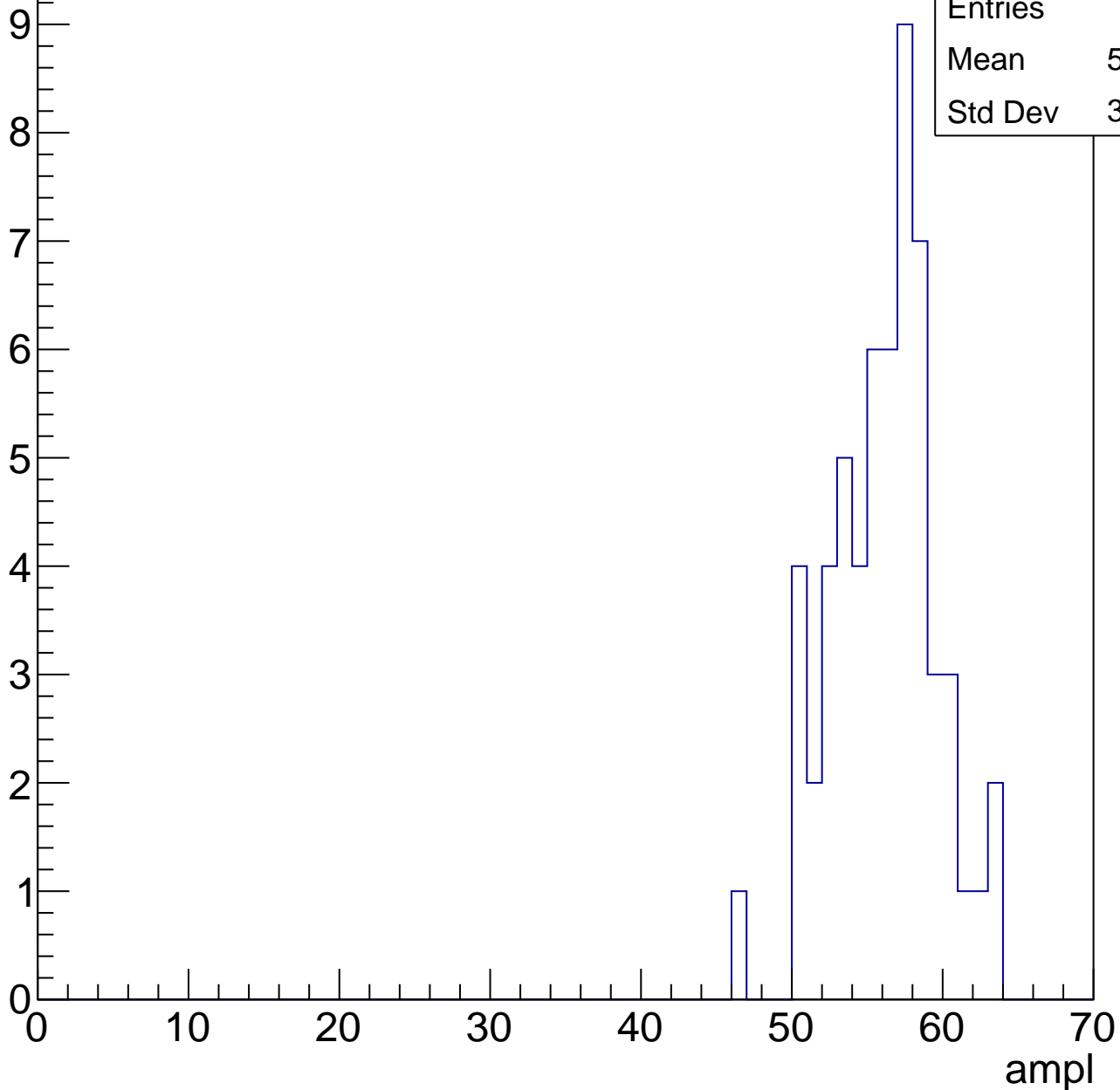


B1L102S, U19-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.66
Std Dev	3.447

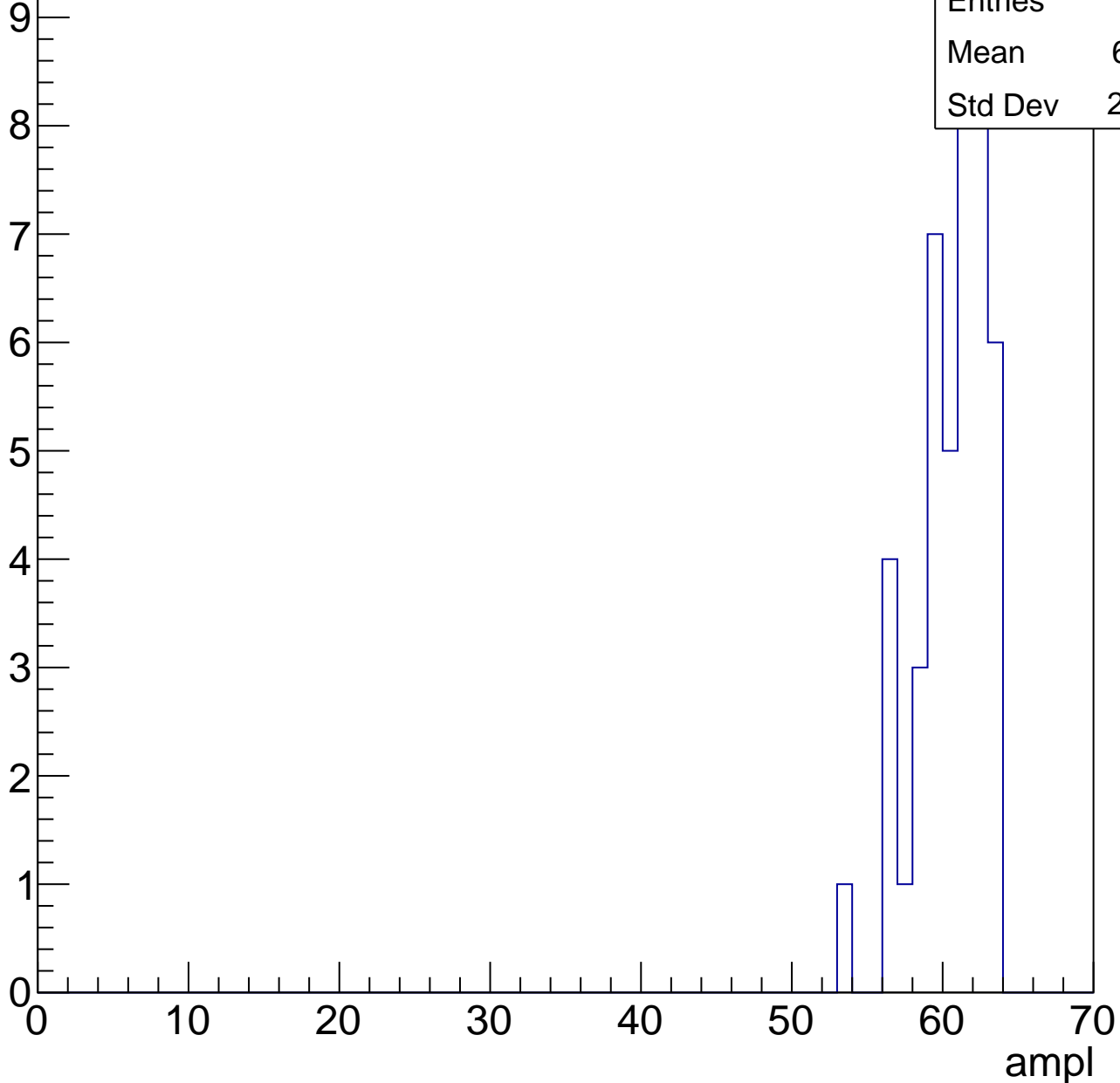


B1L102S, U19-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	60.11
Std Dev	2.328



B1L102S, U19-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

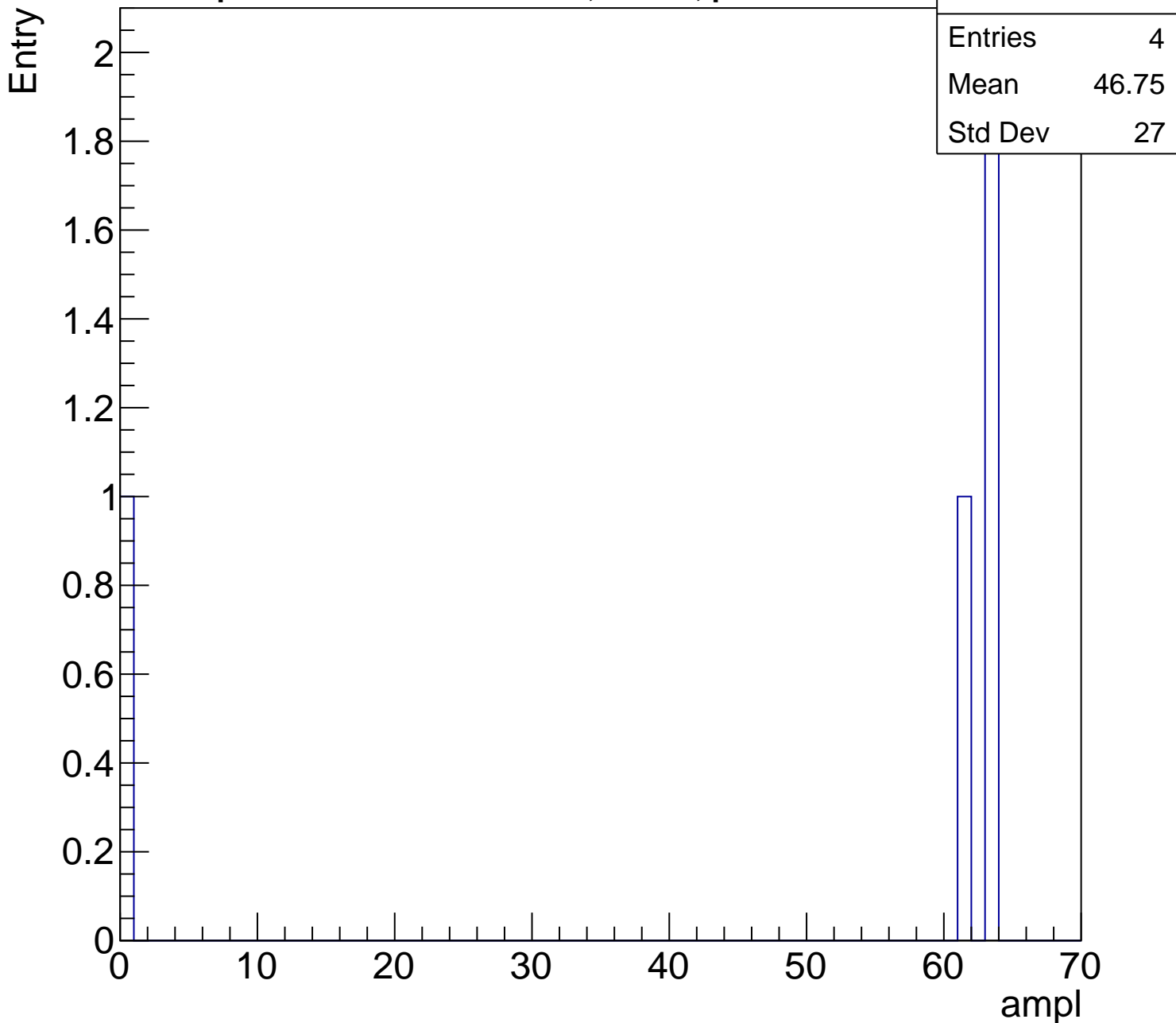
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B1L102S, U19-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch108, adc0

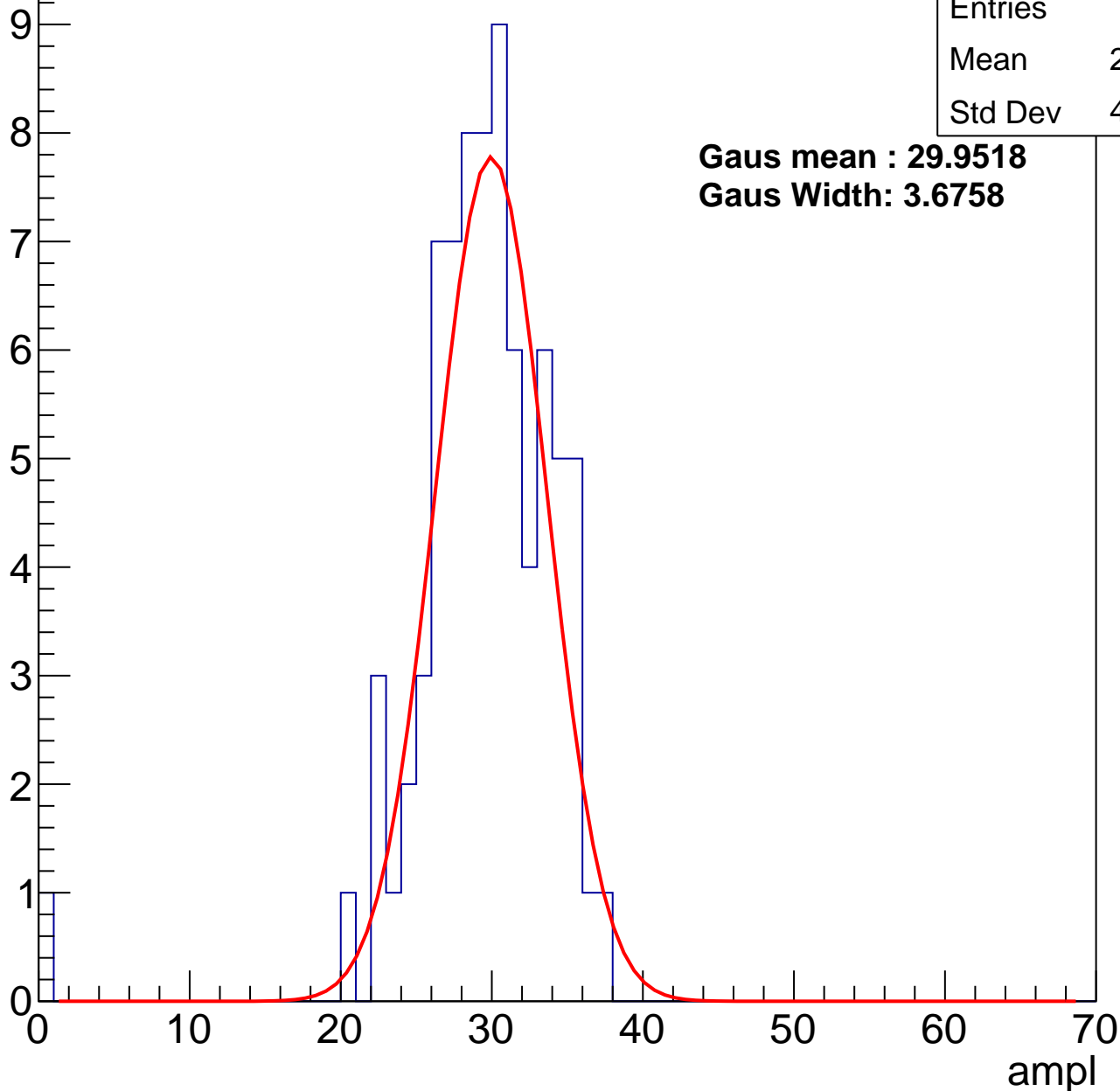
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.96
Std Dev	4.916

Gaus mean : 29.9518

Gaus Width: 3.6758



B1L102S, U19-ch108, adc1

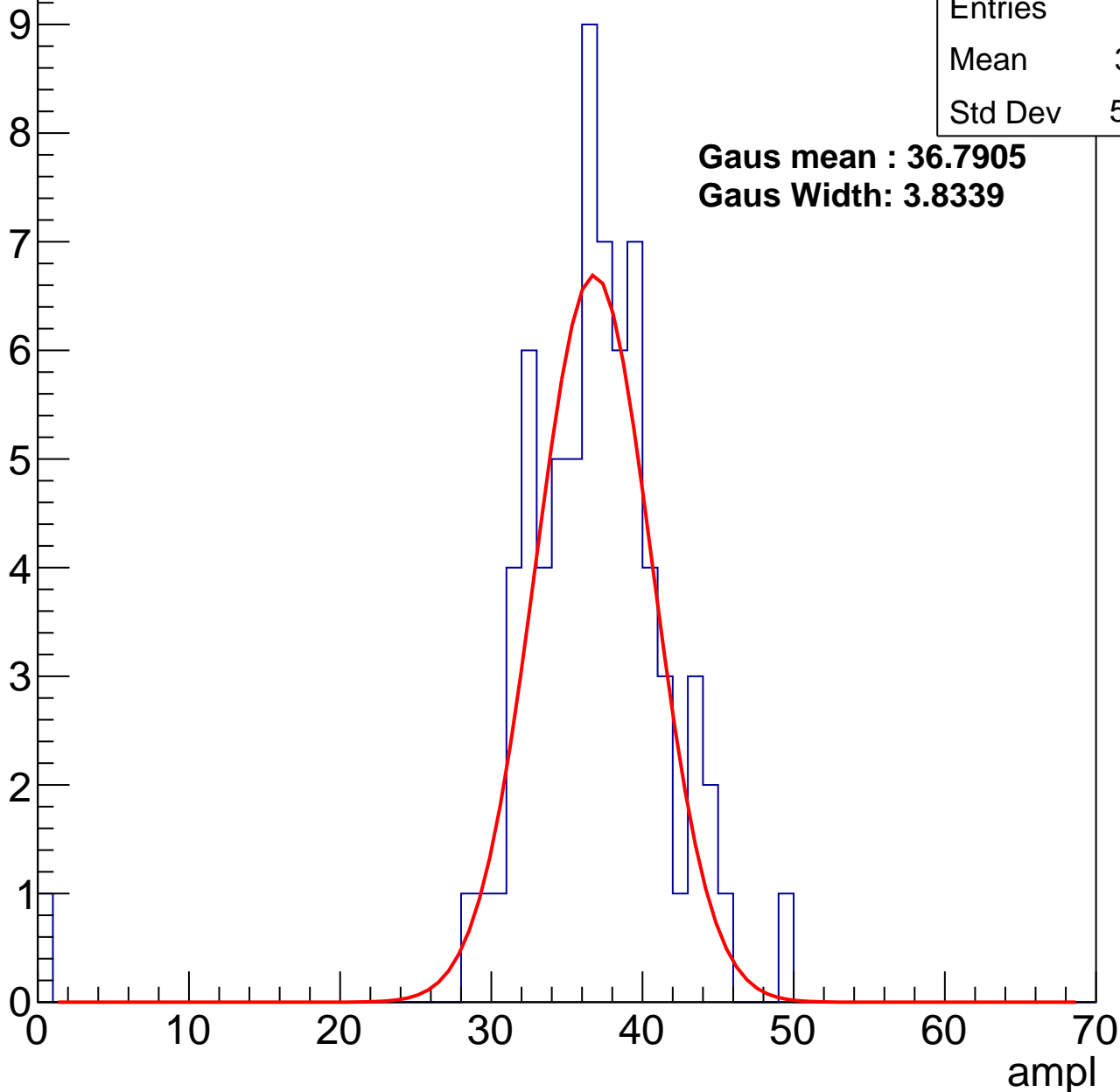
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.11
Std Dev	5.887

Gaus mean : 36.7905

Gaus Width: 3.8339

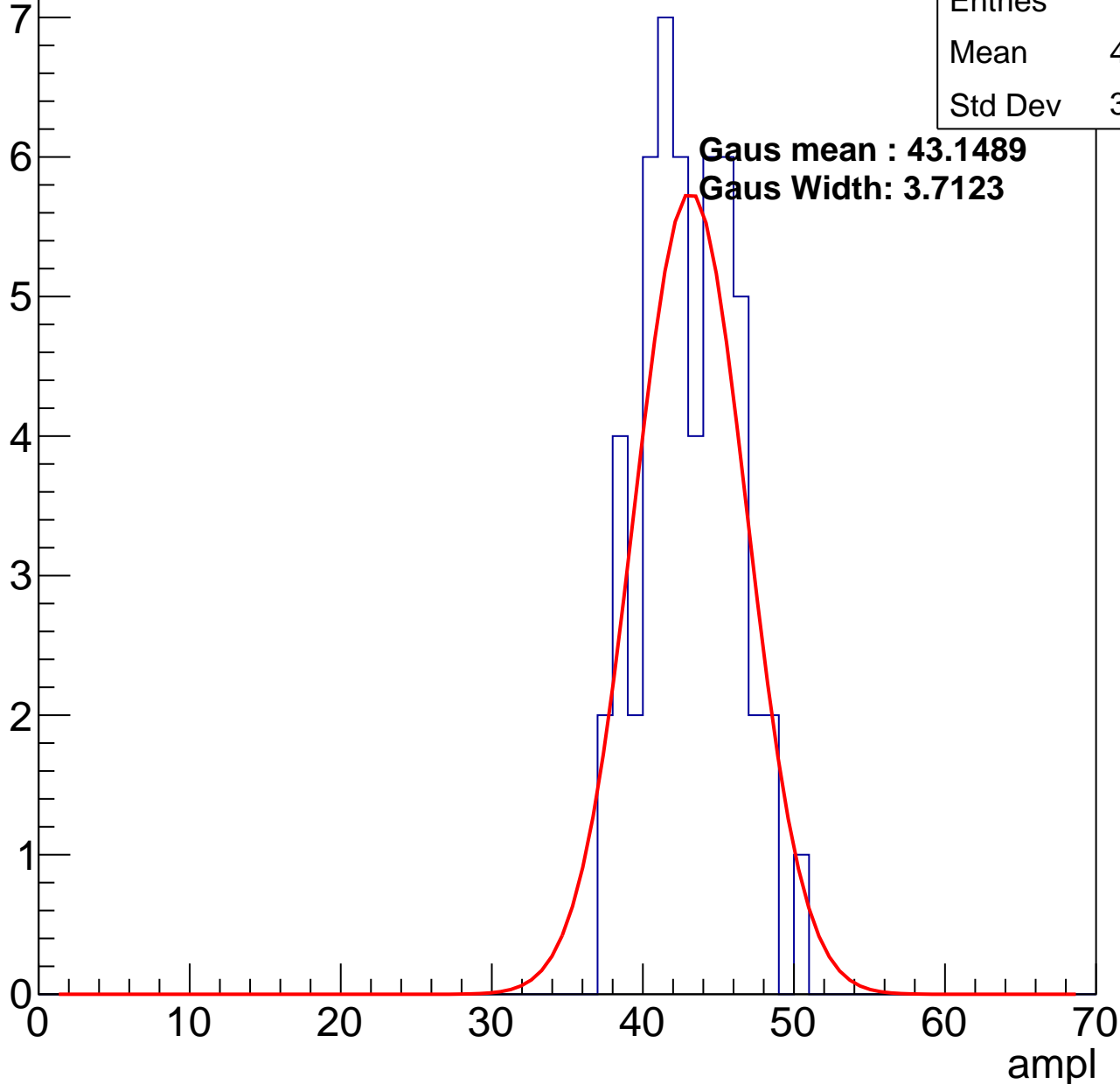


B1L102S, U19-ch108, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.62
Std Dev	3.042

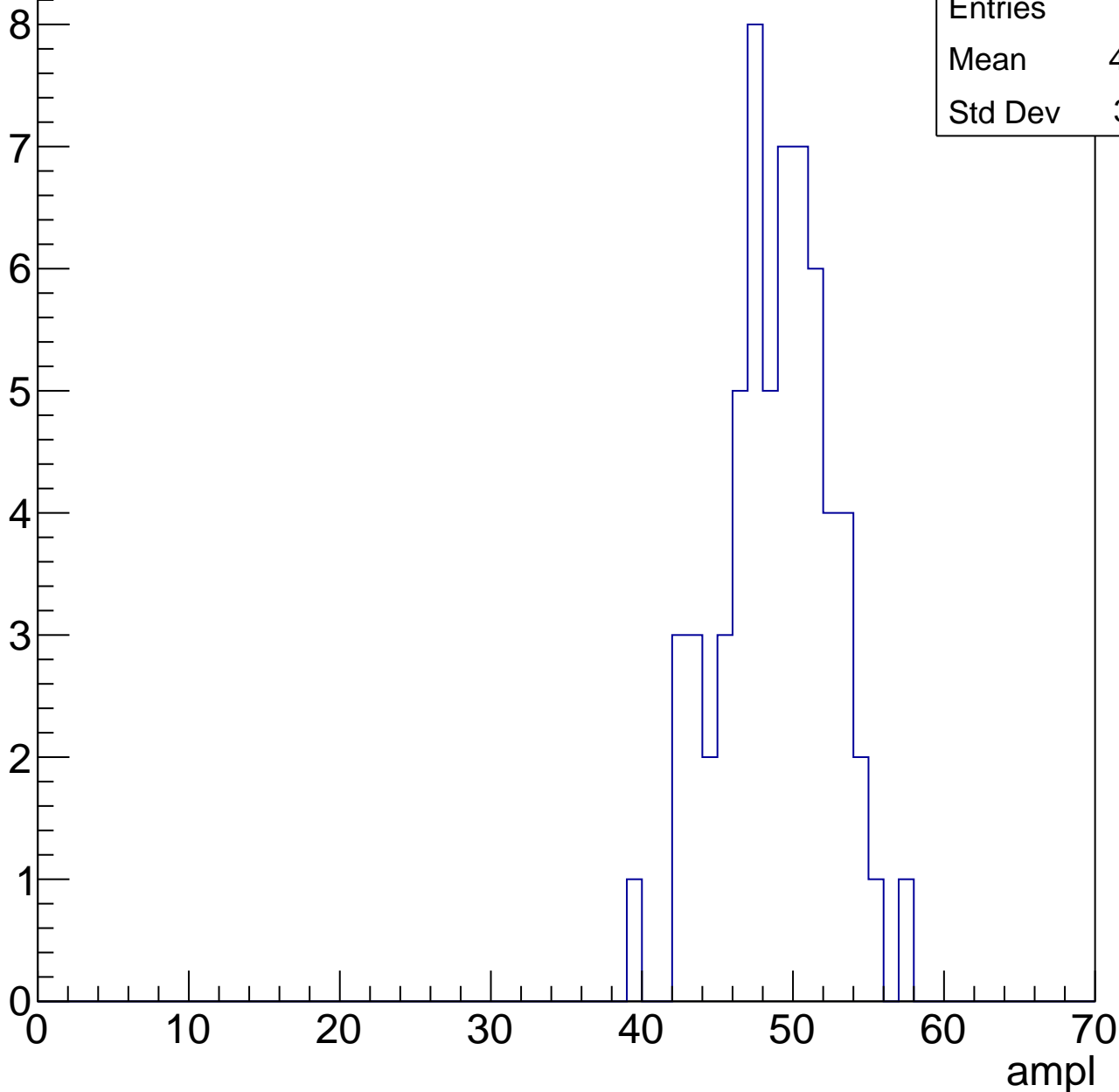


B1L102S, U19-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.42
Std Dev	3.581

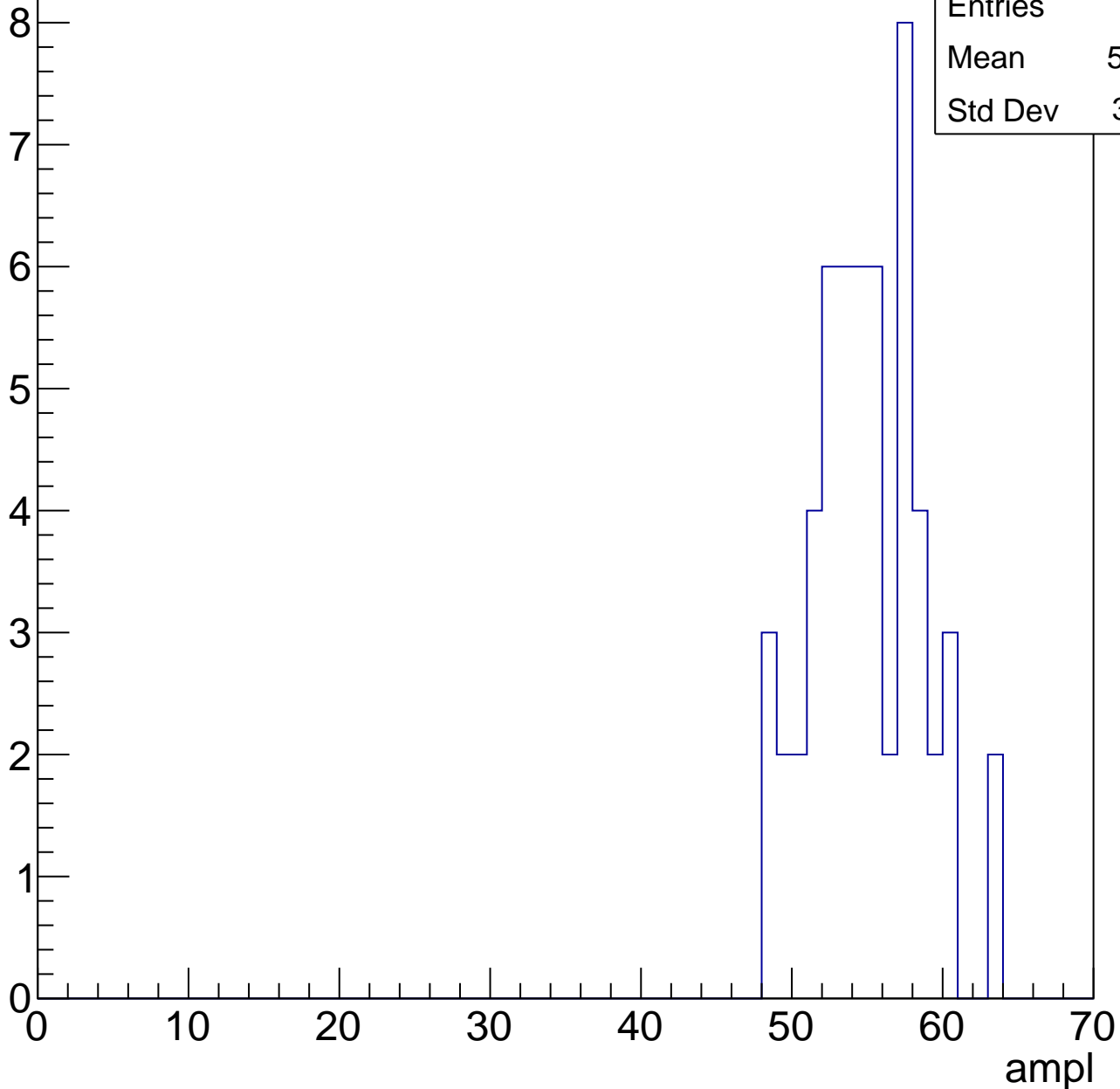


B1L102S, U19-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.54
Std Dev	3.561

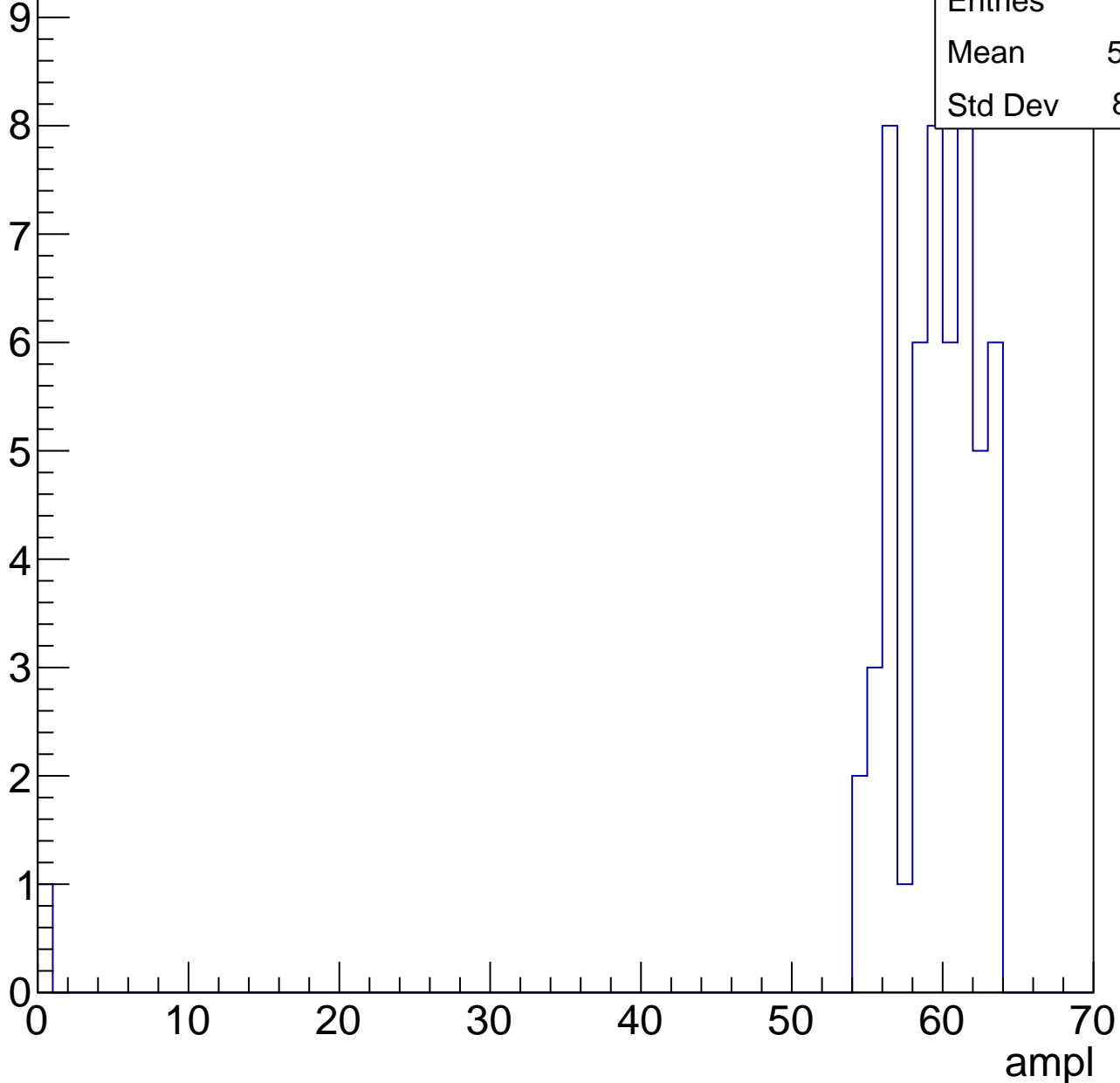


B1L102S, U19-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.09
Std Dev	8.311

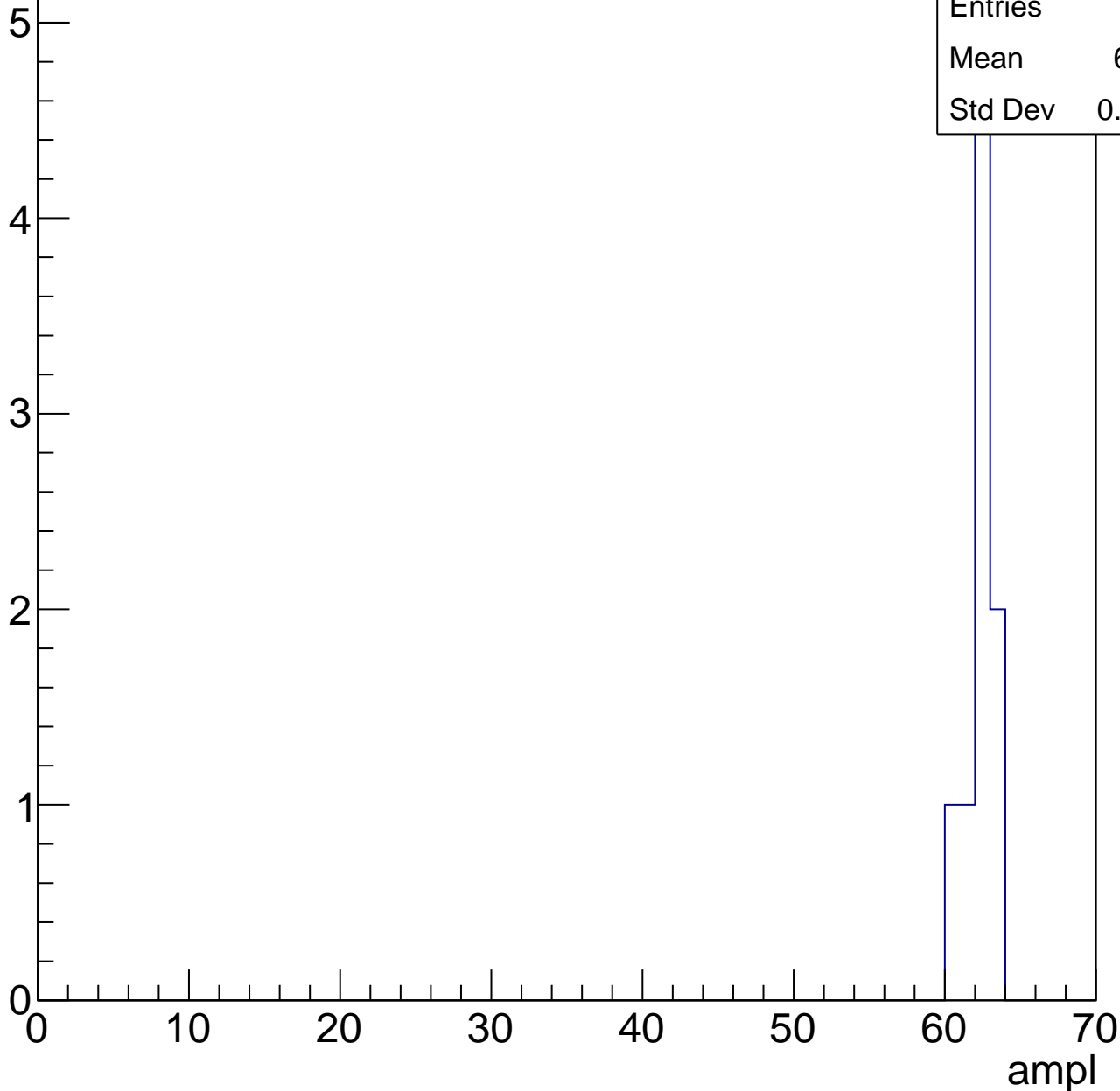


B1L102S, U19-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.89
Std Dev	0.8749



B1L102S, U19-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch109, adc0

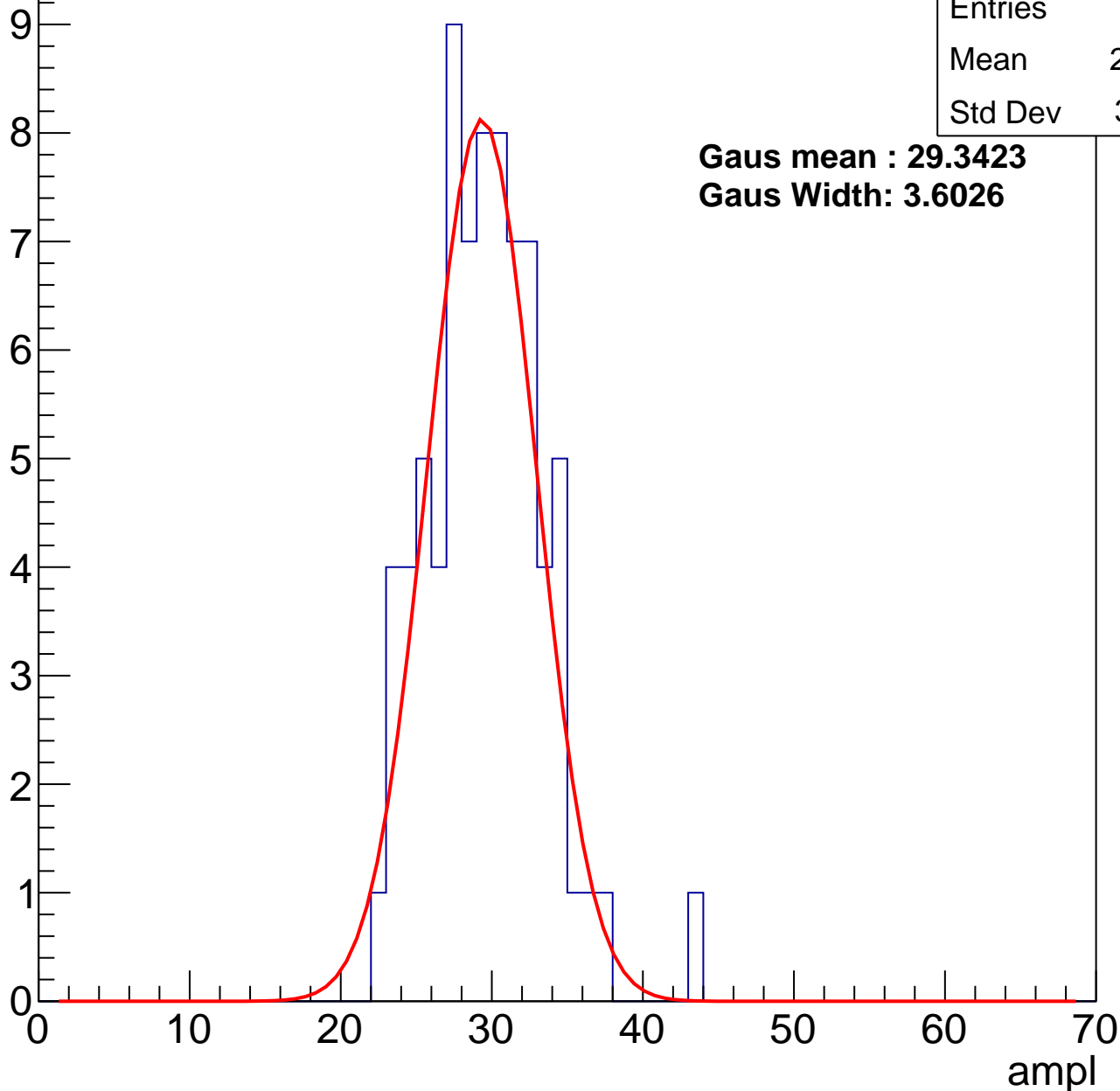
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.14
Std Dev	3.751

Gaus mean : 29.3423

Gaus Width: 3.6026



B1L102S, U19-ch109, adc1

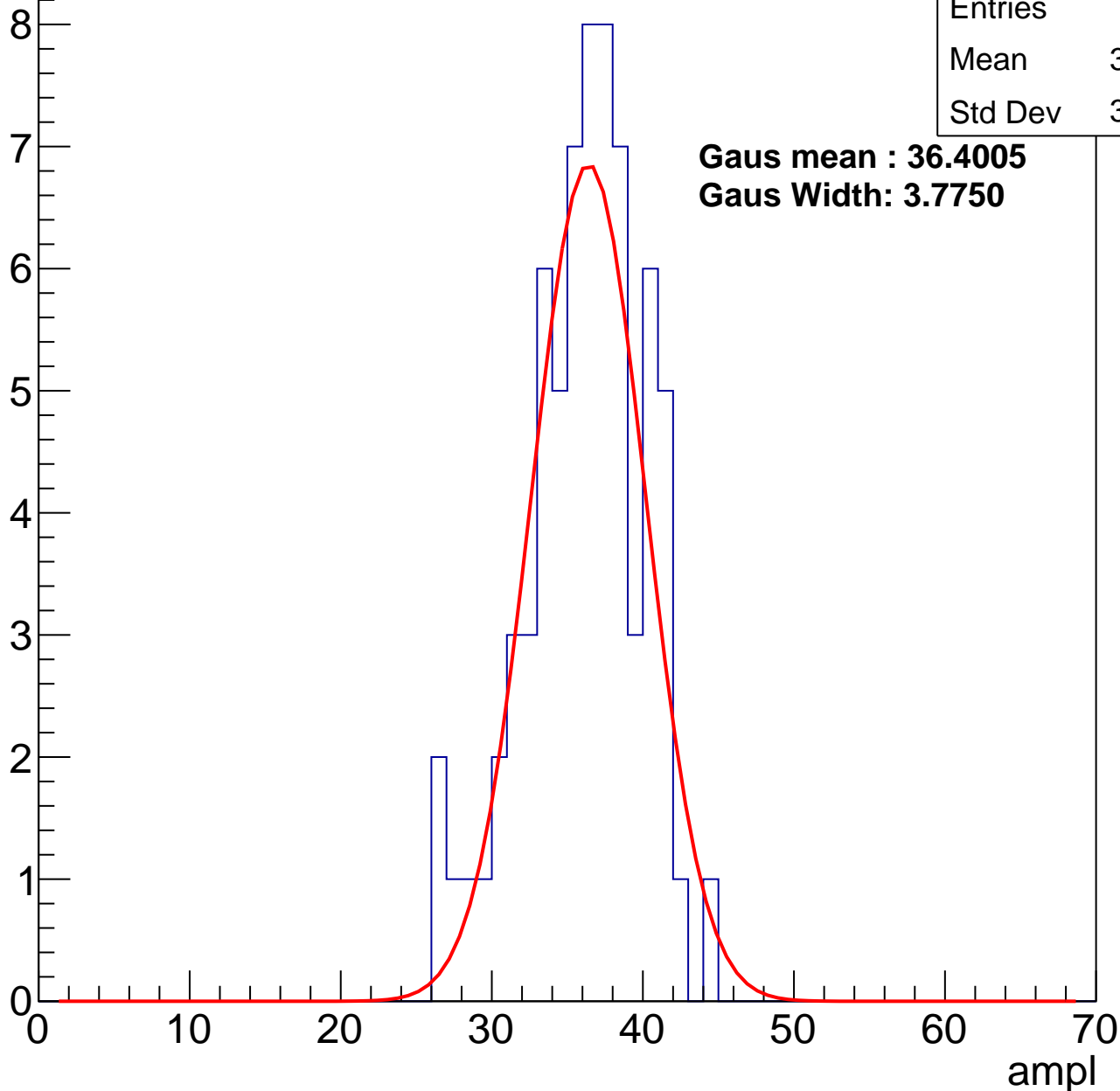
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.66
Std Dev	3.865

Gaus mean : 36.4005

Gaus Width: 3.7750



B1L102S, U19-ch109, adc2

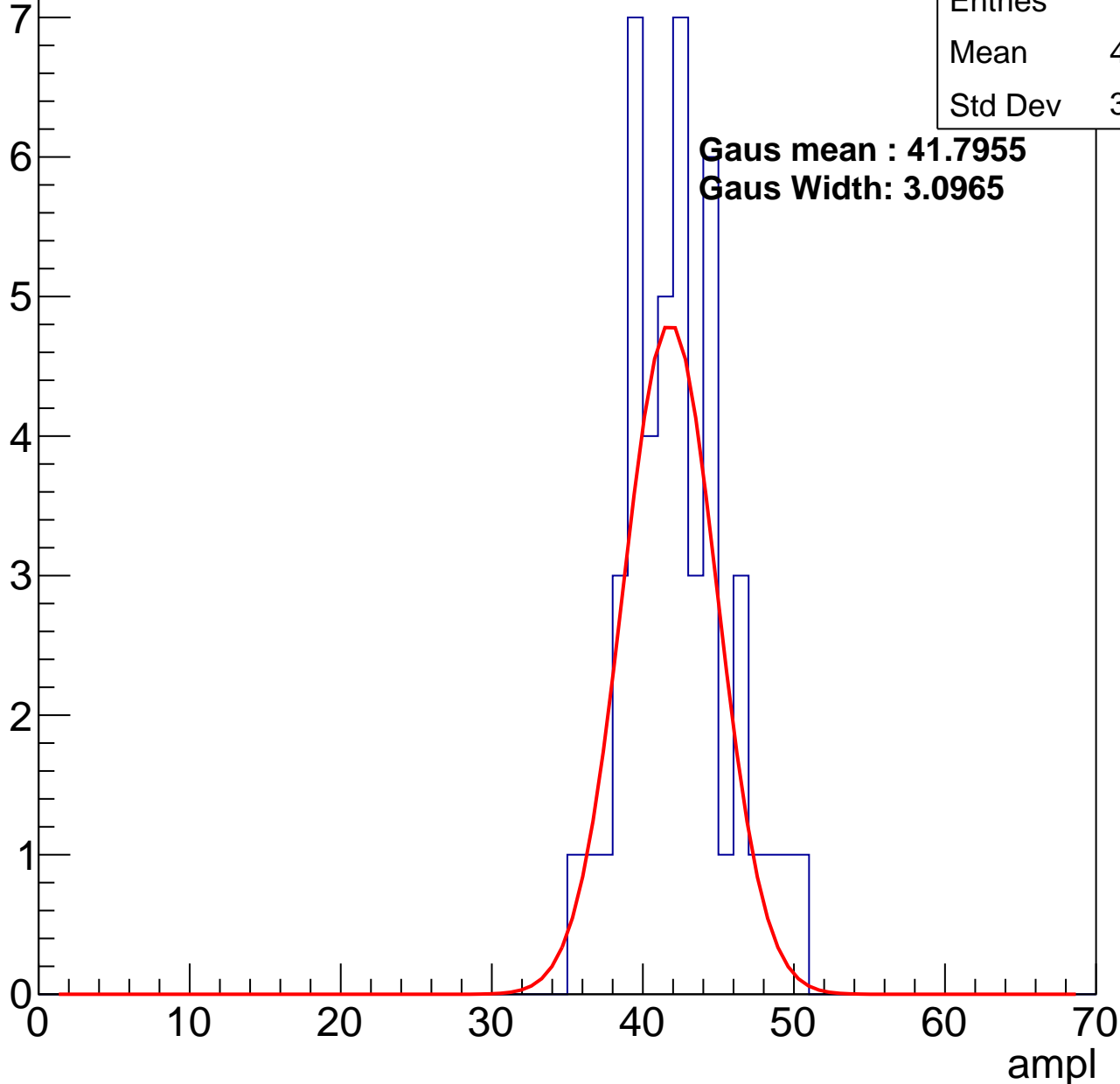
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	41.83
Std Dev	3.292

Gaus mean : 41.7955

Gaus Width: 3.0965

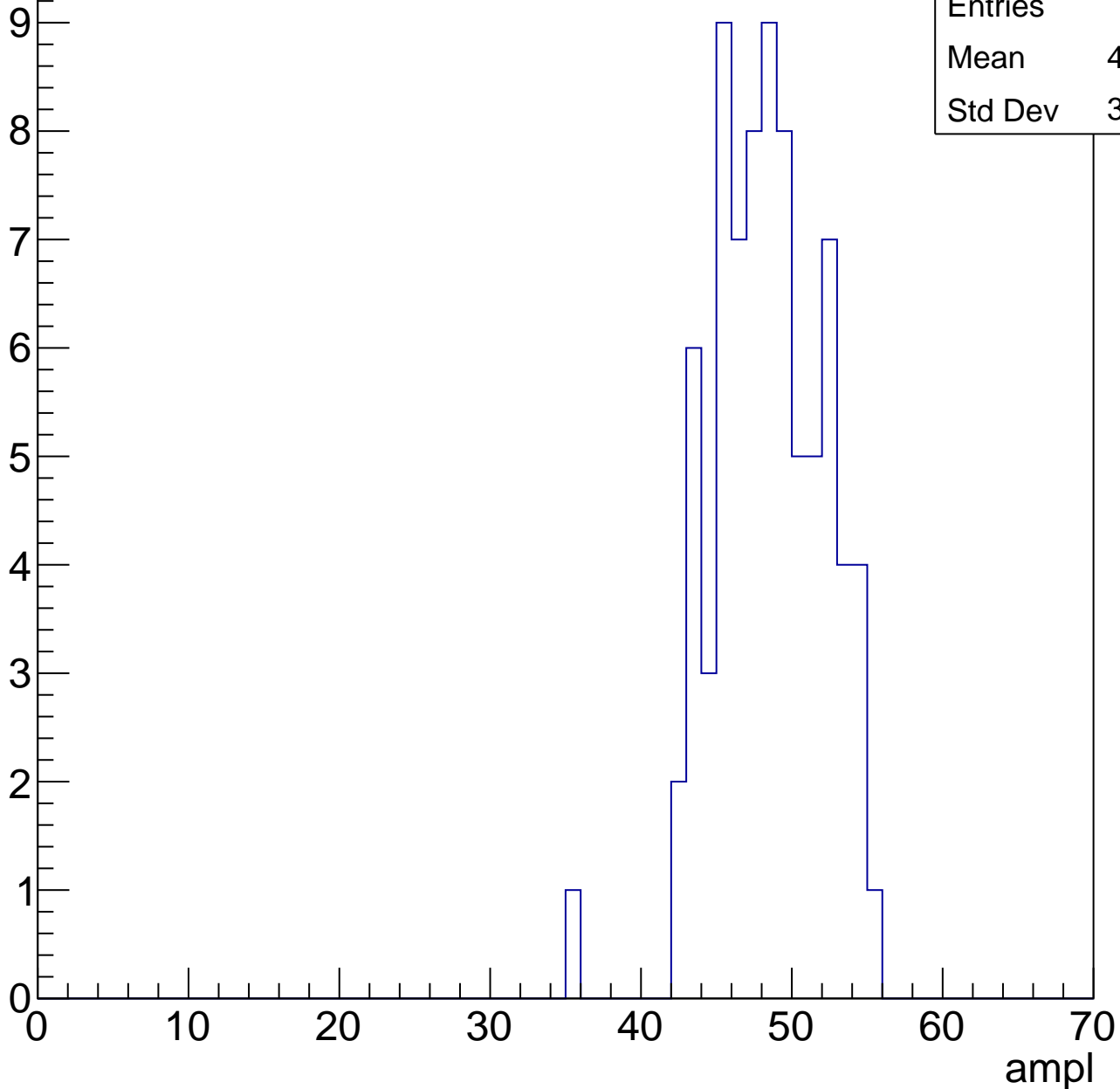


B1L102S, U19-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	47.95
Std Dev	3.617

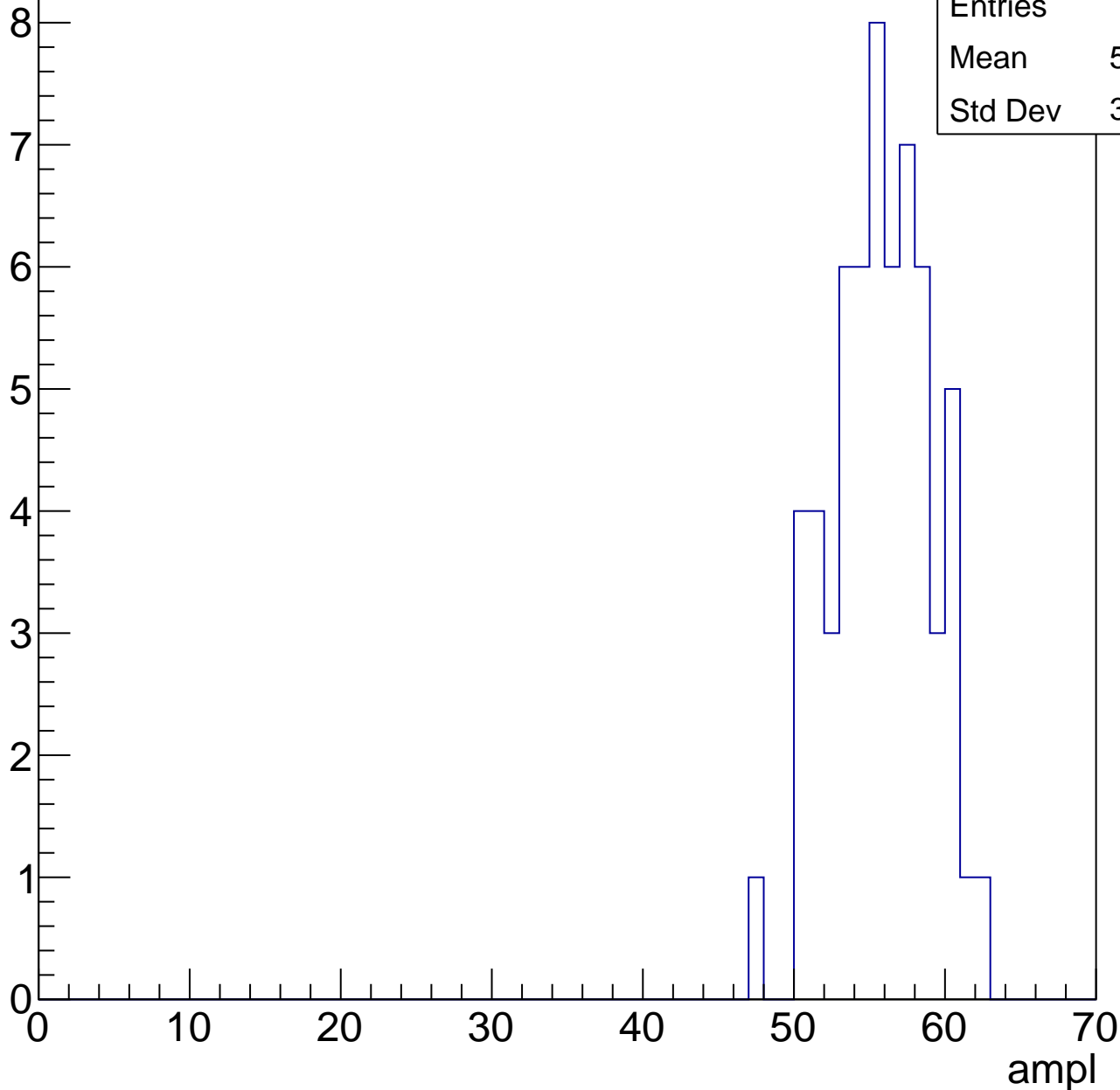


B1L102S, U19-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

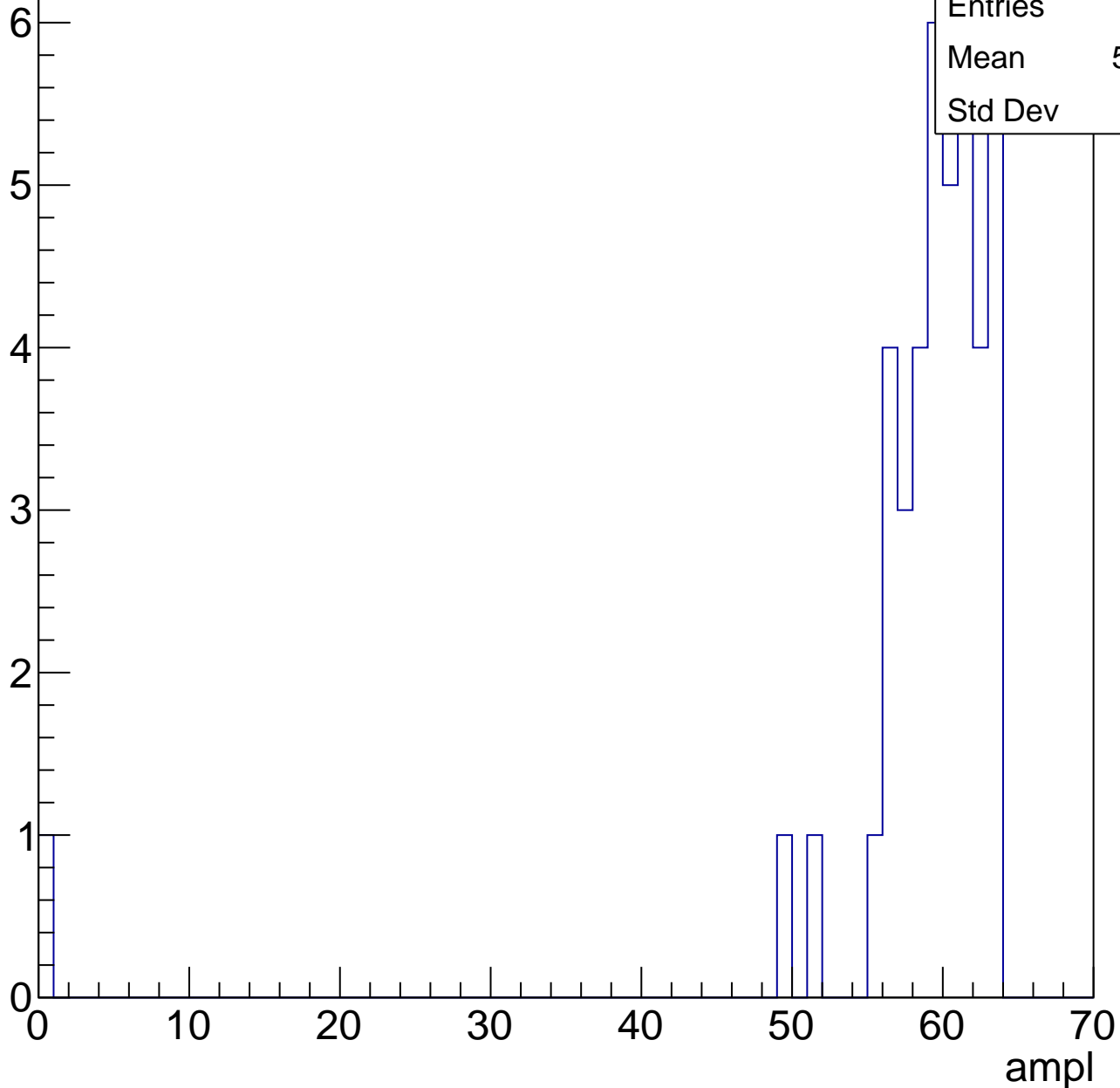
Entries	61
Mean	55.28
Std Dev	3.204



B1L102S, U19-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

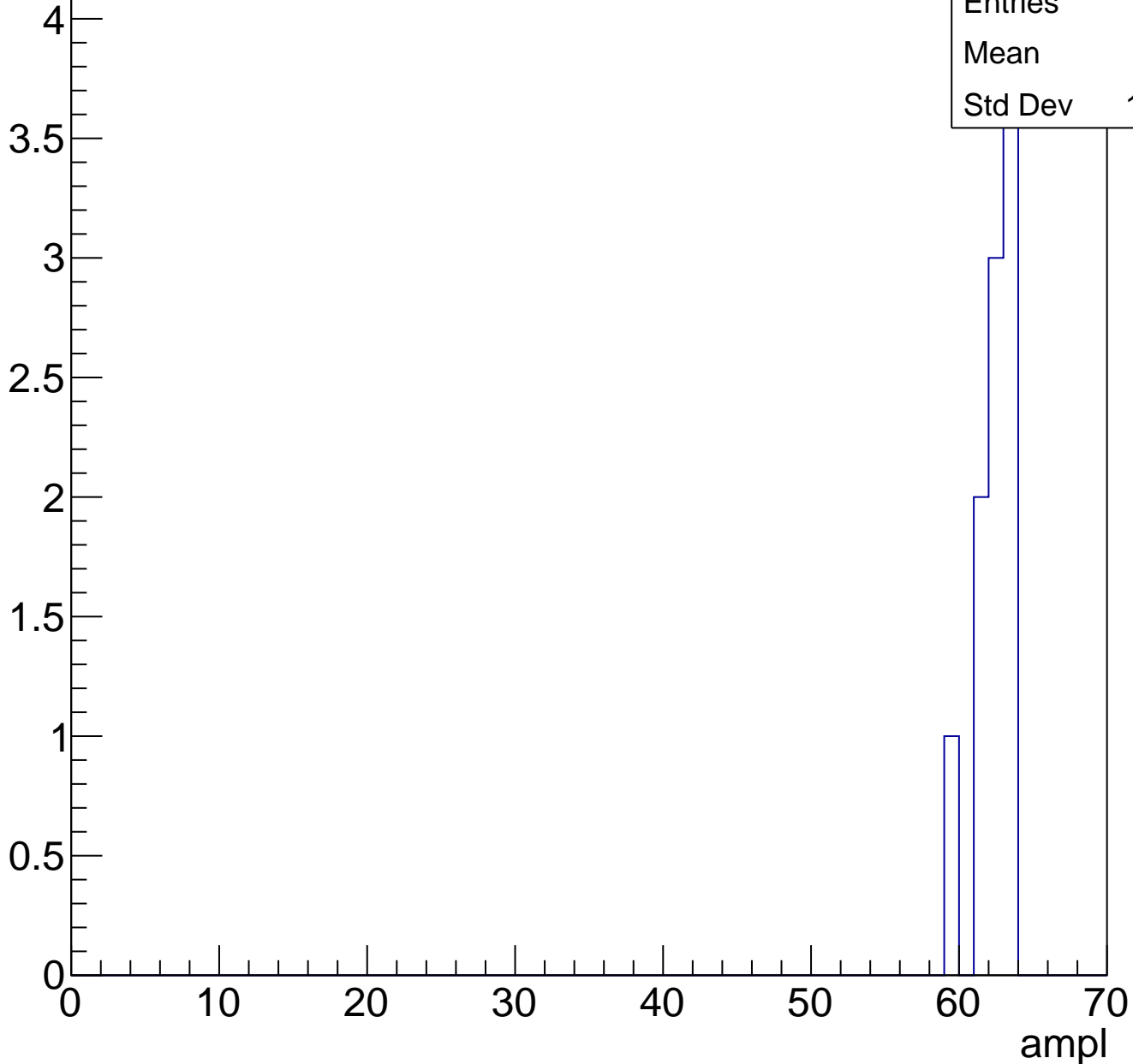
Entry



B1L102S, U19-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

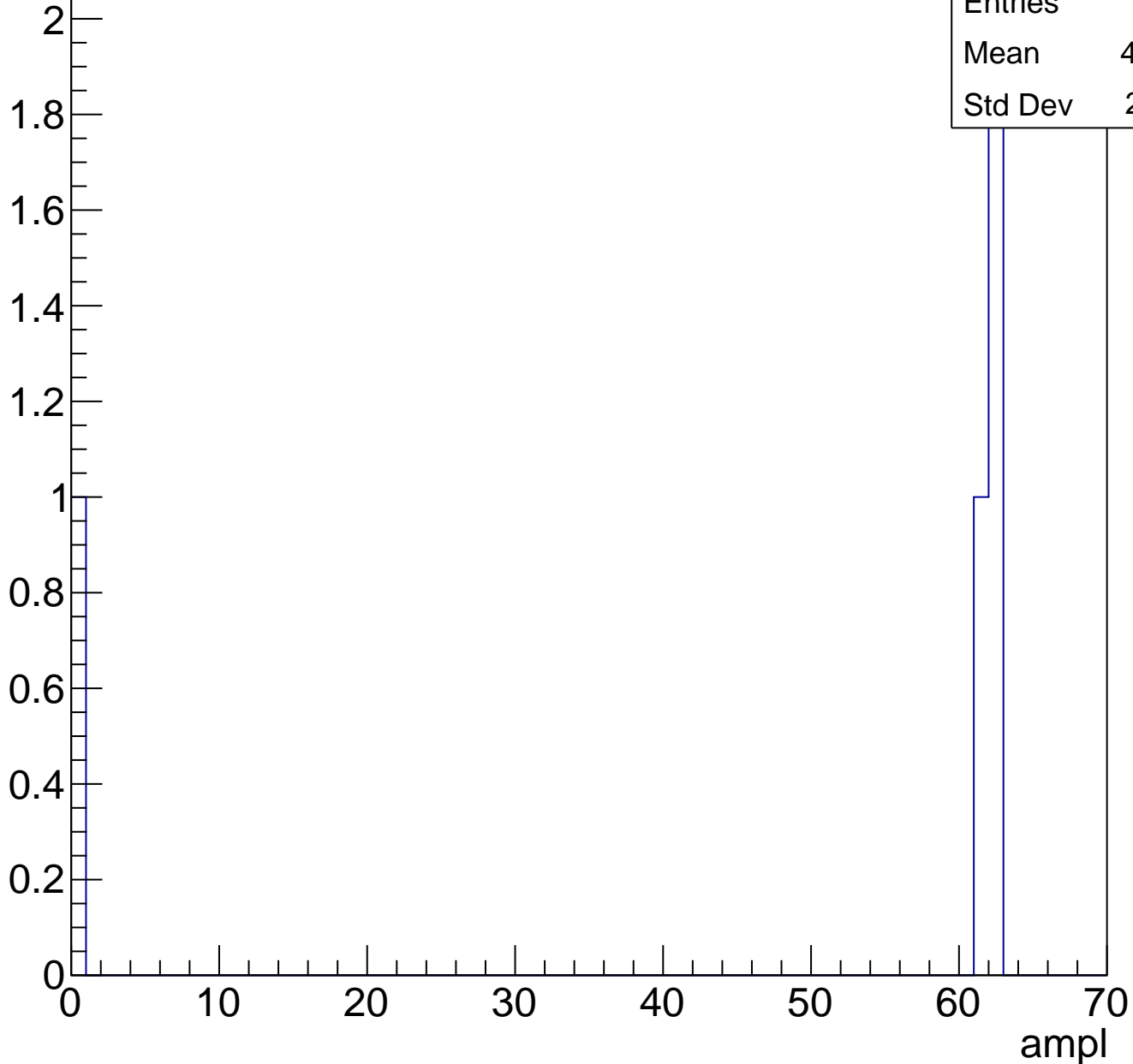
Entry



B1L102S, U19-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch110, adc0

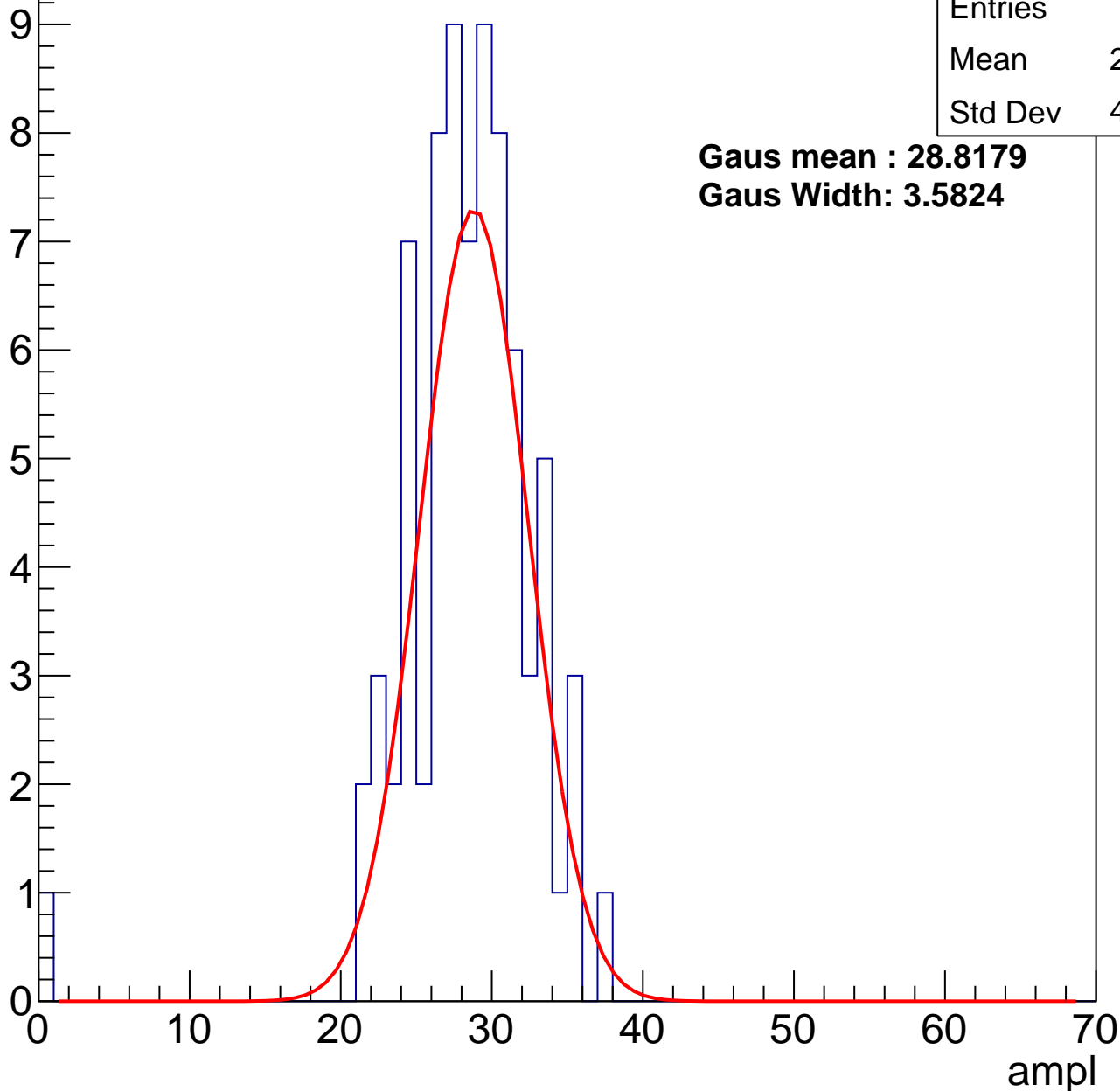
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.83
Std Dev	4.755

Gaus mean : 28.8179

Gaus Width: 3.5824



B1L102S, U19-ch110, adc1

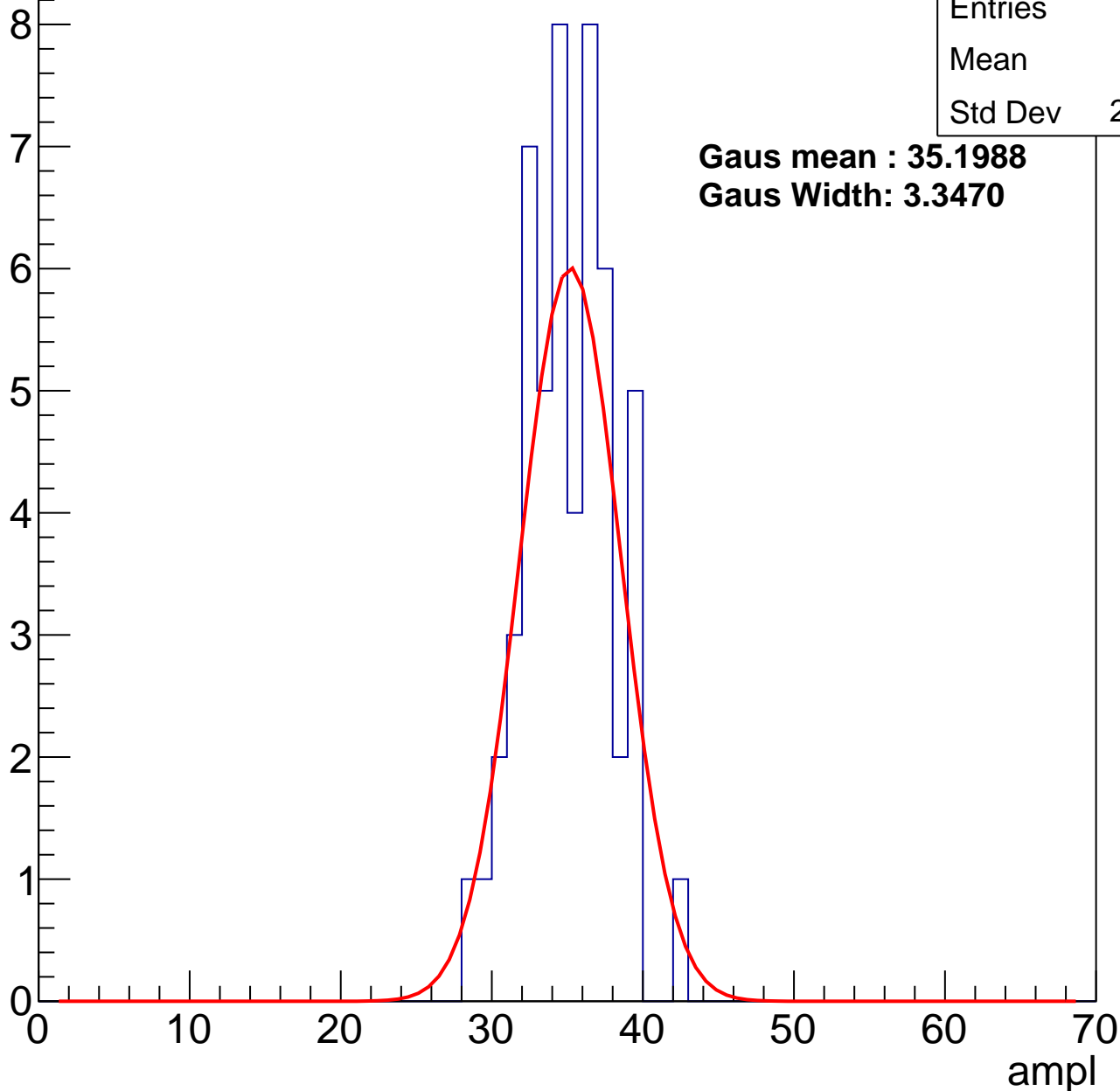
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.6
Std Dev	2.897

Gaus mean : 35.1988

Gaus Width: 3.3470



B1L102S, U19-ch110, adc2

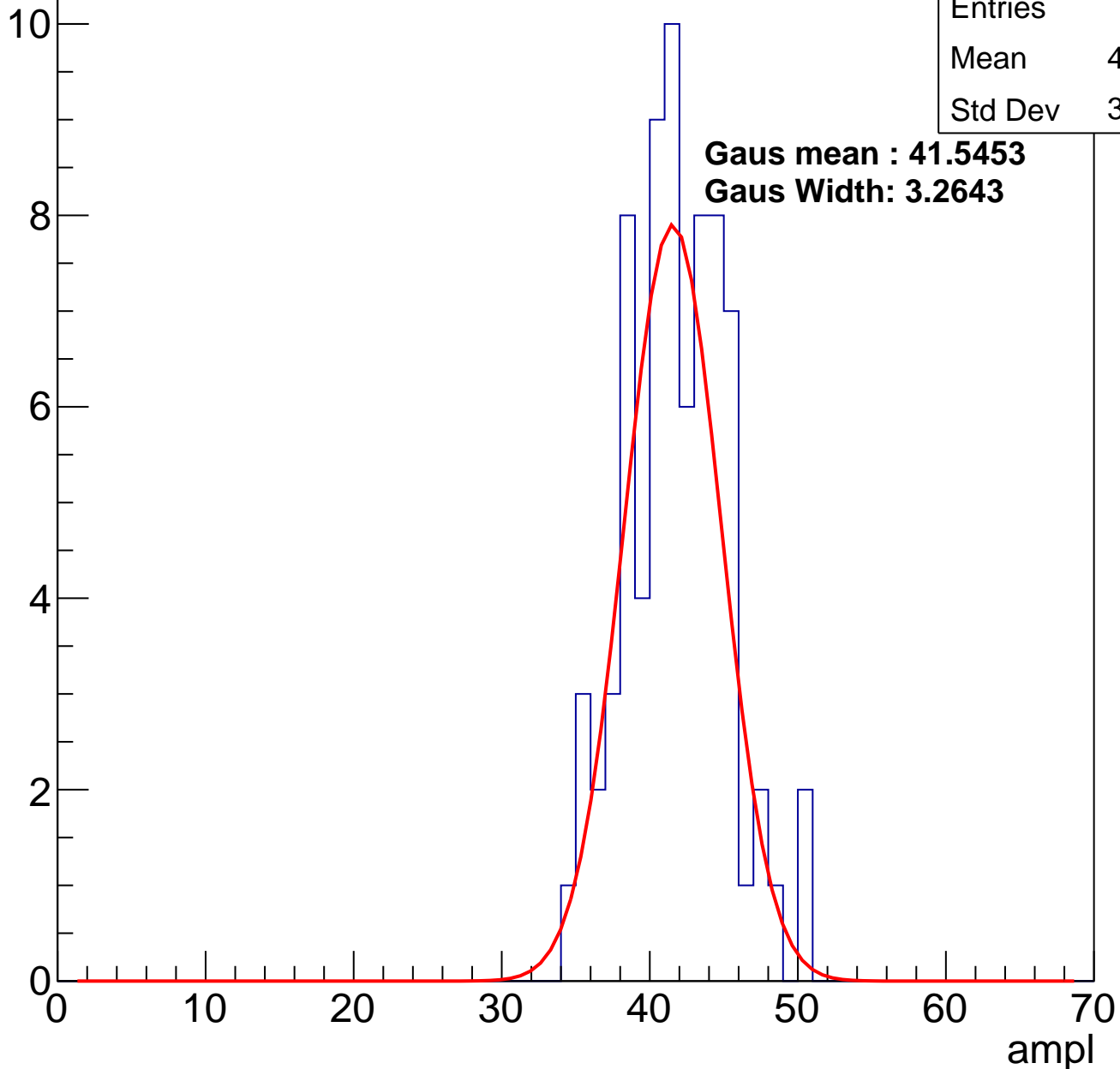
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	41.37
Std Dev	3.409

Gaus mean : 41.5453

Gaus Width: 3.2643

Entry

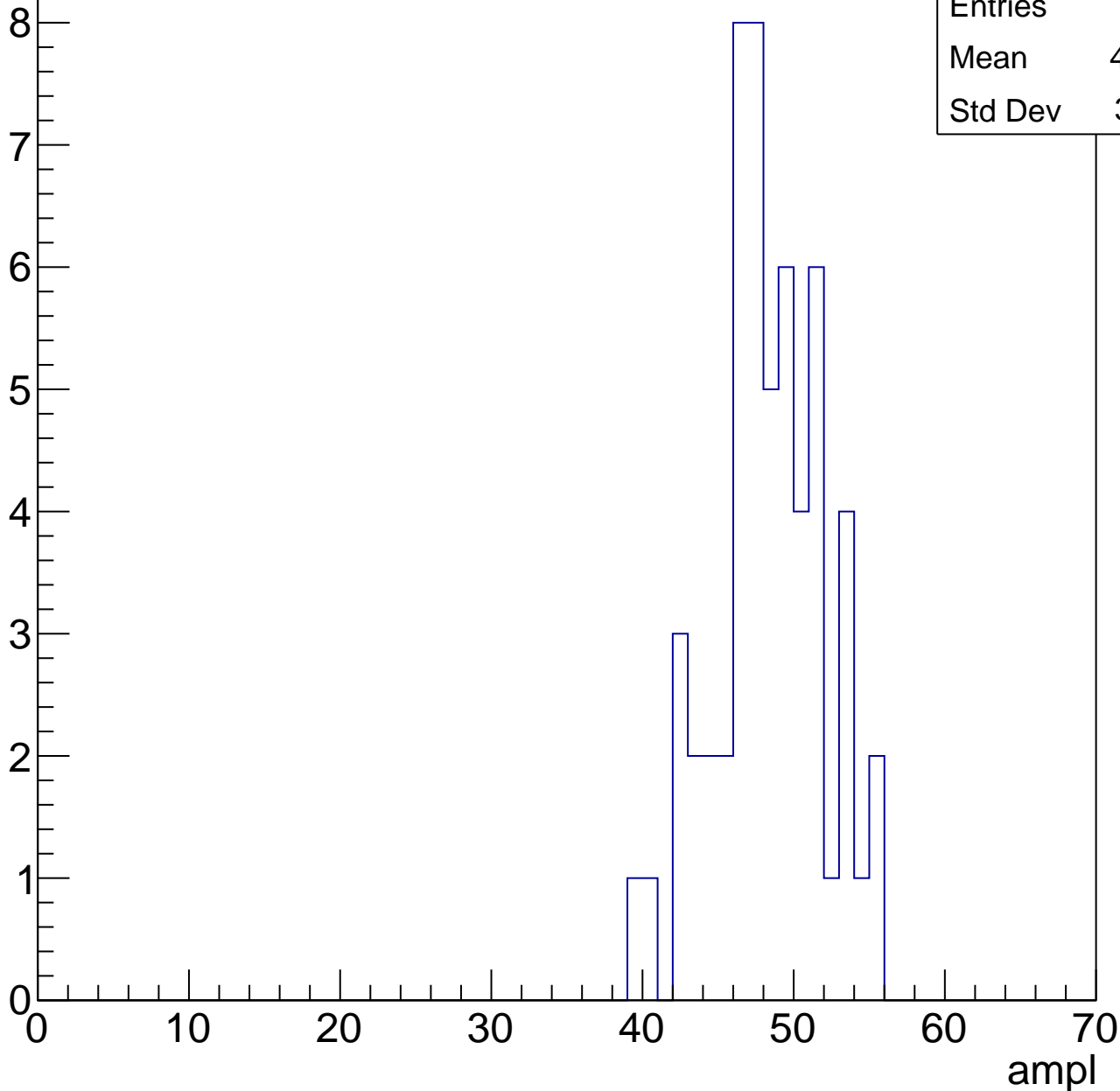


B1L102S, U19-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.88
Std Dev	3.581

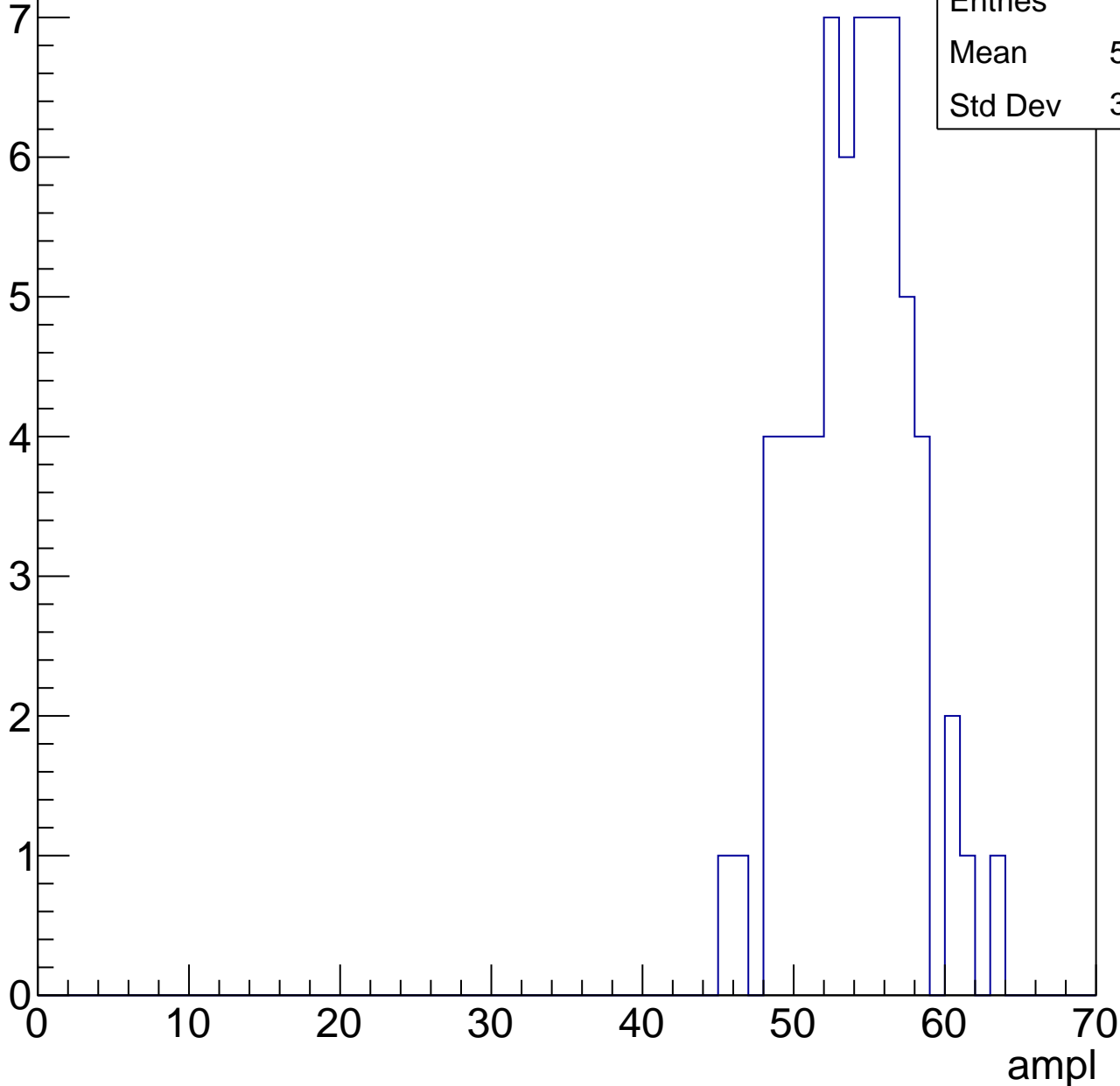


B1L102S, U19-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.55
Std Dev	3.629

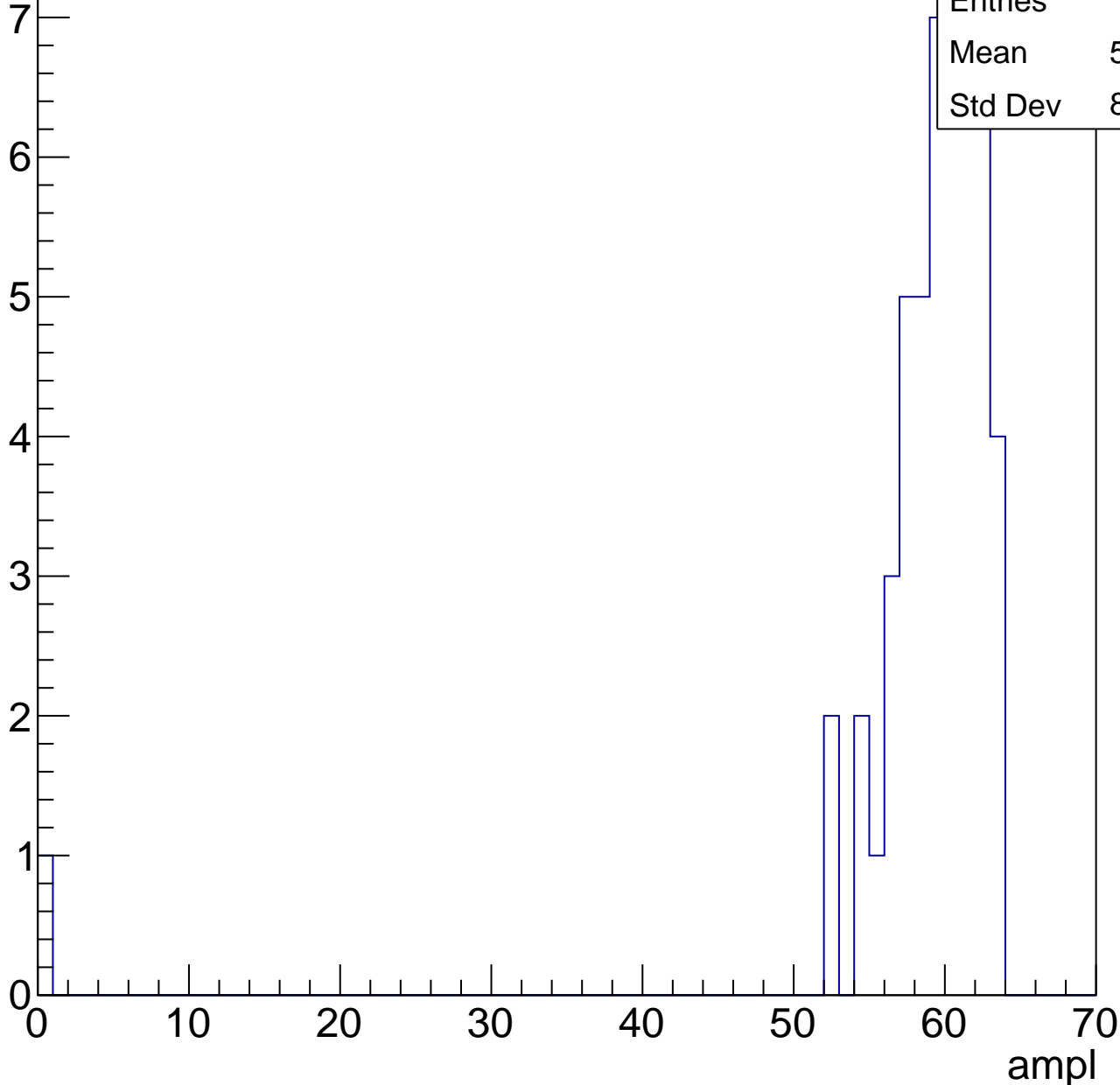


B1L102S, U19-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.96
Std Dev	8.636

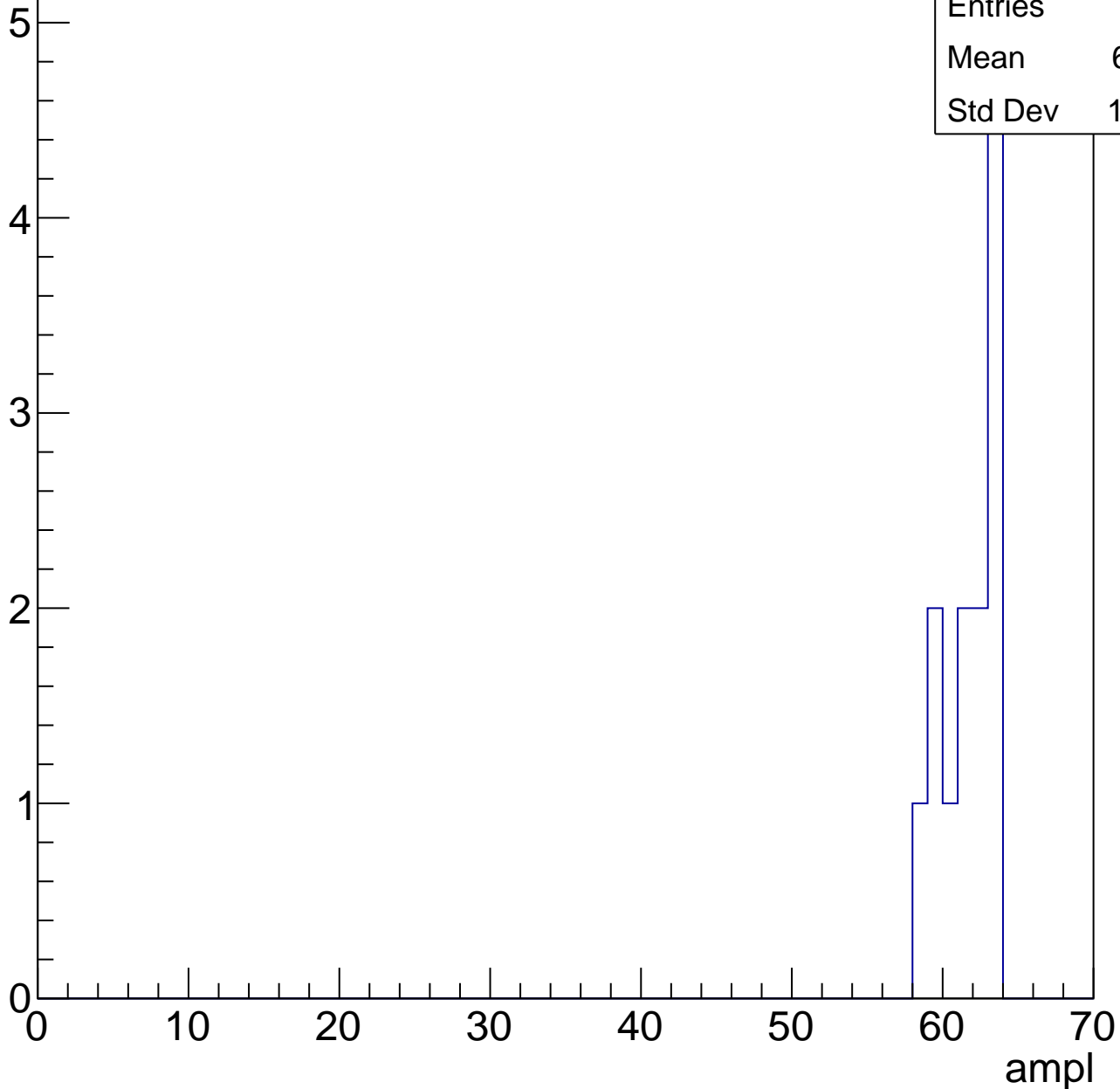


B1L102S, U19-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

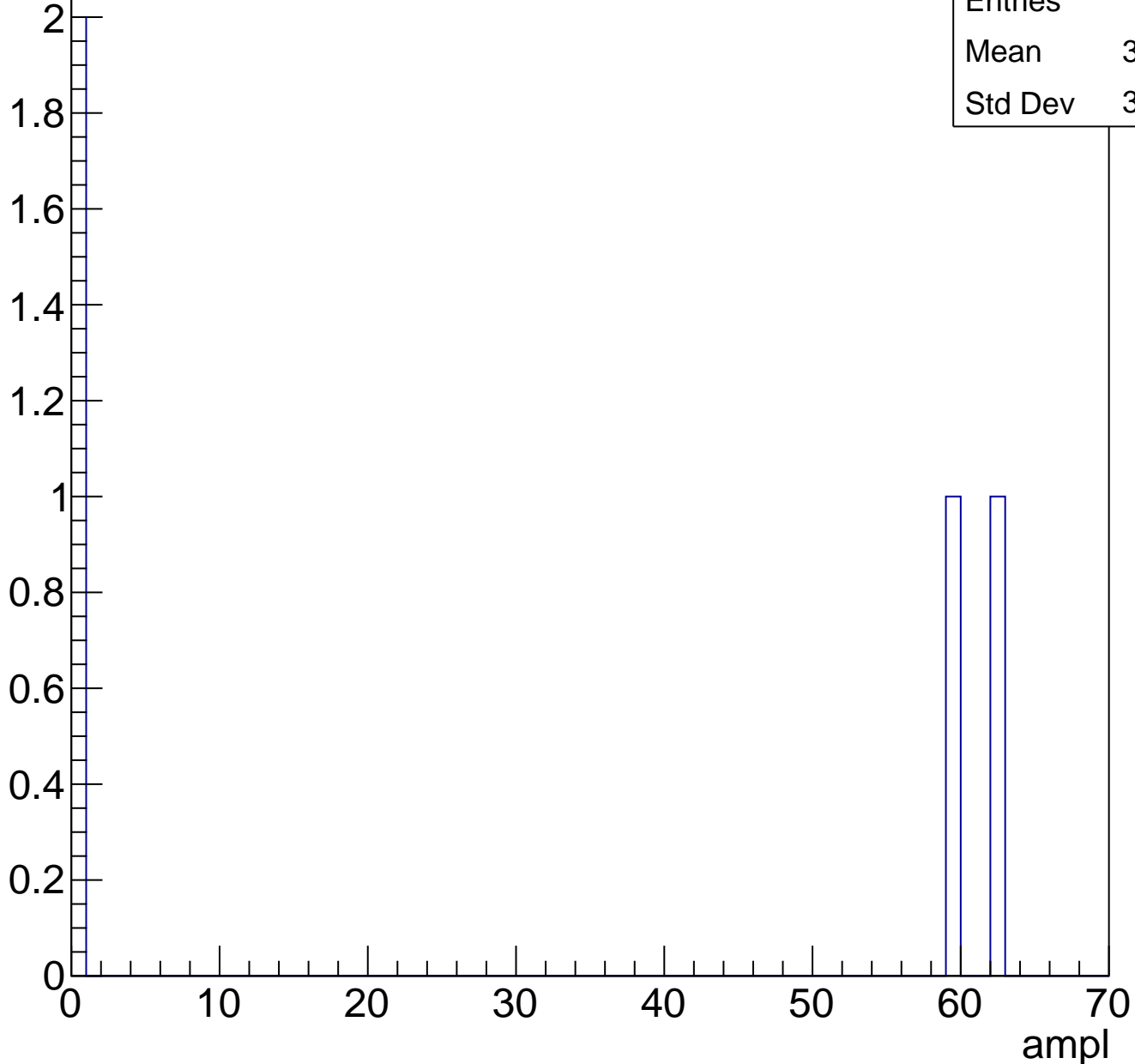
Entries	13
Mean	61.31
Std Dev	1.727



B1L102S, U19-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch111, adc0

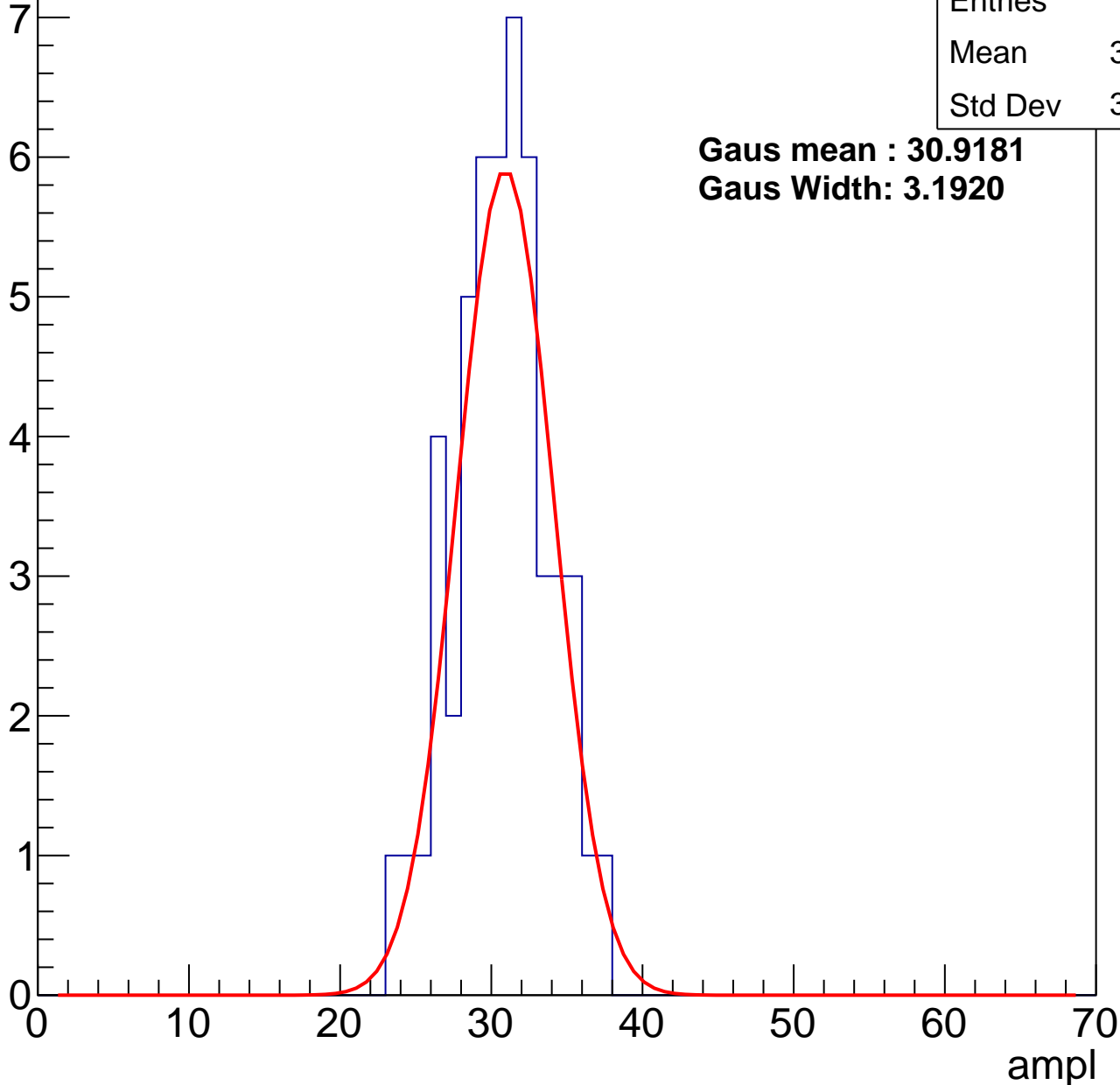
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	30.24
Std Dev	3.102

Gaus mean : 30.9181

Gaus Width: 3.1920



B1L102S, U19-ch111, adc1

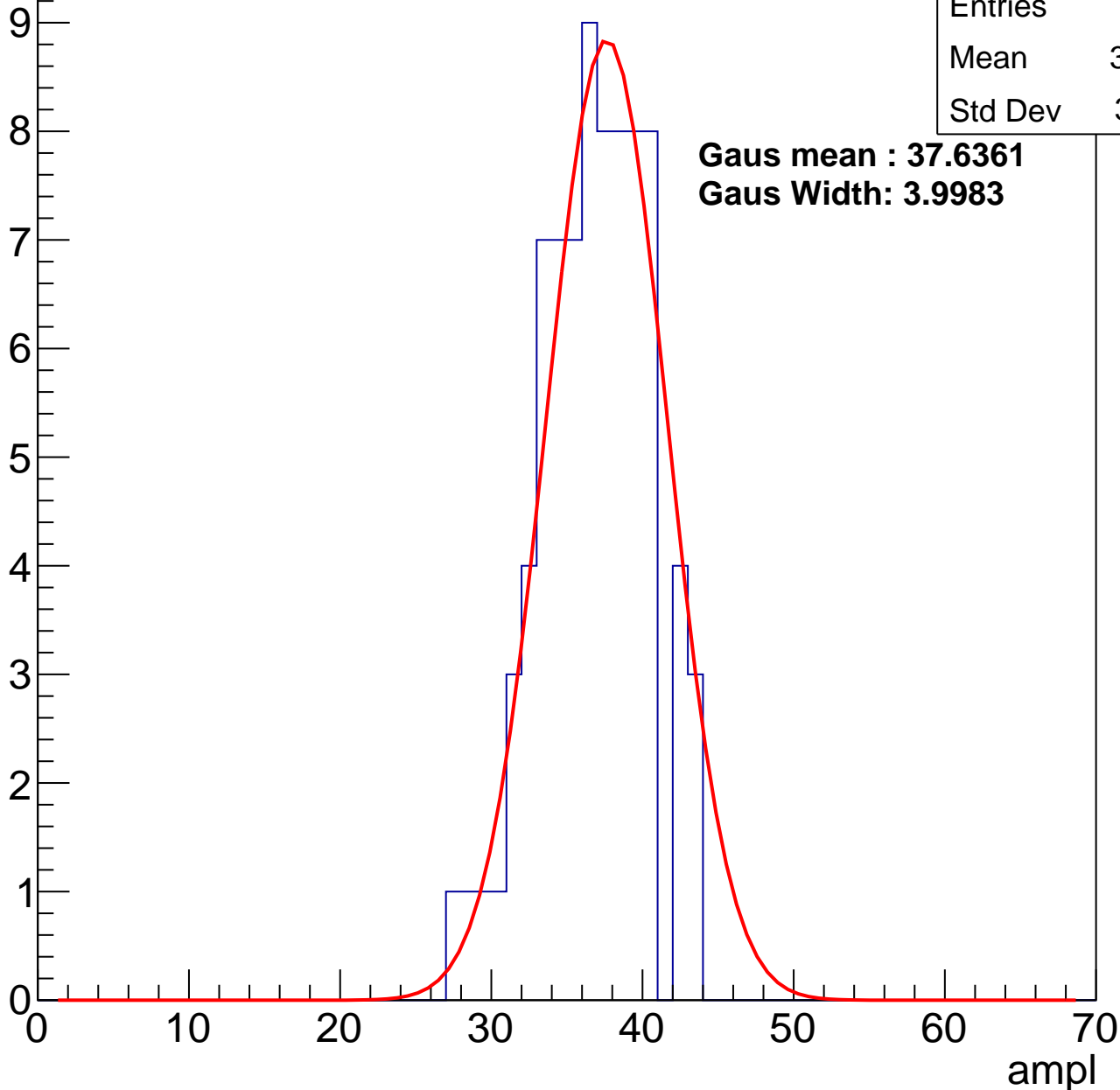
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.27
Std Dev	3.521

Gaus mean : 37.6361

Gaus Width: 3.9983



B1L102S, U19-ch111, adc2

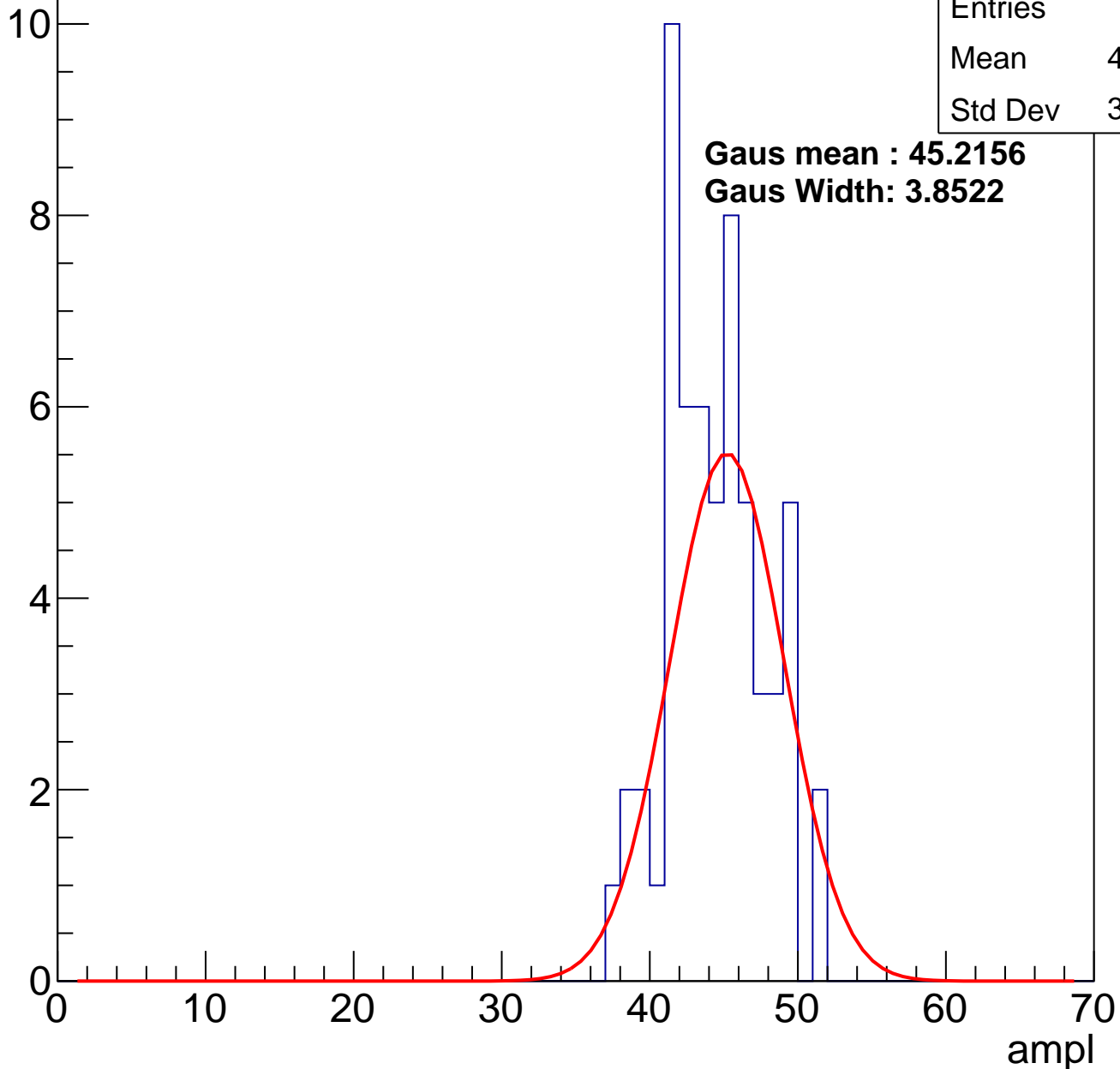
calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	43.95
Std Dev	3.275

Gaus mean : 45.2156

Gaus Width: 3.8522

Entry

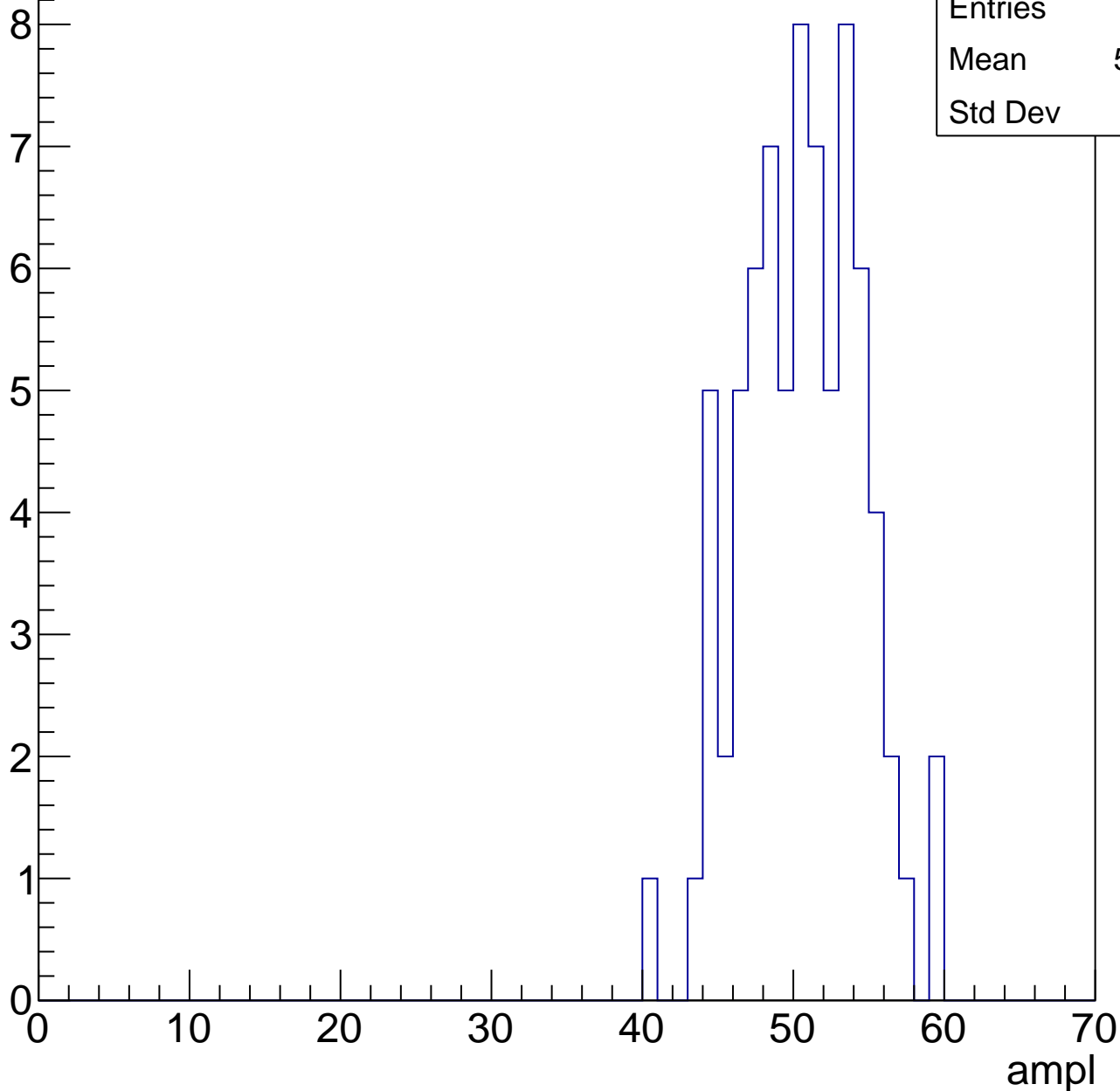


B1L102S, U19-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	50.11
Std Dev	3.87

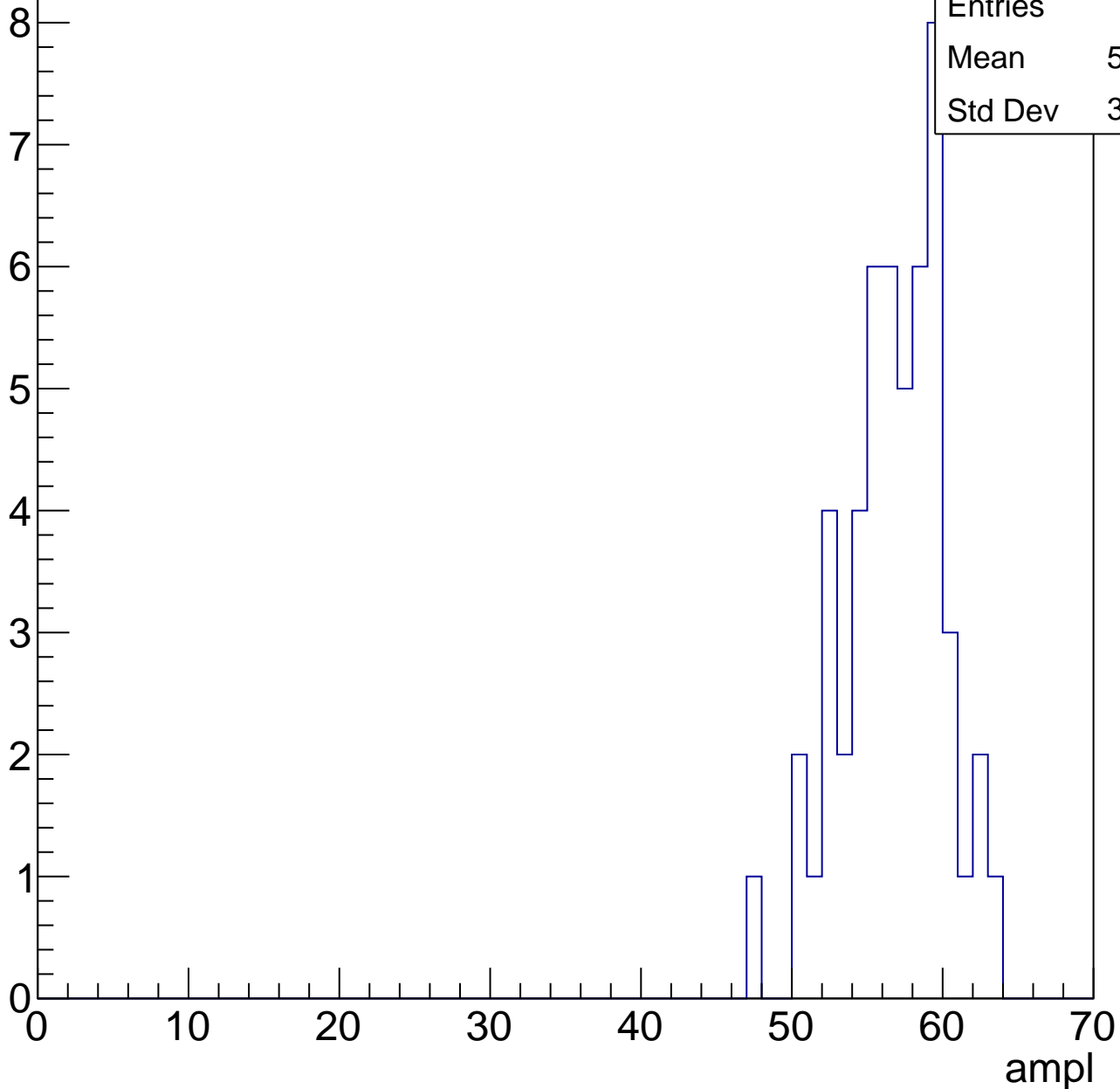


B1L102S, U19-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.29
Std Dev	3.319

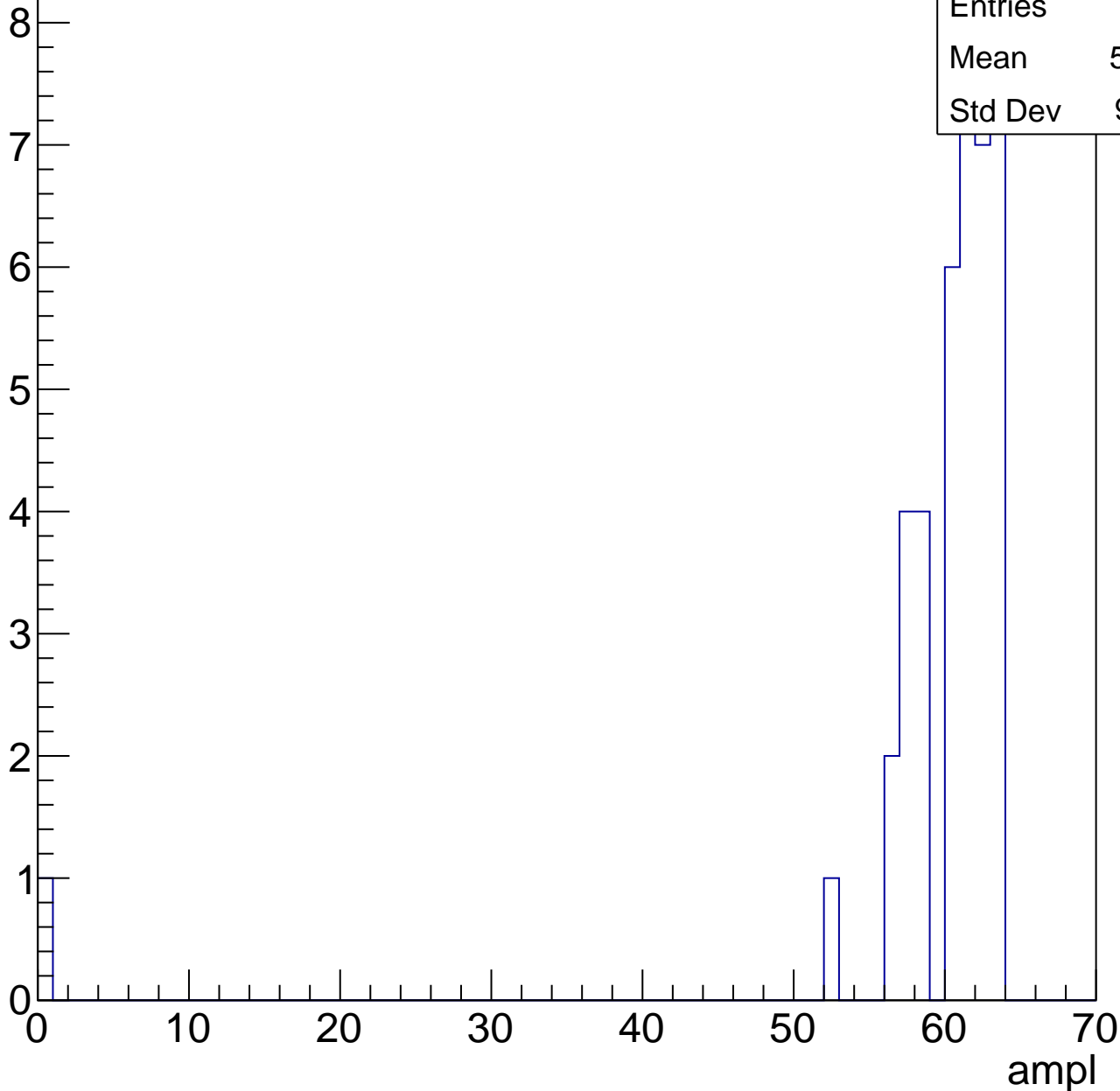


B1L102S, U19-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.78
Std Dev	9.621



B1L102S, U19-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U19-ch112, adc0

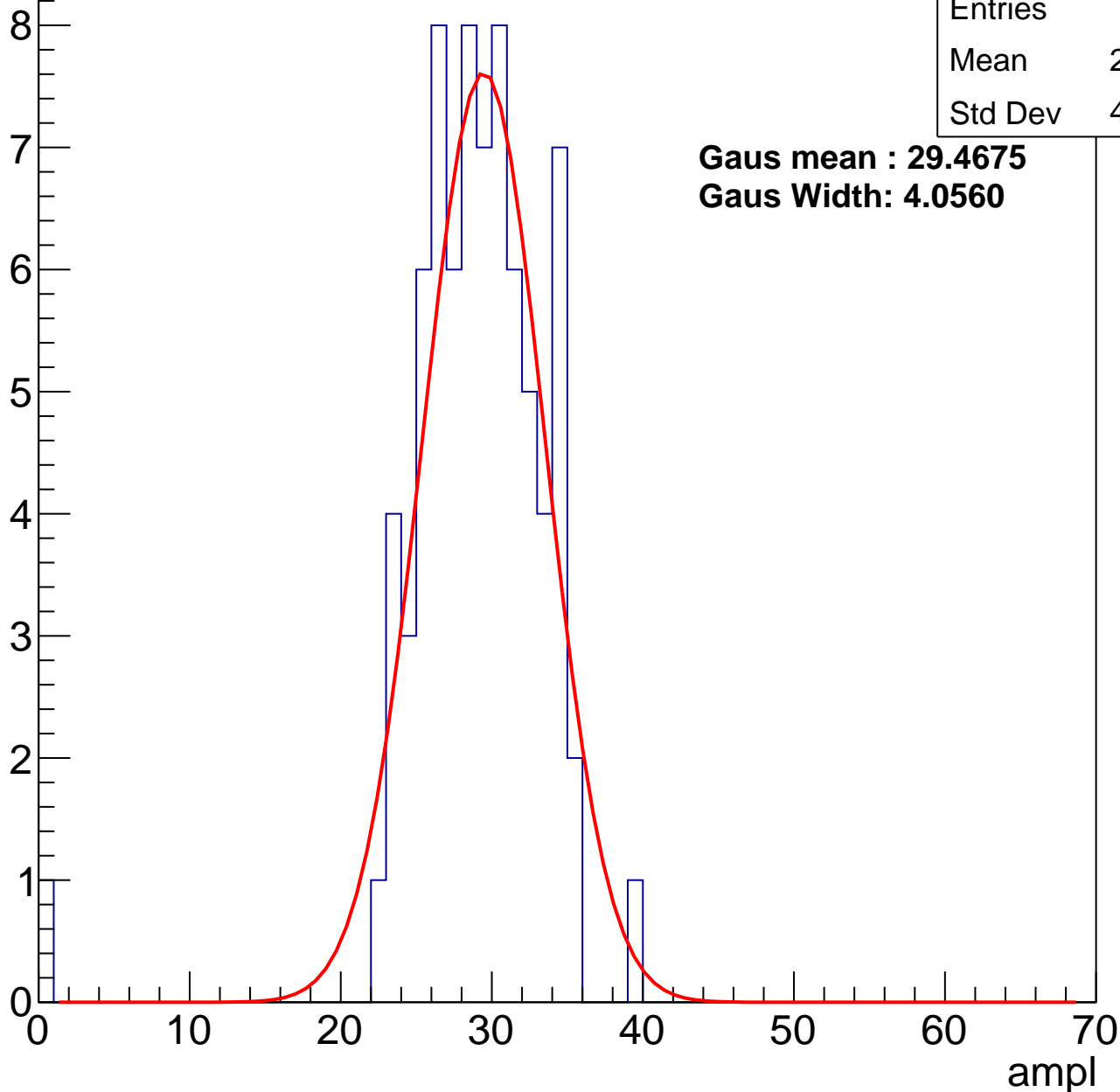
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.55
Std Dev	4.809

Gaus mean : 29.4675

Gaus Width: 4.0560



B1L102S, U19-ch112, adc1

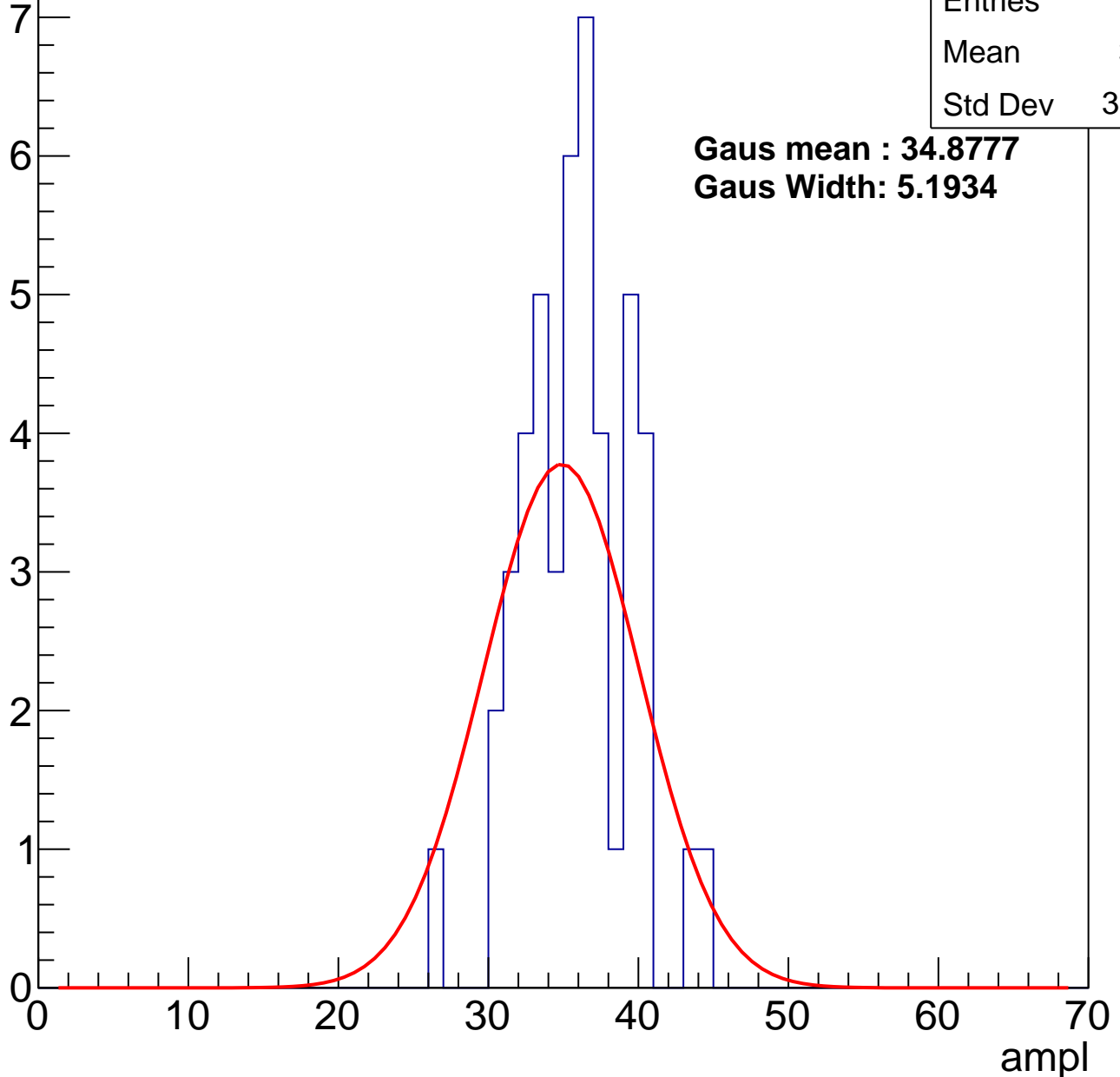
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	35.4
Std Dev	3.535

Gaus mean : 34.8777

Gaus Width: 5.1934



B1L102S, U19-ch112, adc2

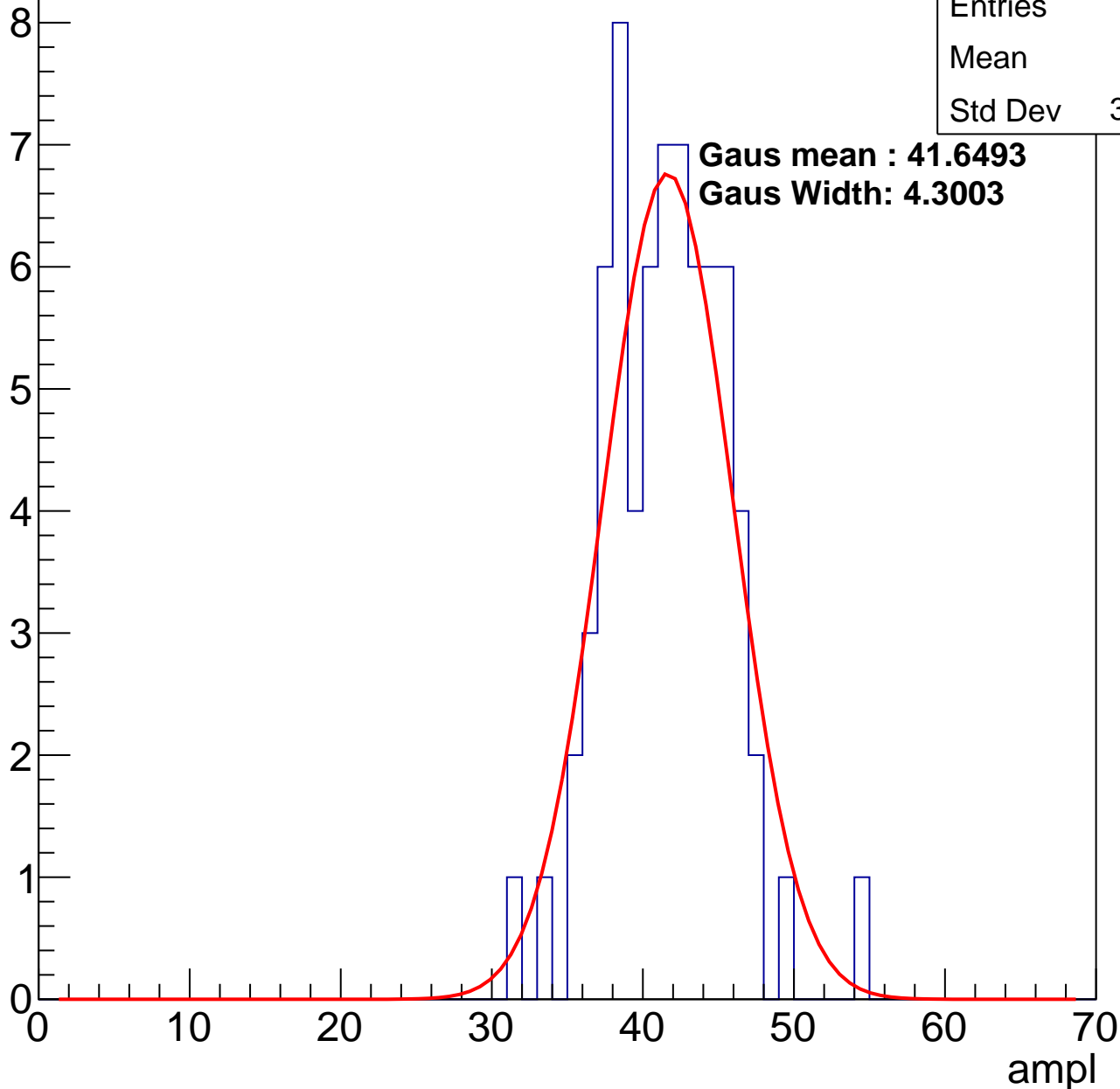
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.1
Std Dev	3.912

Gaus mean : 41.6493

Gaus Width: 4.3003

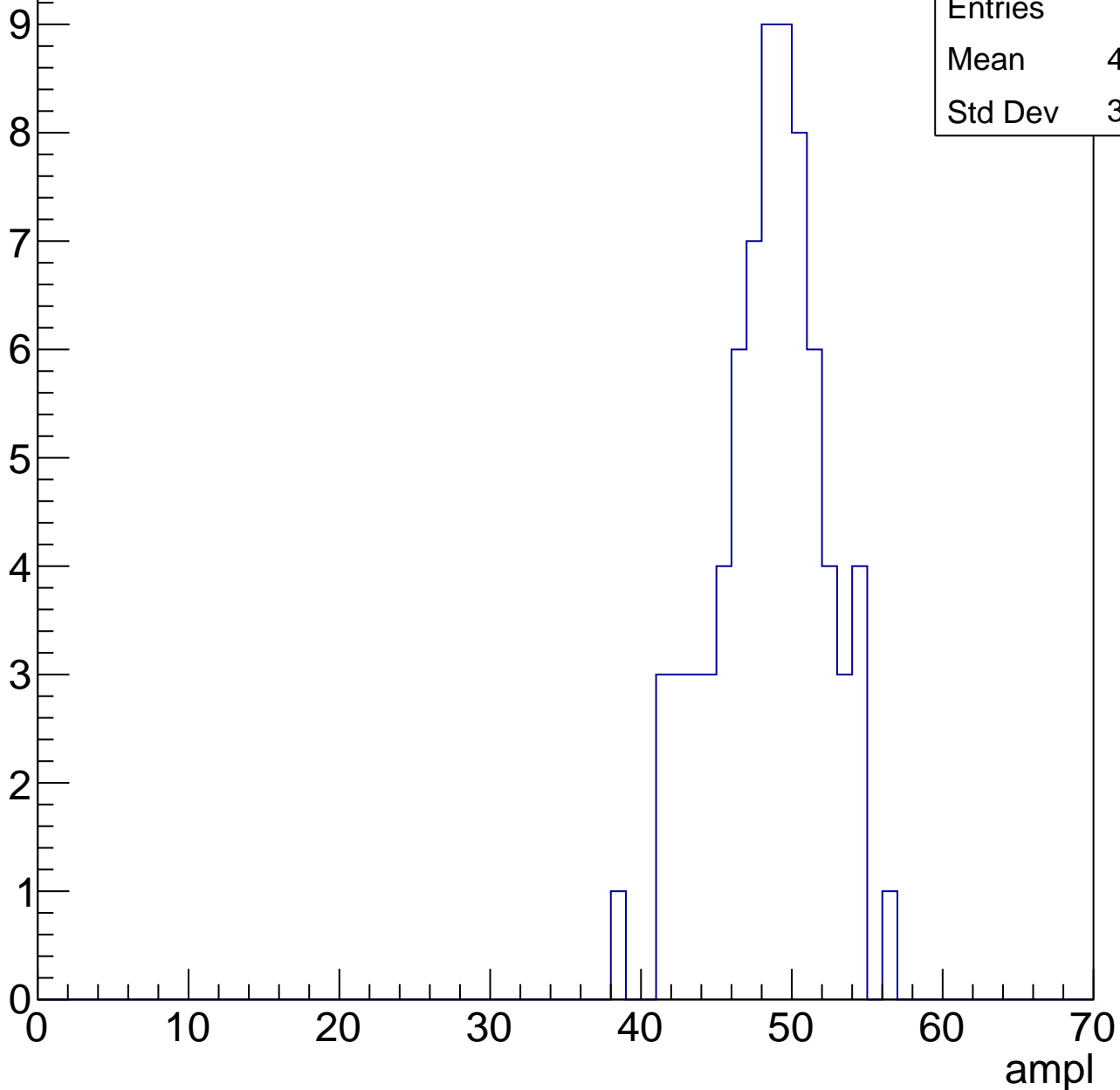


B1L102S, U19-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.99
Std Dev	3.652

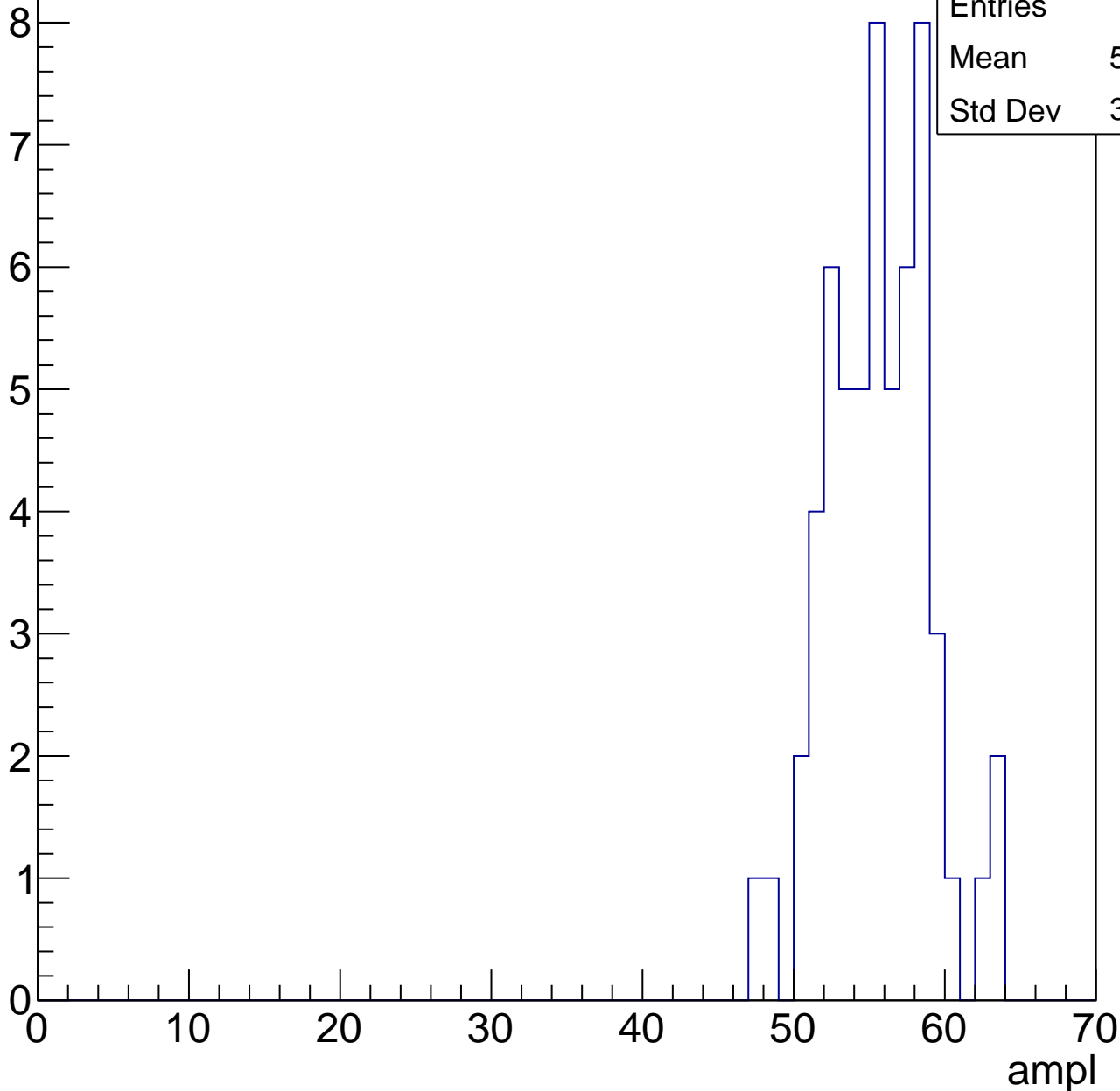


B1L102S, U19-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.12
Std Dev	3.363

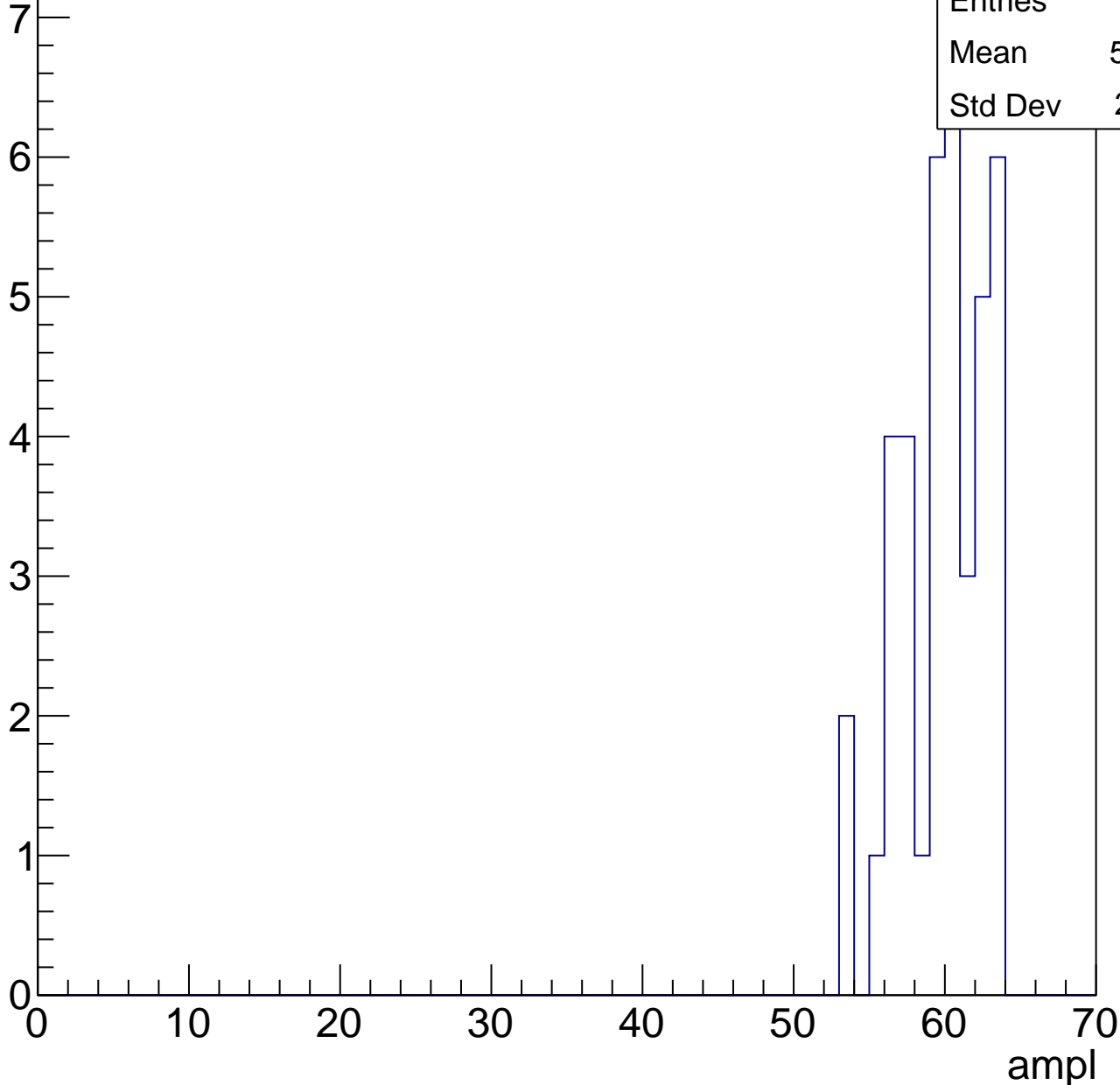


B1L102S, U19-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.38
Std Dev	2.751

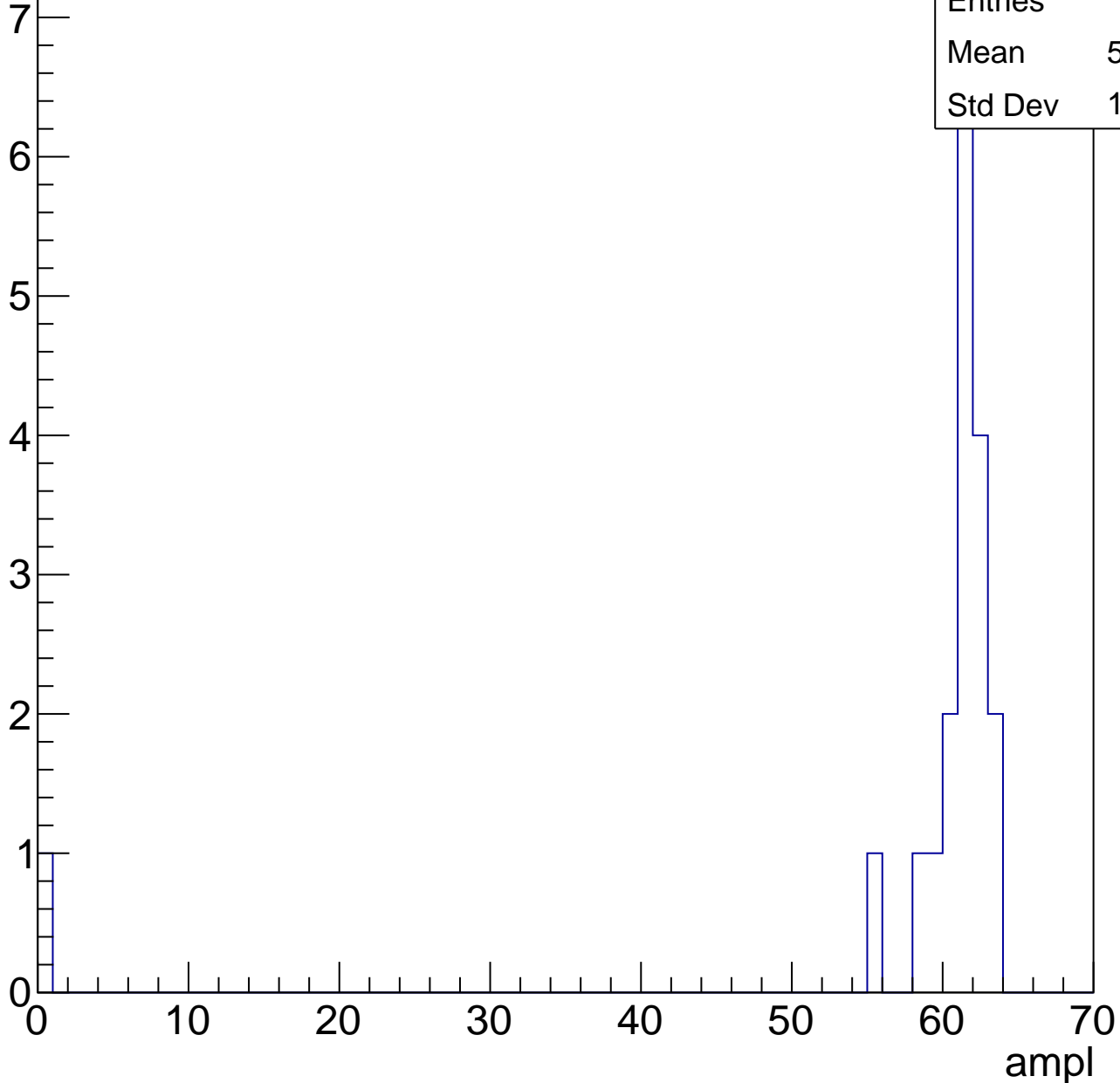


B1L102S, U19-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	57.53
Std Dev	13.68



B1L102S, U19-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch113, adc0

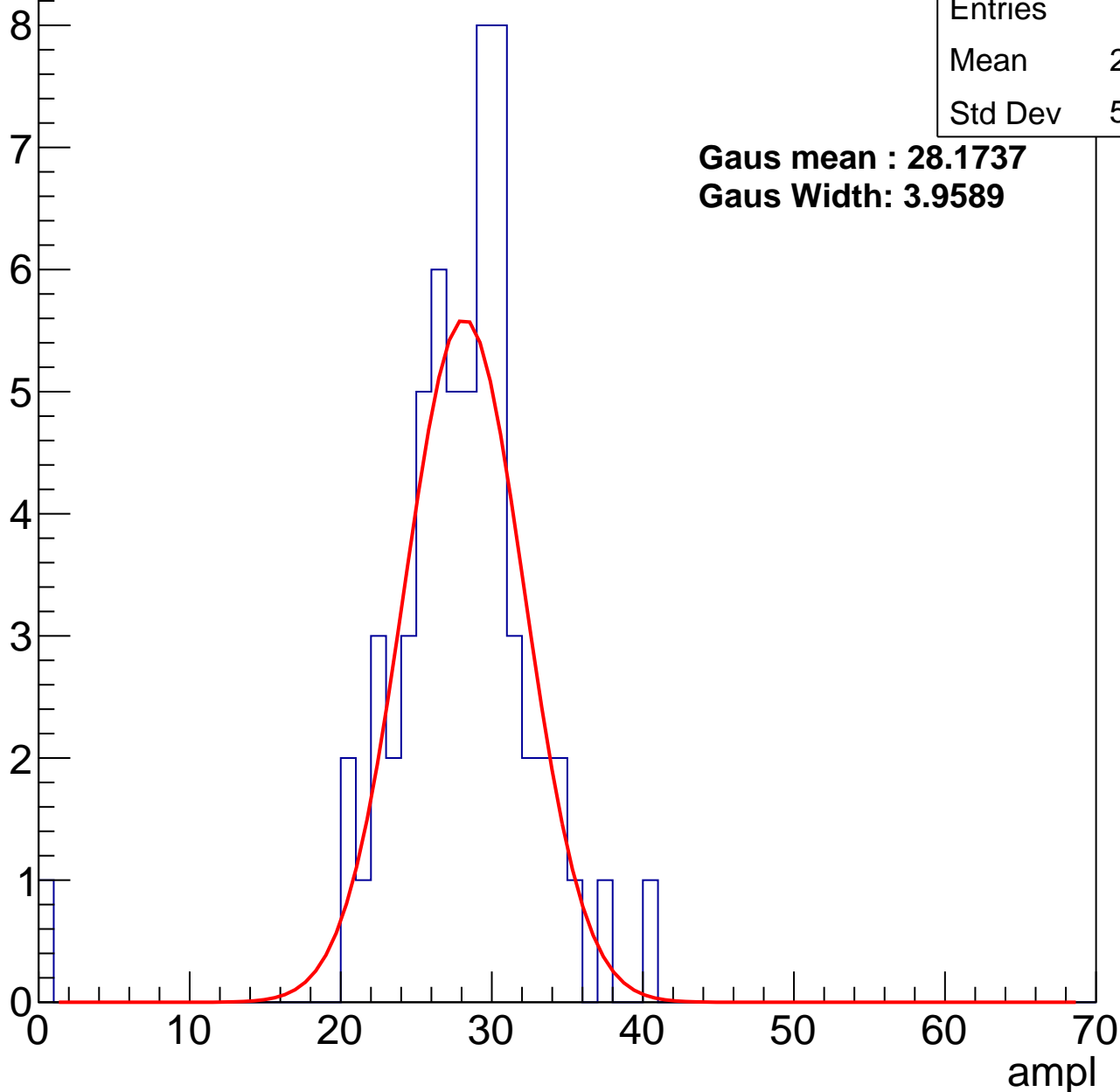
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.48
Std Dev	5.309

Gaus mean : 28.1737

Gaus Width: 3.9589



B1L102S, U19-ch113, adc1

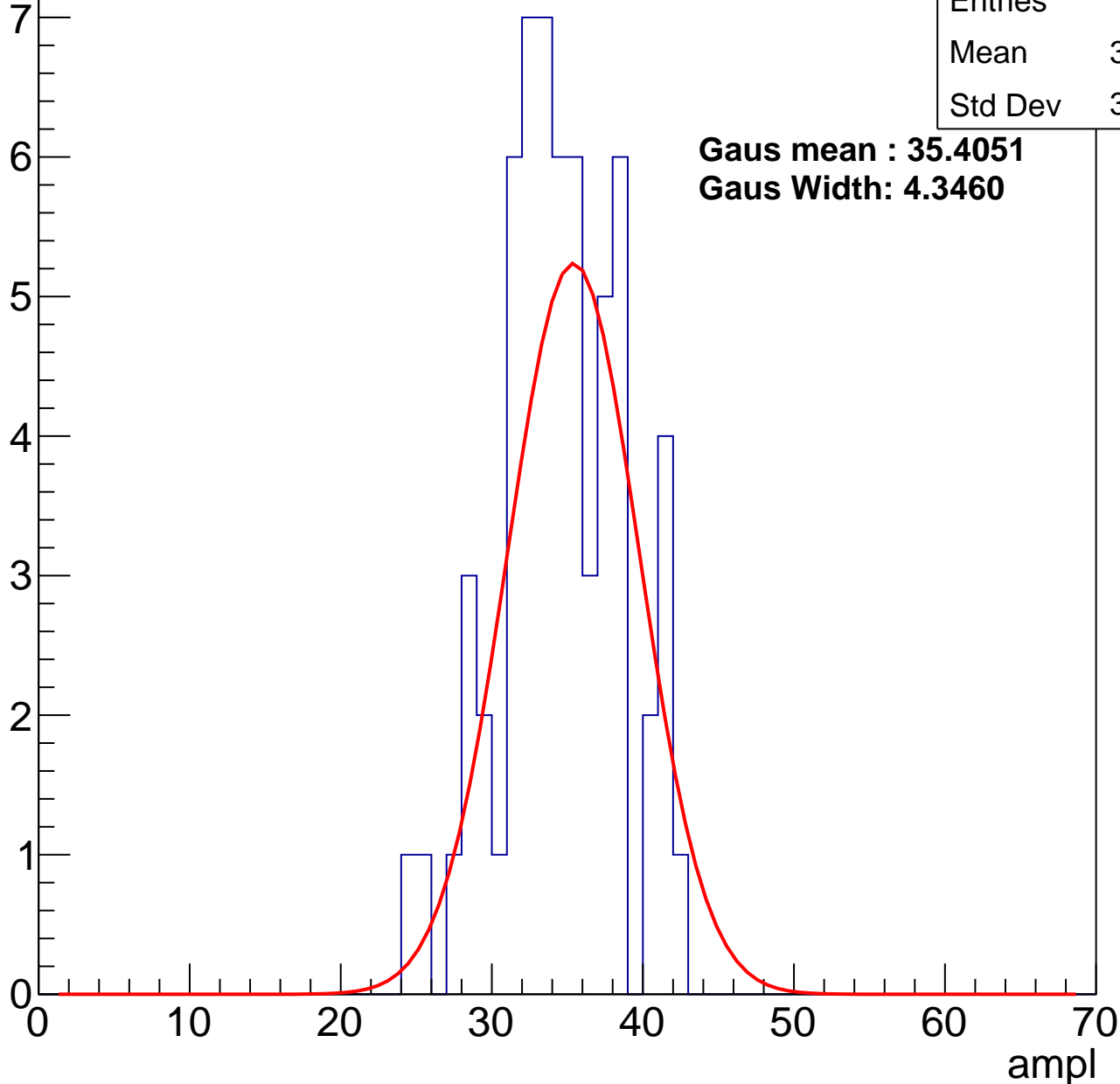
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.03
Std Dev	3.984

Gaus mean : 35.4051

Gaus Width: 4.3460



B1L102S, U19-ch113, adc2

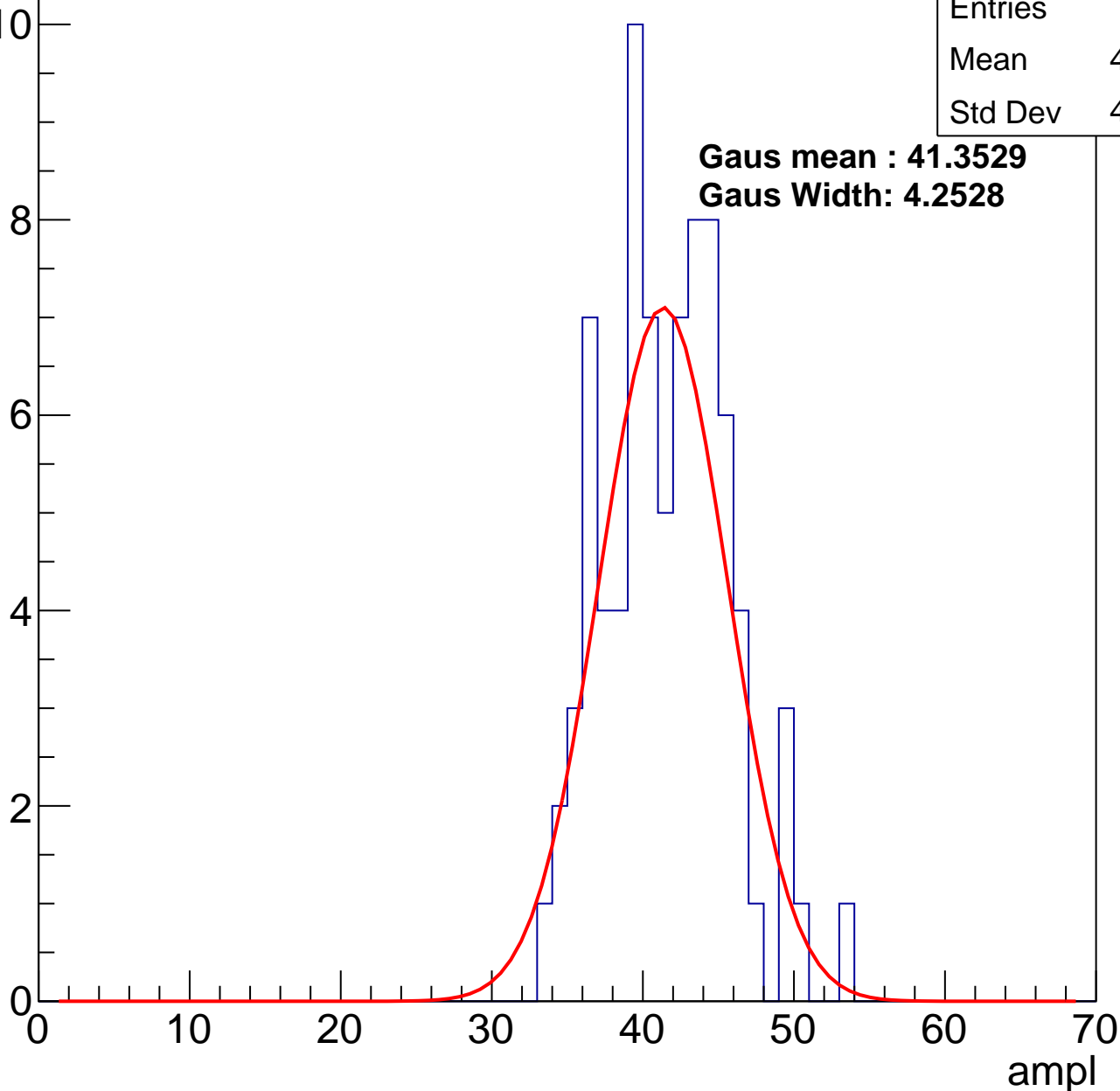
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	41.15
Std Dev	4.058

Gaus mean : 41.3529

Gaus Width: 4.2528

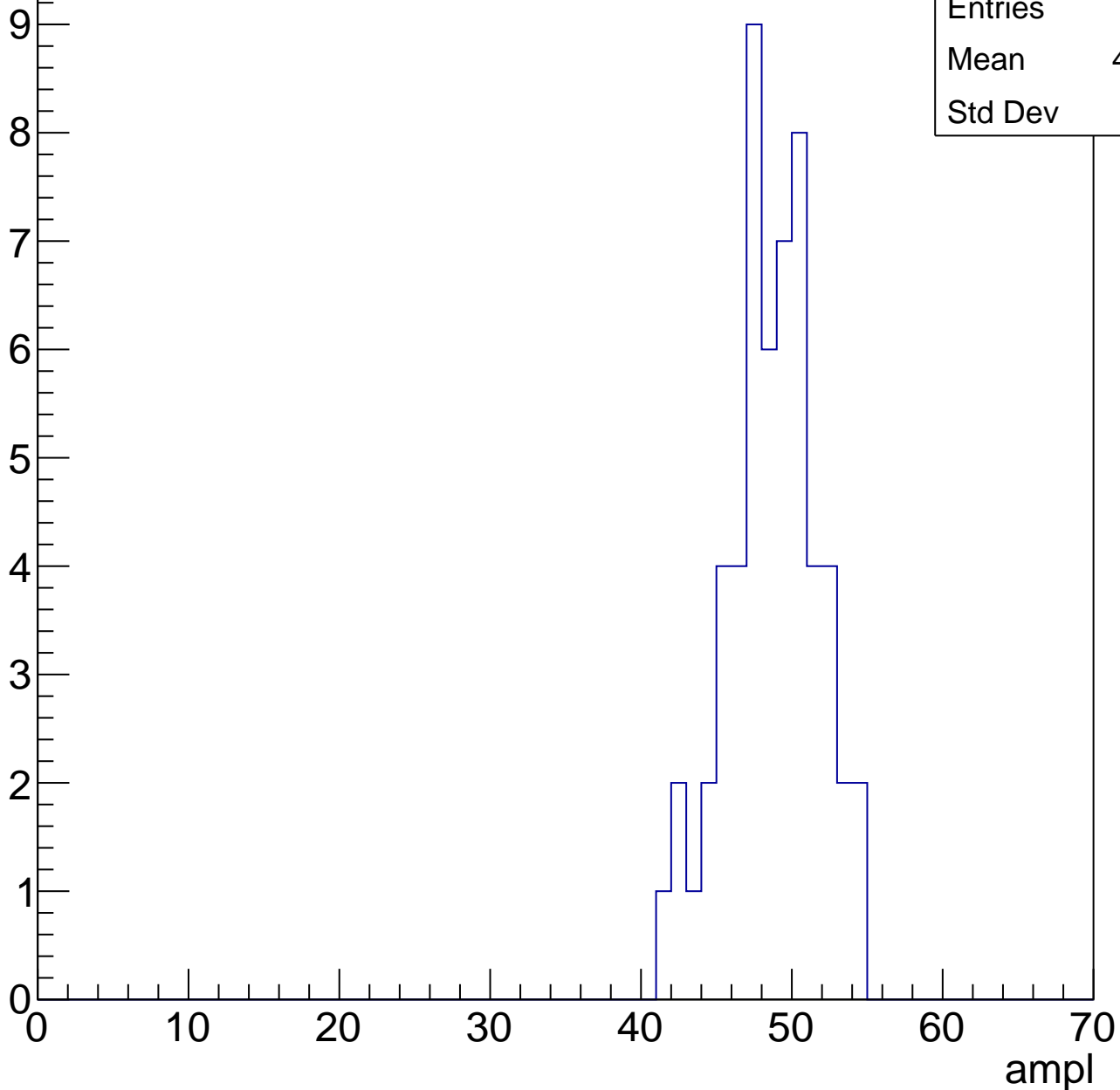


B1L102S, U19-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.21
Std Dev	2.98

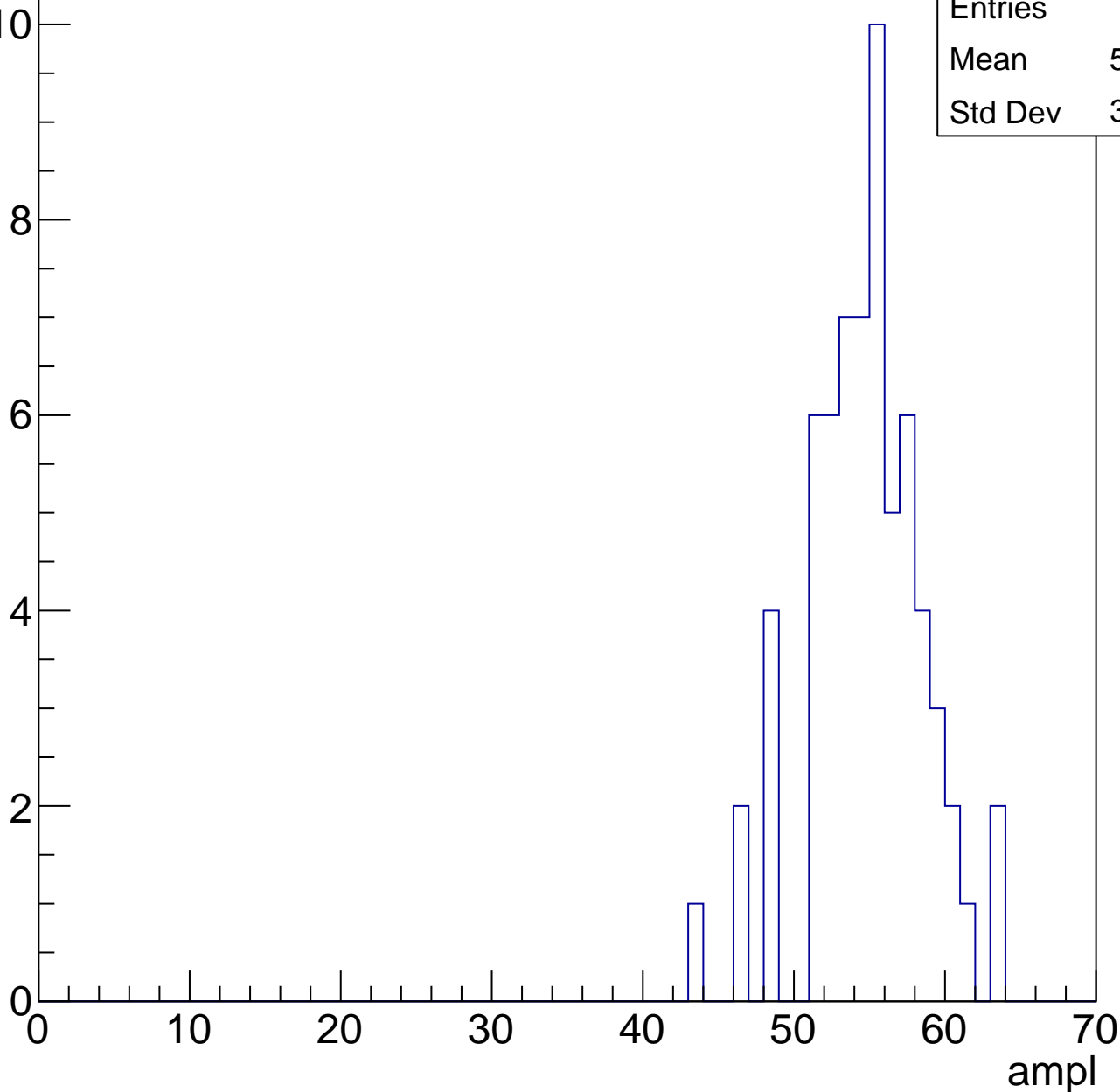


B1L102S, U19-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.27
Std Dev	3.852

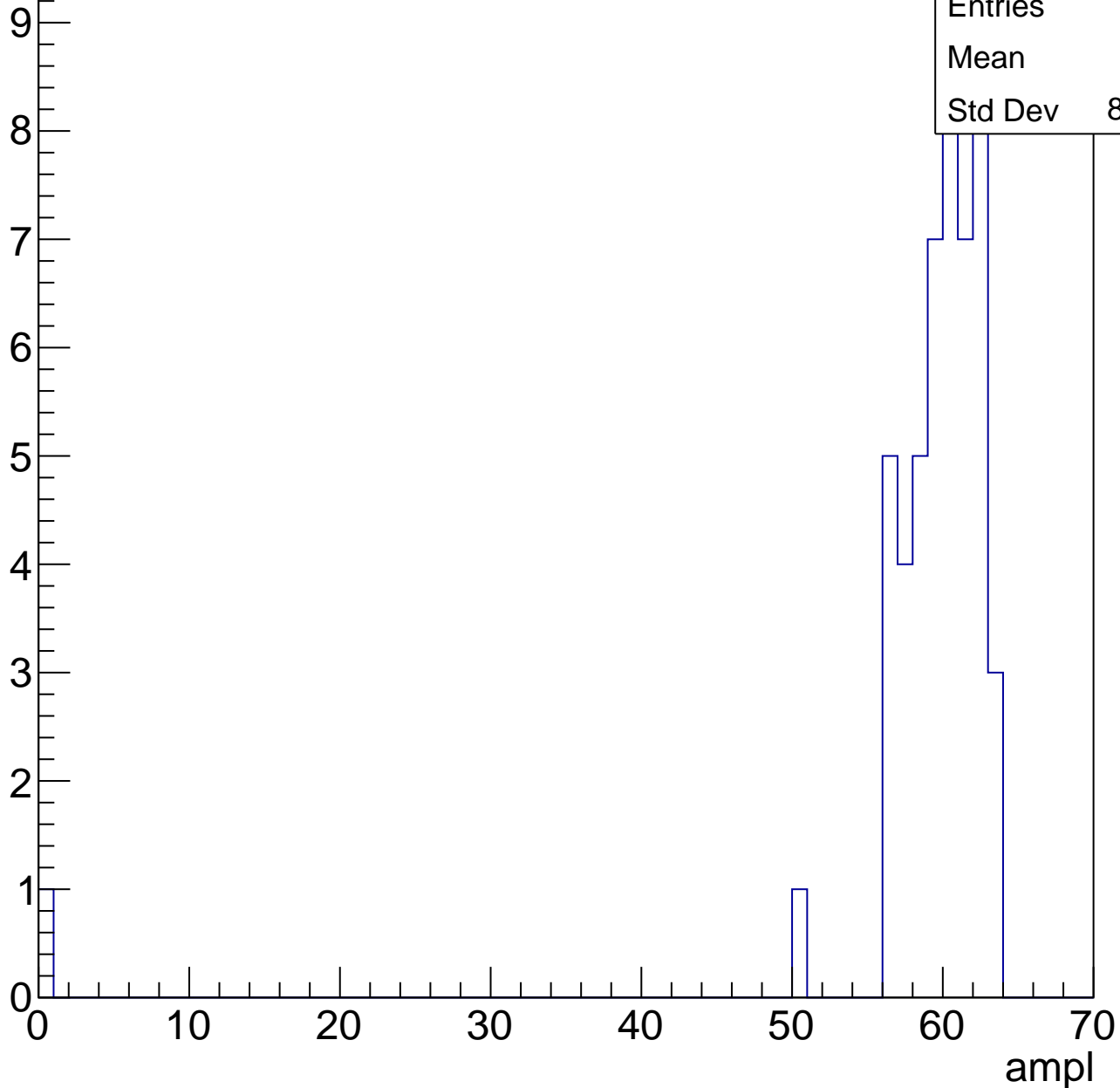


B1L102S, U19-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.3
Std Dev	8.682

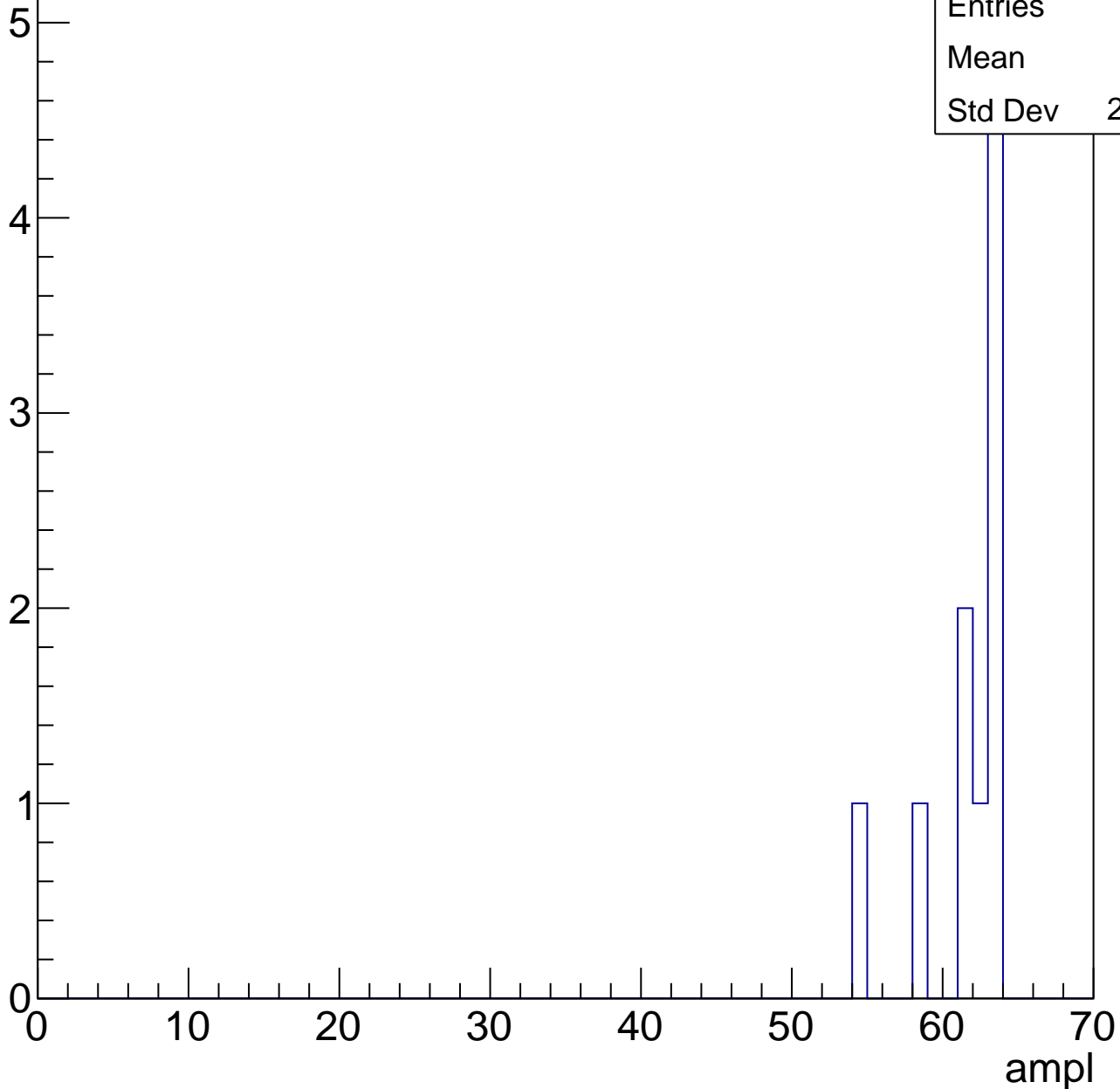


B1L102S, U19-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

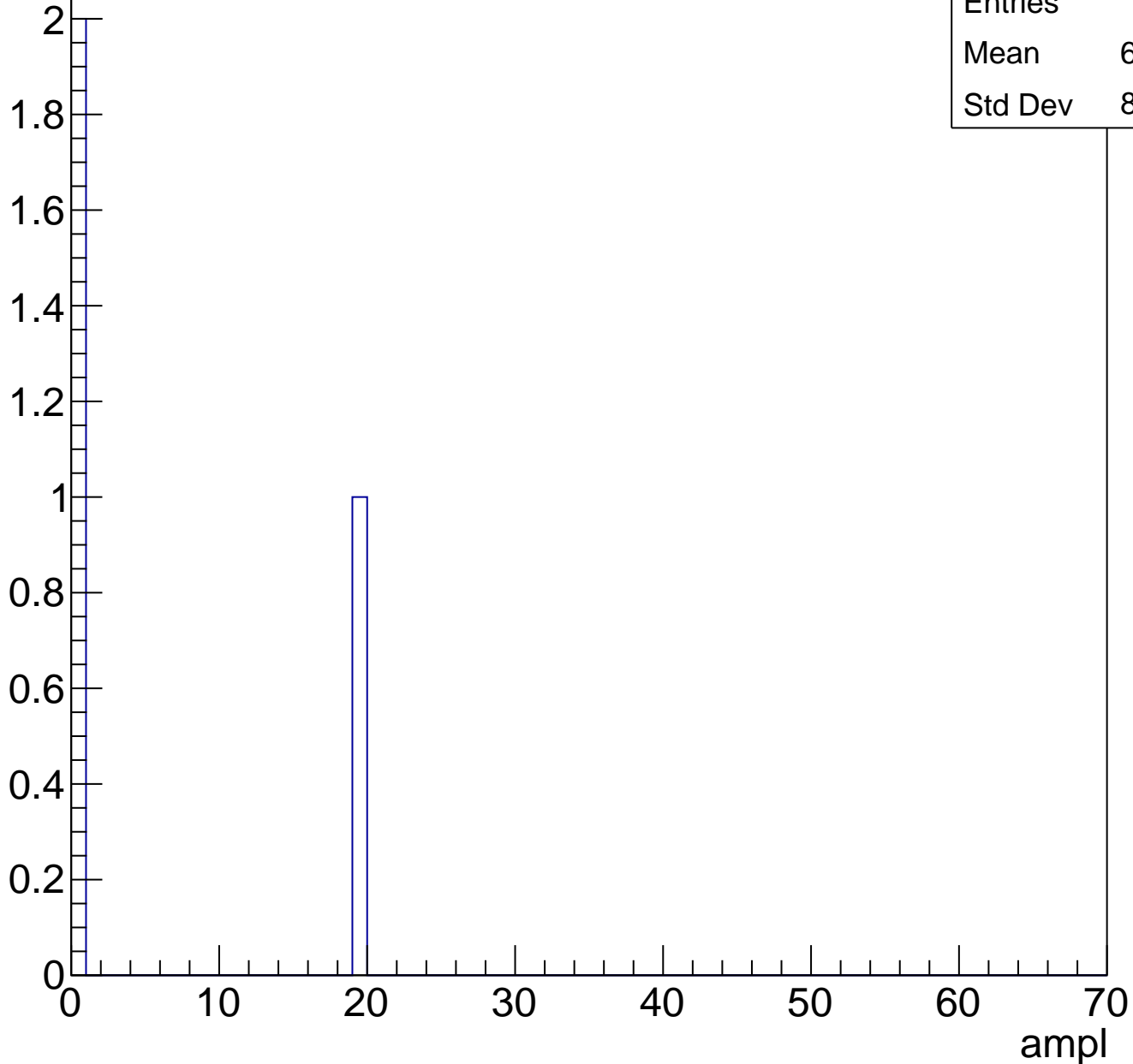
Entries	10
Mean	61.1
Std Dev	2.809



B1L102S, U19-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L102S, U19-ch114, adc0

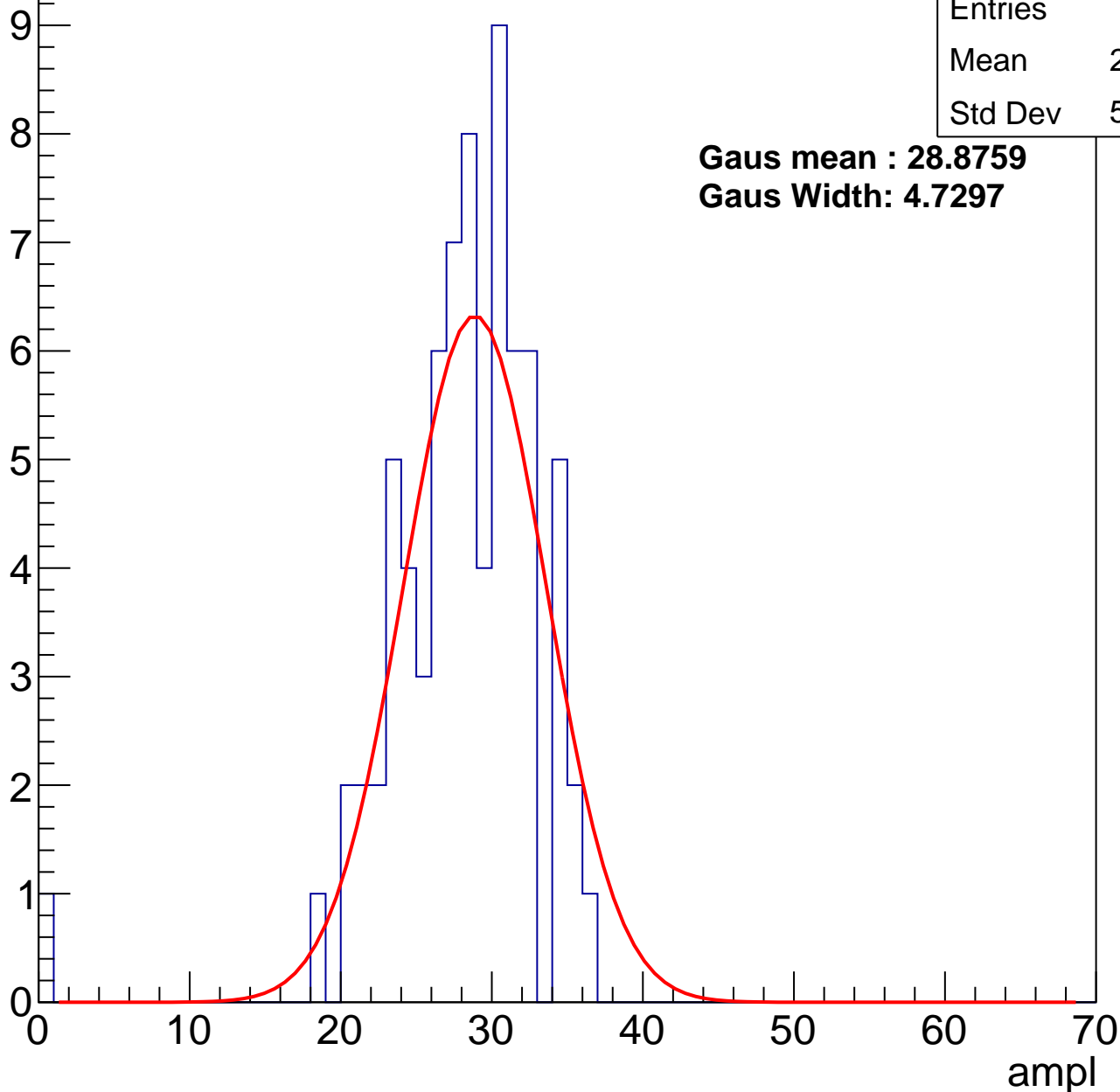
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.55
Std Dev	5.128

Gaus mean : 28.8759

Gaus Width: 4.7297



B1L102S, U19-ch114, adc1

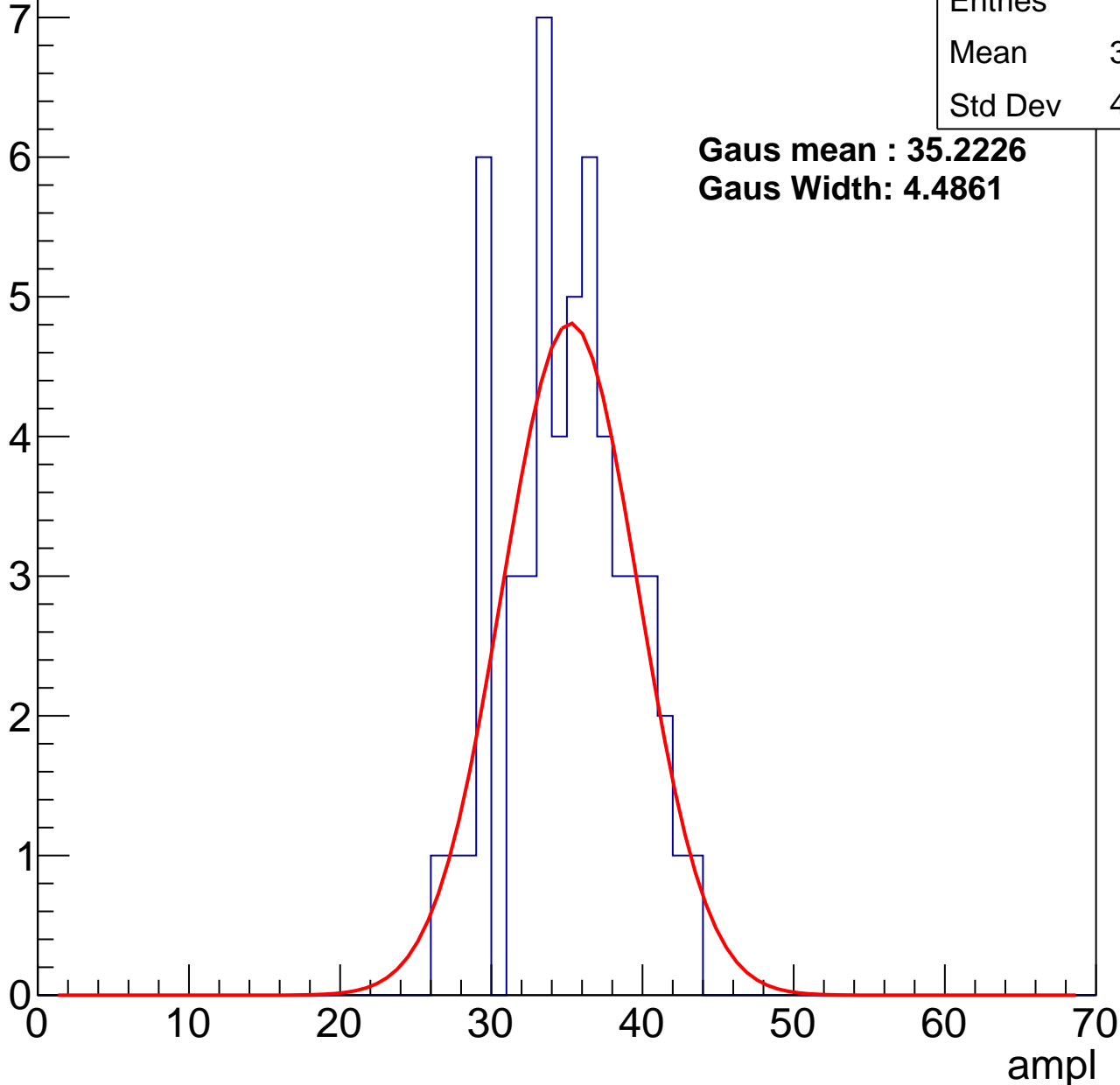
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.59
Std Dev	4.002

Gaus mean : 35.2226

Gaus Width: 4.4861



B1L102S, U19-ch114, adc2

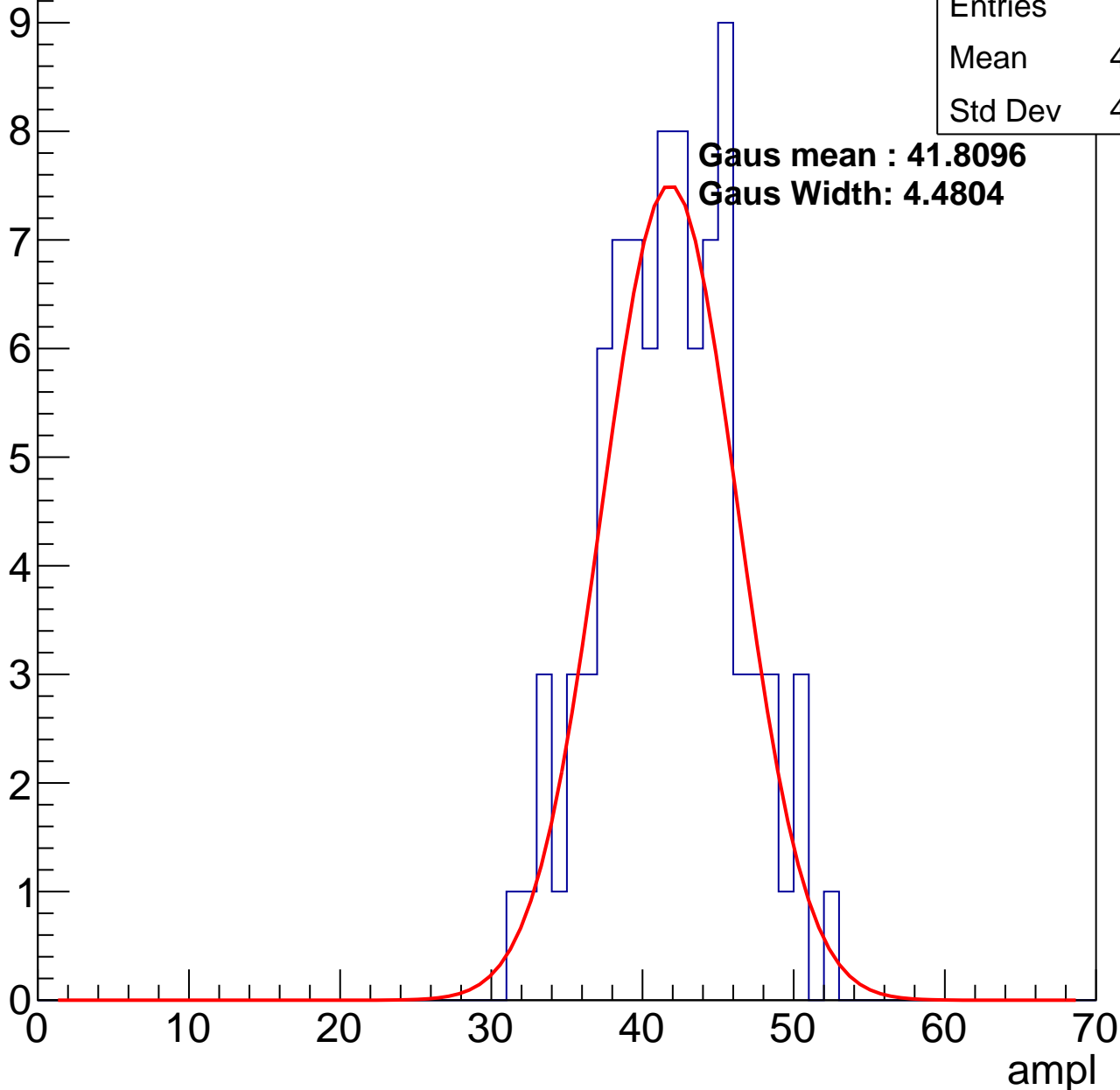
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	41.32
Std Dev	4.454

Gaus mean : 41.8096

Gaus Width: 4.4804

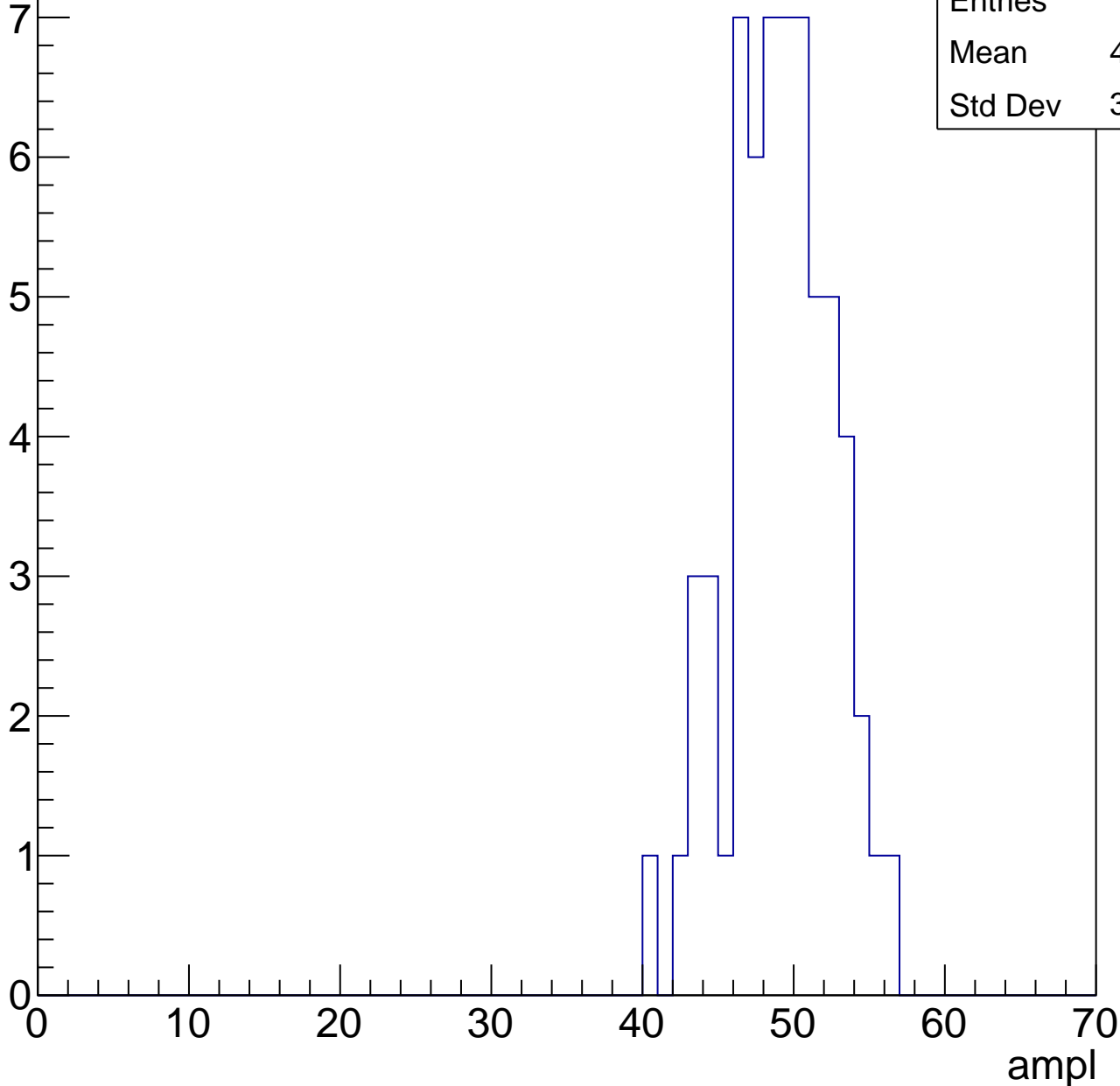


B1L102S, U19-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.64
Std Dev	3.354

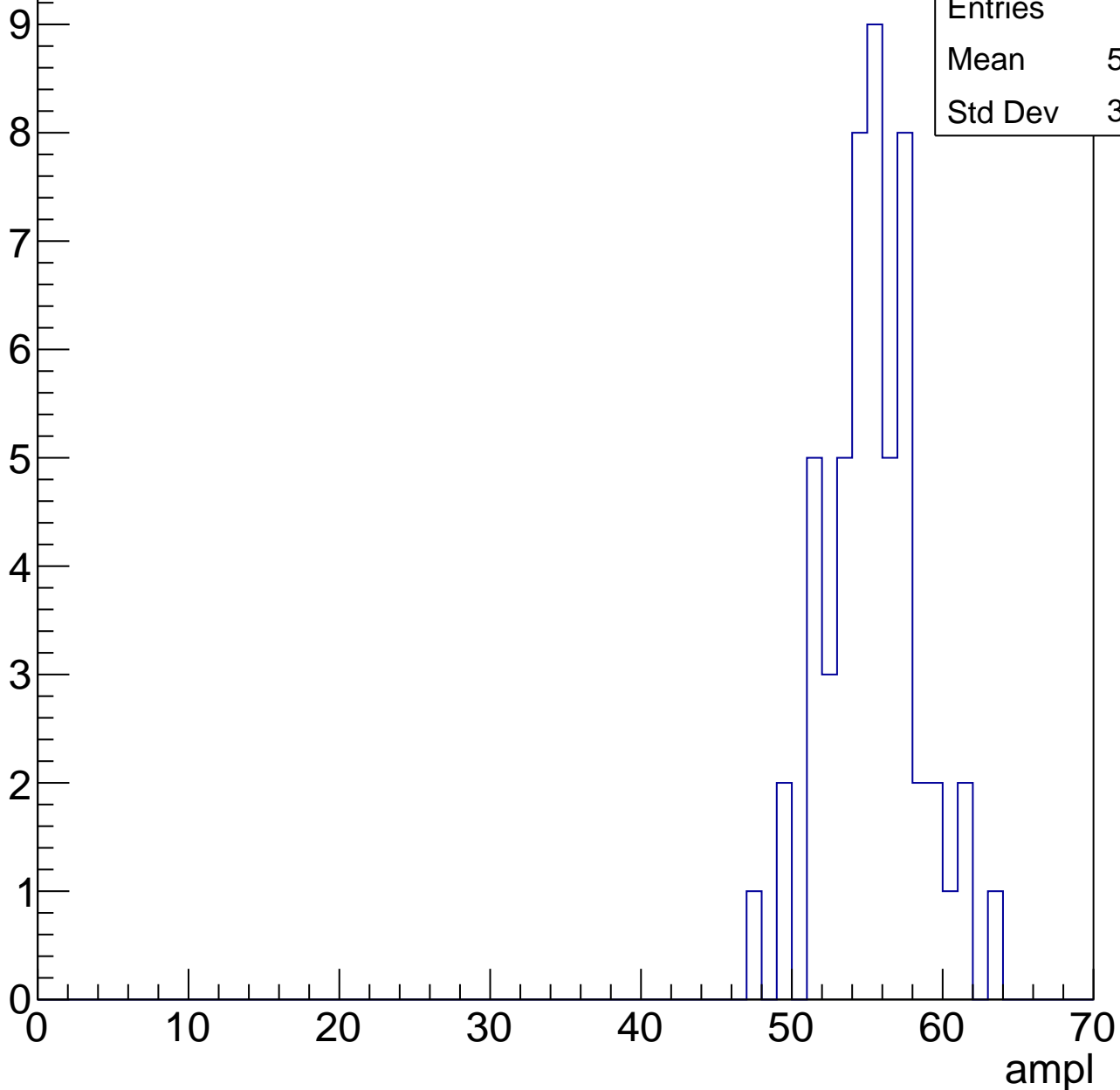


B1L102S, U19-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.87
Std Dev	3.097

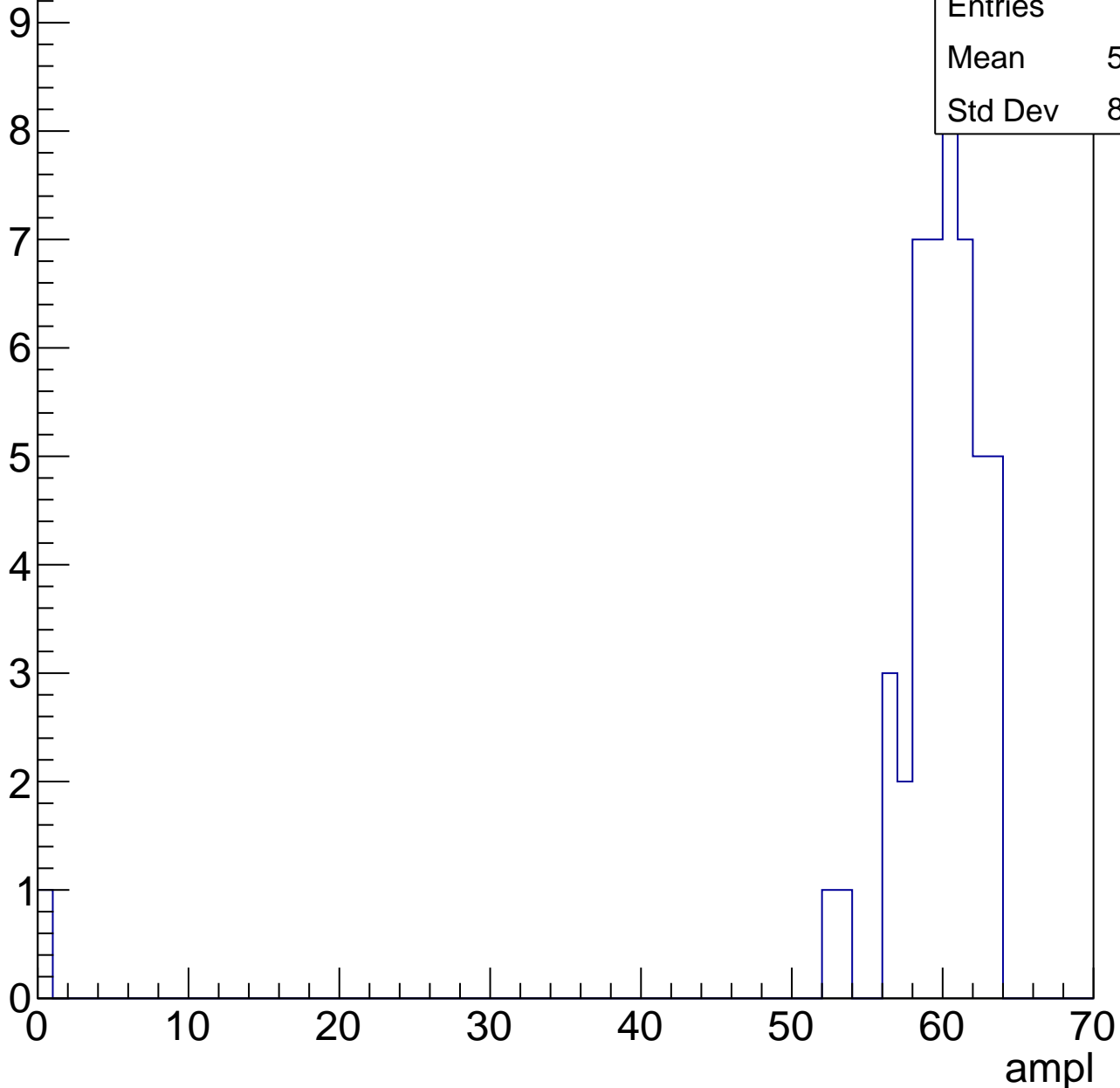


B1L102S, U19-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.29
Std Dev	8.834

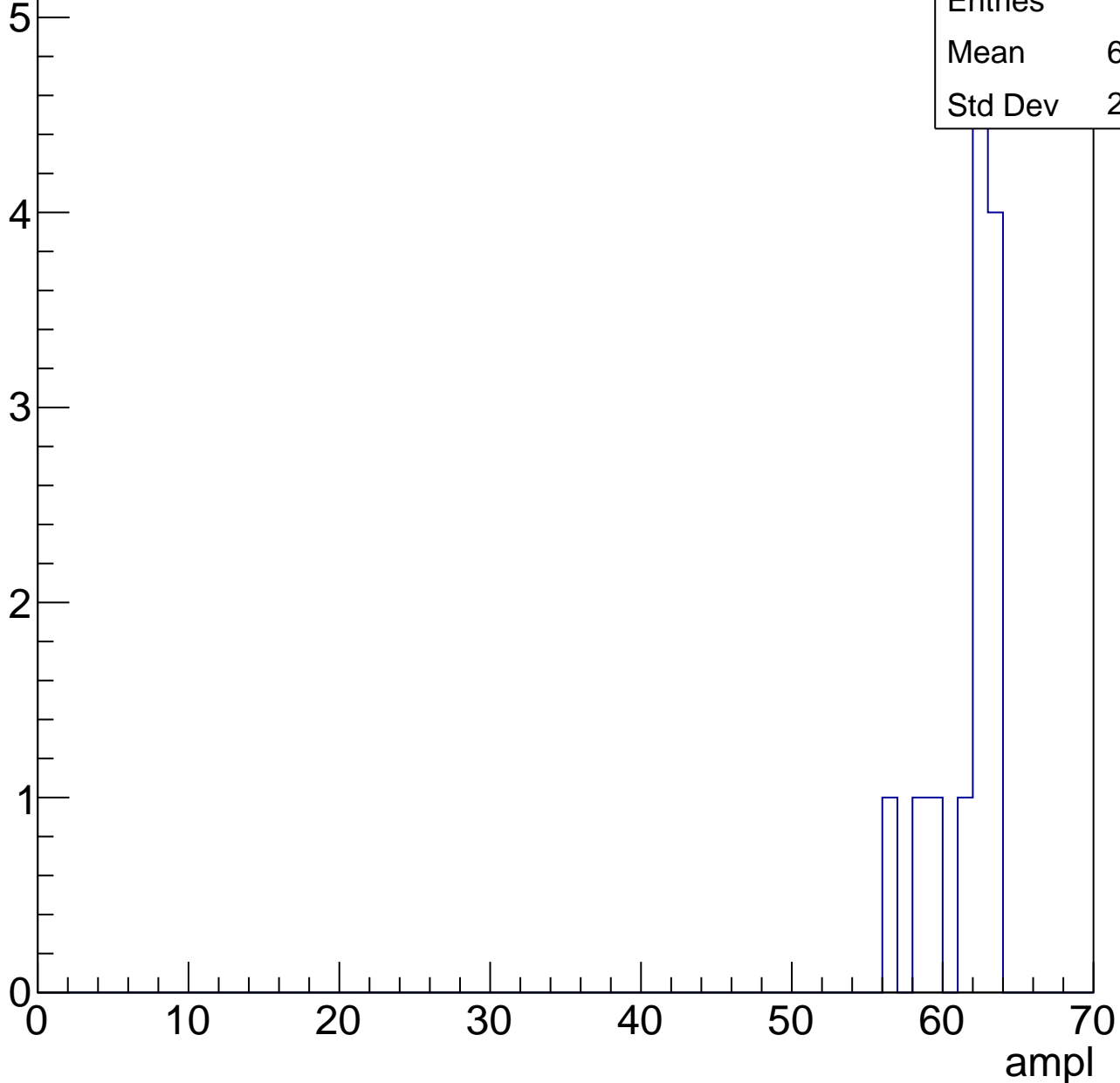


B1L102S, U19-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.23
Std Dev	2.118



B1L102S, U19-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch115, adc0

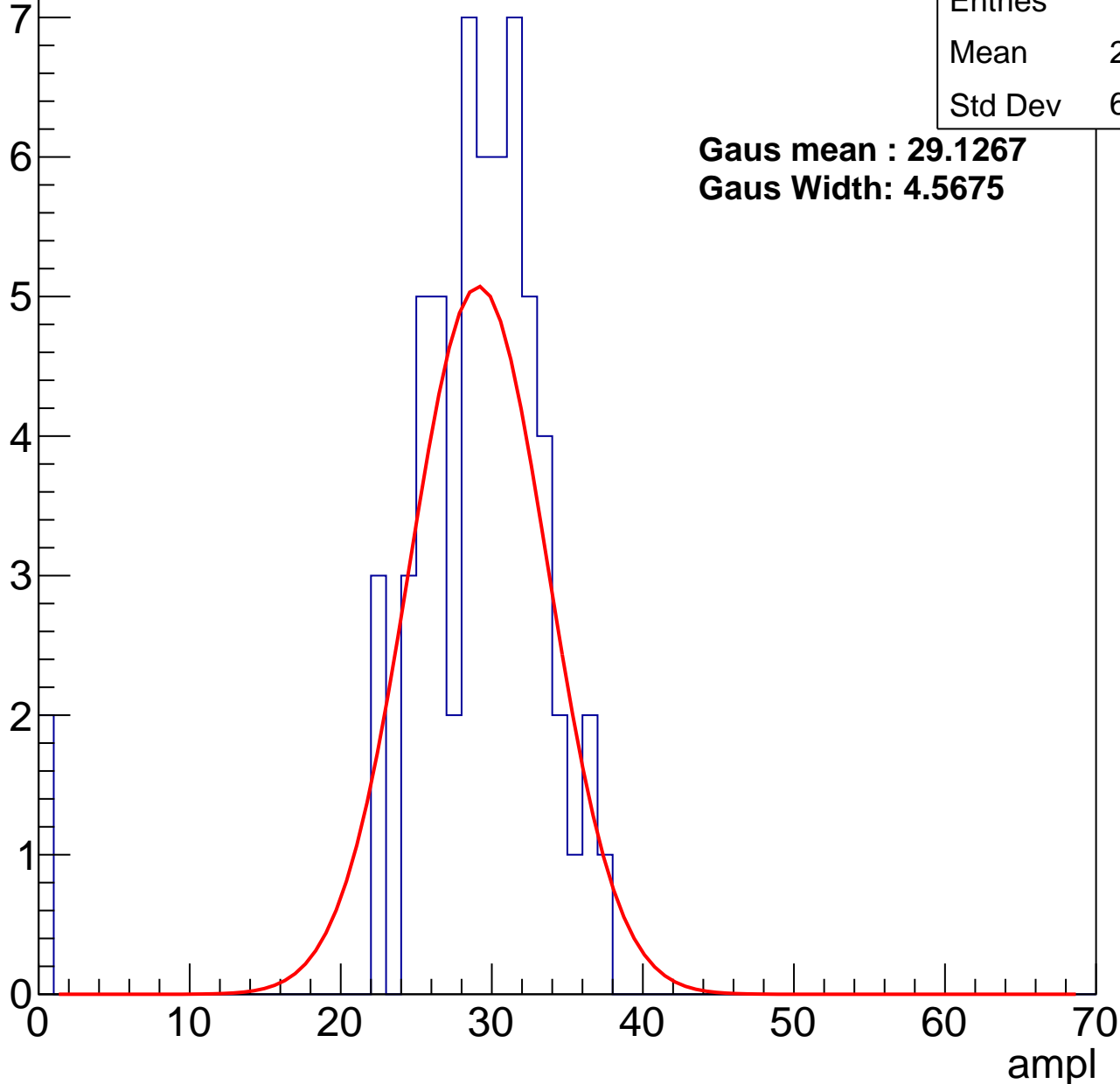
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.16
Std Dev	6.263

Gaus mean : 29.1267

Gaus Width: 4.5675



B1L102S, U19-ch115, adc1

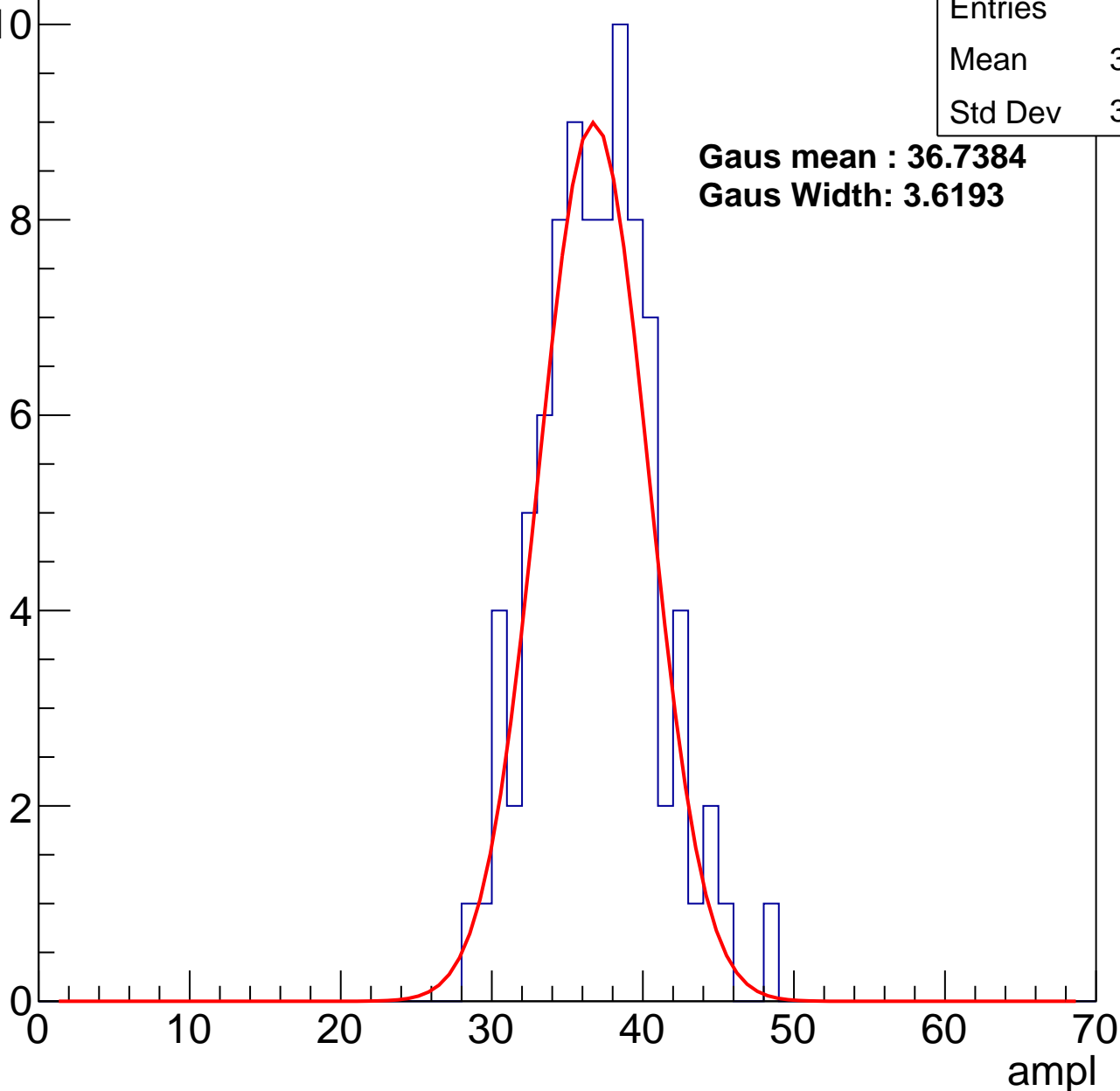
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	36.52
Std Dev	3.823

Gaus mean : 36.7384

Gaus Width: 3.6193



B1L102S, U19-ch115, adc2

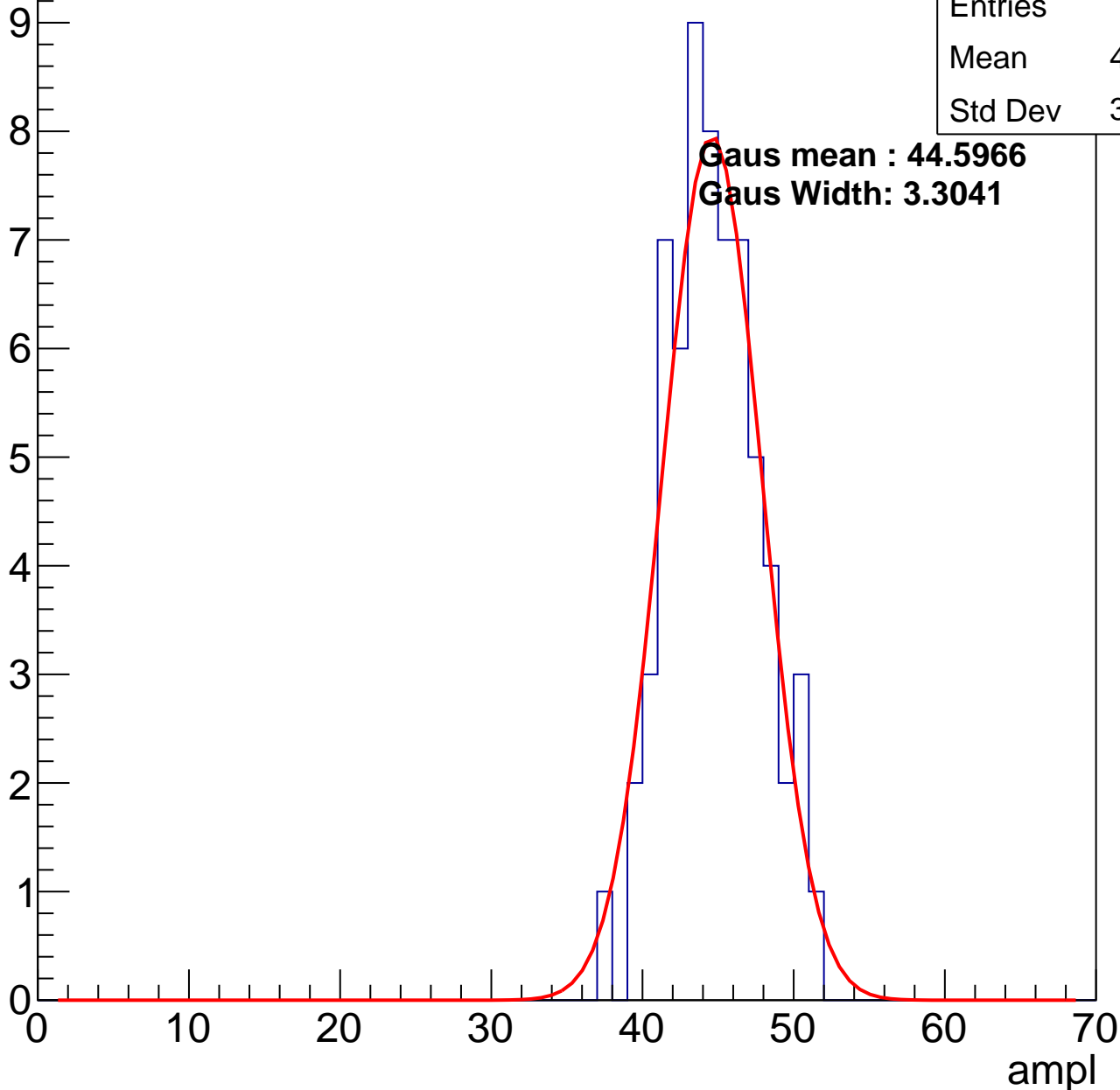
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.25
Std Dev	3.013

Gaus mean : 44.5966

Gaus Width: 3.3041

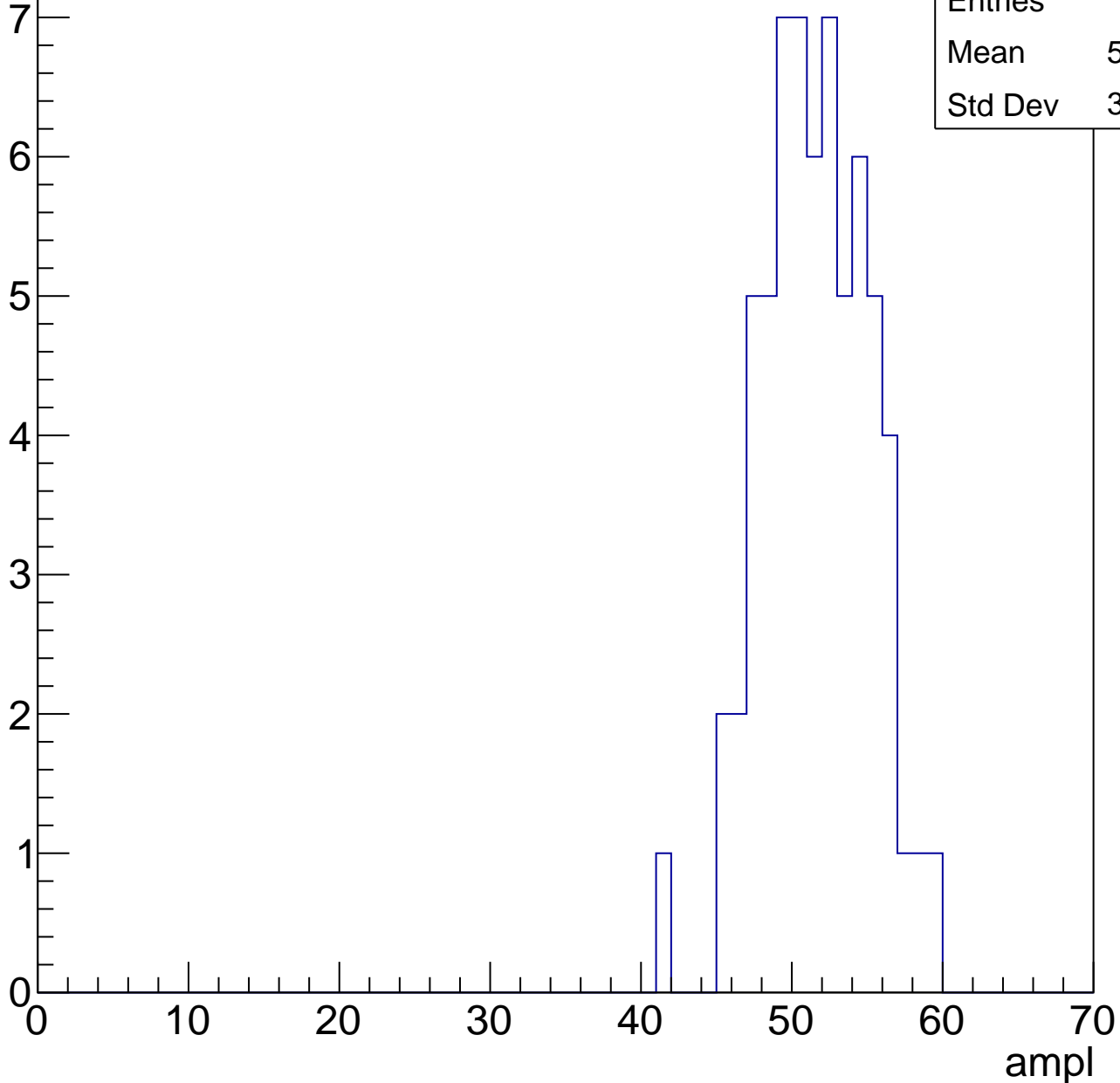


B1L102S, U19-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	51.12
Std Dev	3.488

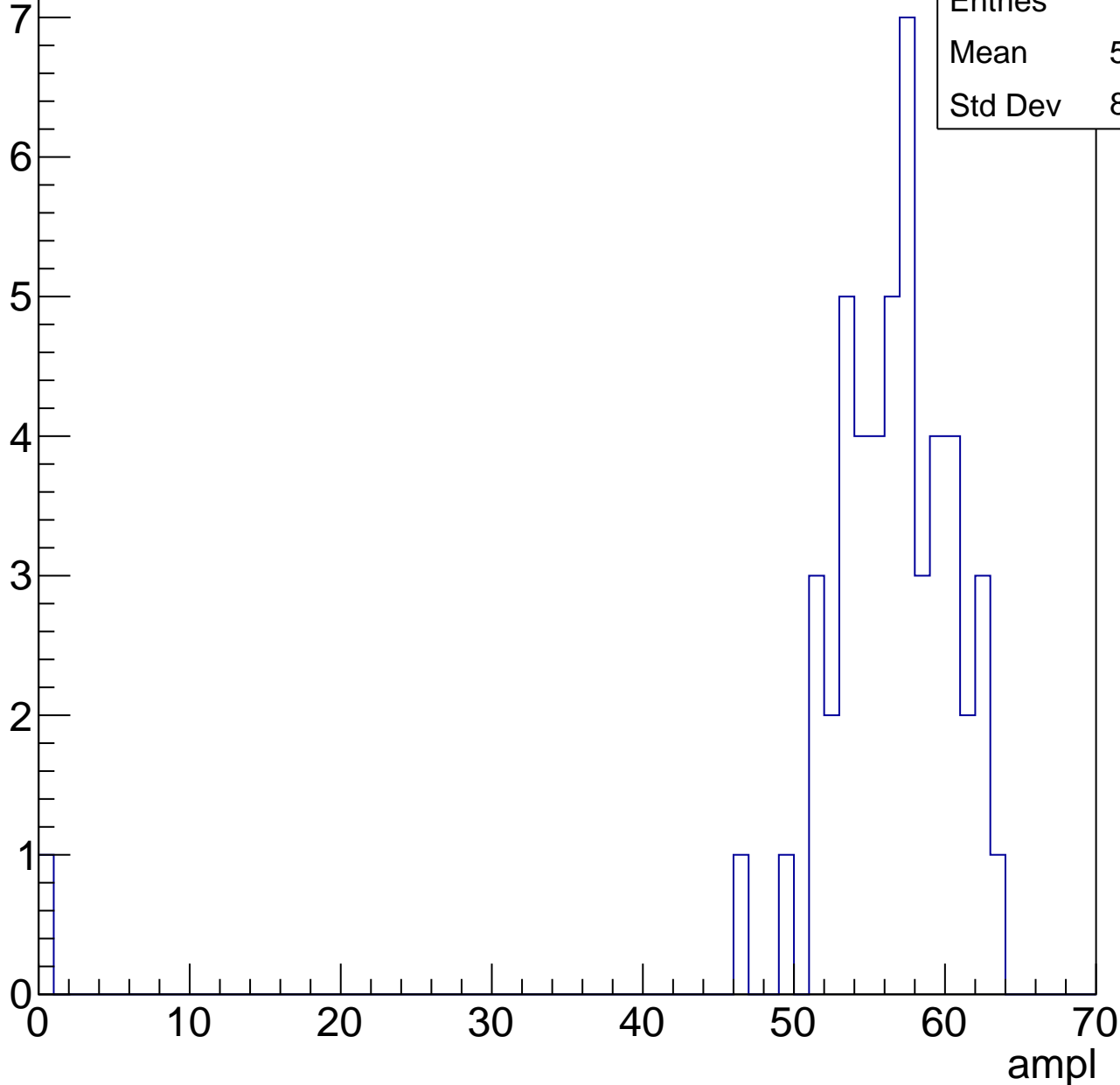


B1L102S, U19-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.06
Std Dev	8.647

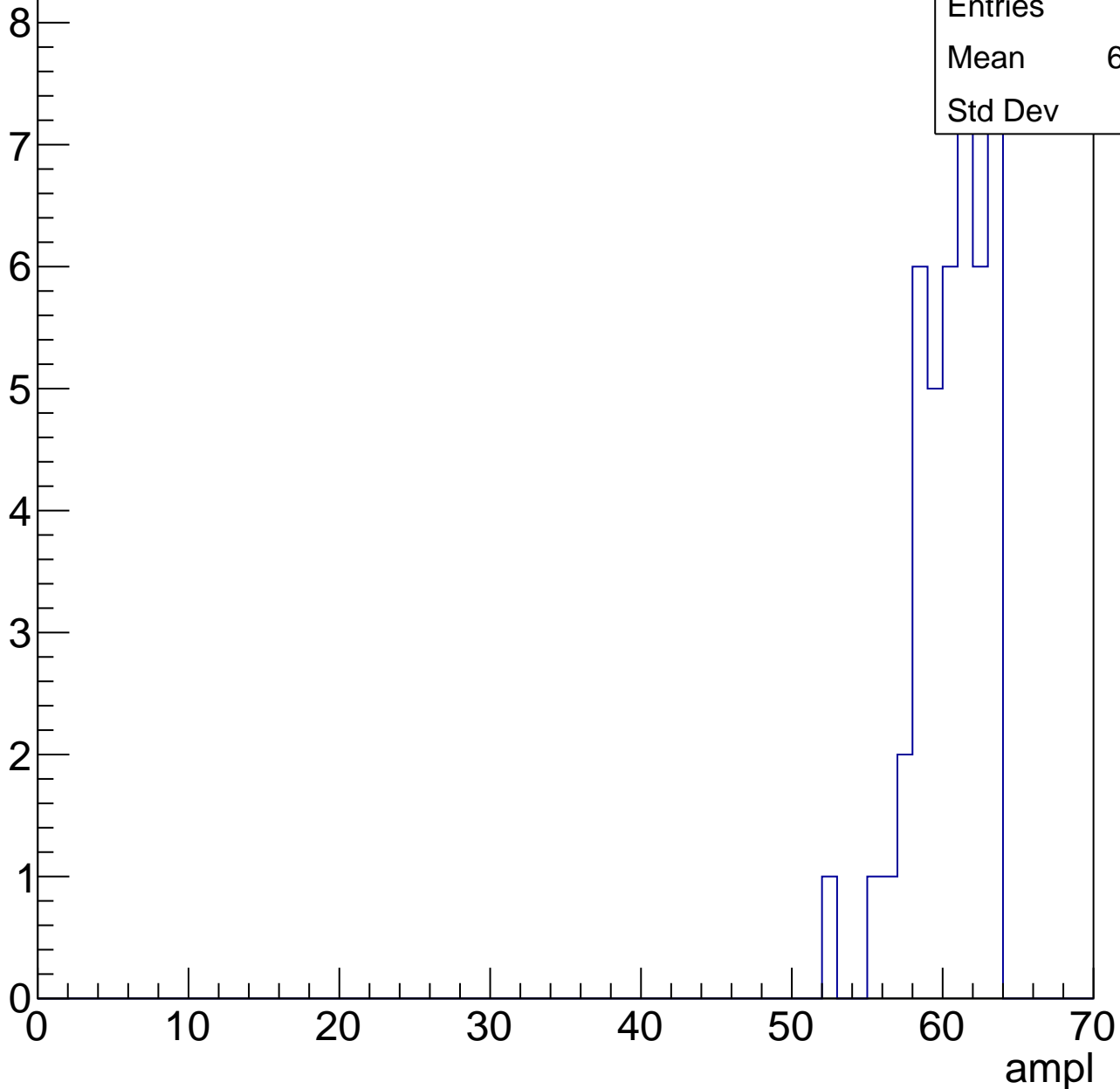


B1L102S, U19-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	60.09
Std Dev	2.41



B1L102S, U19-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch116, adc0

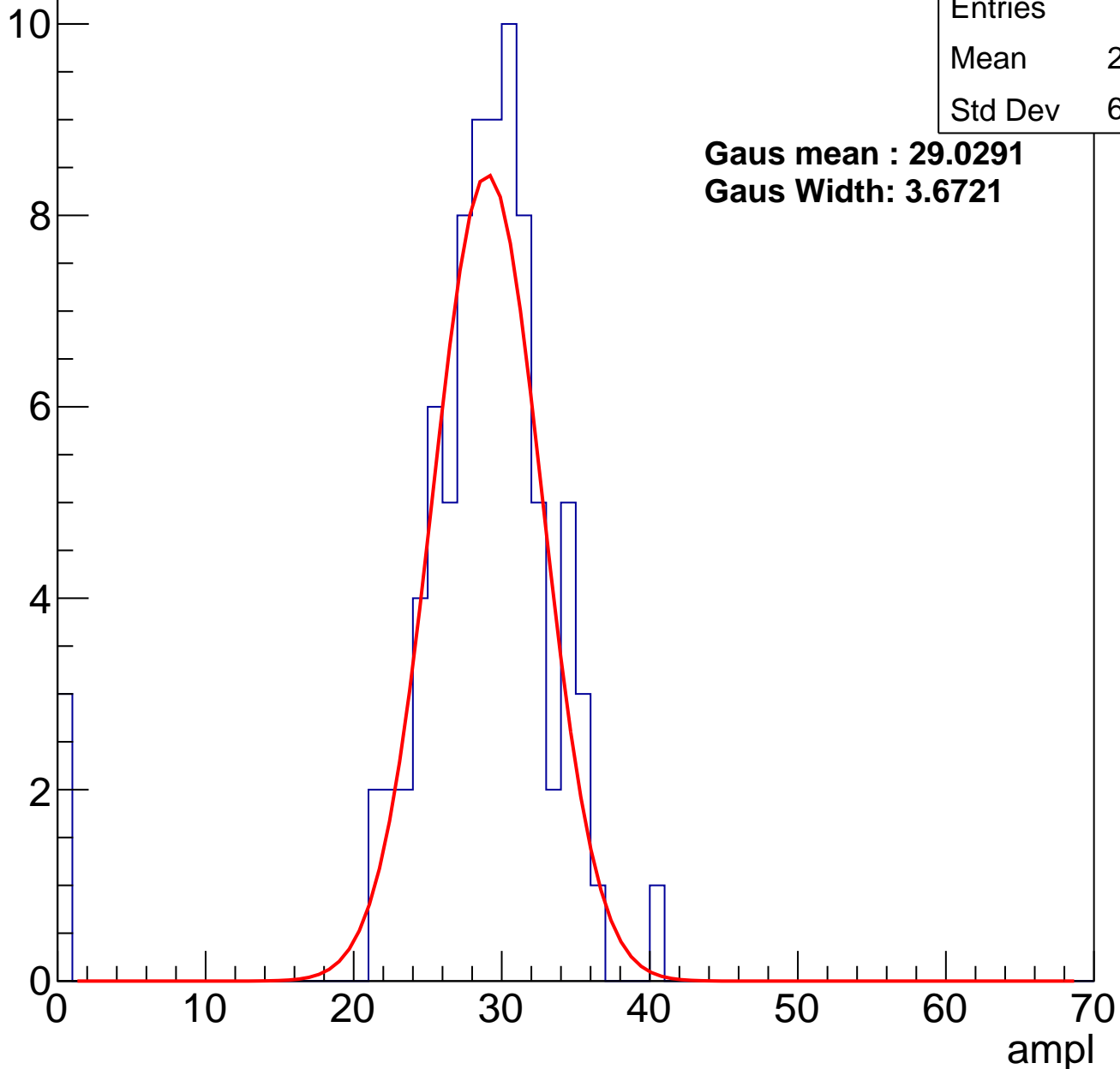
calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	27.79
Std Dev	6.417

Gaus mean : 29.0291

Gaus Width: 3.6721

Entry



B1L102S, U19-ch116, adc1

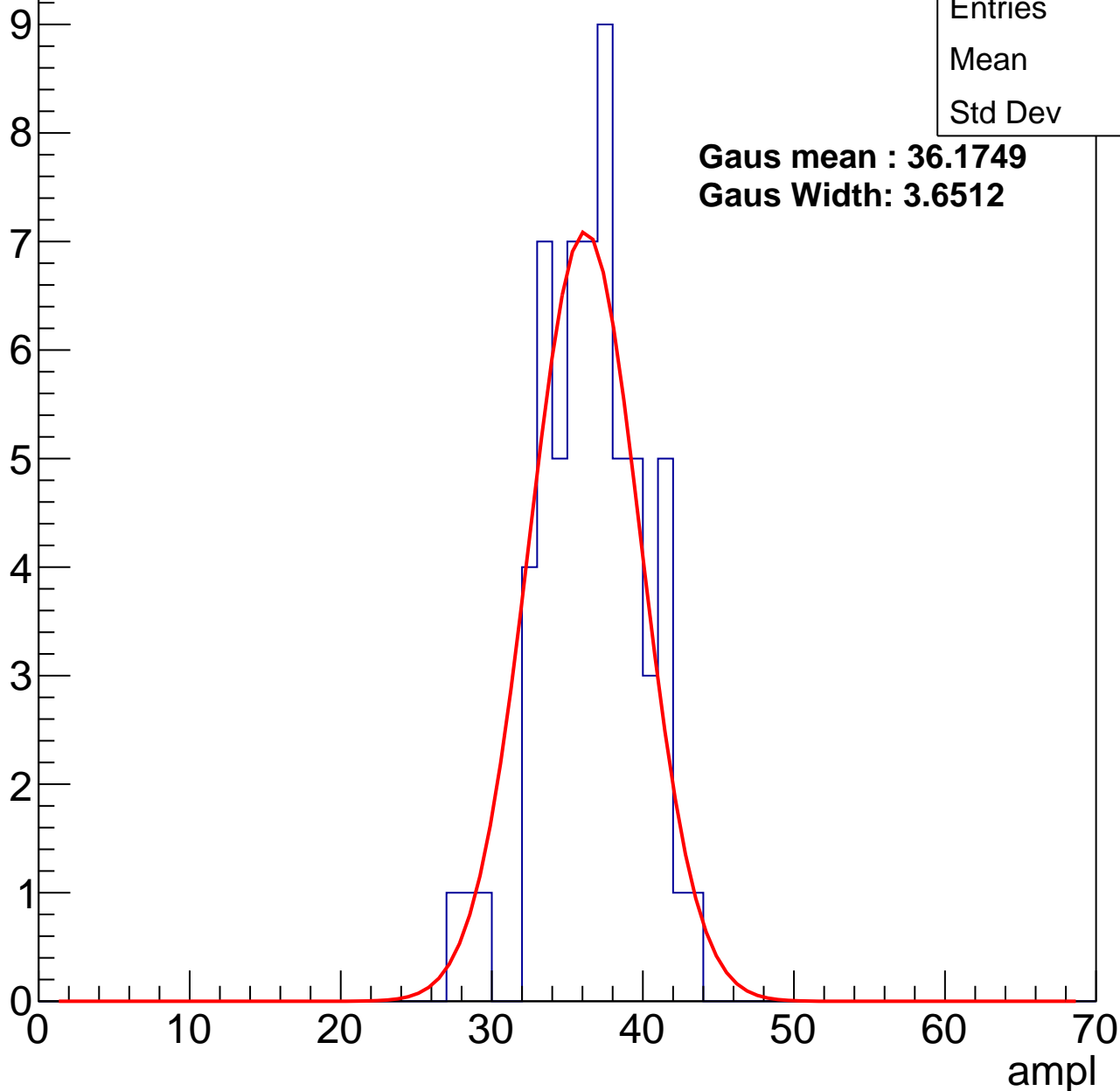
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.1
Std Dev	3.31

Gaus mean : 36.1749

Gaus Width: 3.6512



B1L102S, U19-ch116, adc2

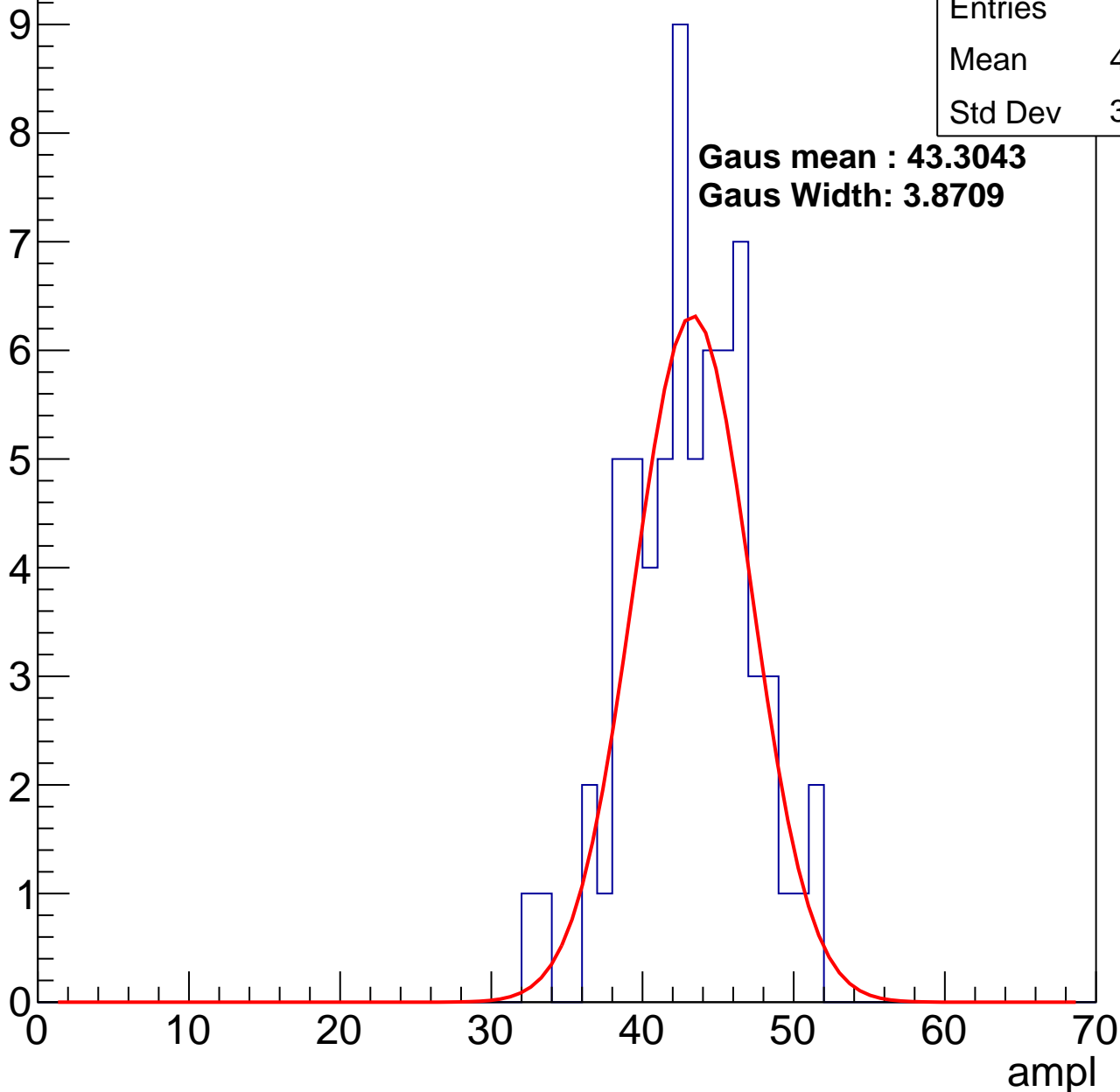
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.67
Std Dev	3.953

Gaus mean : 43.3043

Gaus Width: 3.8709

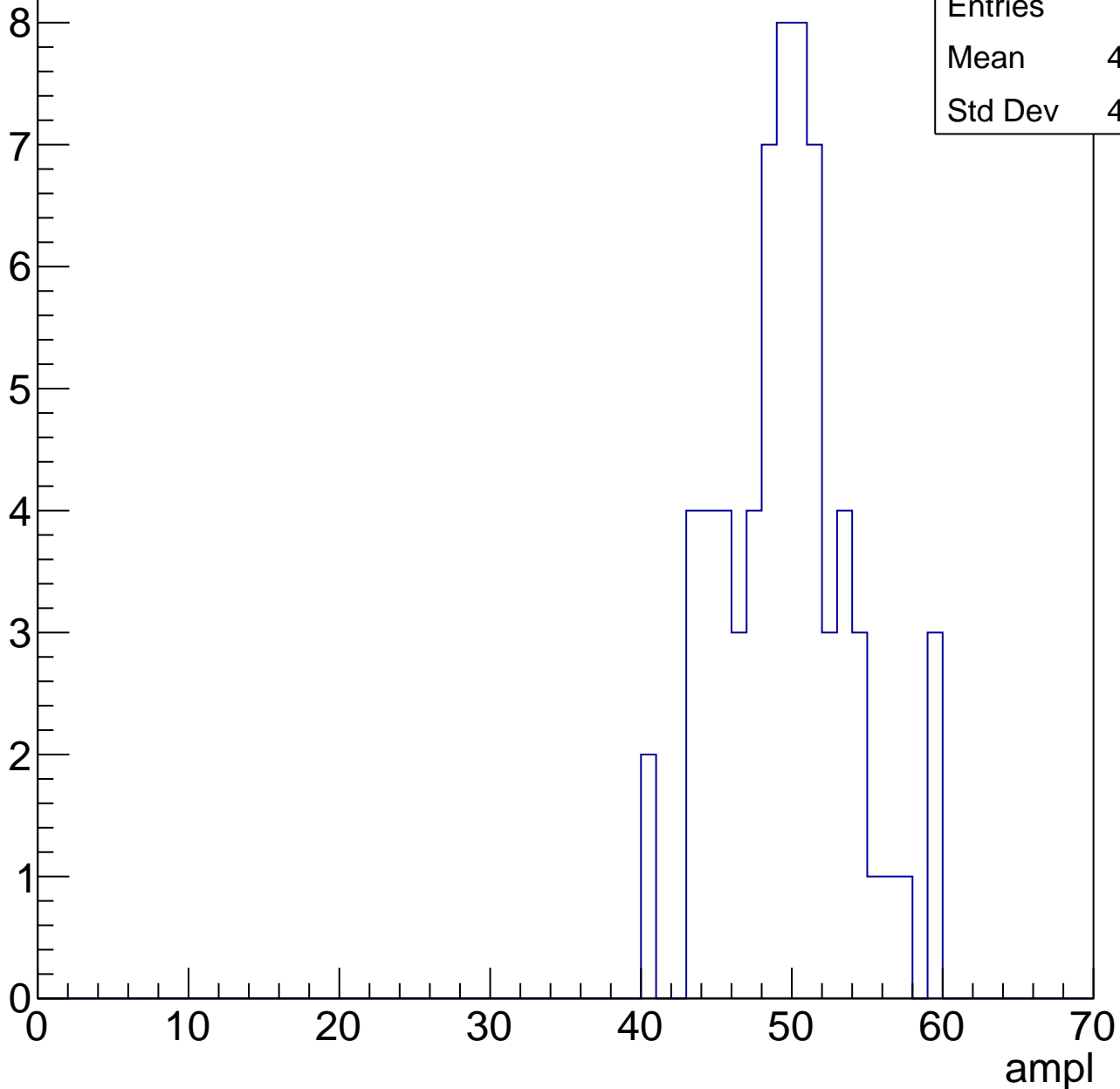


B1L102S, U19-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.16
Std Dev	4.177

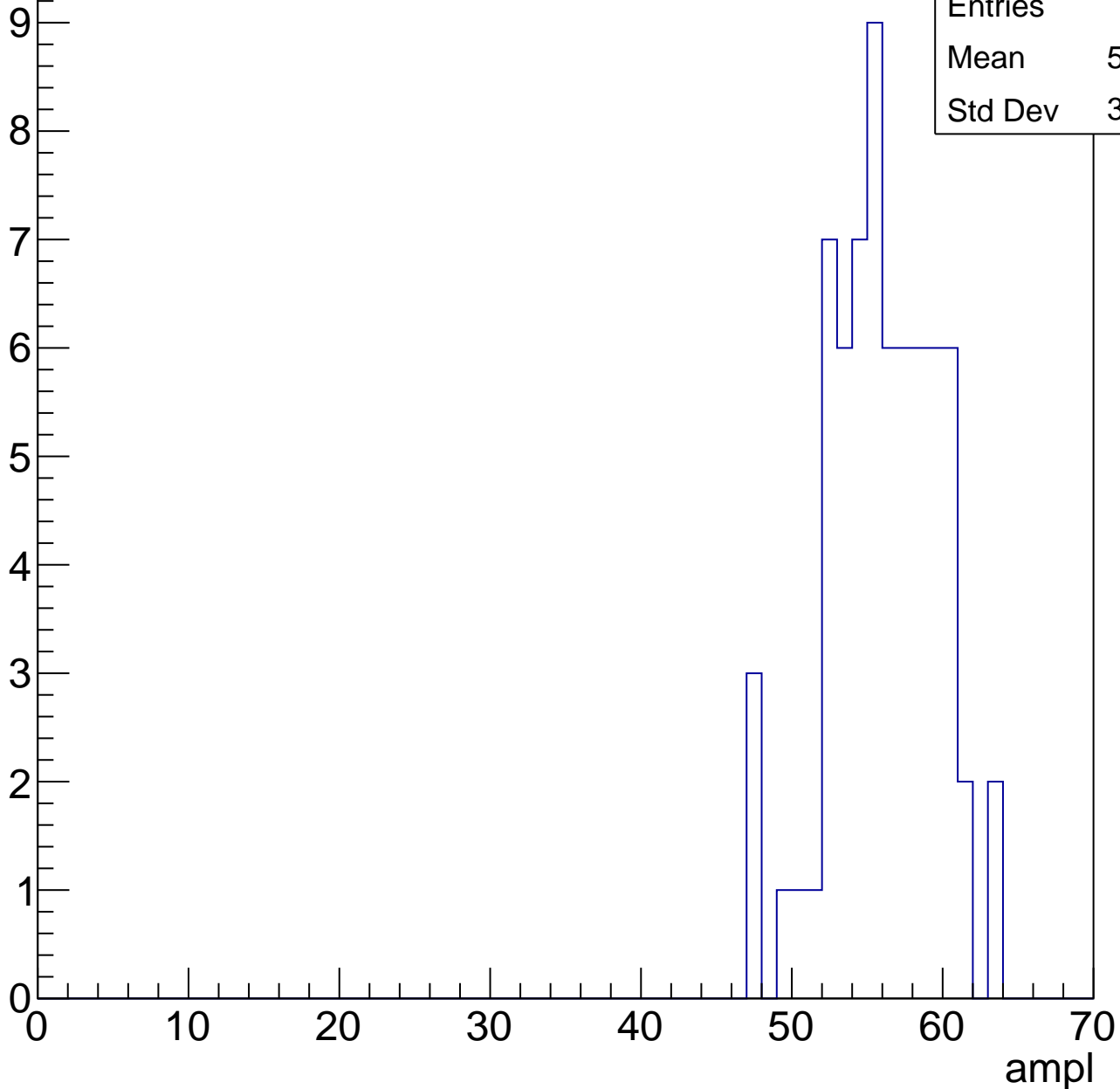


B1L102S, U19-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	55.57
Std Dev	3.553

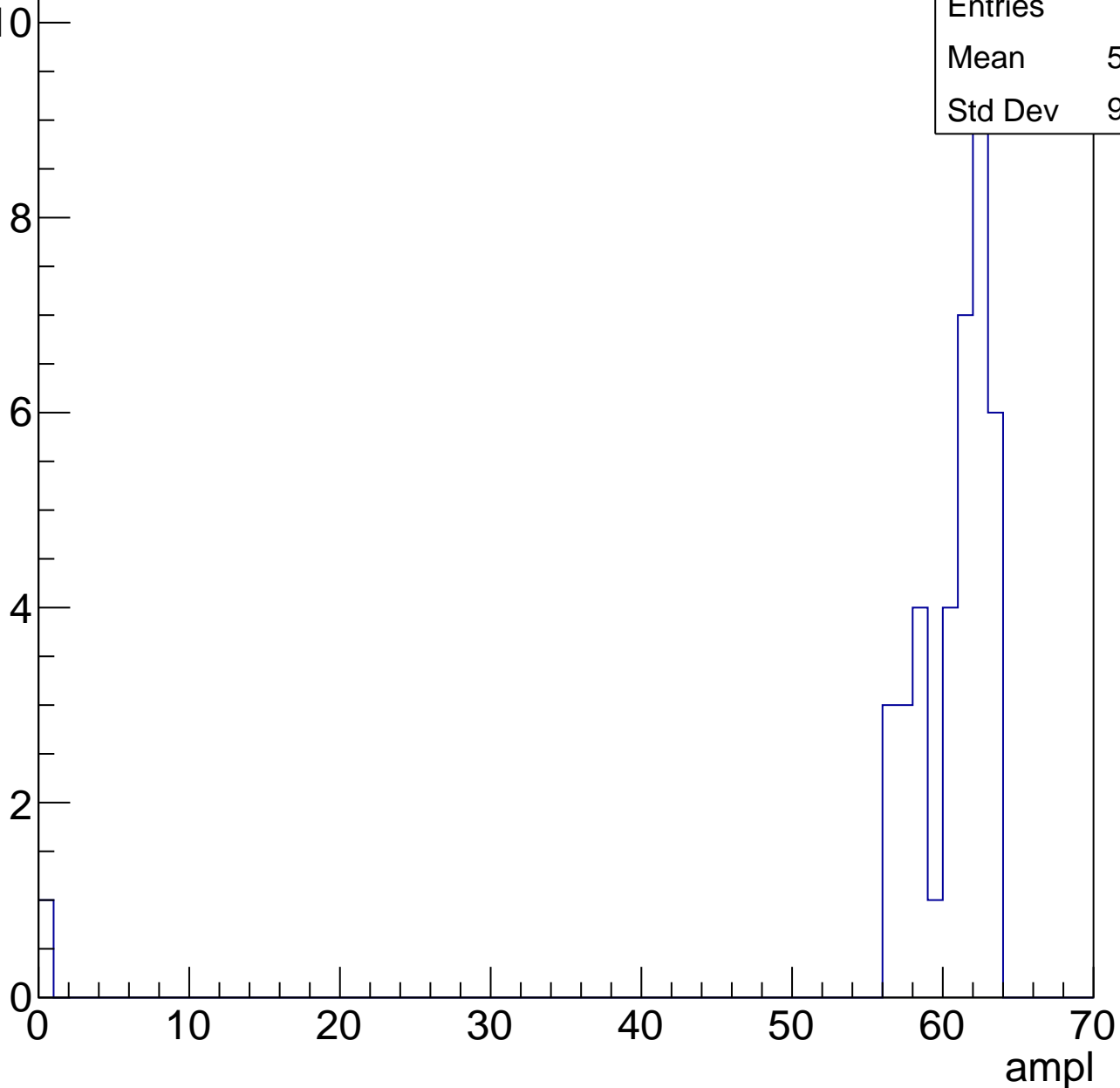


B1L102S, U19-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

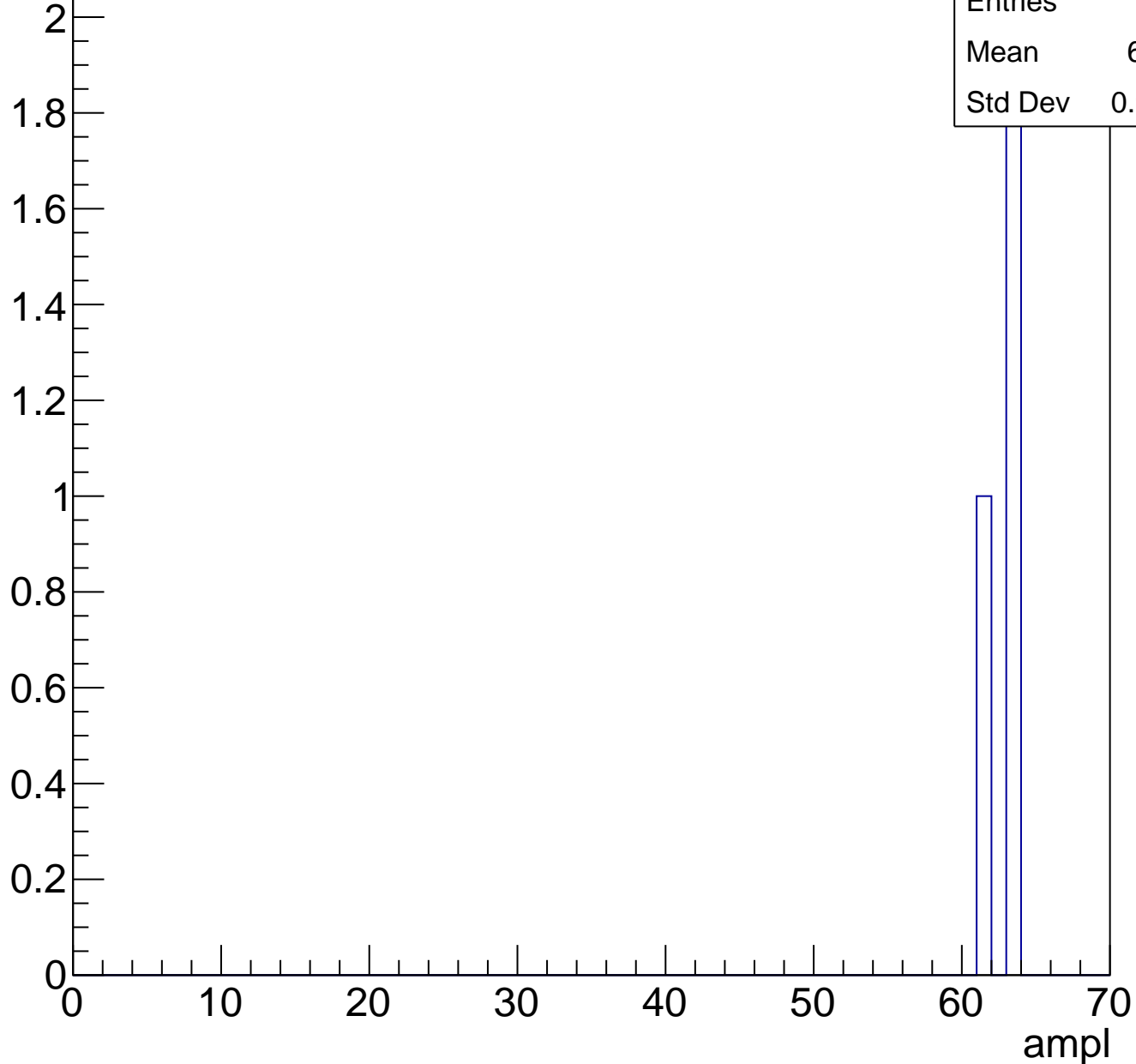
Entries	39
Mean	58.85
Std Dev	9.794



B1L102S, U19-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch117, adc0

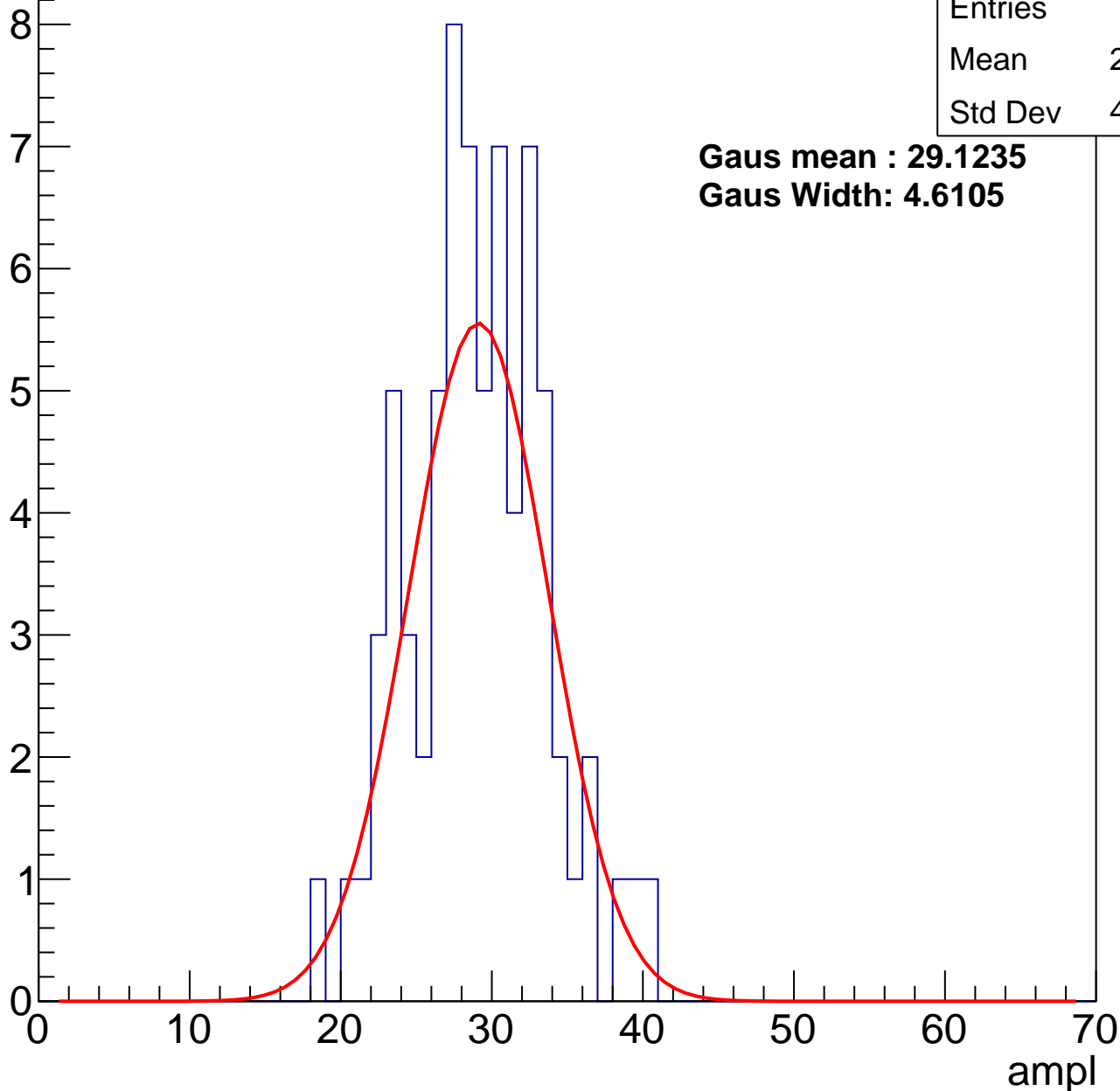
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.67
Std Dev	4.453

Gaus mean : 29.1235

Gaus Width: 4.6105



B1L102S, U19-ch117, adc1

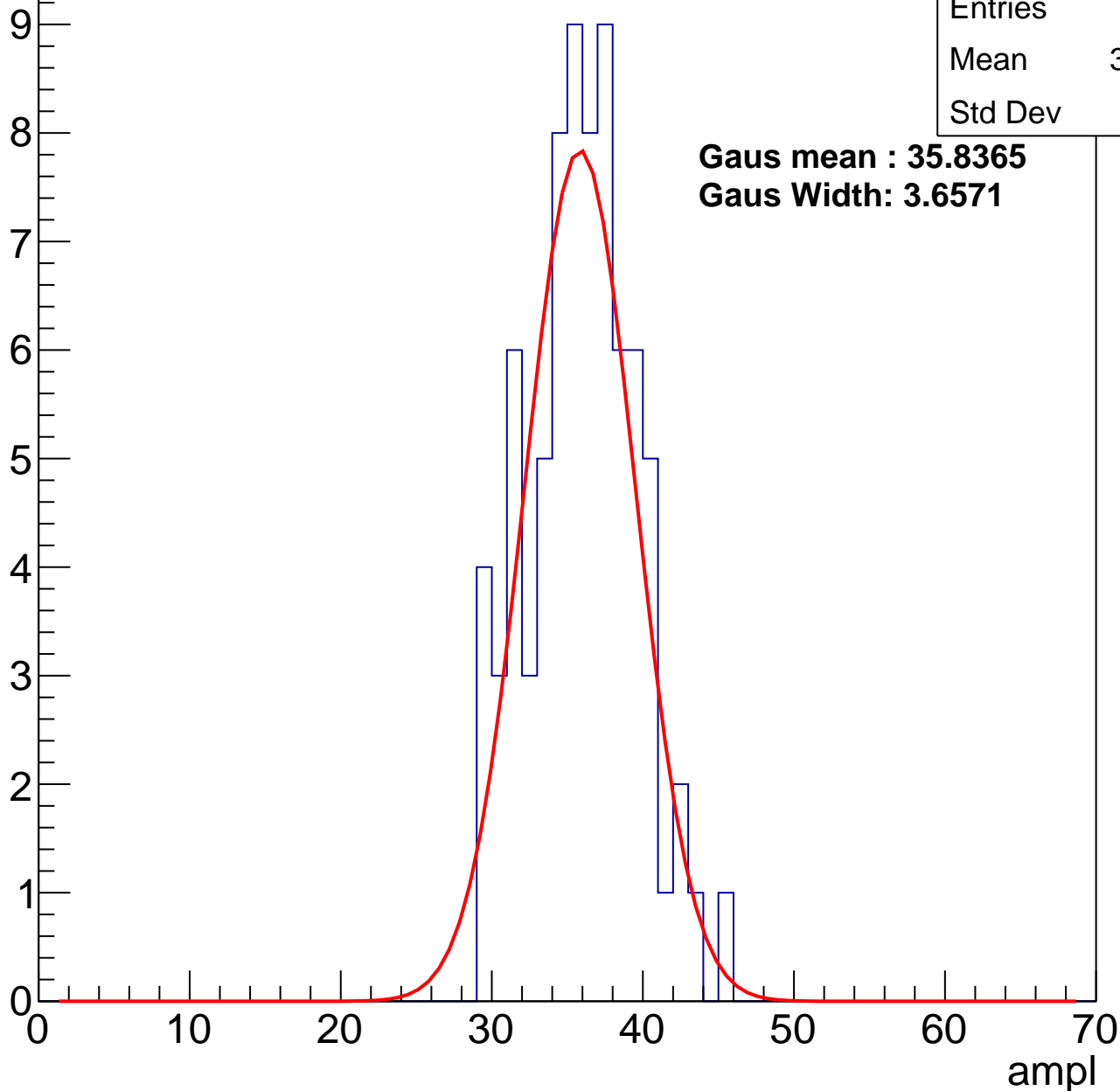
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.53
Std Dev	3.54

Gaus mean : 35.8365

Gaus Width: 3.6571



B1L102S, U19-ch117, adc2

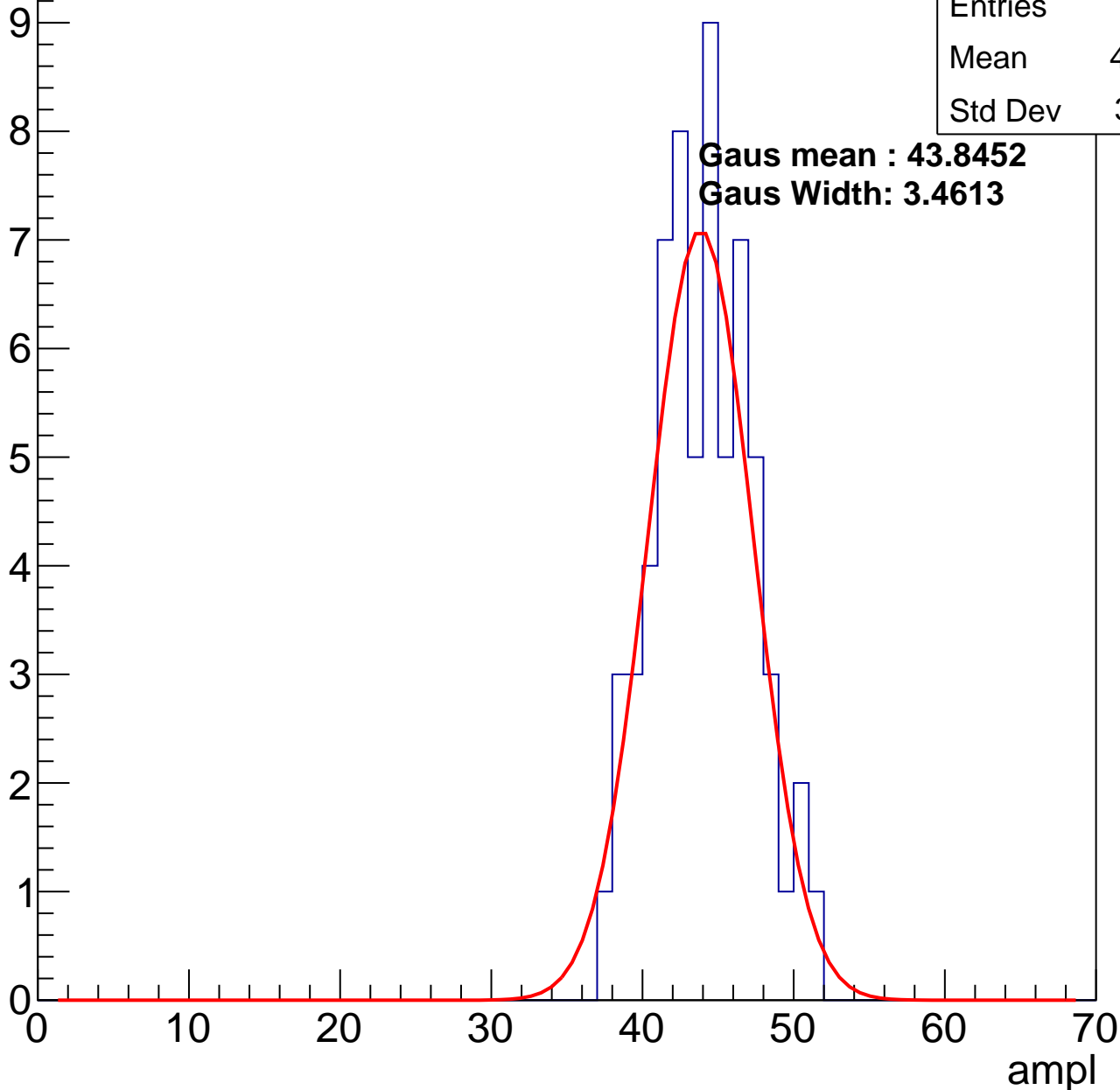
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.56
Std Dev	3.201

Gaus mean : 43.8452

Gaus Width: 3.4613



B1L102S, U19-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

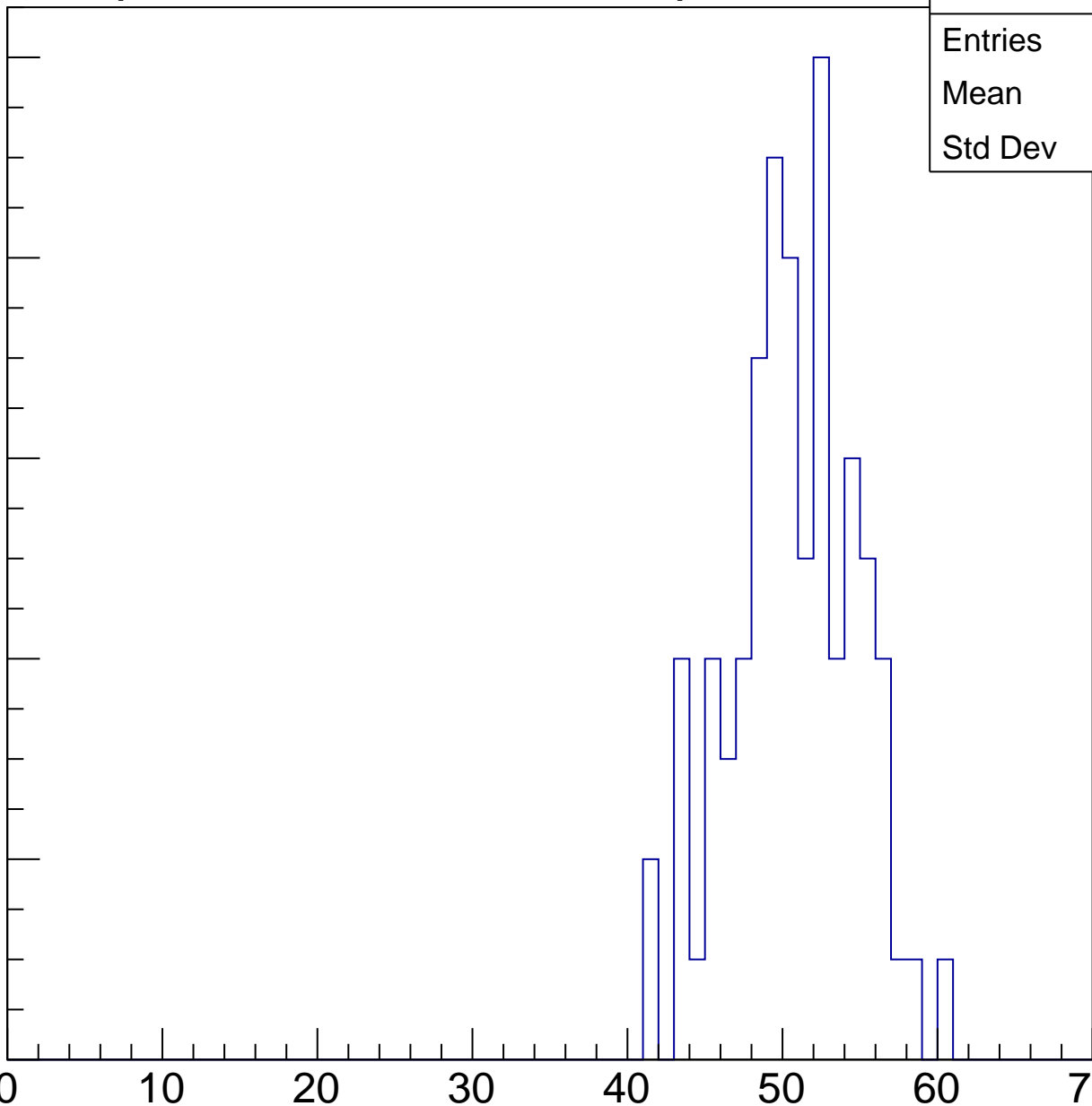
Entries	79
Mean	50.2
Std Dev	4.011

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

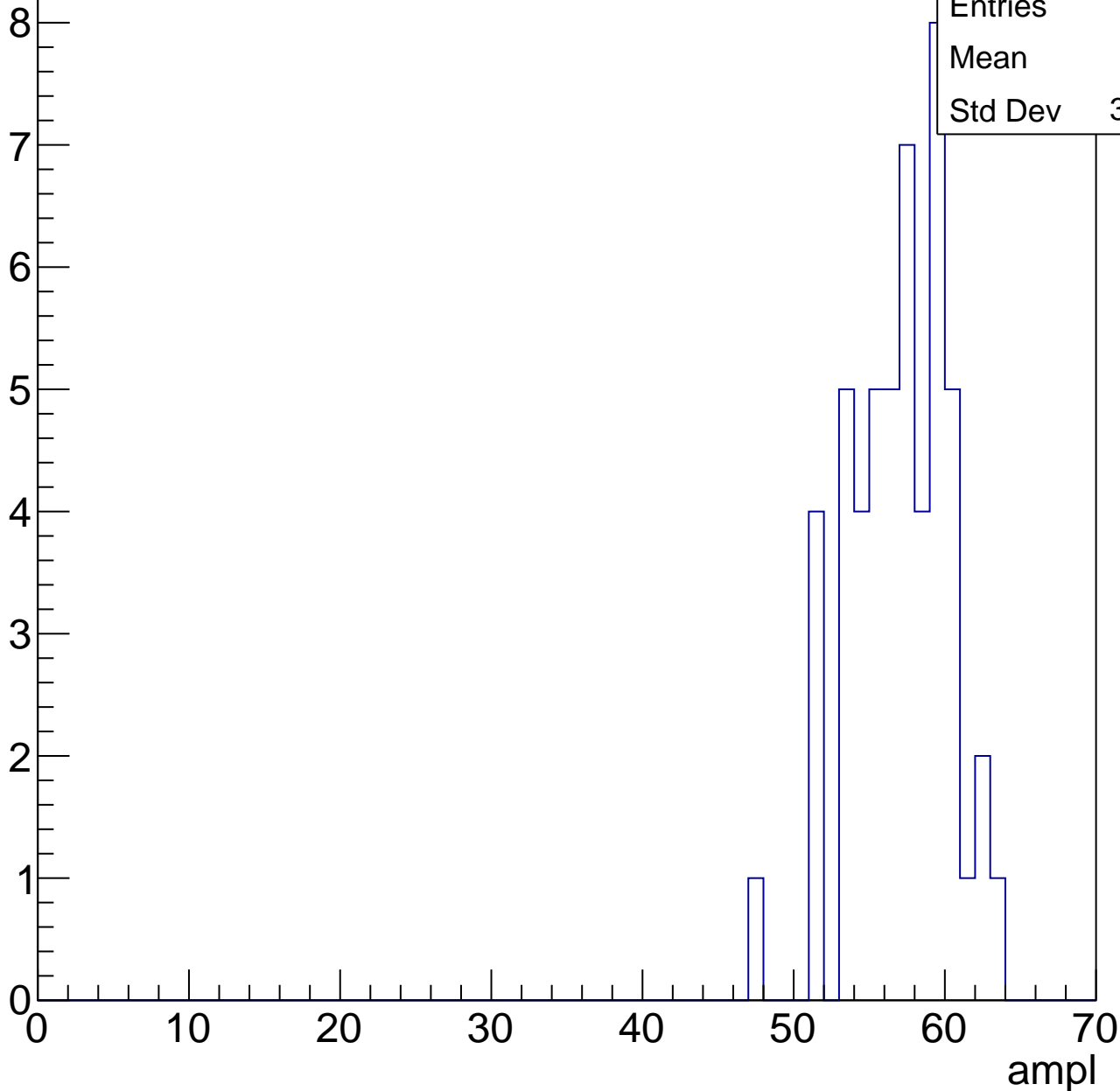


B1L102S, U19-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.5
Std Dev	3.267

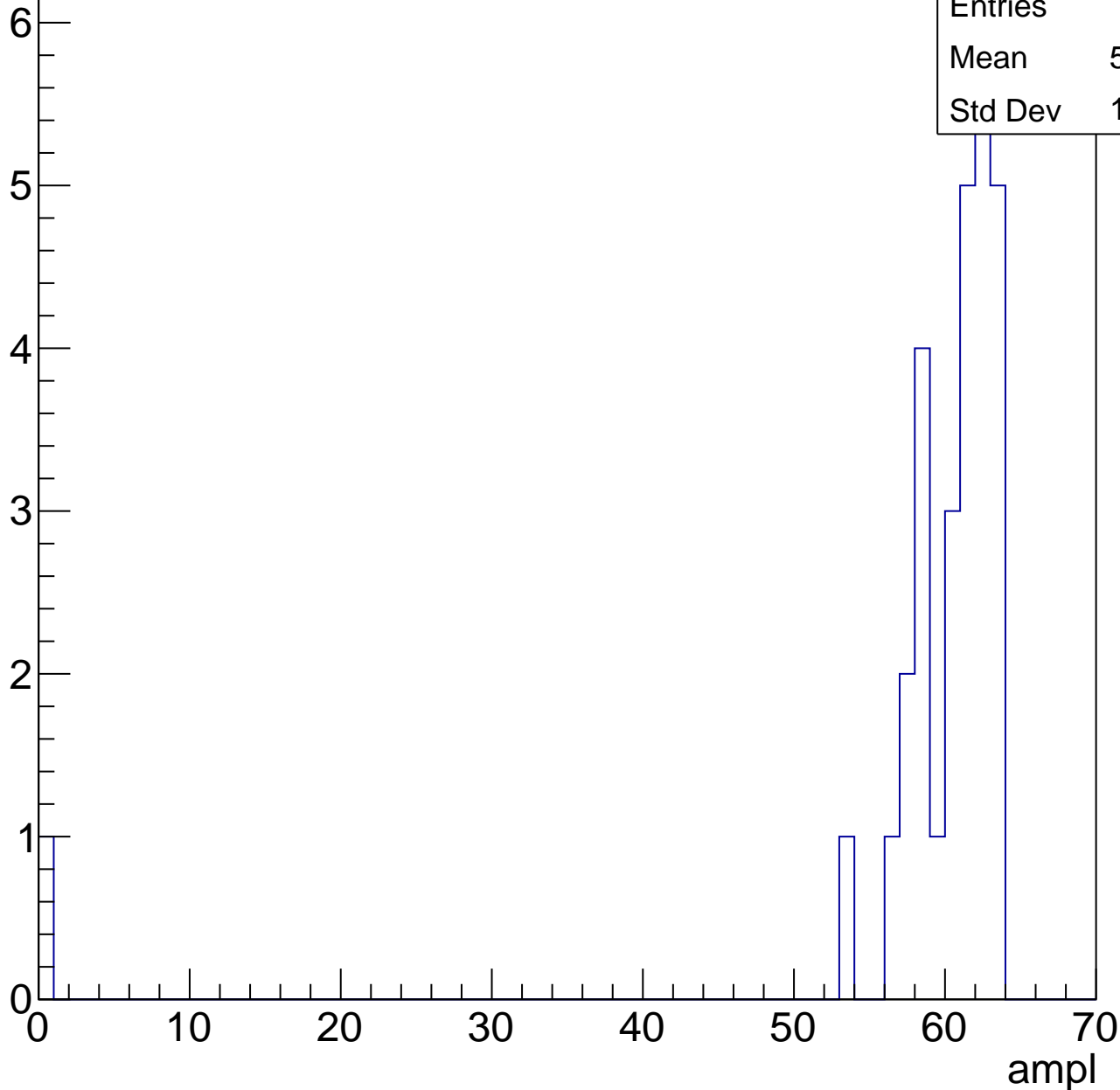


B1L102S, U19-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

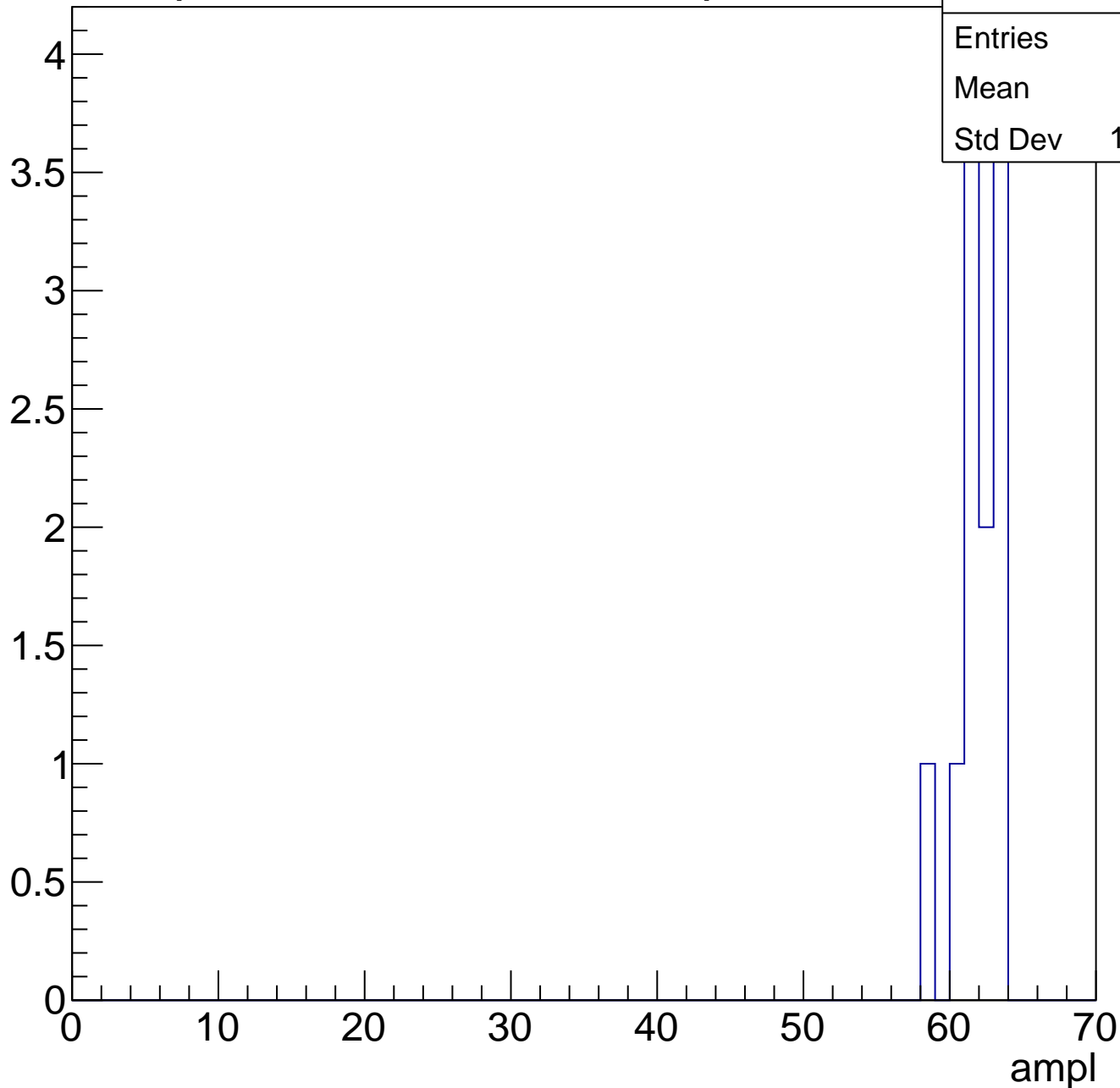
Entries	29
Mean	58.14
Std Dev	11.25



B1L102S, U19-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

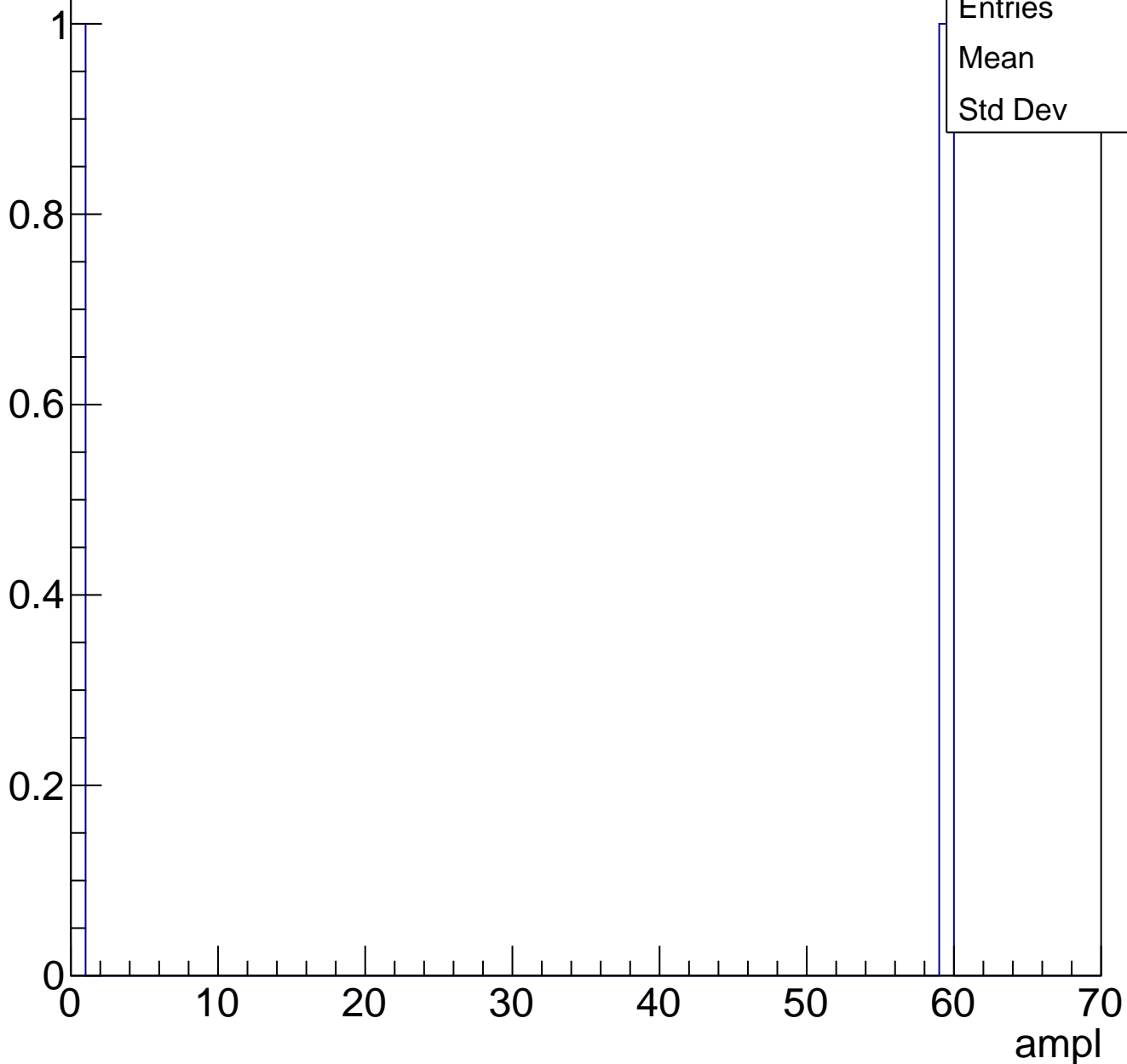
Entry



B1L102S, U19-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch118, adc0

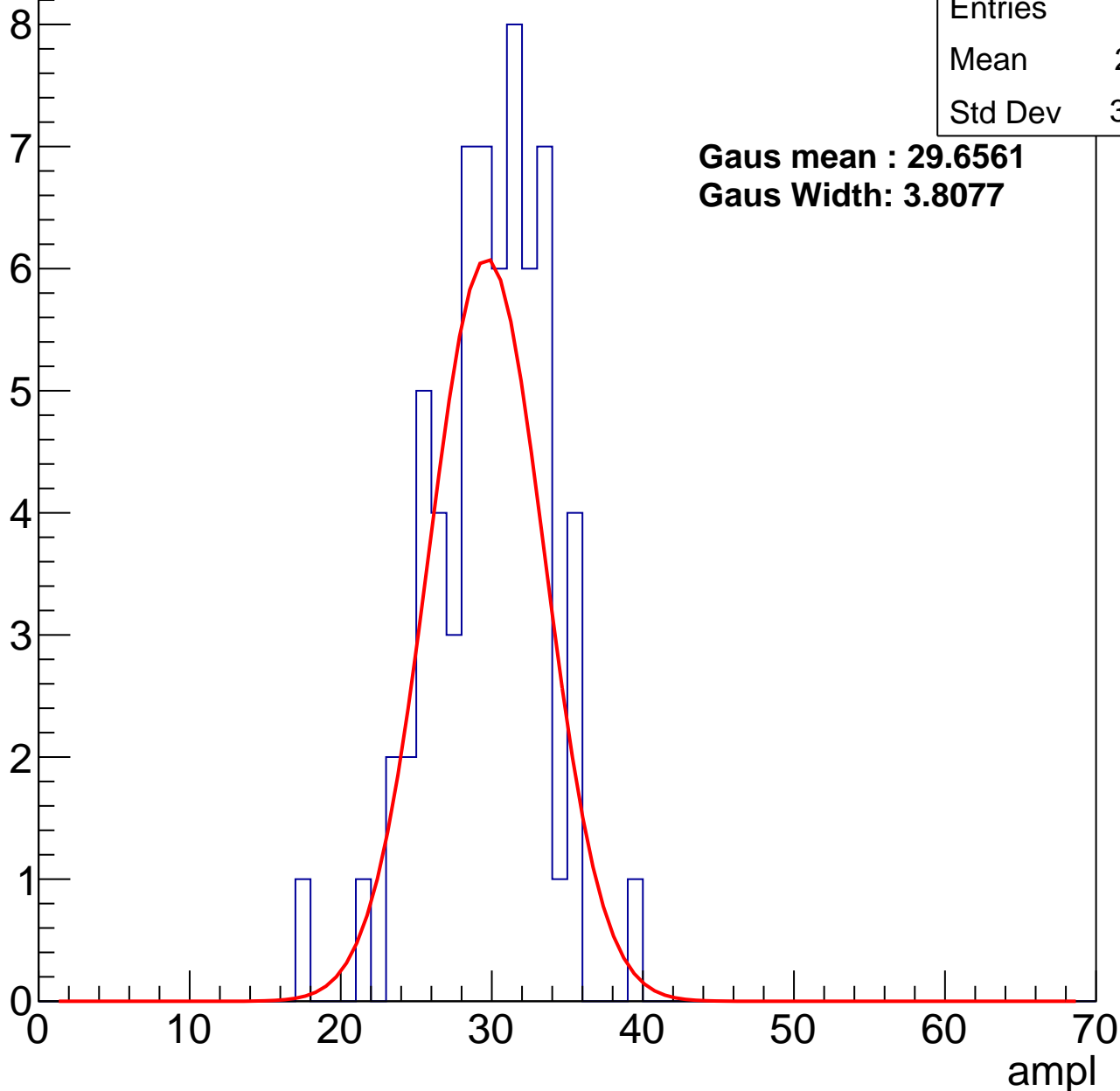
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.31
Std Dev	3.786

Gaus mean : 29.6561

Gaus Width: 3.8077



B1L102S, U19-ch118, adc1

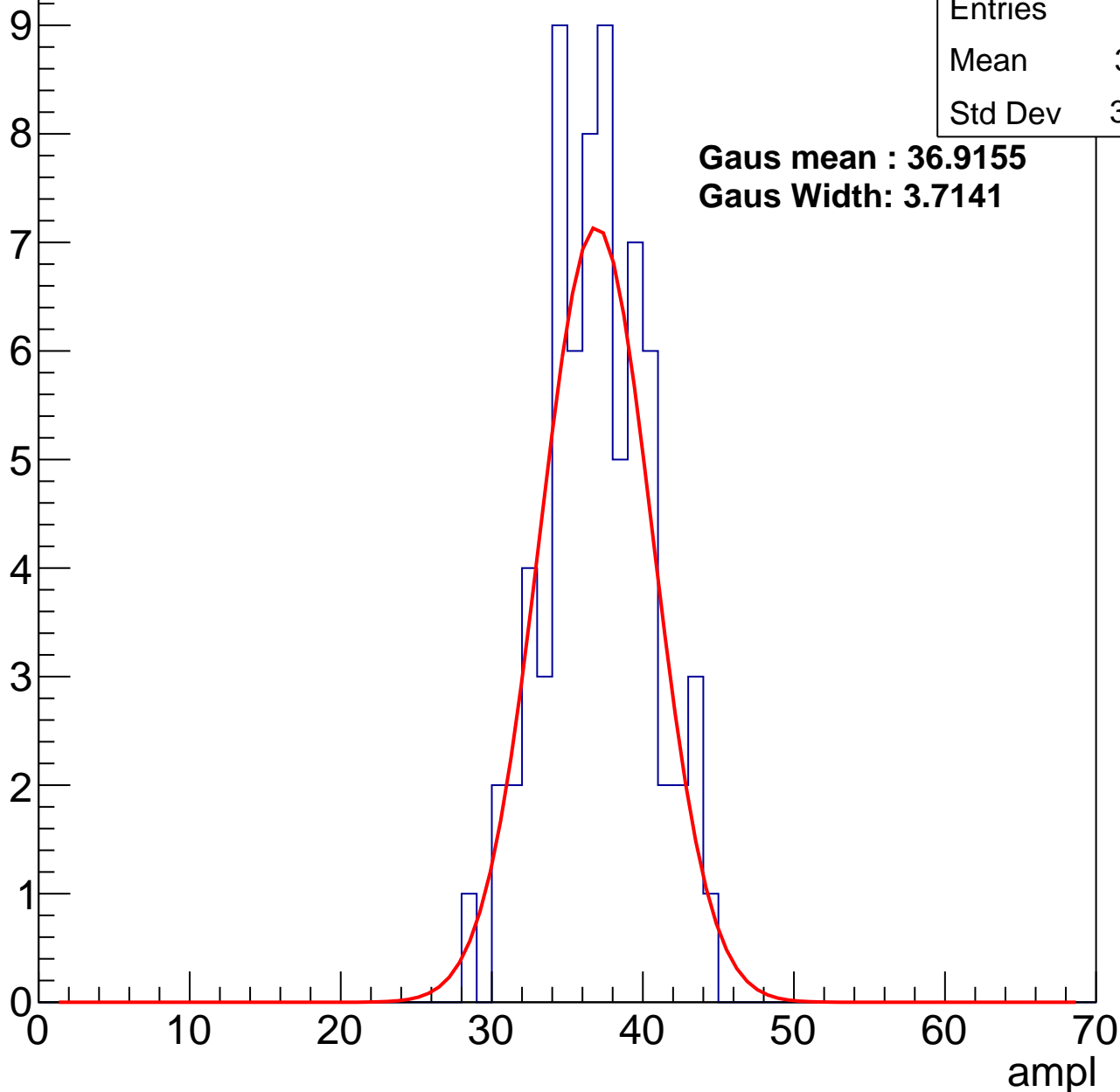
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.51
Std Dev	3.426

Gaus mean : 36.9155

Gaus Width: 3.7141



B1L102S, U19-ch118, adc2

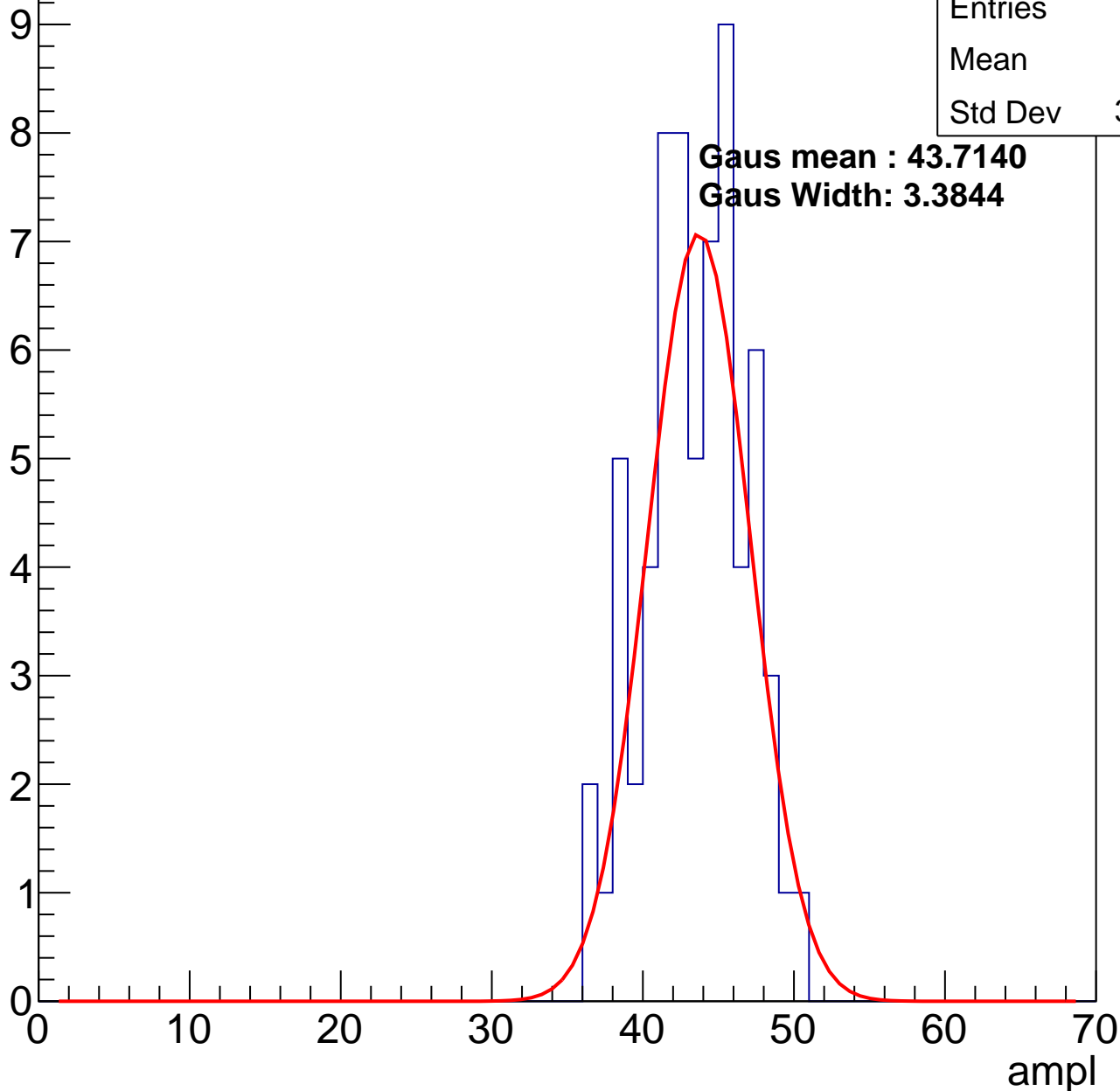
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43
Std Dev	3.261

Gaus mean : 43.7140

Gaus Width: 3.3844

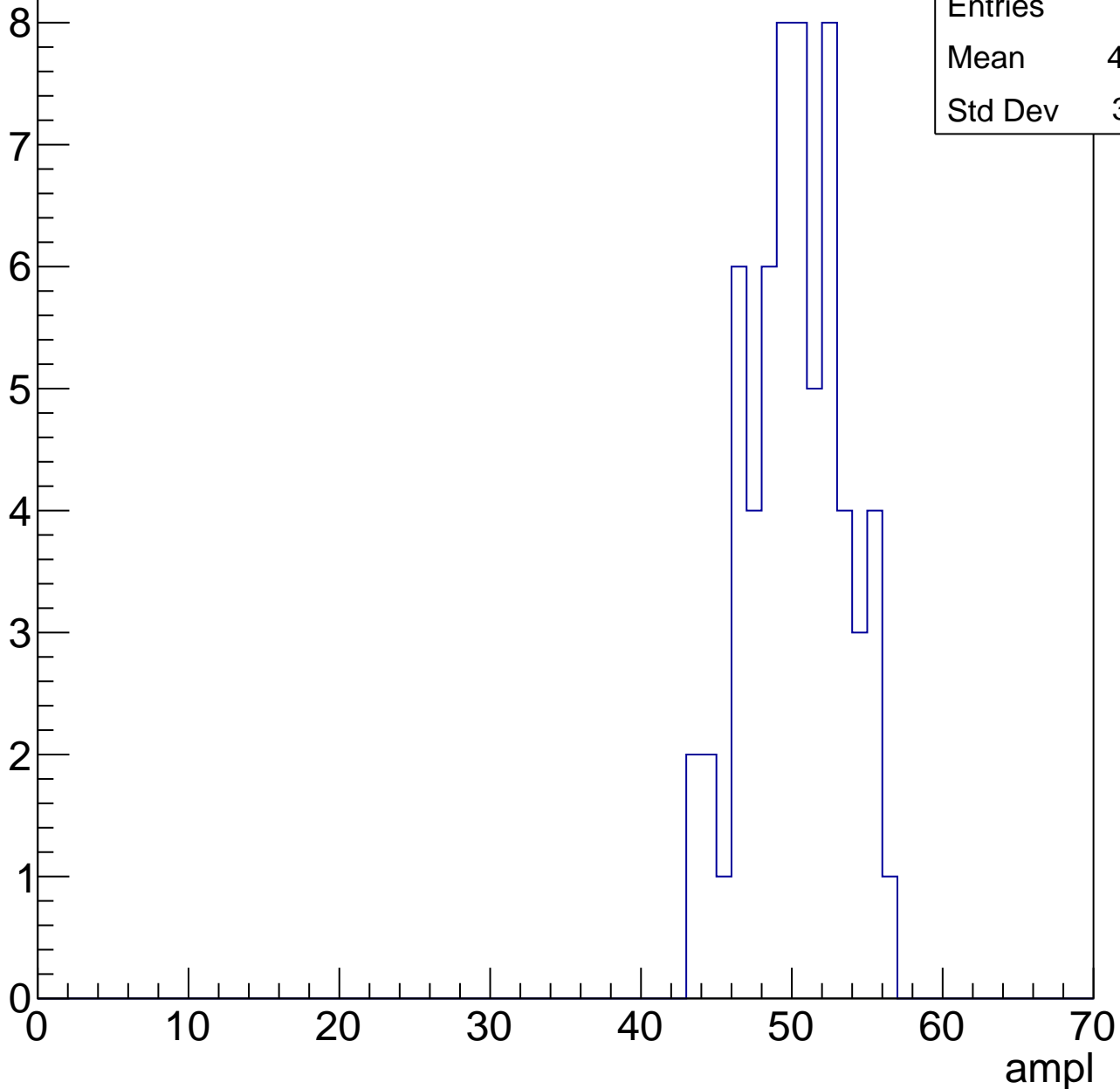


B1L102S, U19-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.74
Std Dev	3.141

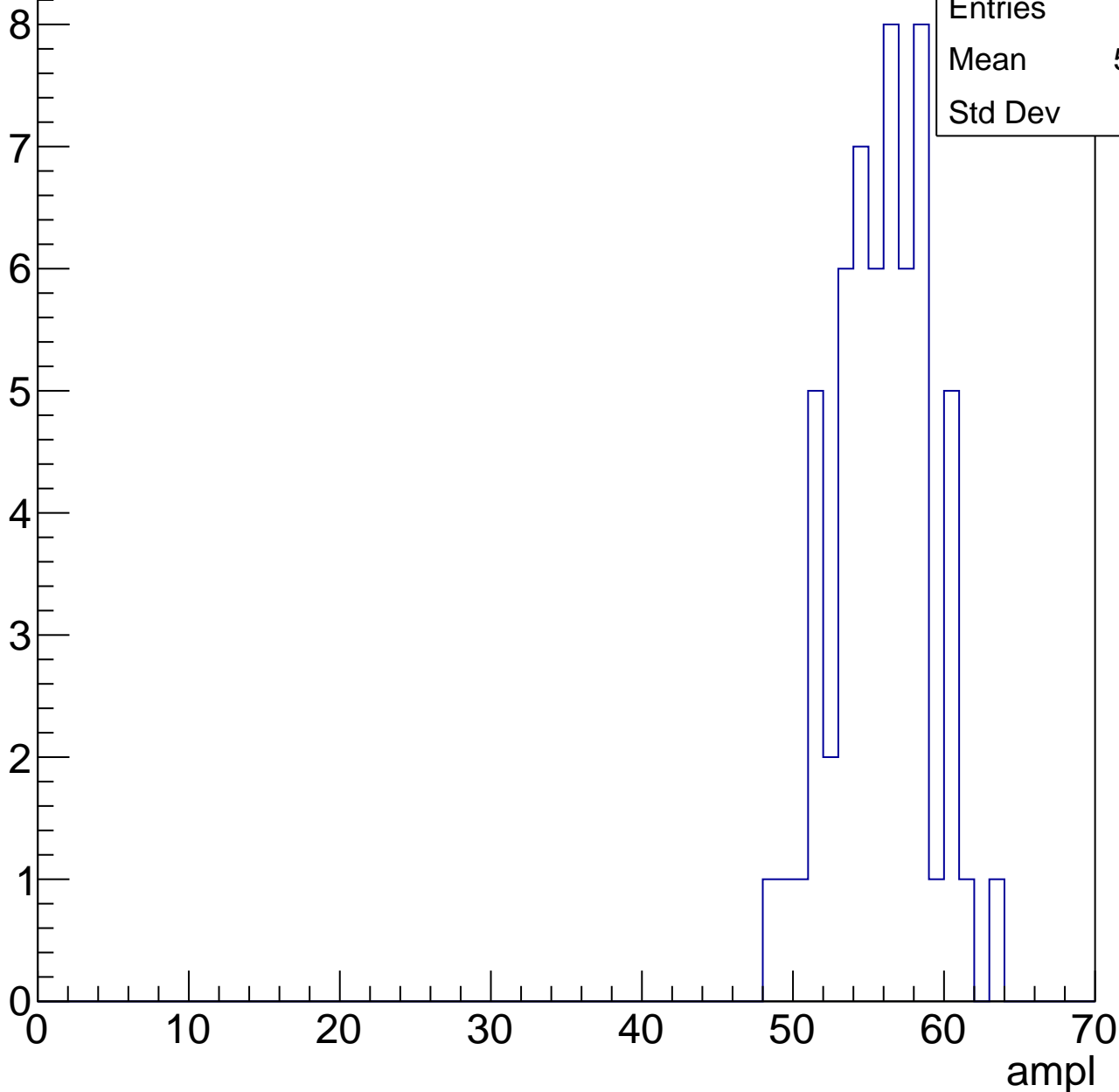


B1L102S, U19-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

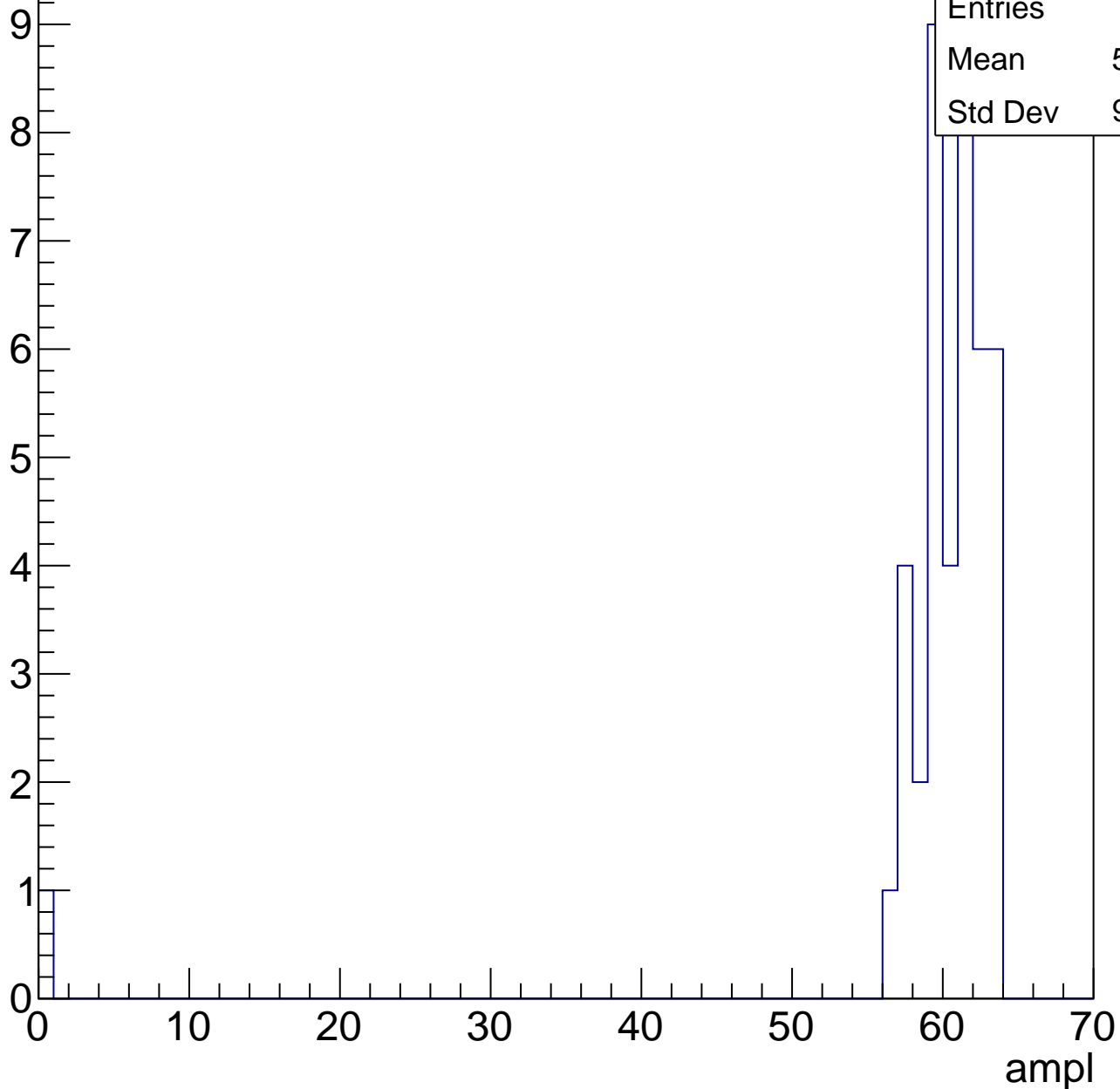
Entries	59
Mean	55.41
Std Dev	3.12



B1L102S, U19-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

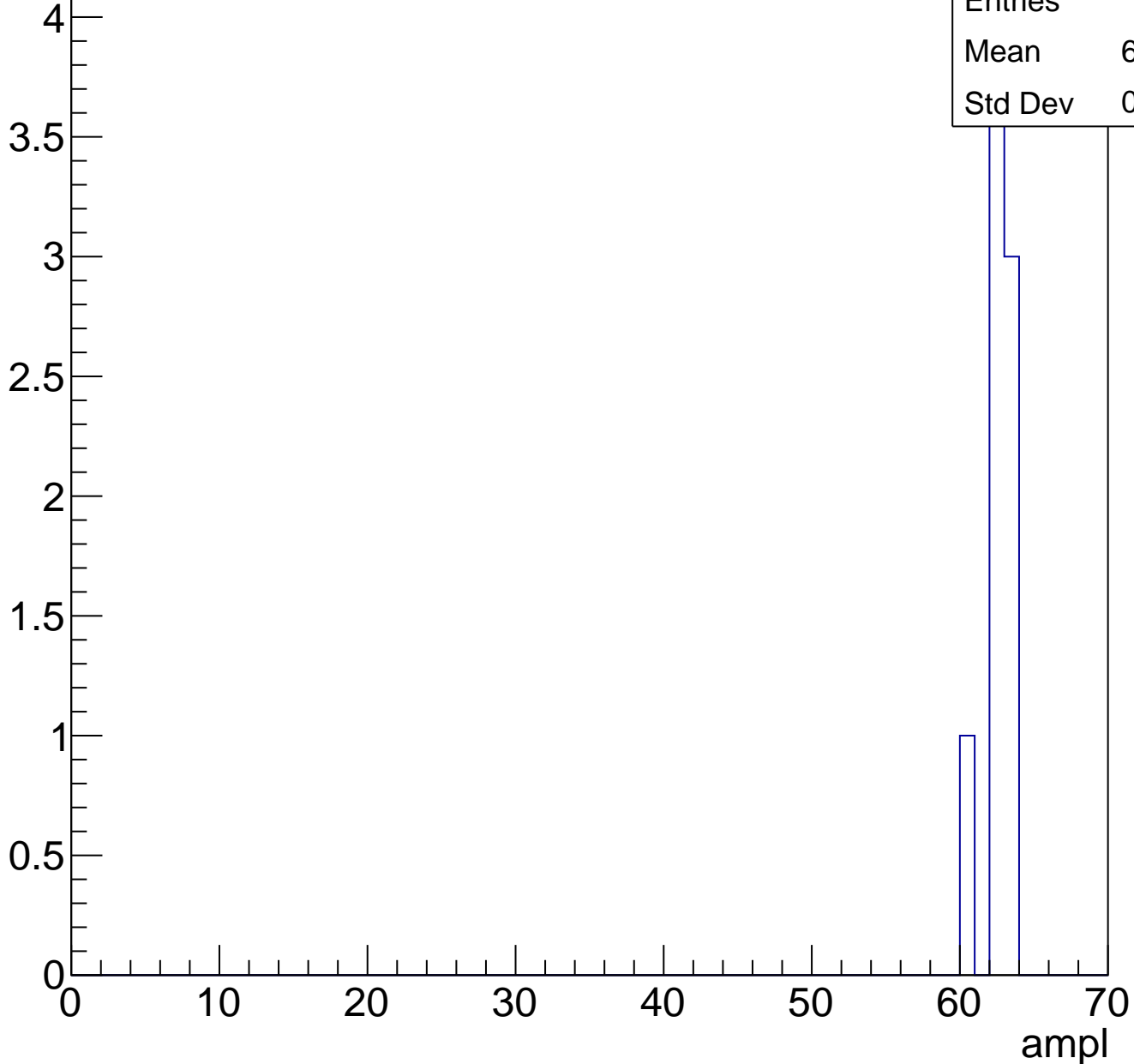
Entry



B1L102S, U19-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U19-ch119, adc0

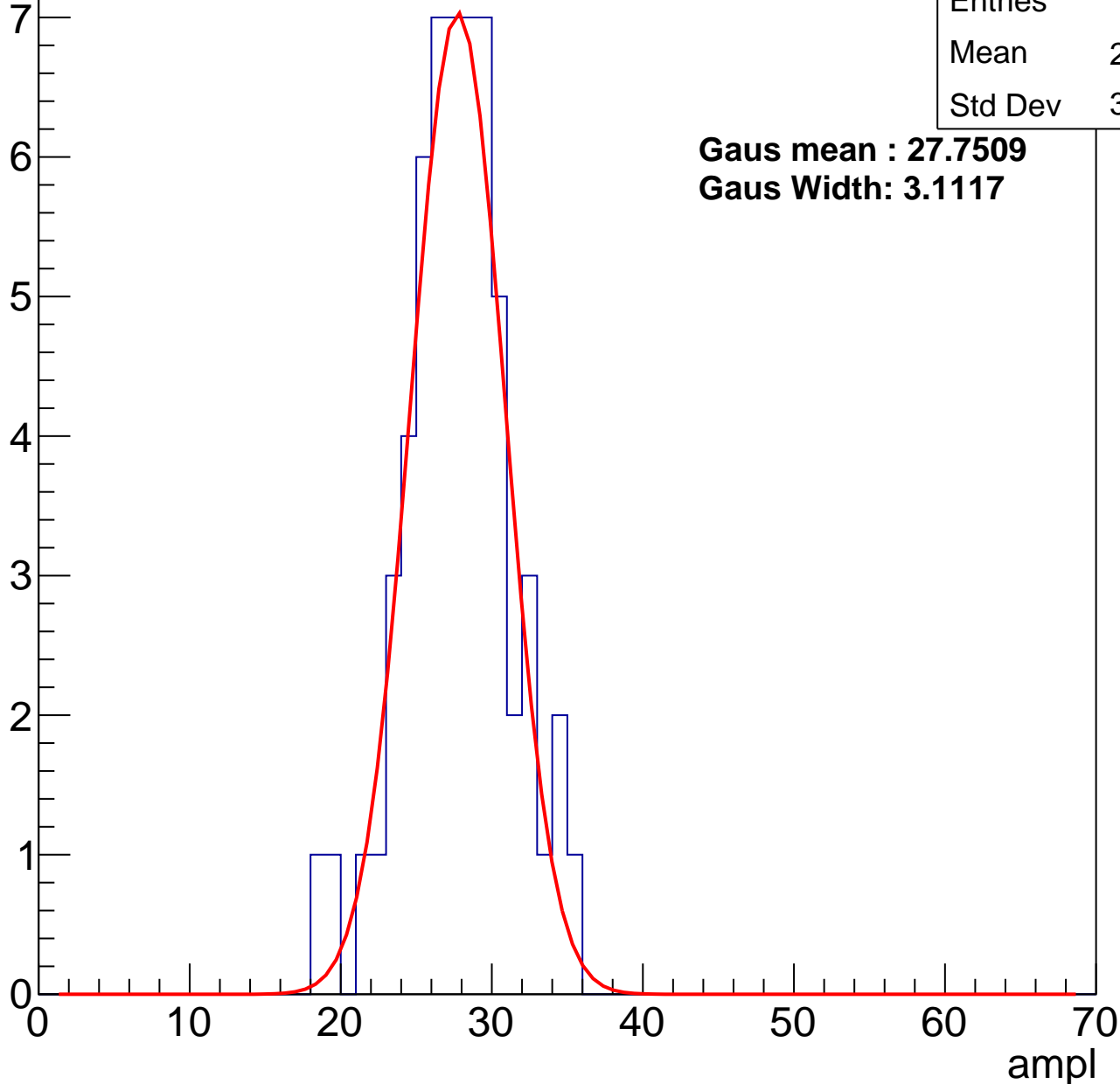
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.27
Std Dev	3.449

Gaus mean : 27.7509

Gaus Width: 3.1117



B1L102S, U19-ch119, adc1

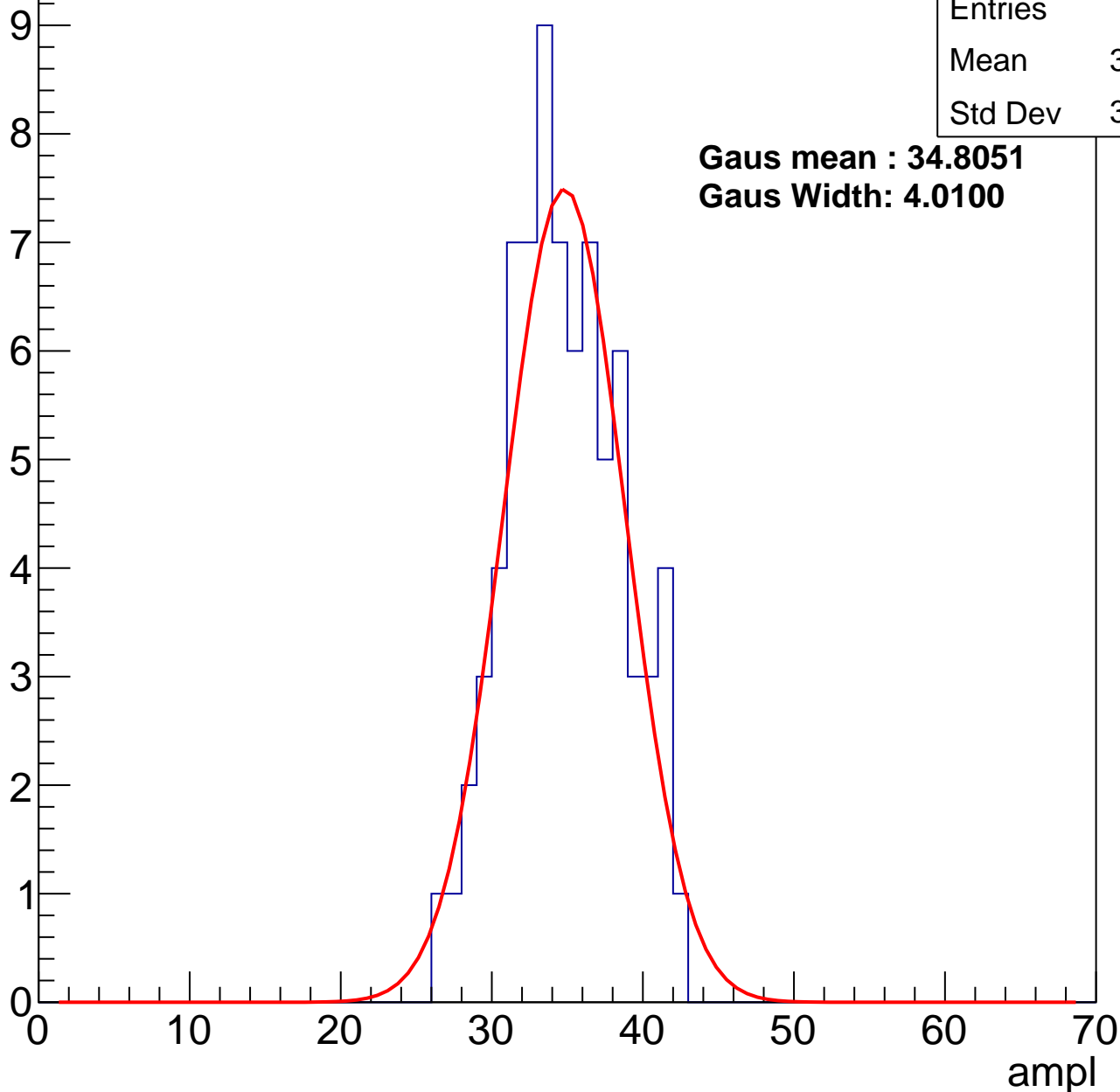
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.34
Std Dev	3.687

Gaus mean : 34.8051

Gaus Width: 4.0100



B1L102S, U19-ch119, adc2

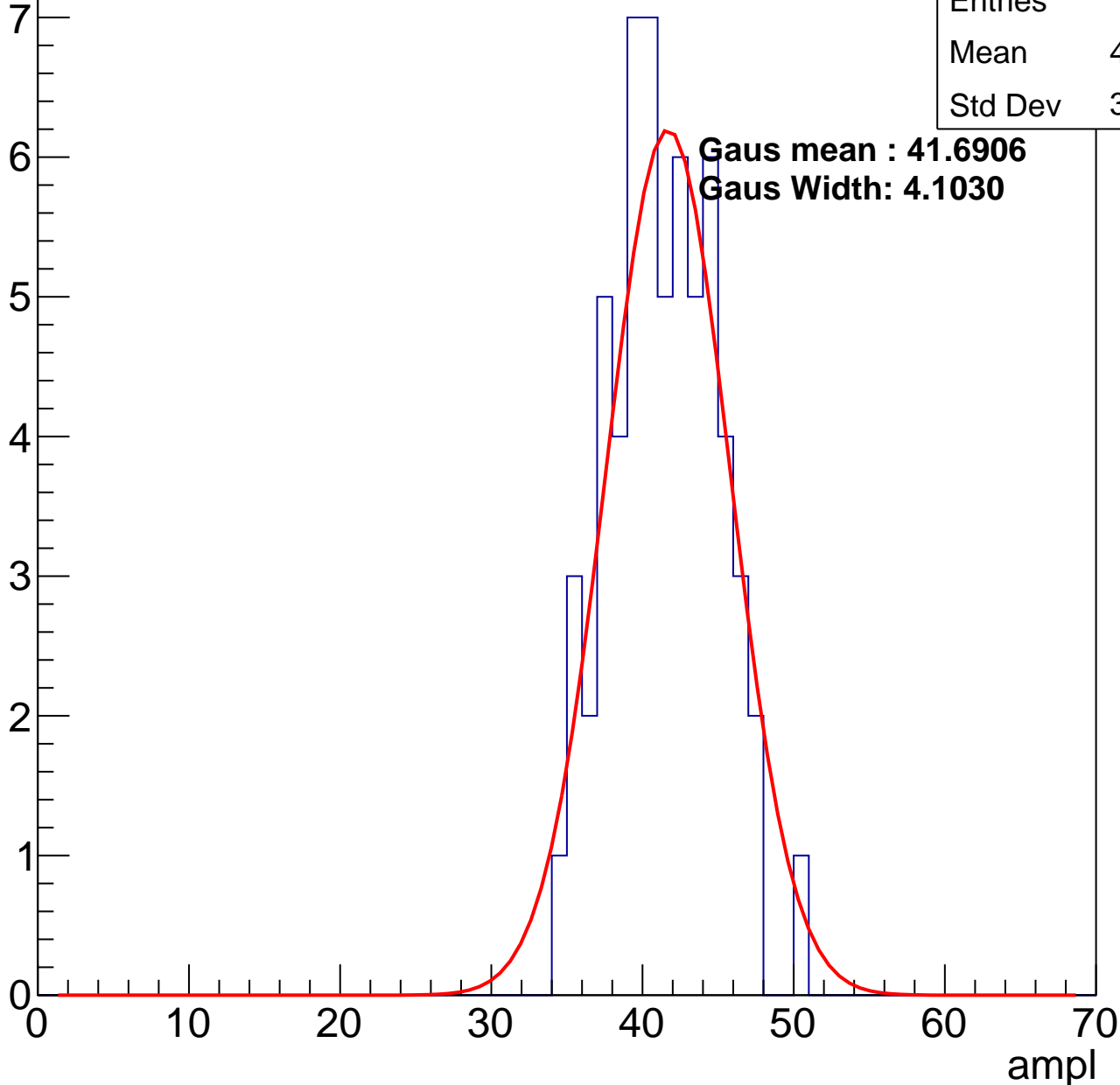
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	40.97
Std Dev	3.459

Gaus mean : 41.6906

Gaus Width: 4.1030

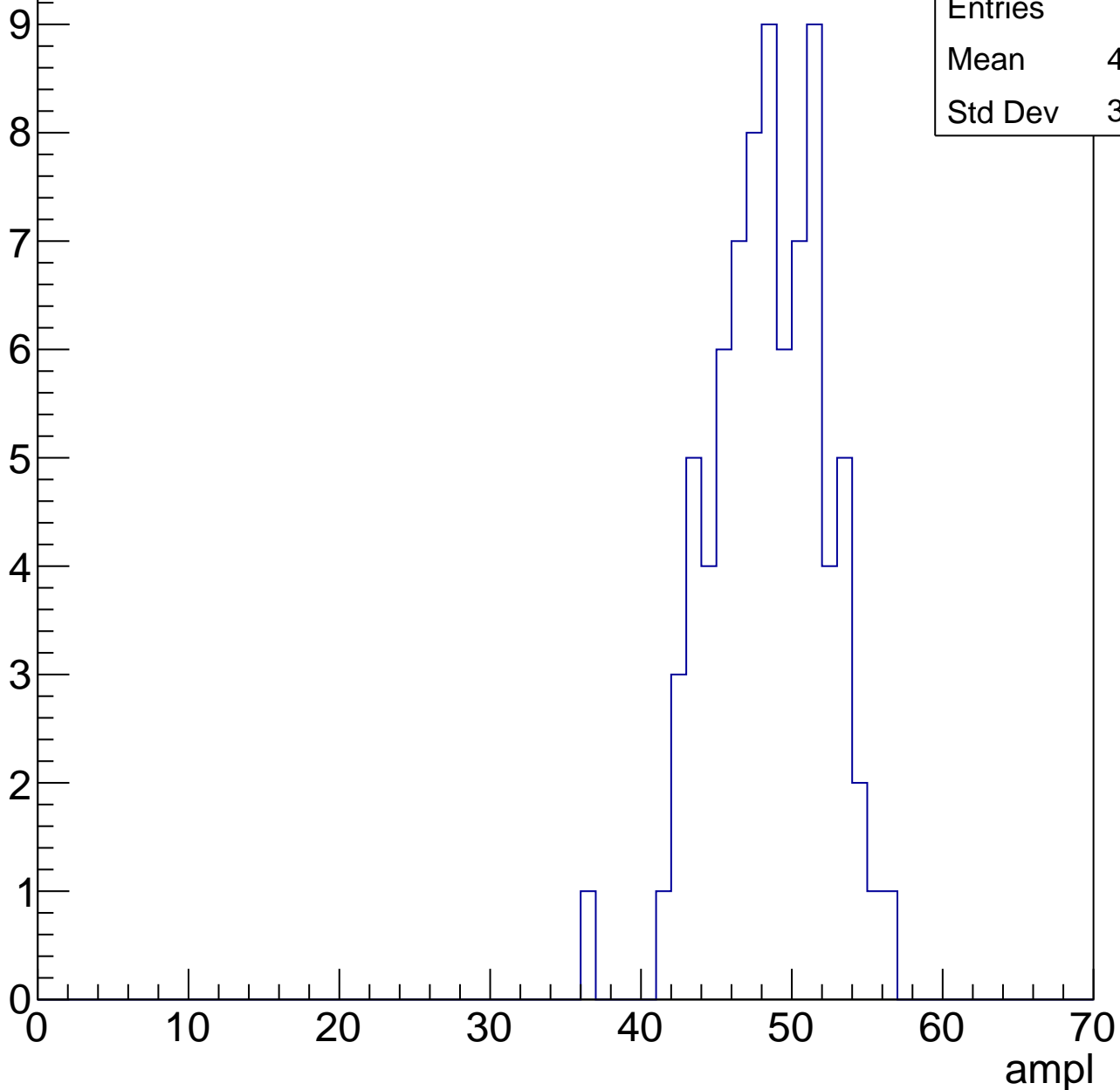


B1L102S, U19-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	47.96
Std Dev	3.678

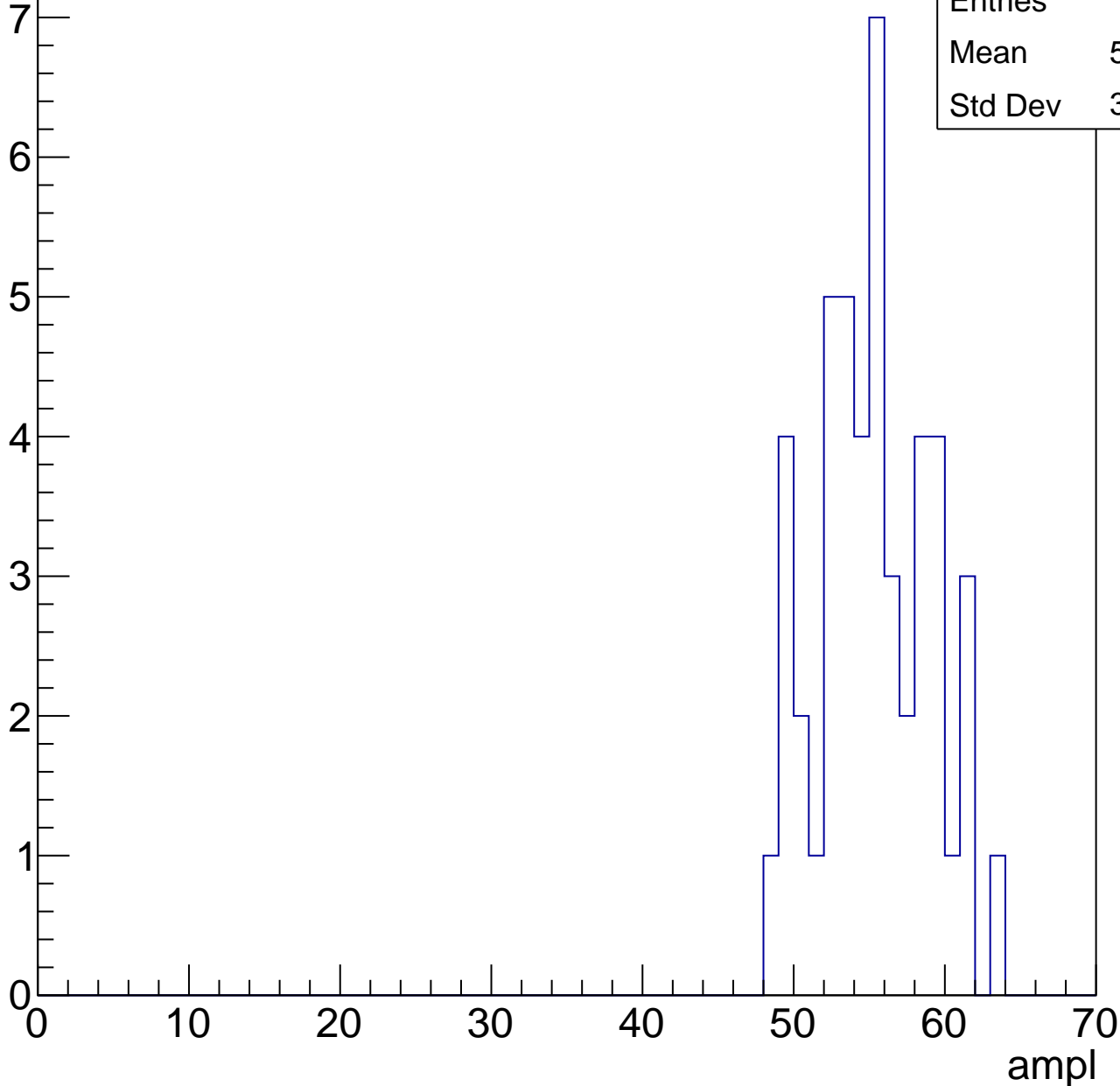


B1L102S, U19-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.83
Std Dev	3.675

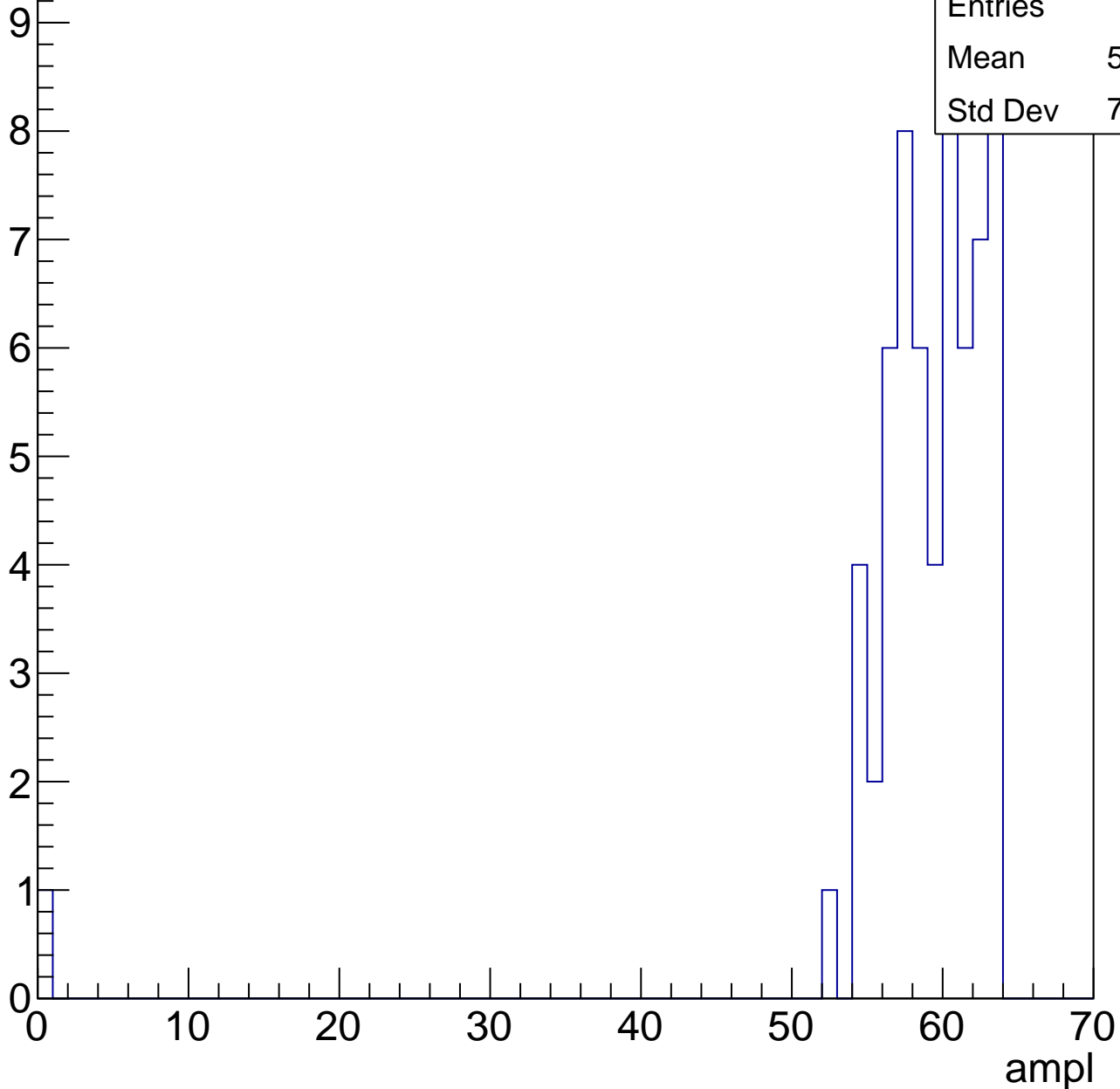


B1L102S, U19-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

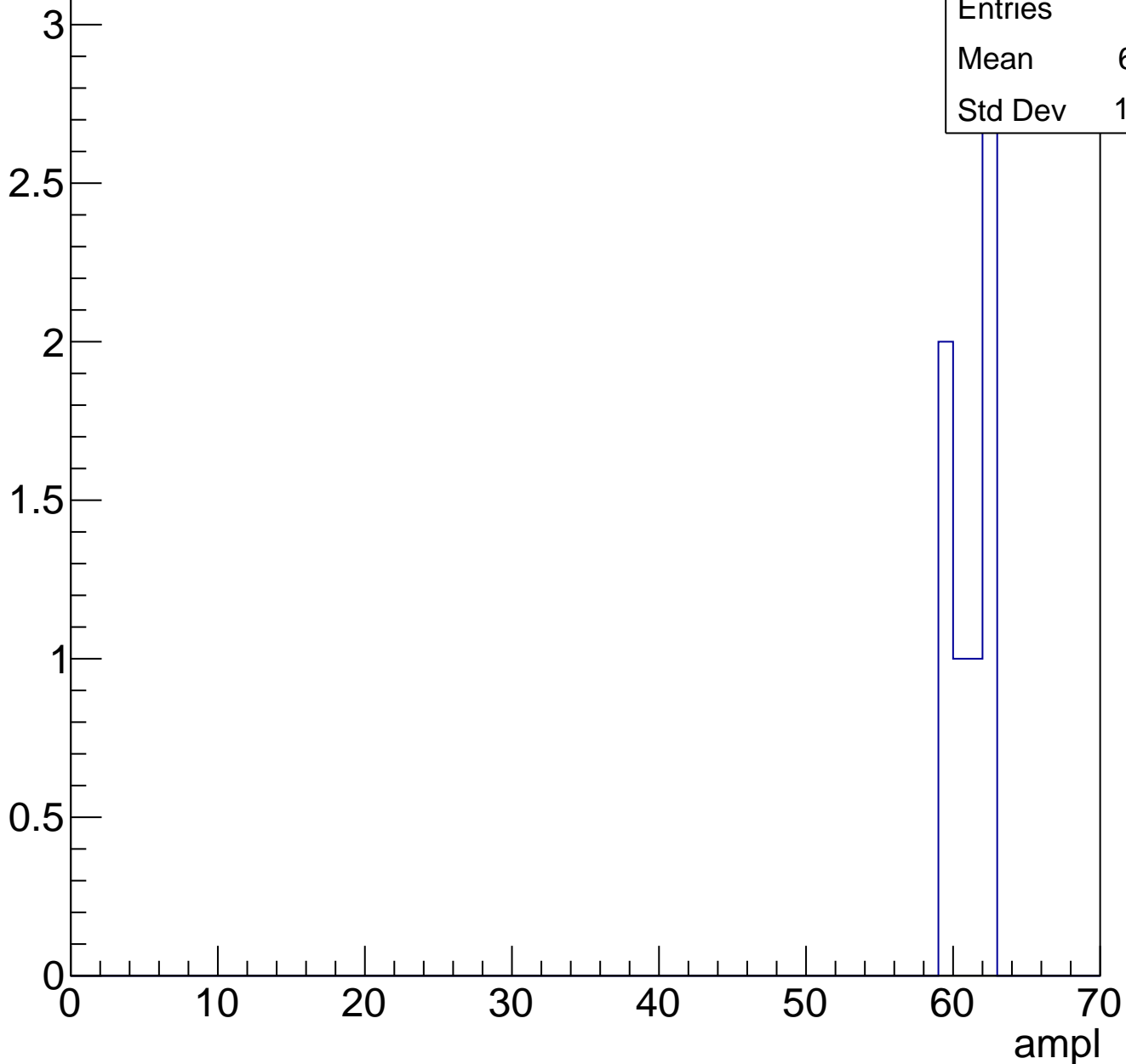
Entries	62
Mean	58.08
Std Dev	7.972



B1L102S, U19-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U19-ch120, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	96
Mean	27.53
Std Dev	6.401

Gaus mean : 28.9686

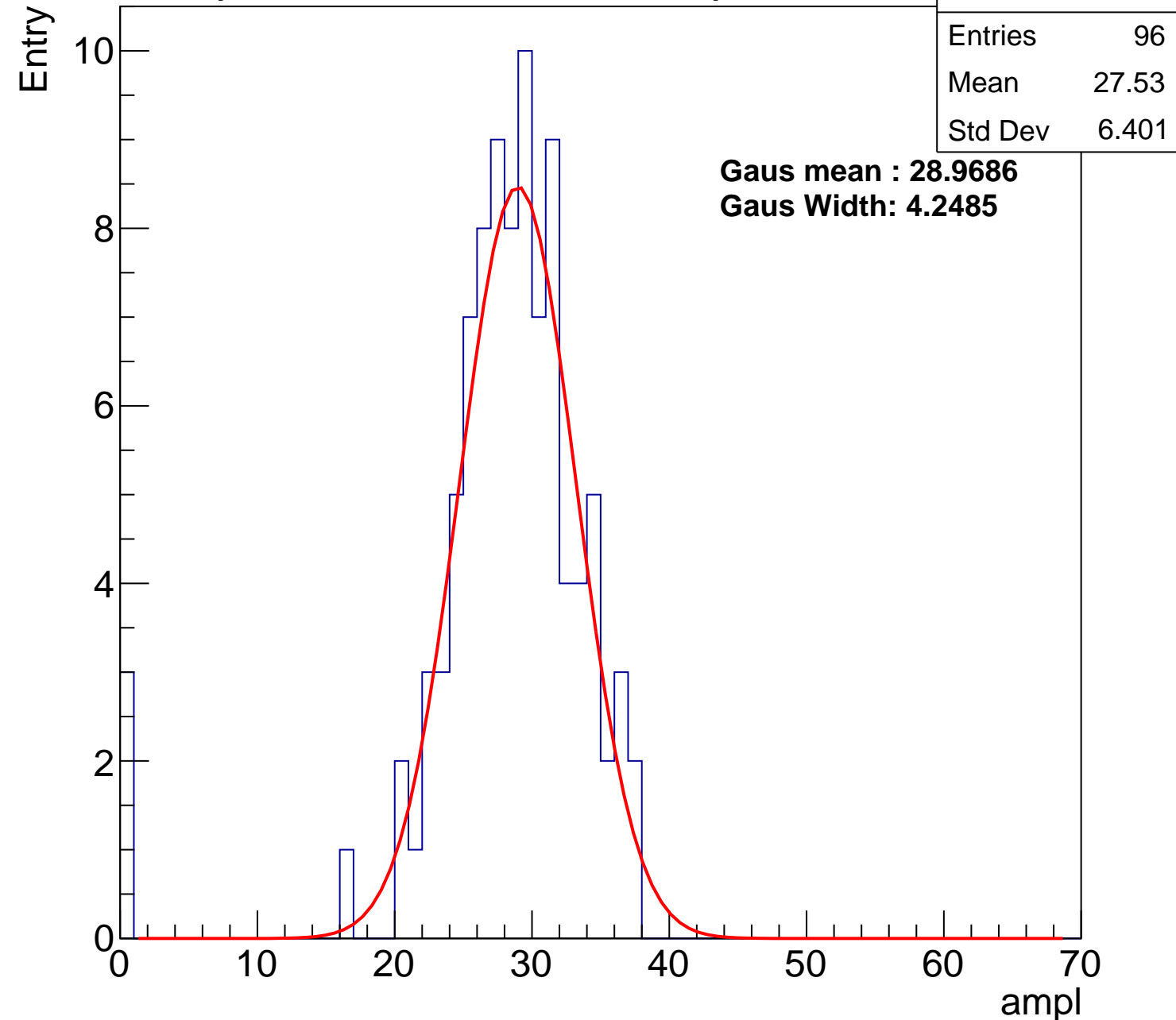
Gaus Width: 4.2485

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch120, adc1

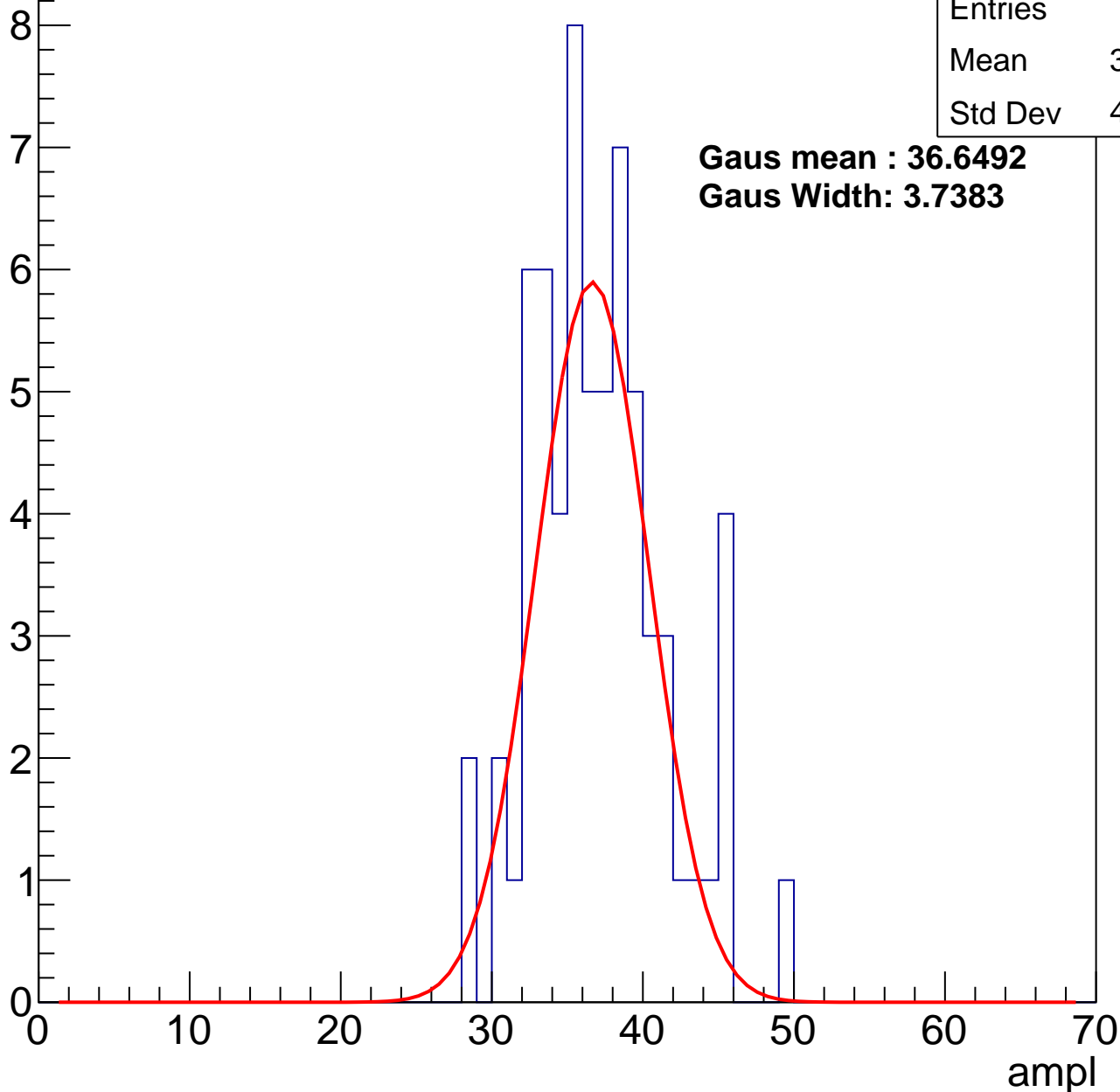
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.62
Std Dev	4.328

Gaus mean : 36.6492

Gaus Width: 3.7383



B1L102S, U19-ch120, adc2

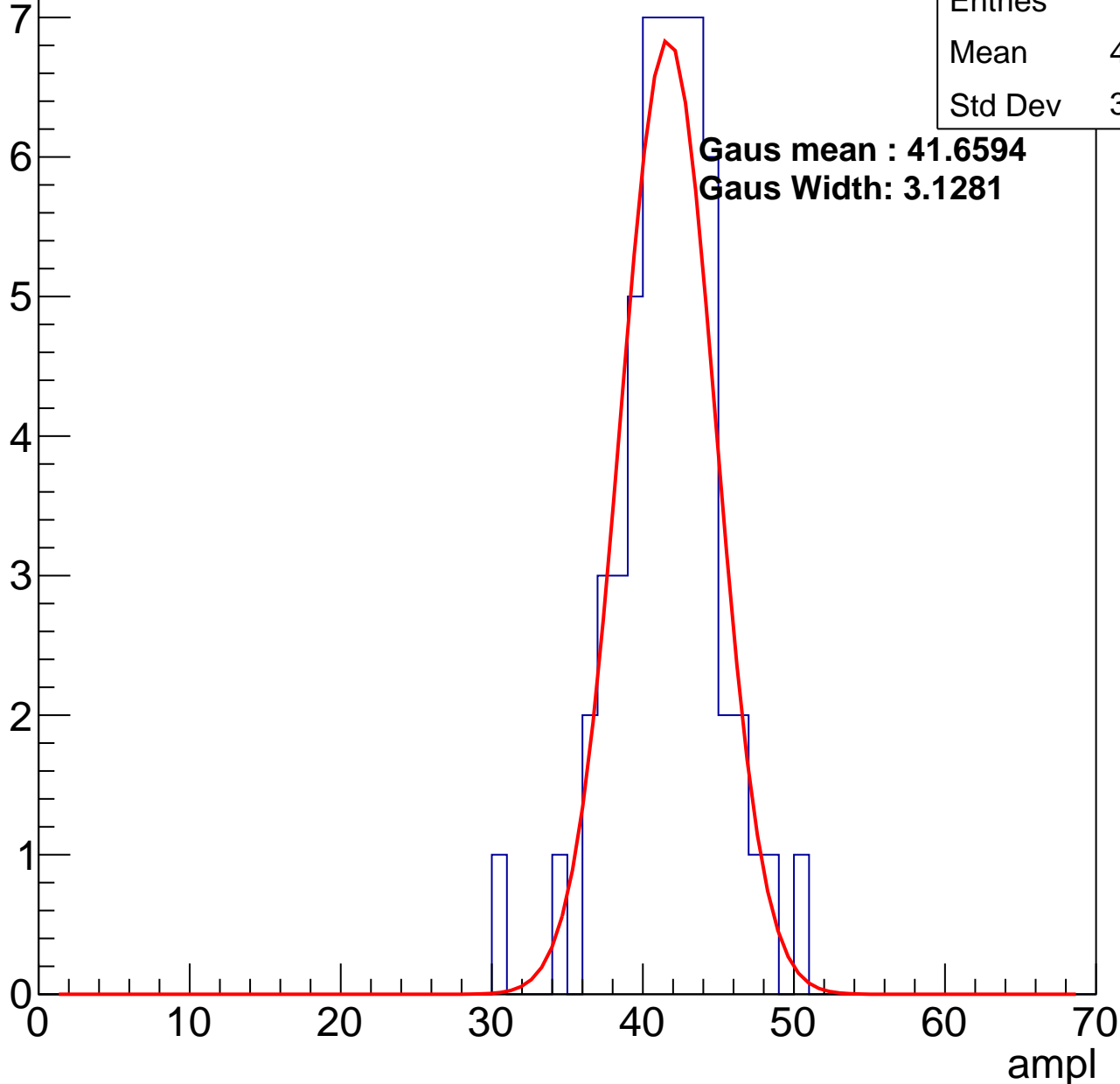
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.23
Std Dev	3.423

Gaus mean : 41.6594

Gaus Width: 3.1281

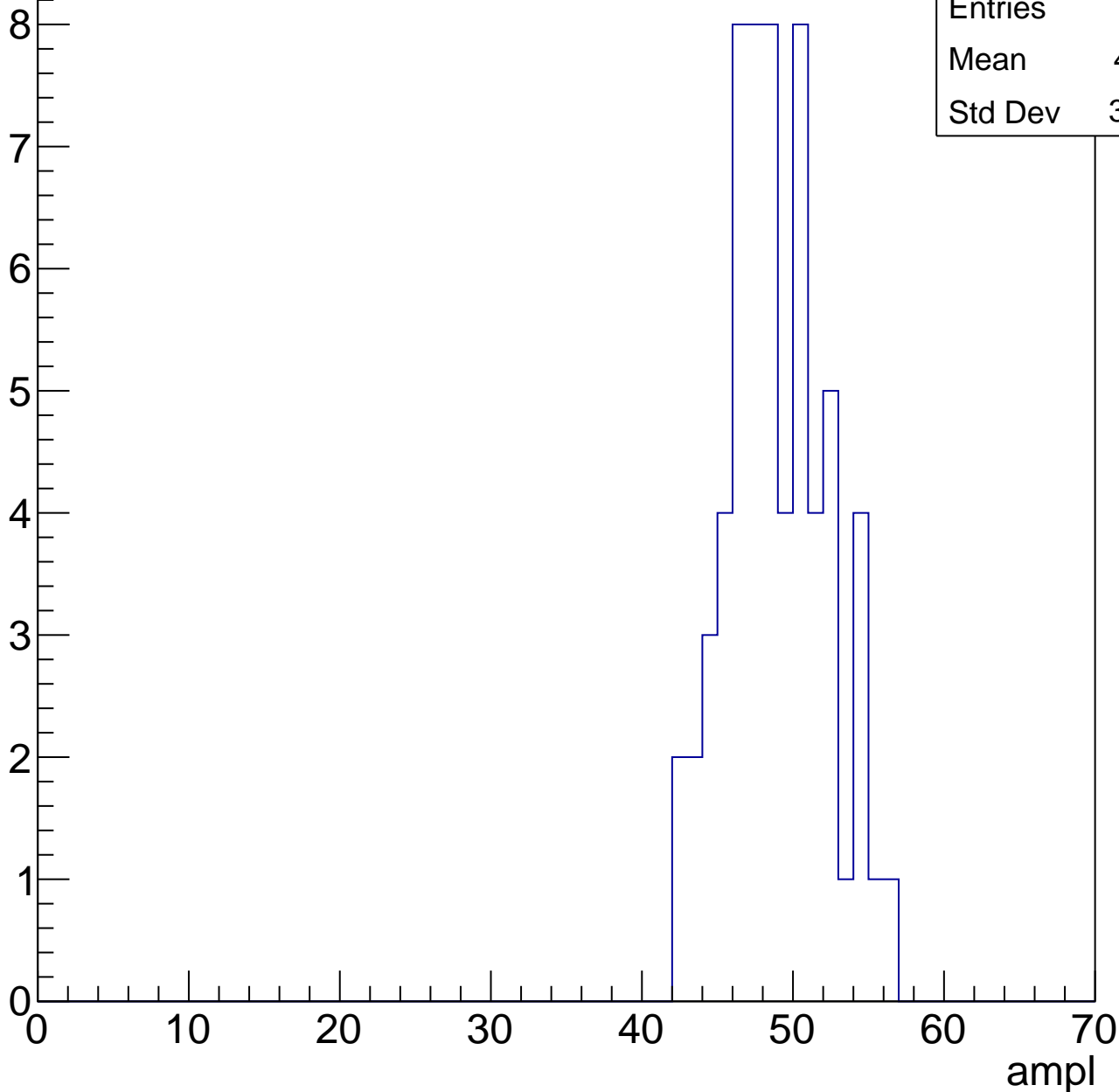


B1L102S, U19-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.41
Std Dev	3.259

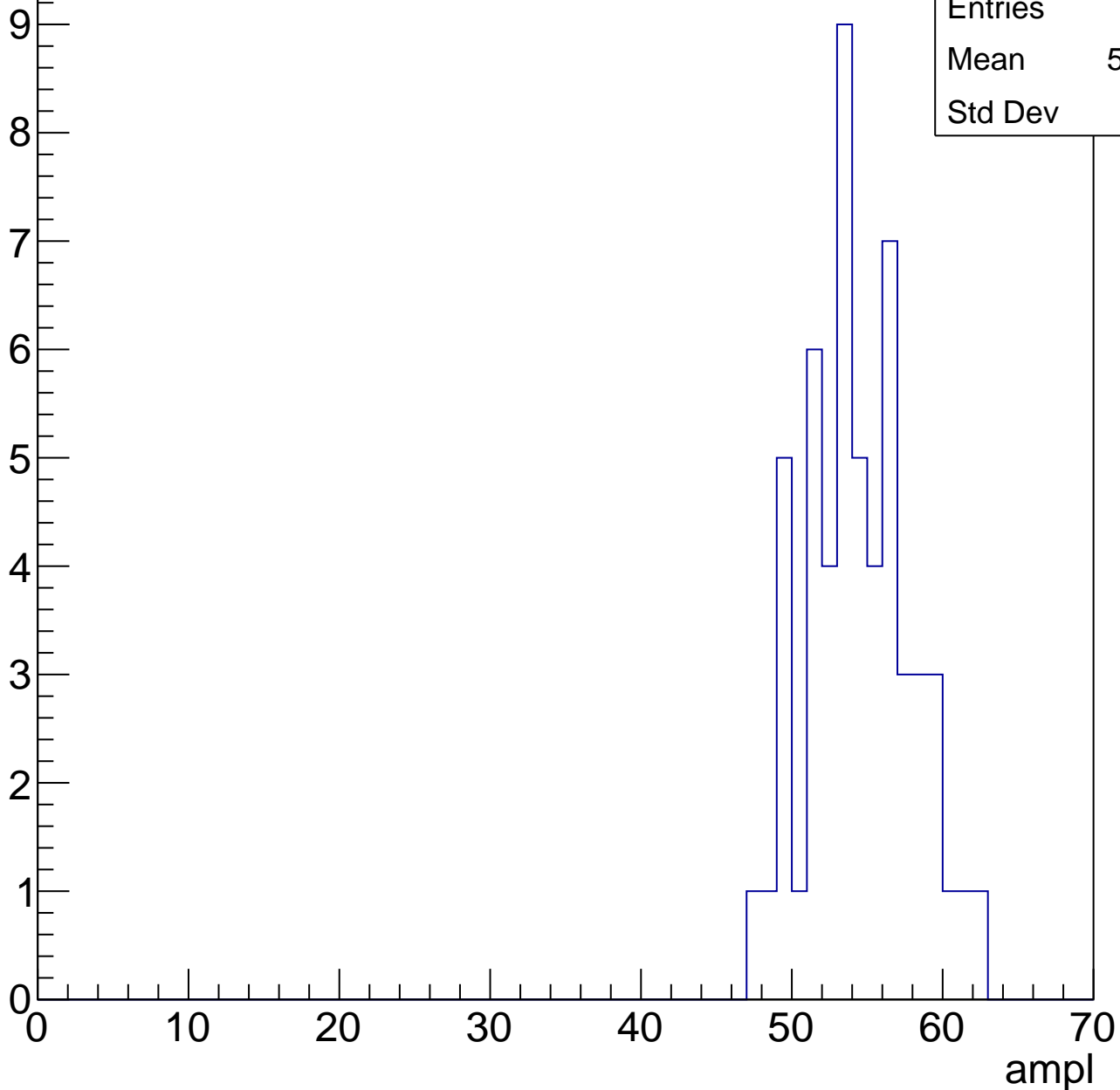


B1L102S, U19-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.96
Std Dev	3.4

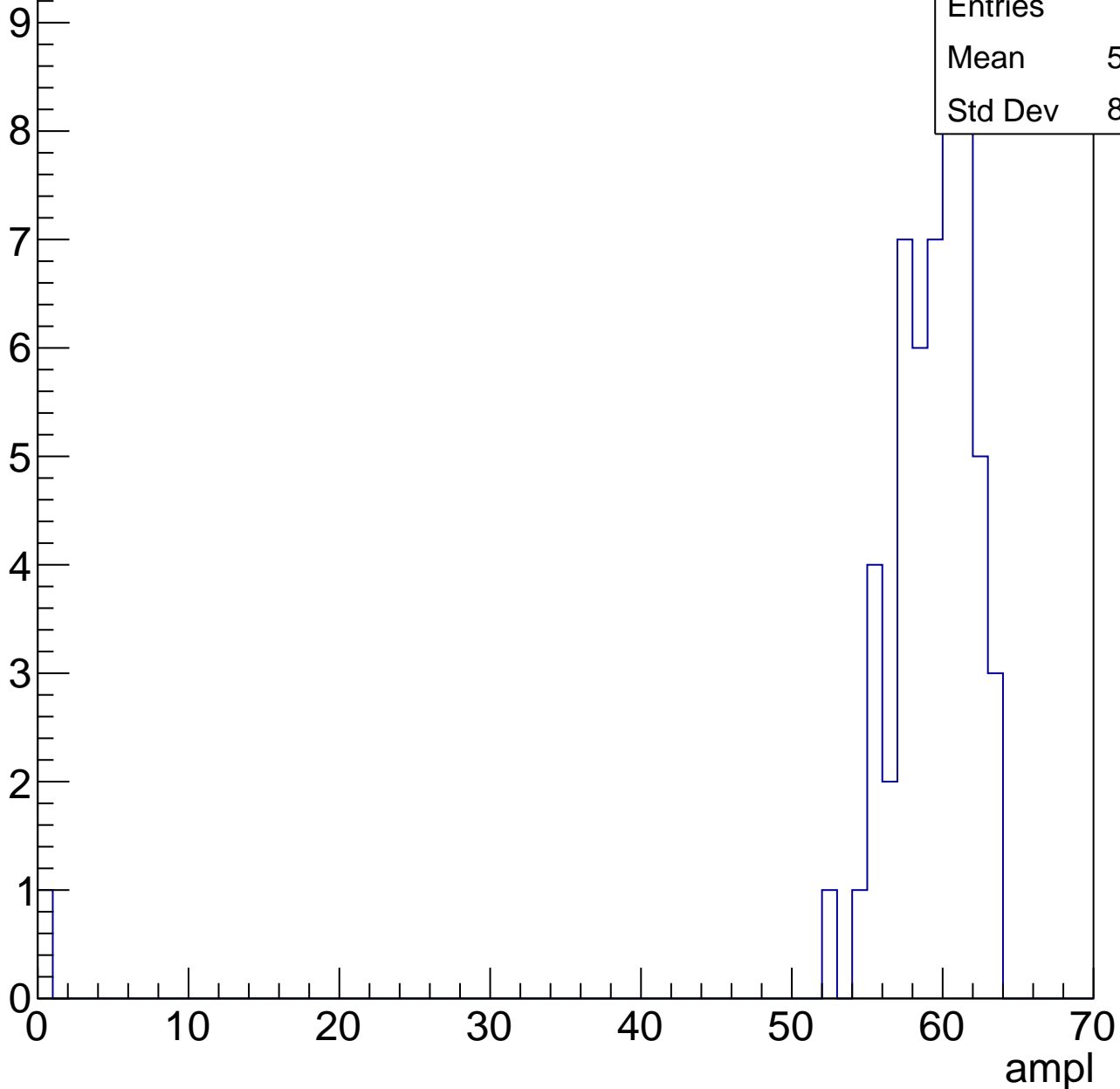


B1L102S, U19-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.87
Std Dev	8.318

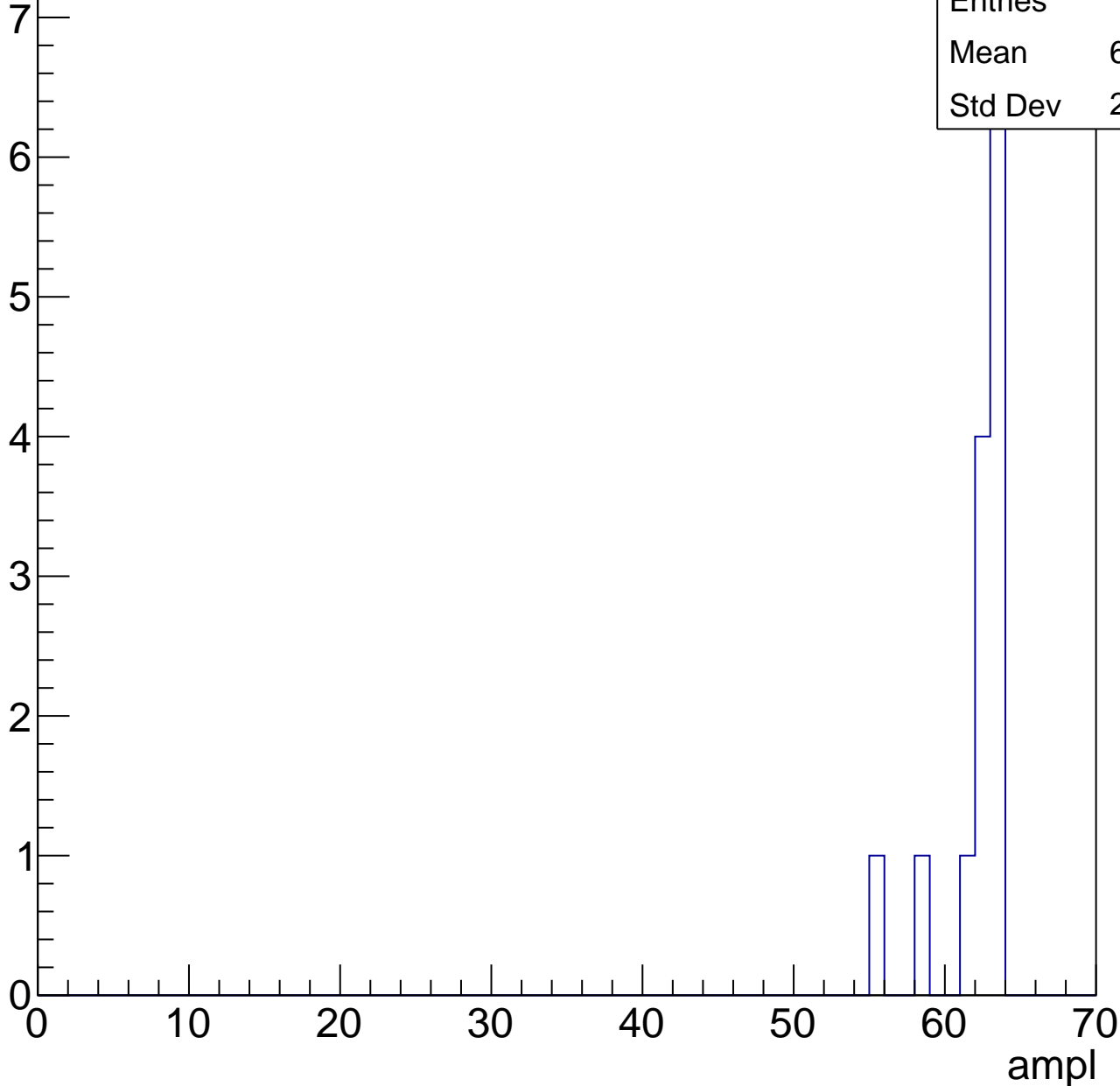


B1L102S, U19-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.64
Std Dev	2.255



B1L102S, U19-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch121, adc0

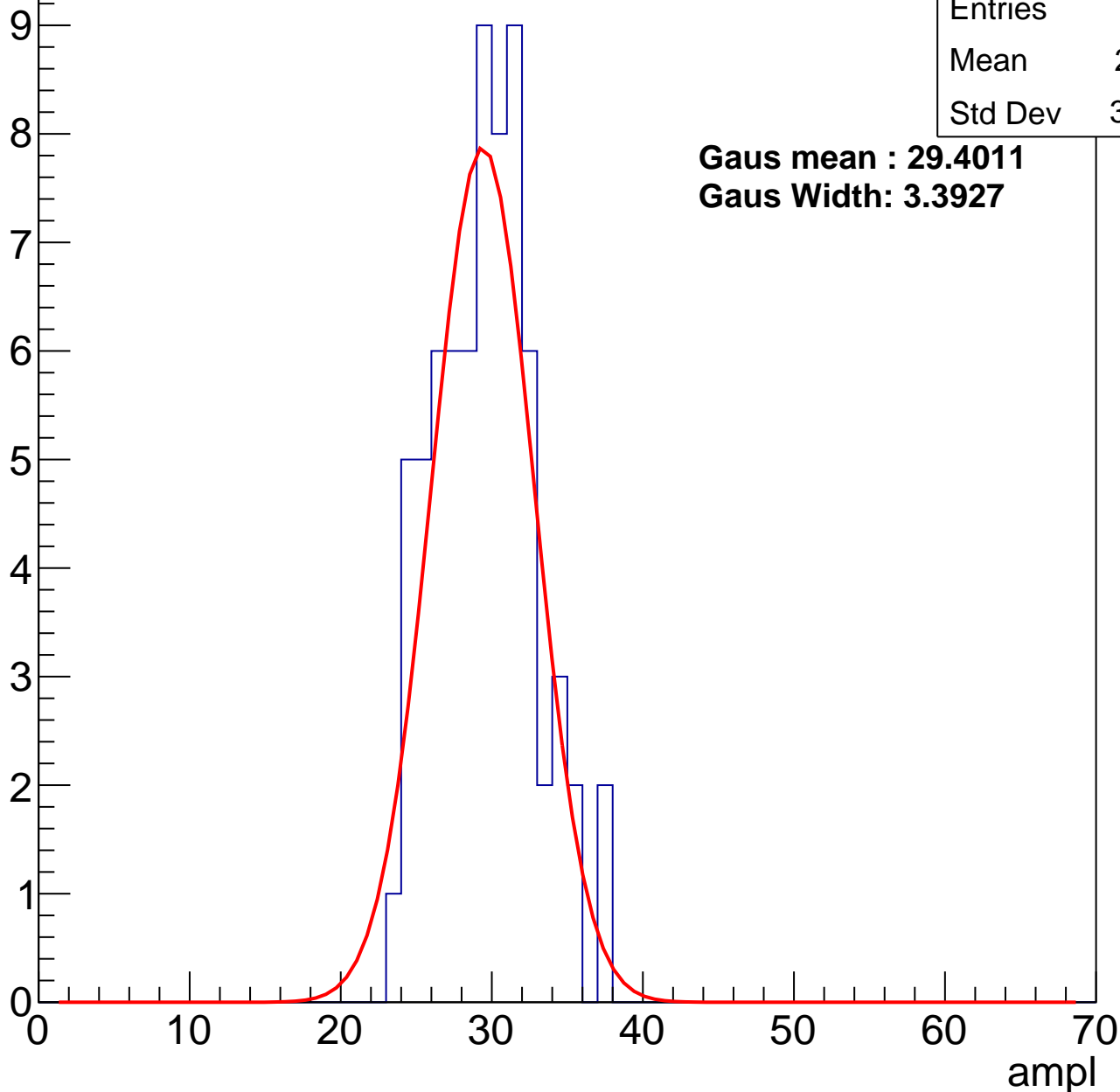
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.11
Std Dev	3.227

Gaus mean : 29.4011

Gaus Width: 3.3927



B1L102S, U19-ch121, adc1

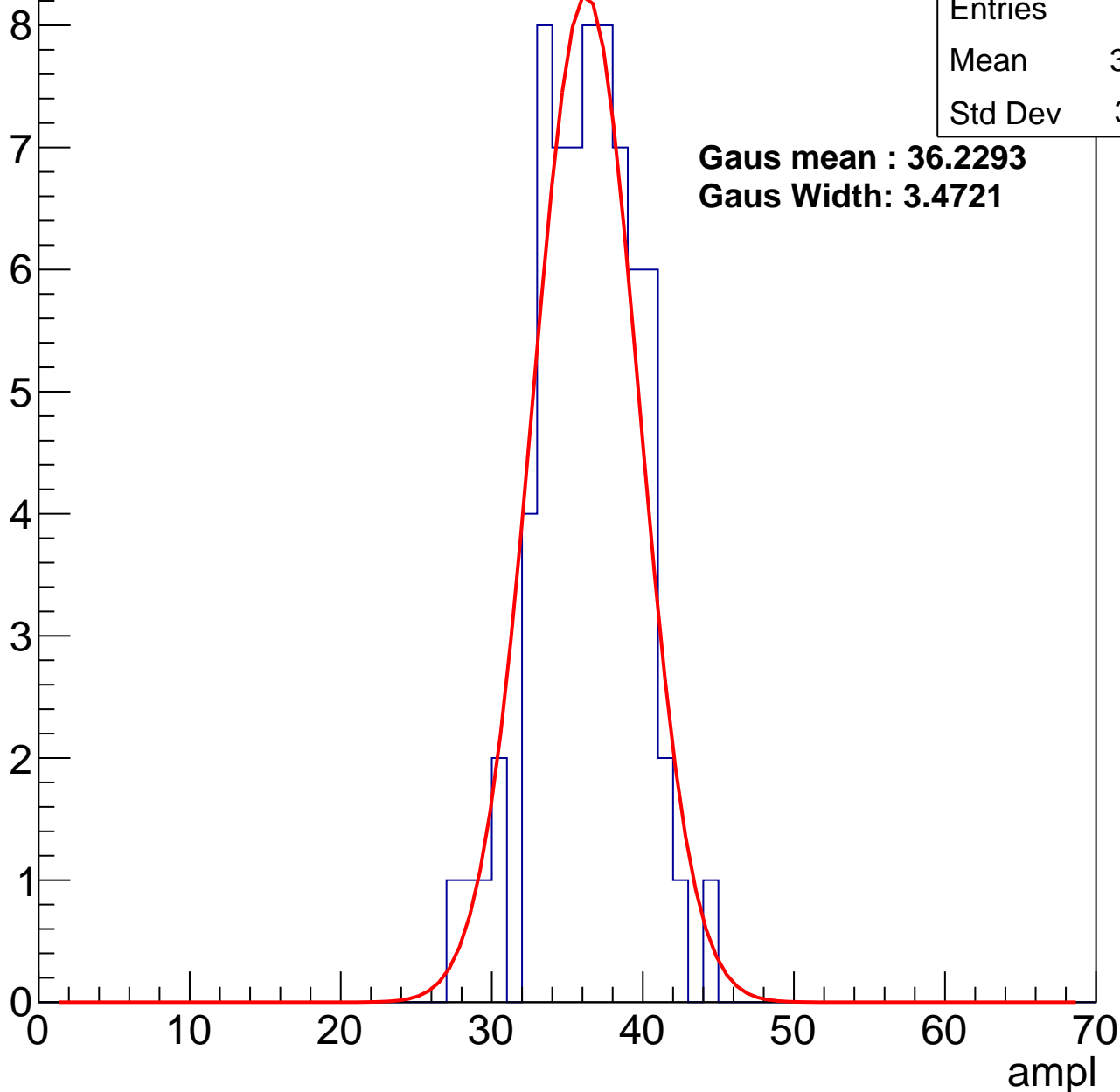
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.87
Std Dev	3.321

Gaus mean : 36.2293

Gaus Width: 3.4721



B1L102S, U19-ch121, adc2

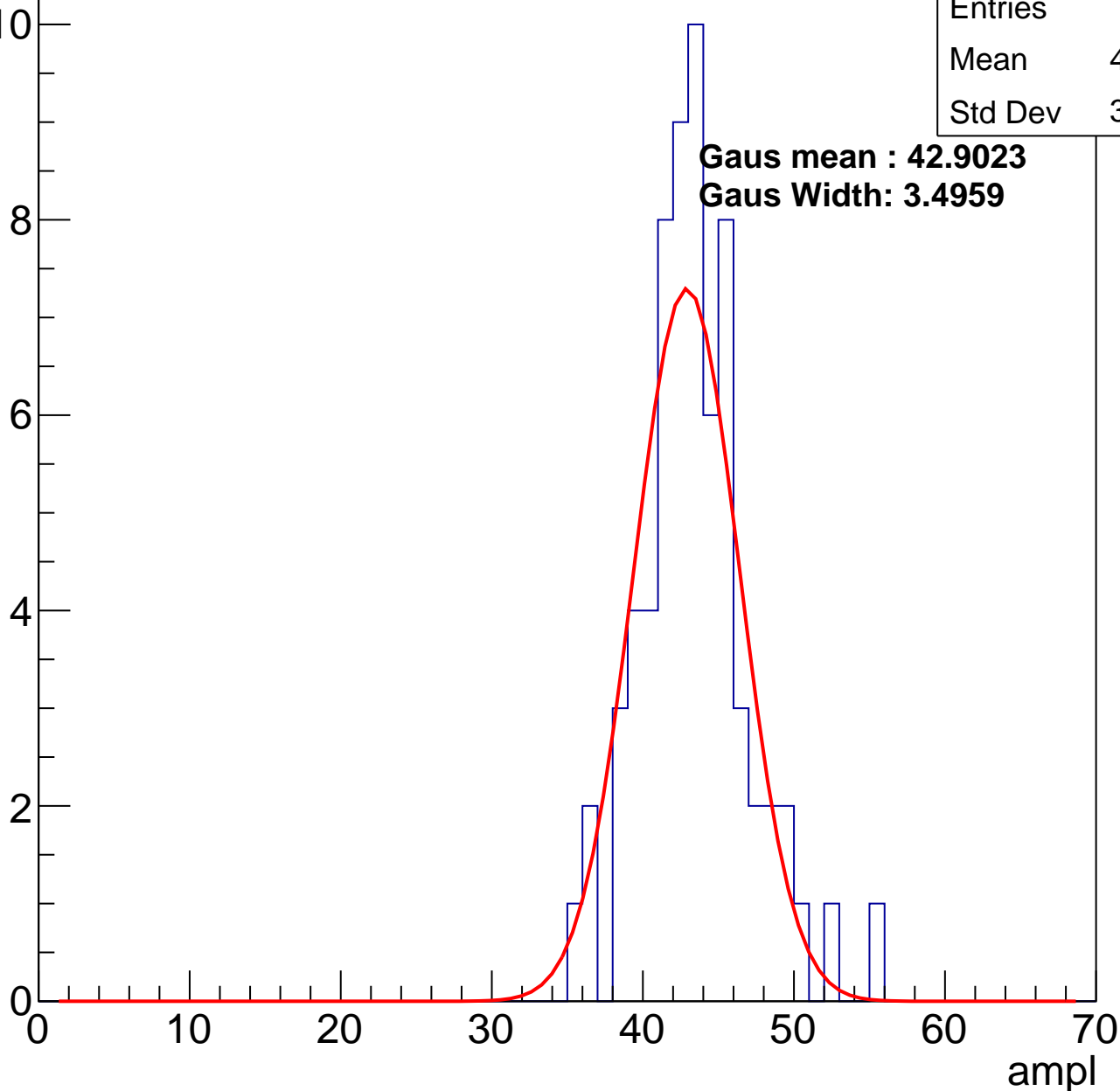
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.99
Std Dev	3.606

Gaus mean : 42.9023

Gaus Width: 3.4959

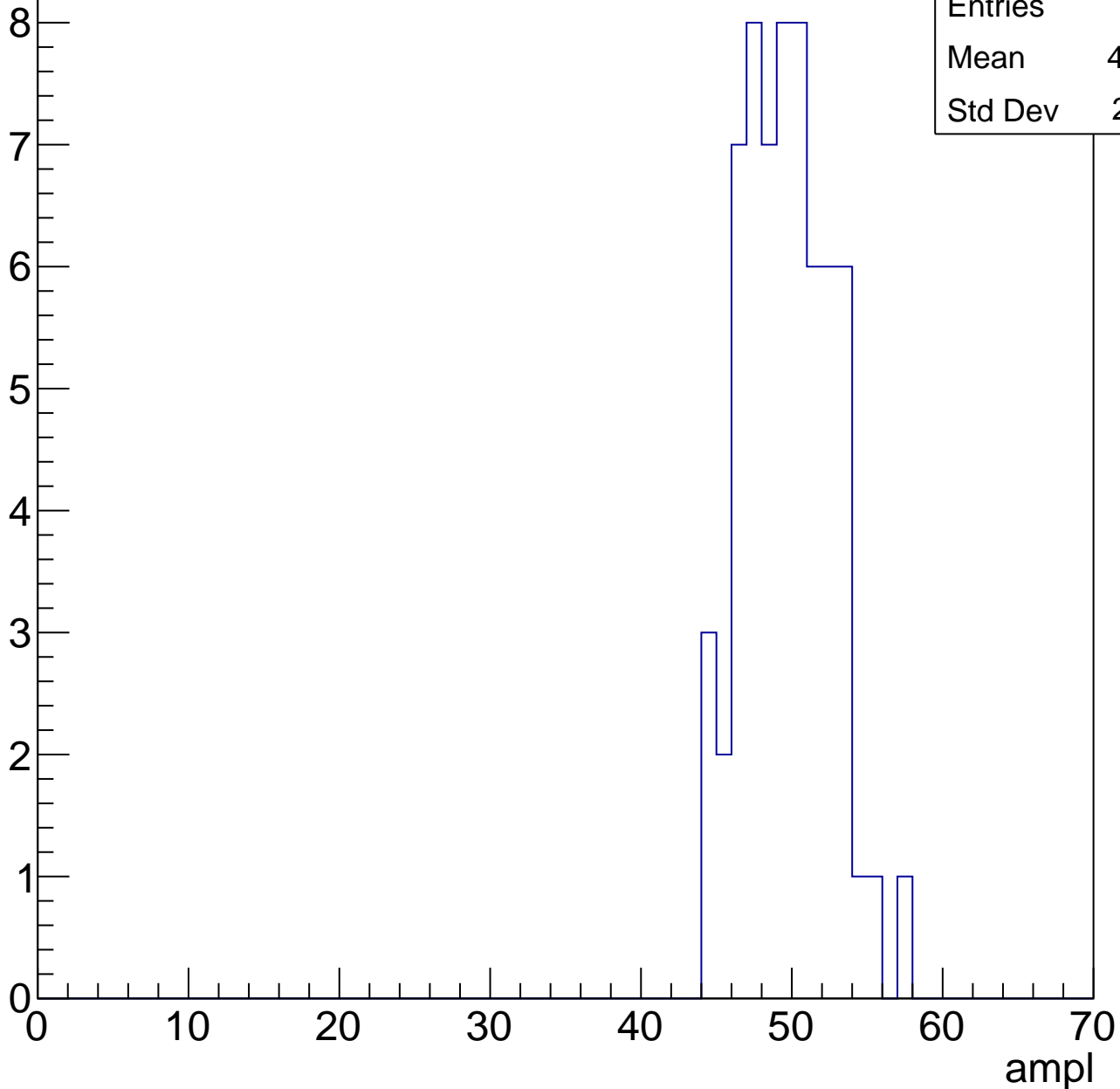


B1L102S, U19-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.22
Std Dev	2.831

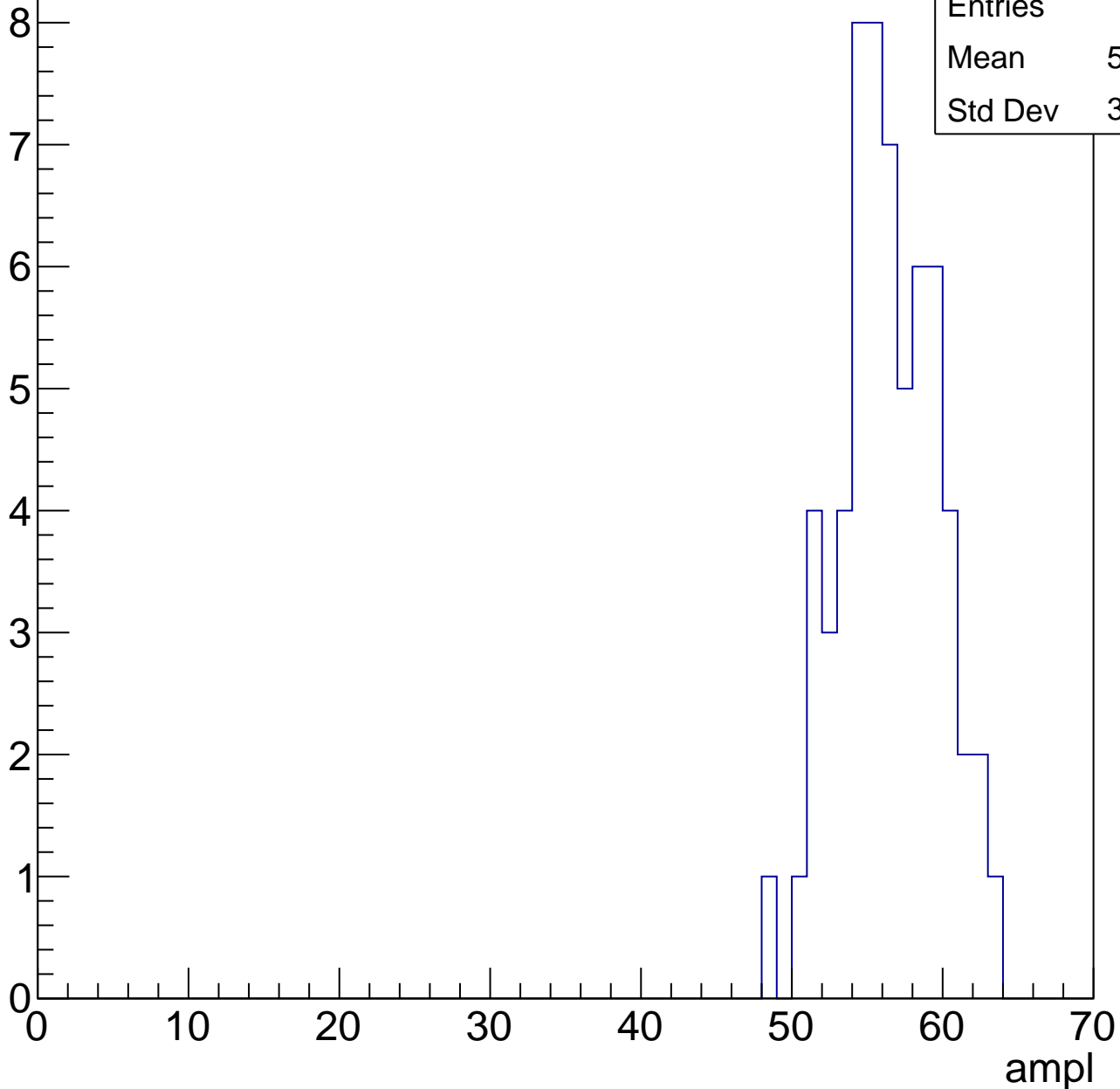


B1L102S, U19-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.97
Std Dev	3.218

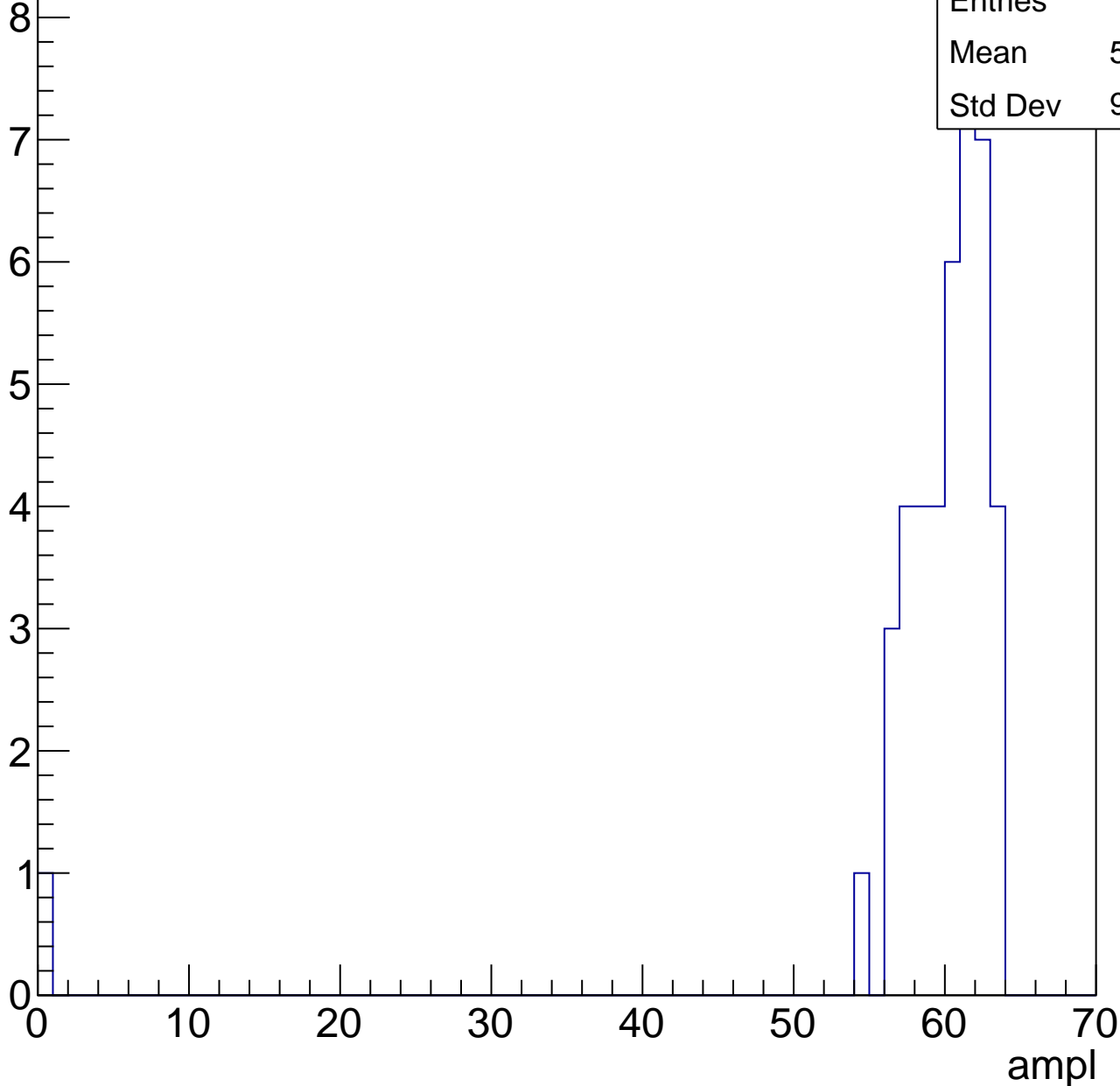


B1L102S, U19-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.38
Std Dev	9.388



B1L102S, U19-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

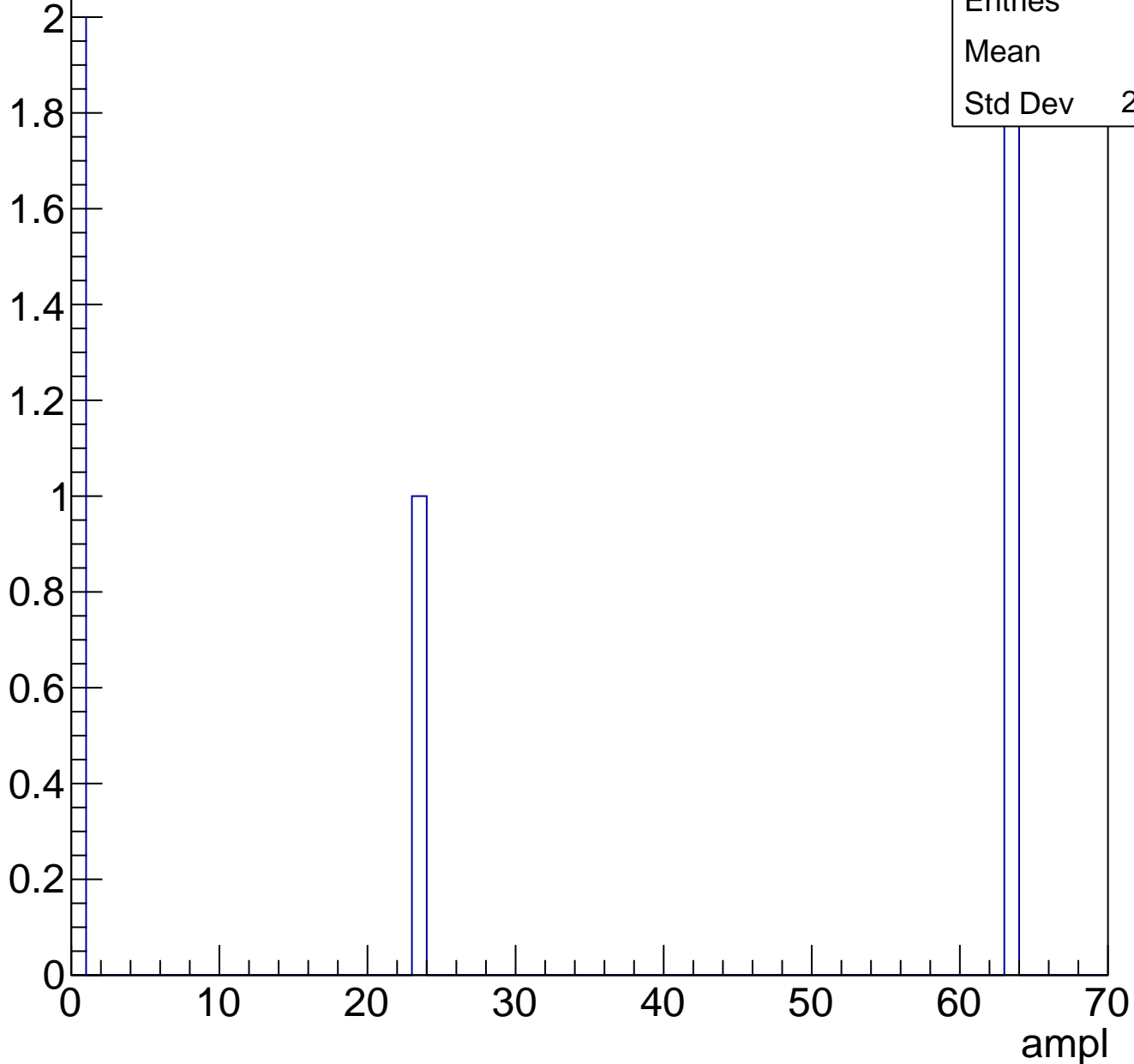
Entry



B1L102S, U19-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch122, adc0

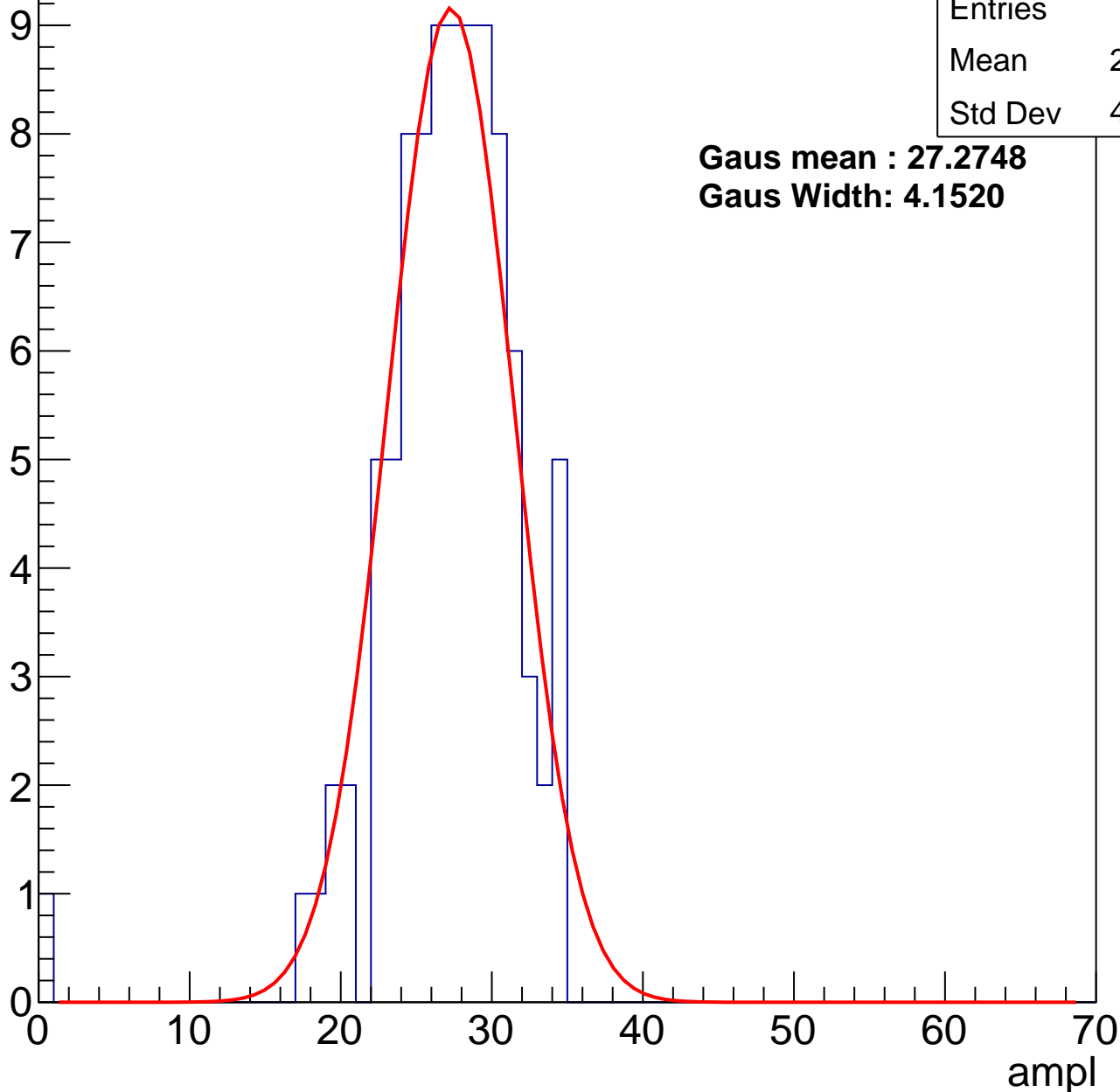
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	26.65
Std Dev	4.692

Gaus mean : 27.2748

Gaus Width: 4.1520



B1L102S, U19-ch122, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	36.05
Std Dev	3.663

Gaus mean : 36.6185

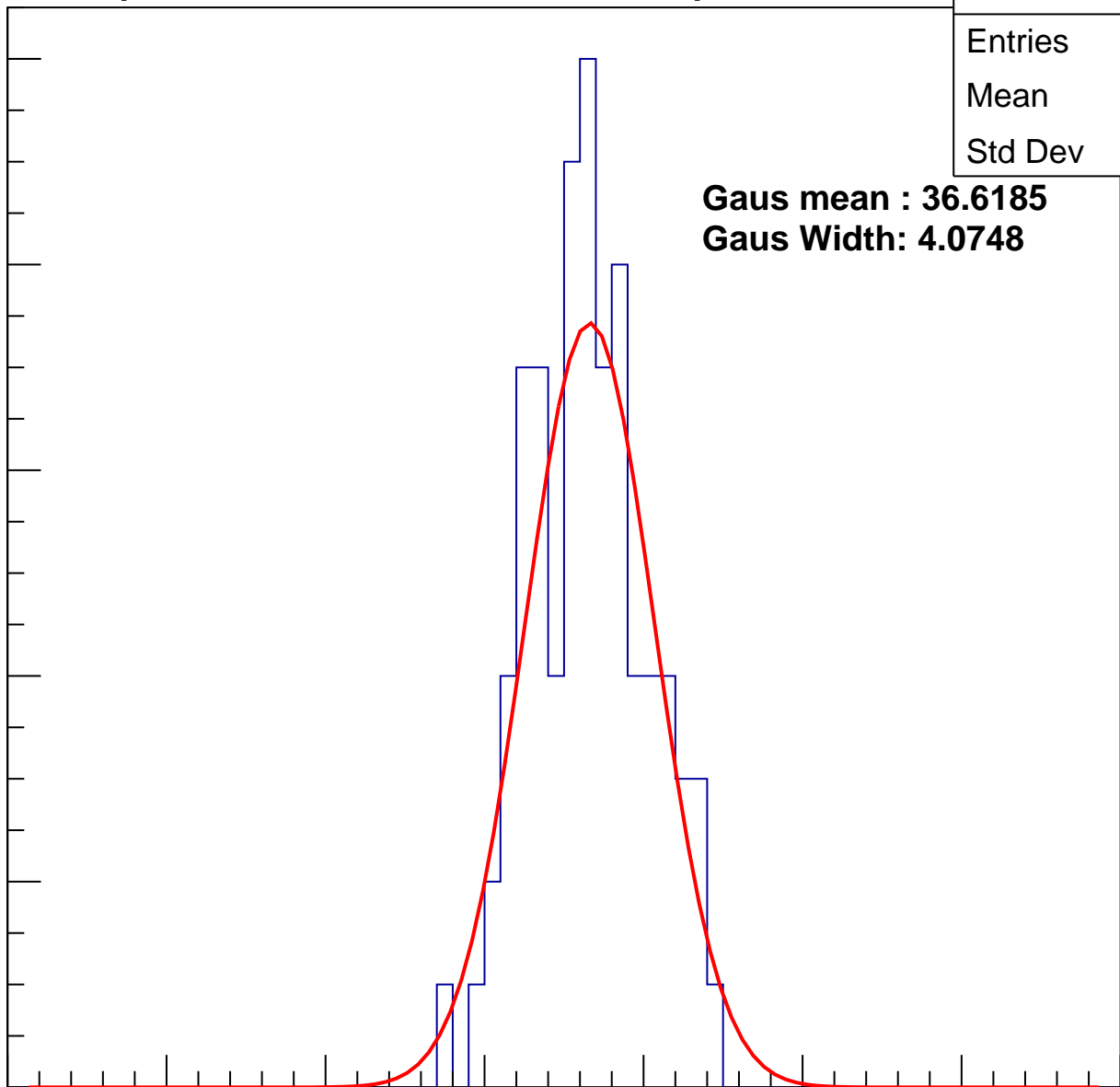
Gaus Width: 4.0748

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U19-ch122, adc2

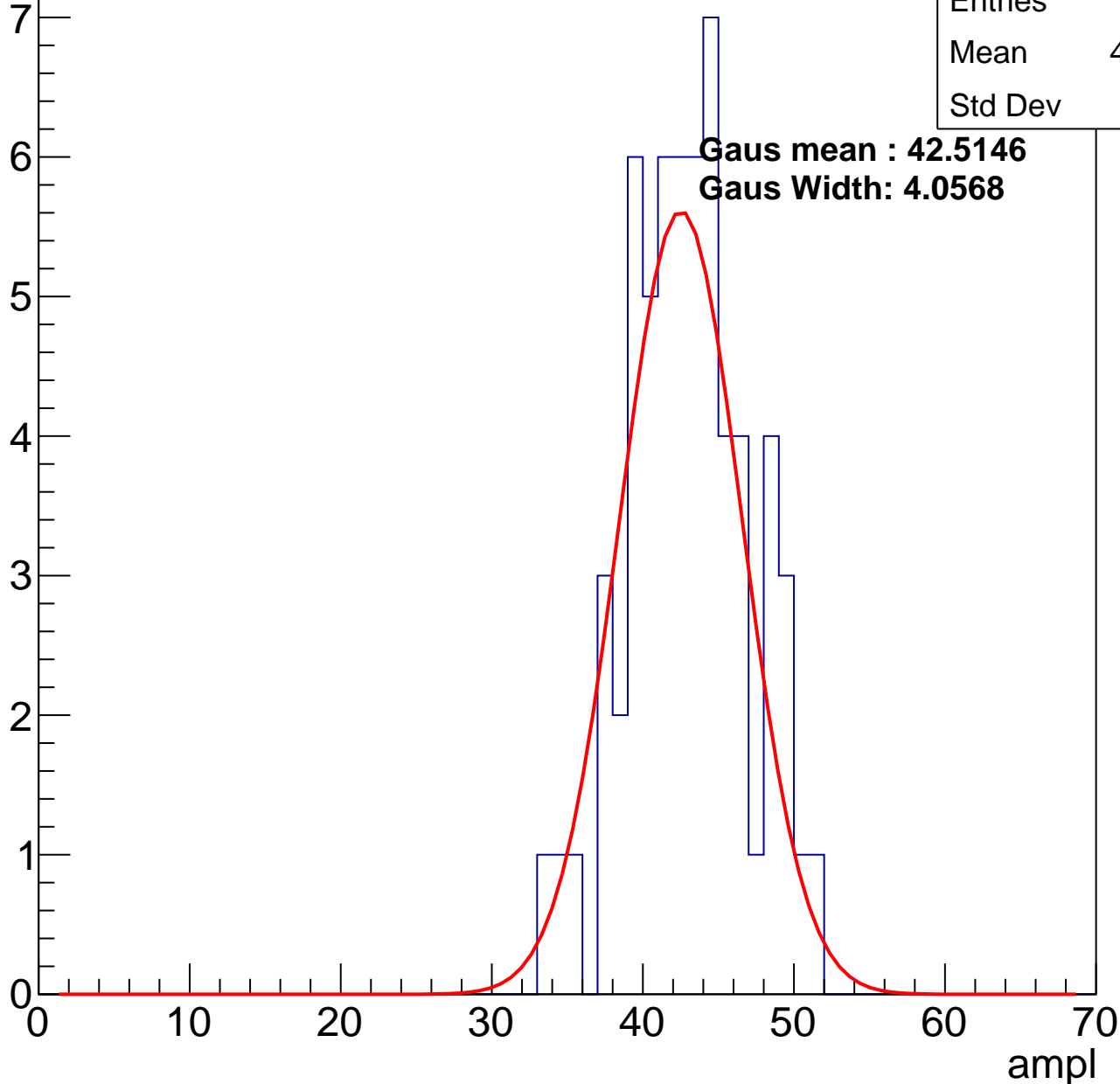
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.55
Std Dev	3.93

Gaus mean : 42.5146

Gaus Width: 4.0568

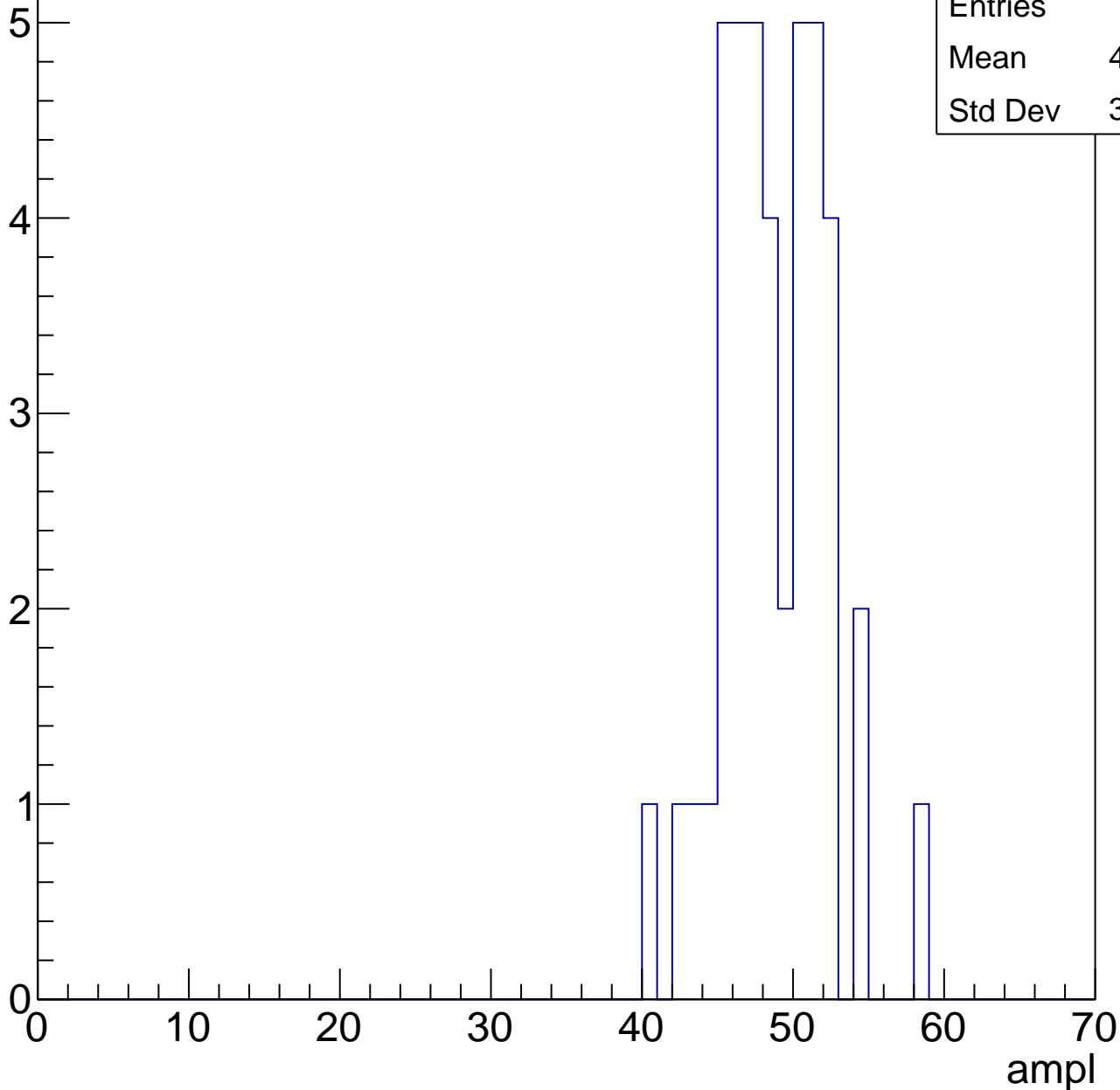


B1L102S, U19-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	48.29
Std Dev	3.487

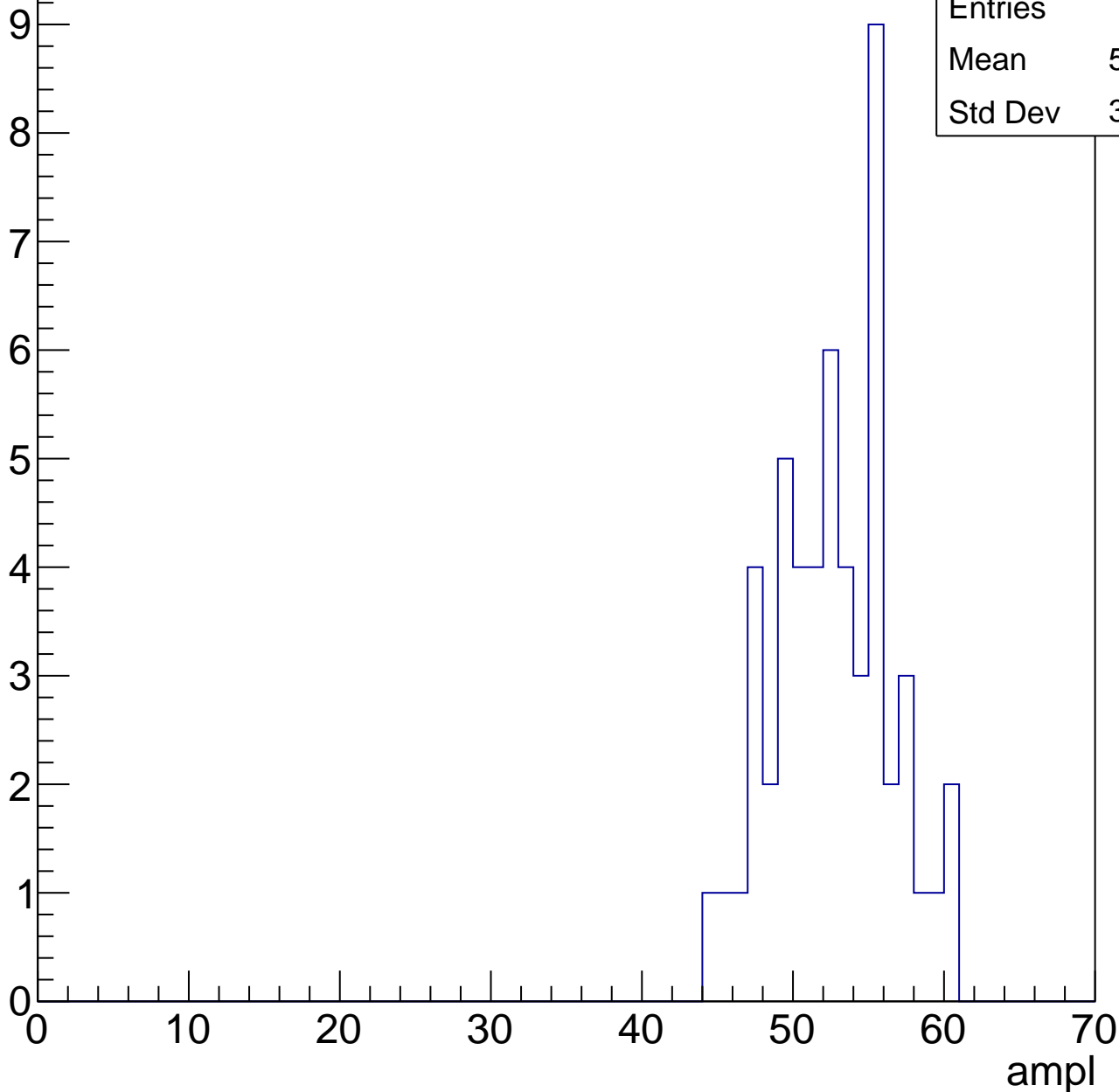


B1L102S, U19-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	52.25
Std Dev	3.786

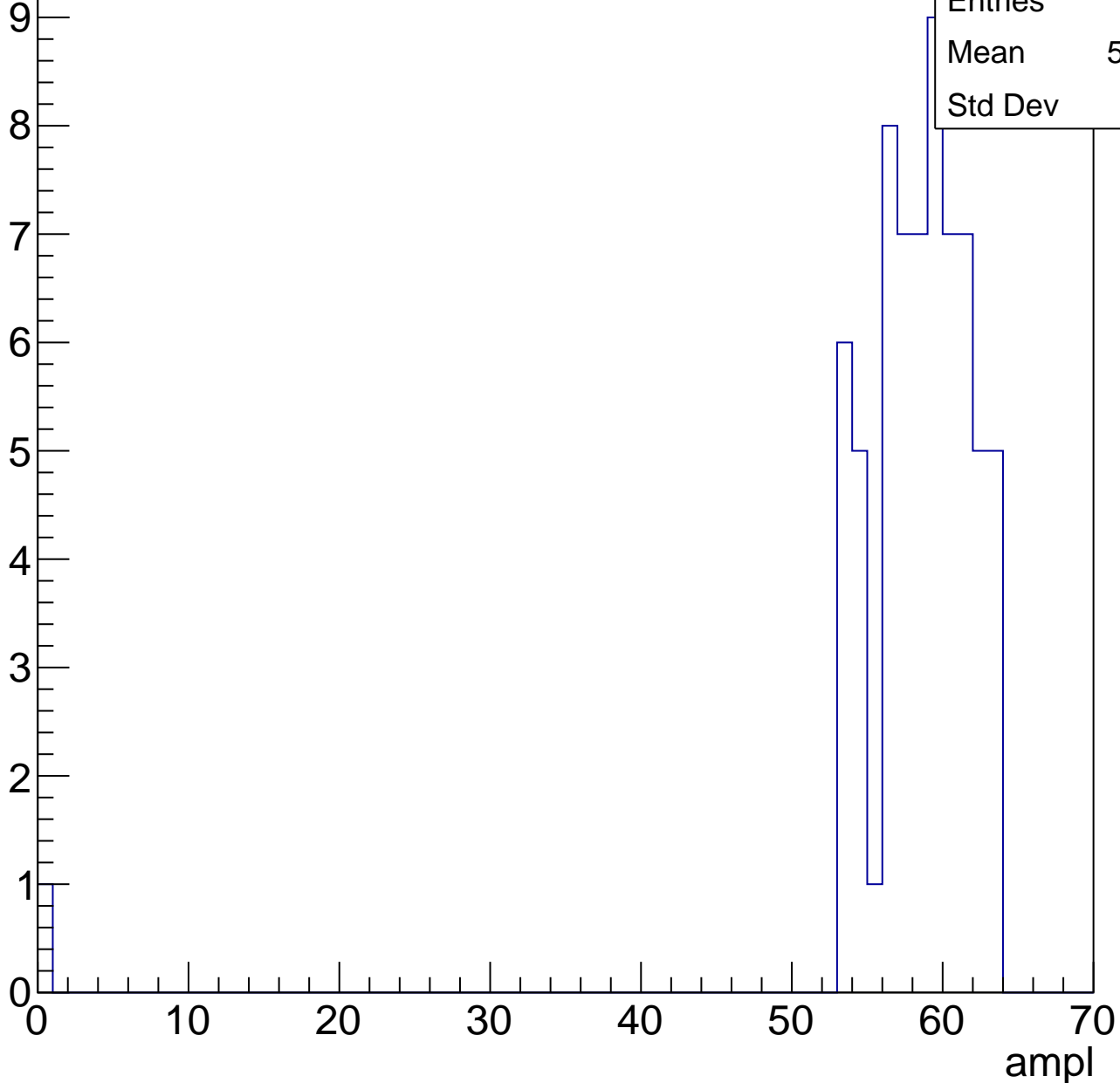


B1L102S, U19-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	57.34
Std Dev	7.59

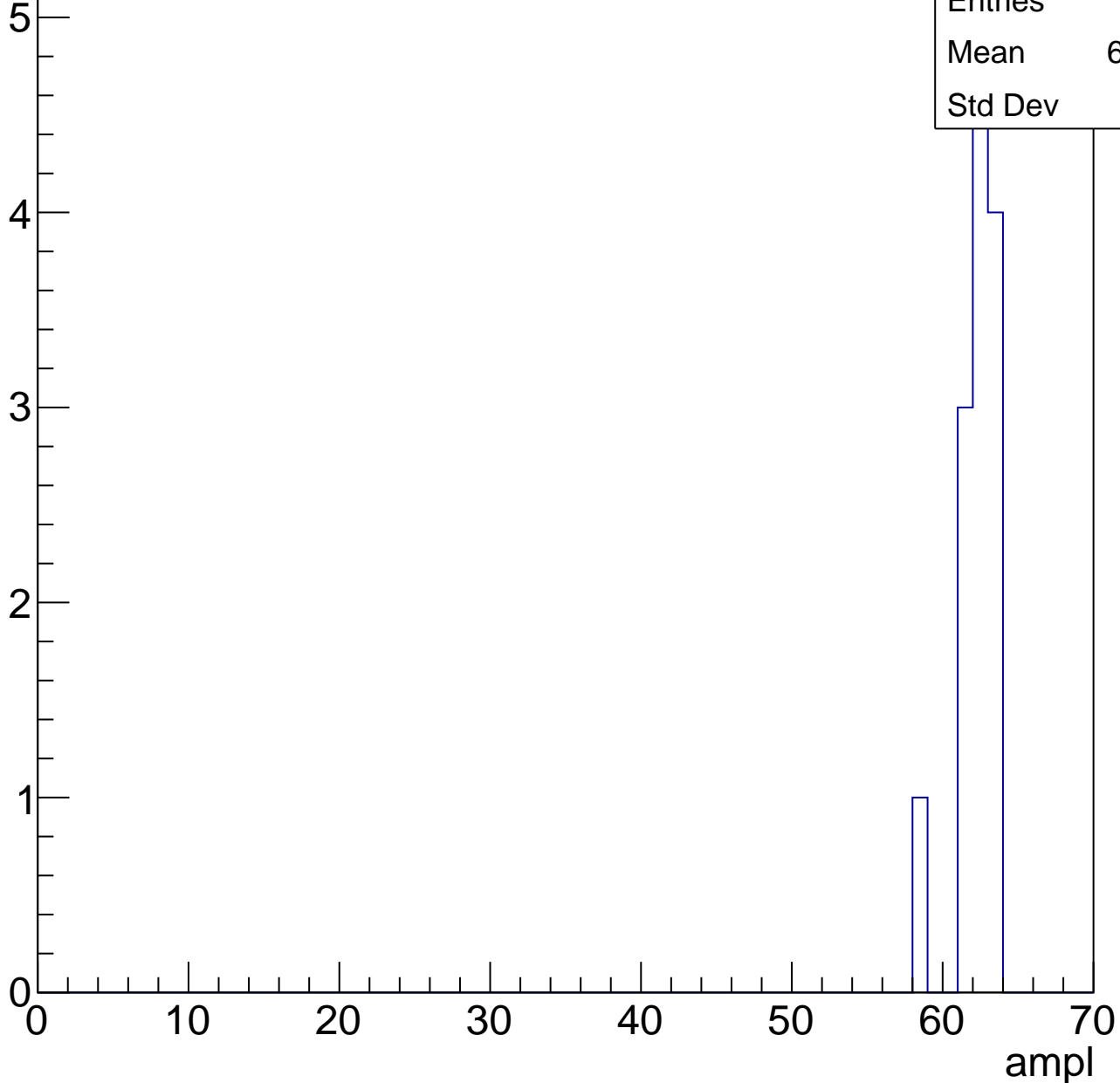


B1L102S, U19-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

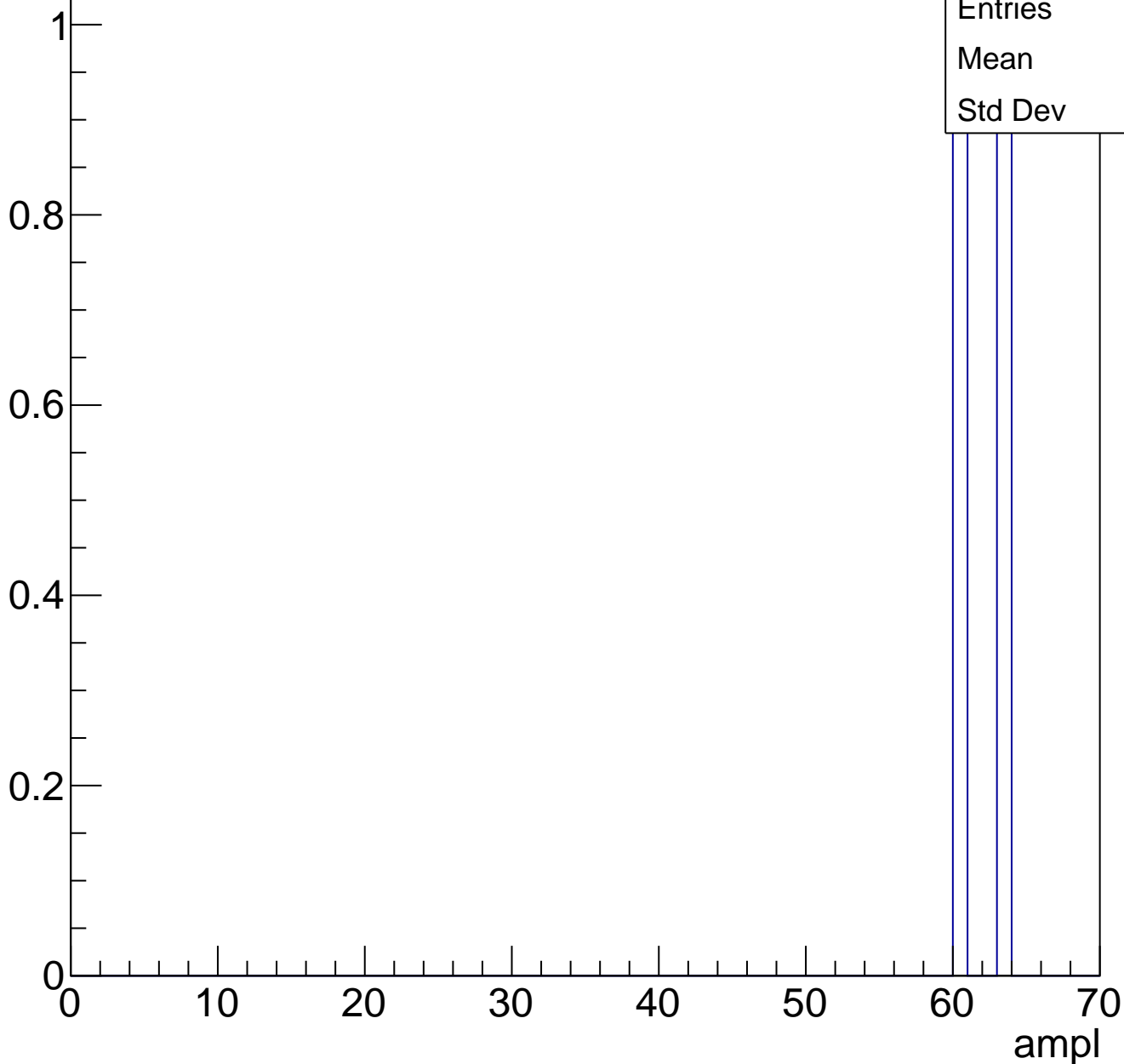
Entries	13
Mean	61.77
Std Dev	1.31



B1L102S, U19-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch123, adc0

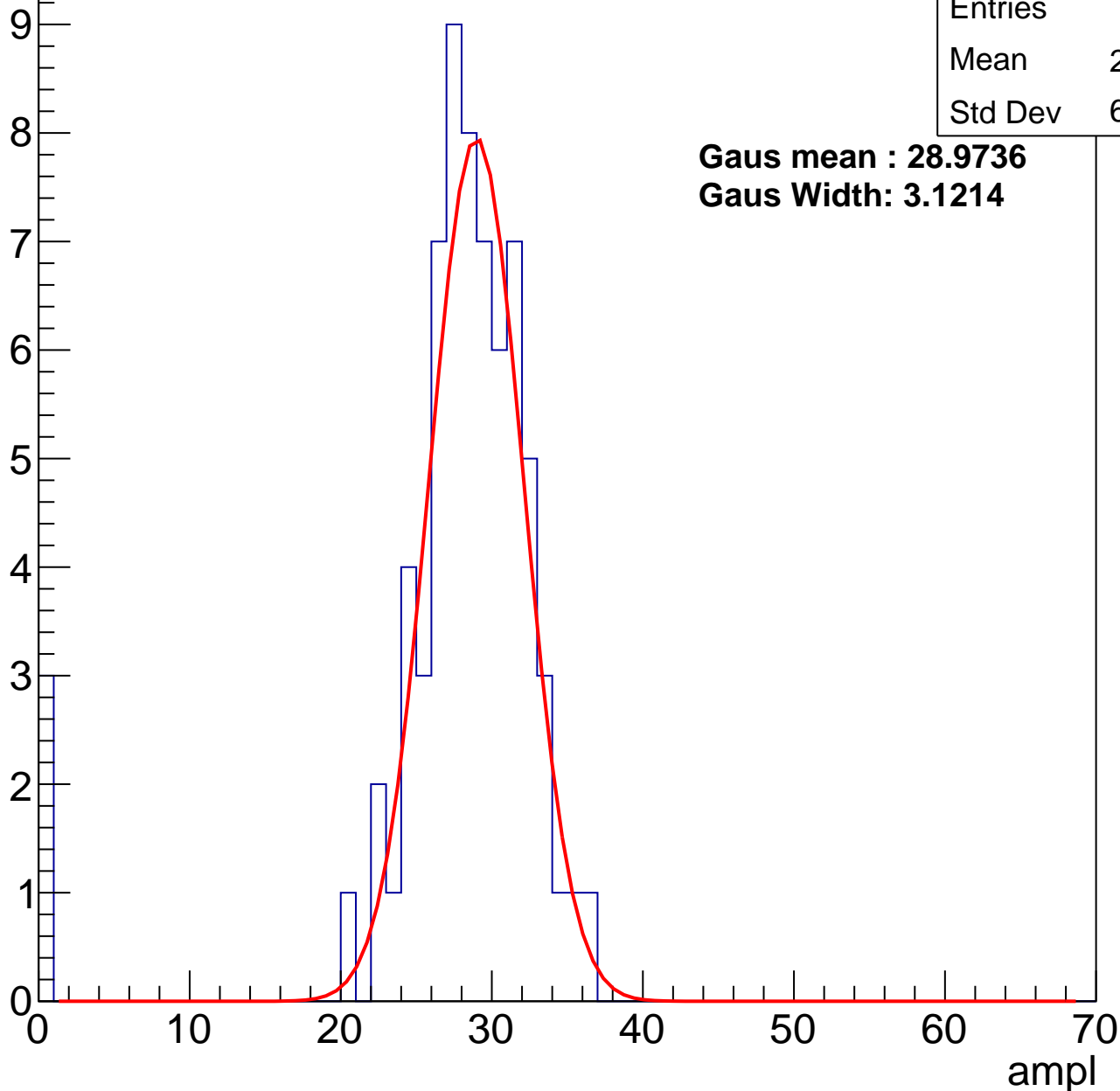
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.12
Std Dev	6.577

Gaus mean : 28.9736

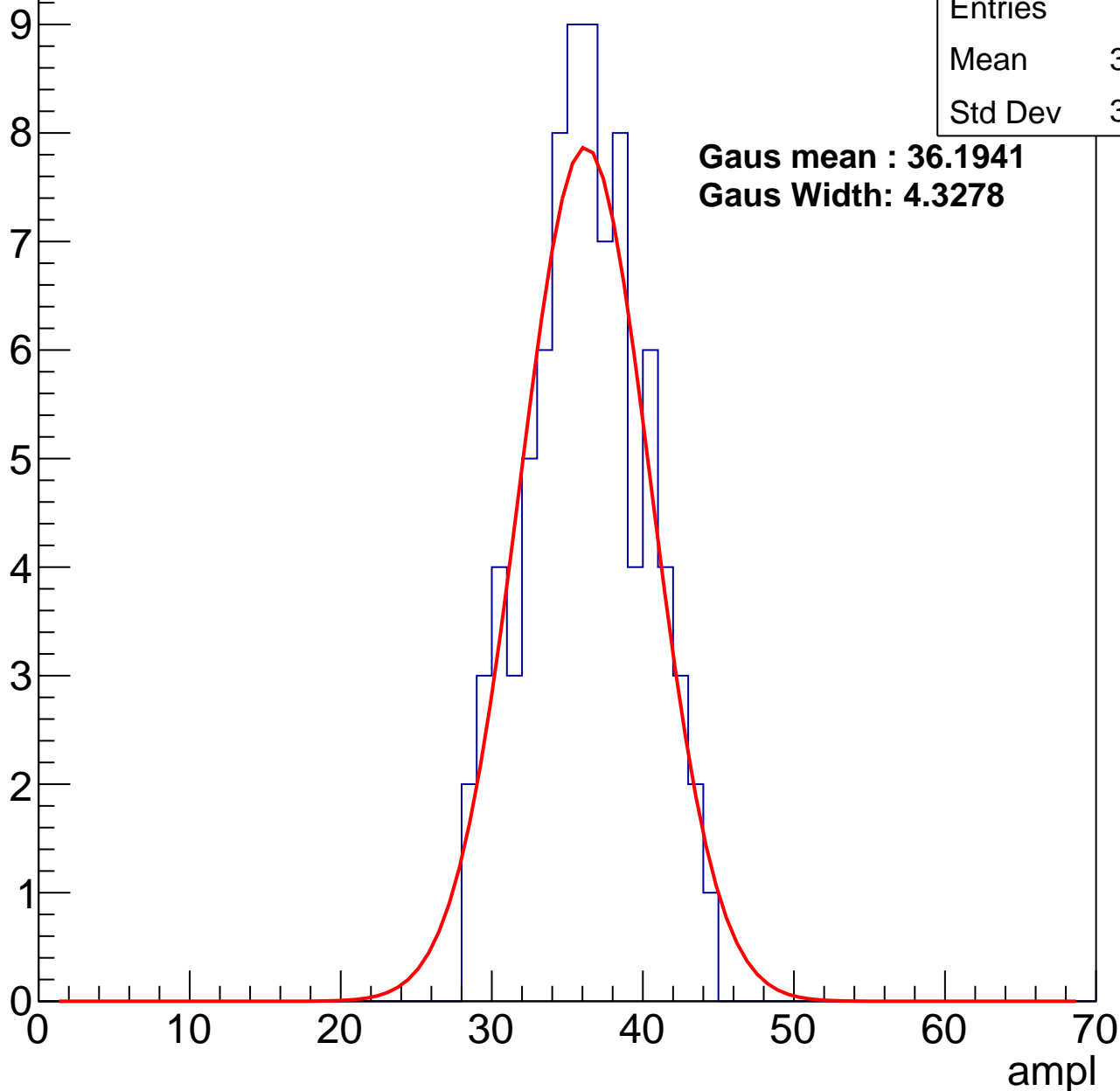
Gaus Width: 3.1214



B1L102S, U19-ch123, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch123, adc2

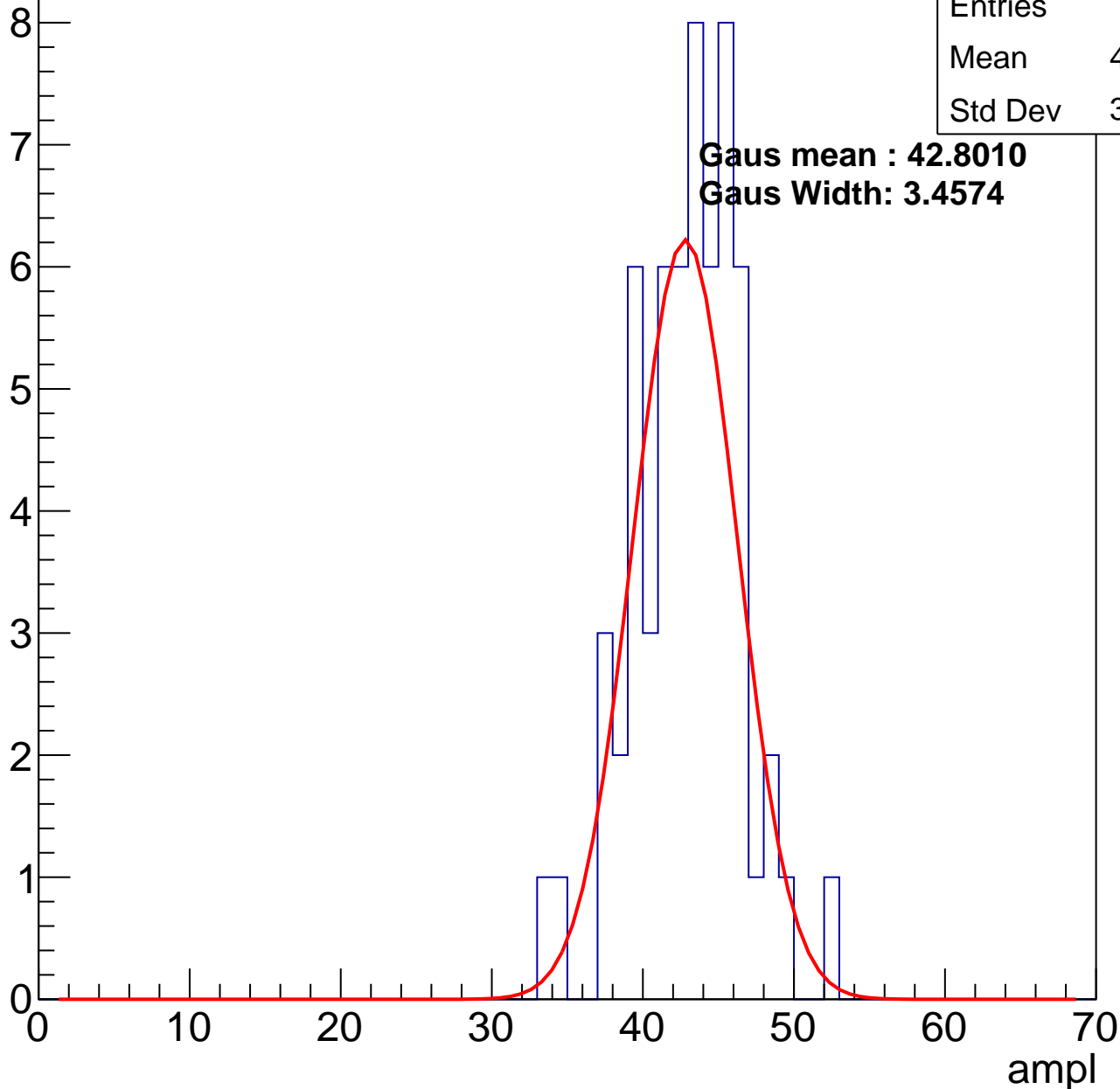
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.52
Std Dev	3.514

Gaus mean : 42.8010

Gaus Width: 3.4574

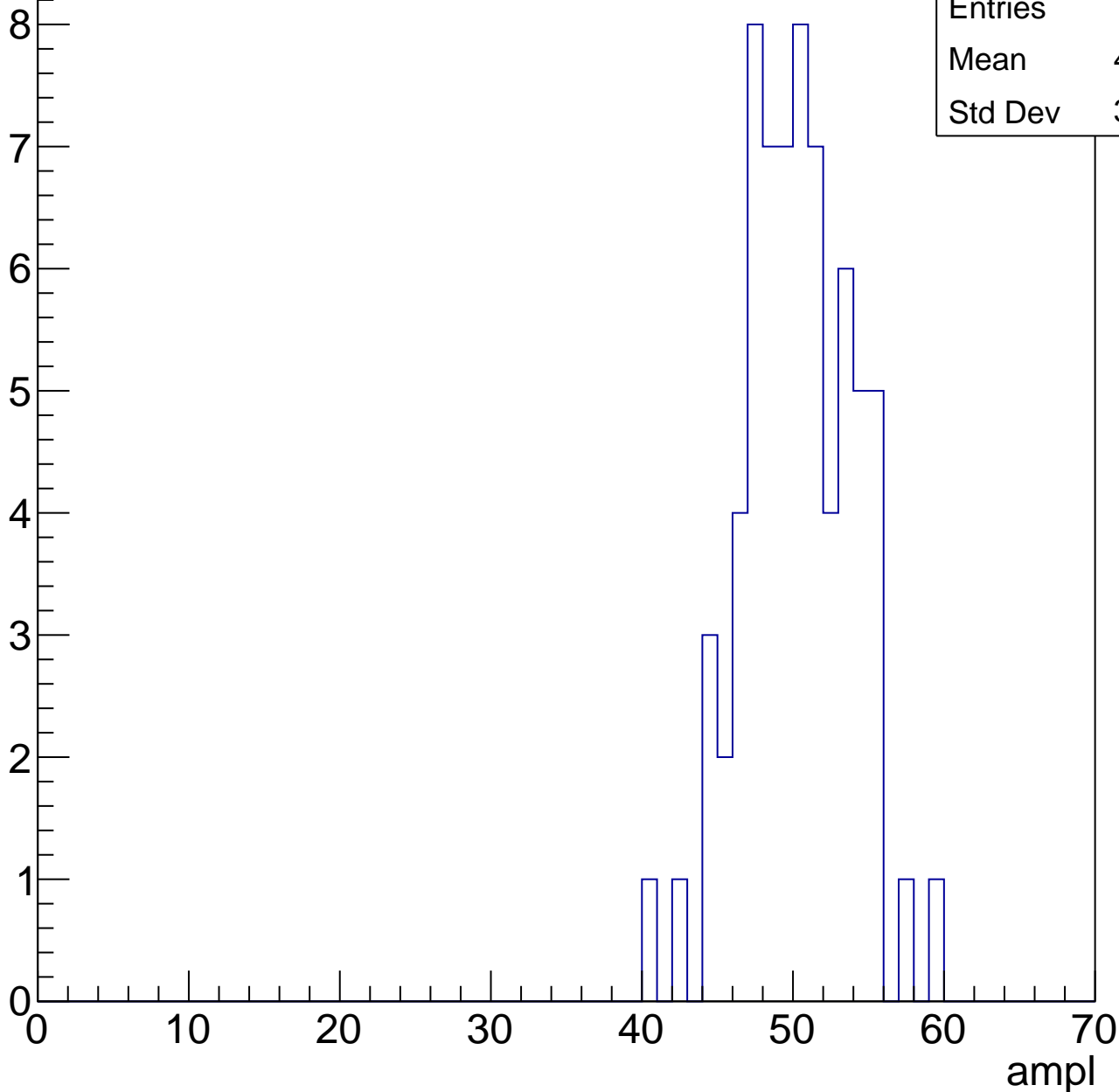


B1L102S, U19-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.81
Std Dev	3.591

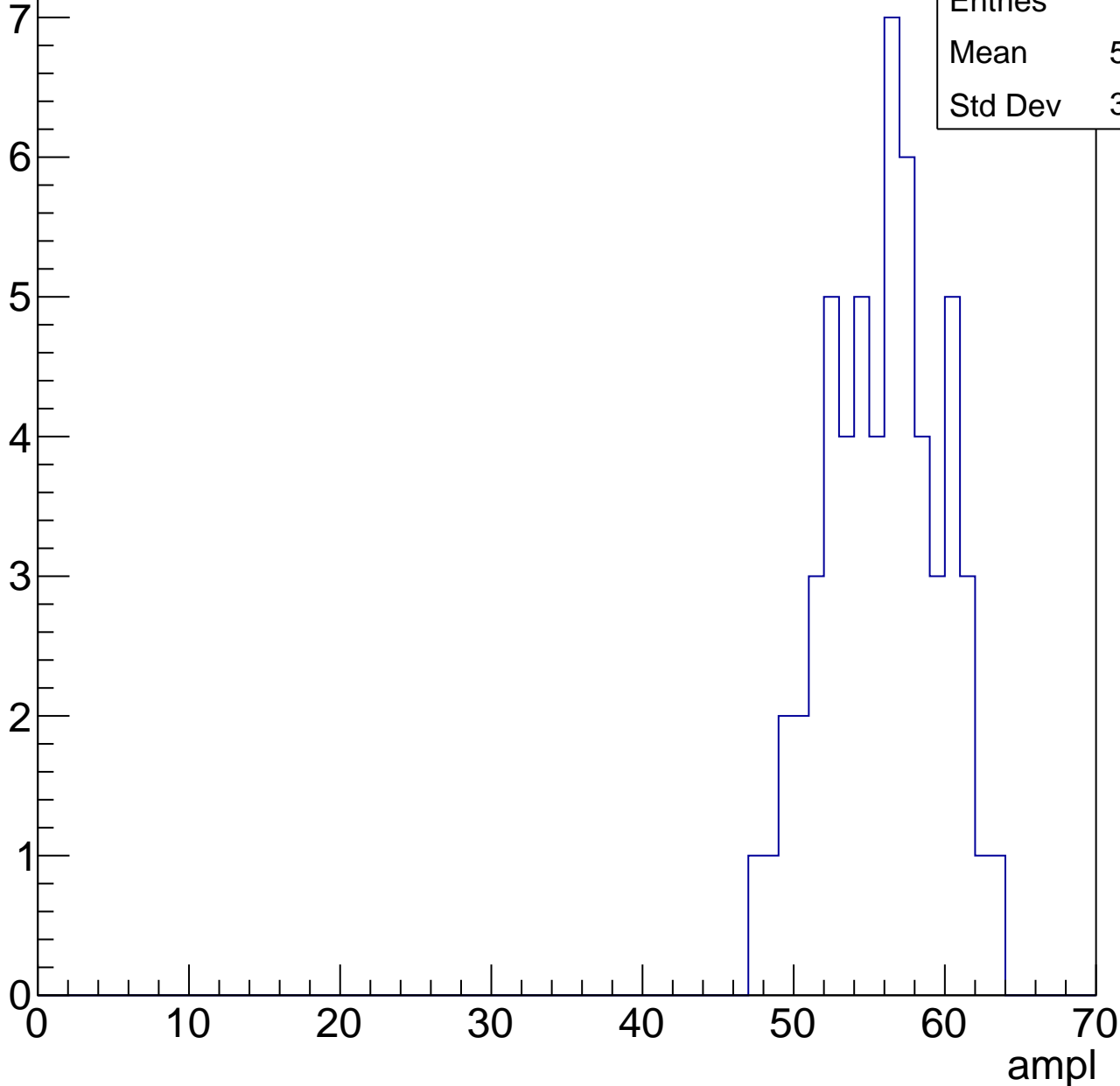


B1L102S, U19-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.42
Std Dev	3.746

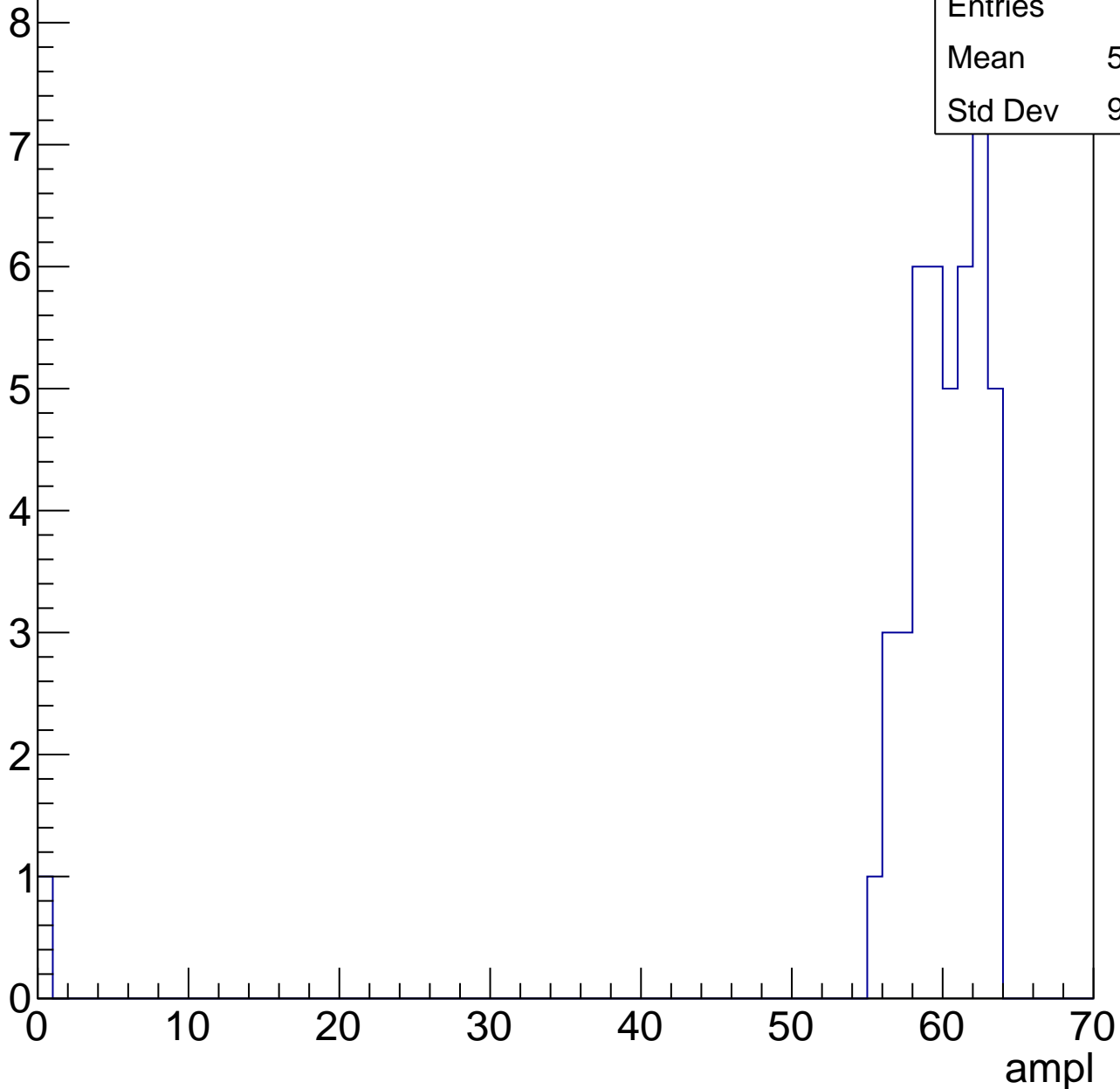


B1L102S, U19-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.48
Std Dev	9.184



B1L102S, U19-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	62.5
Std Dev	0.7638

B1L102S, U19-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch124, adc0

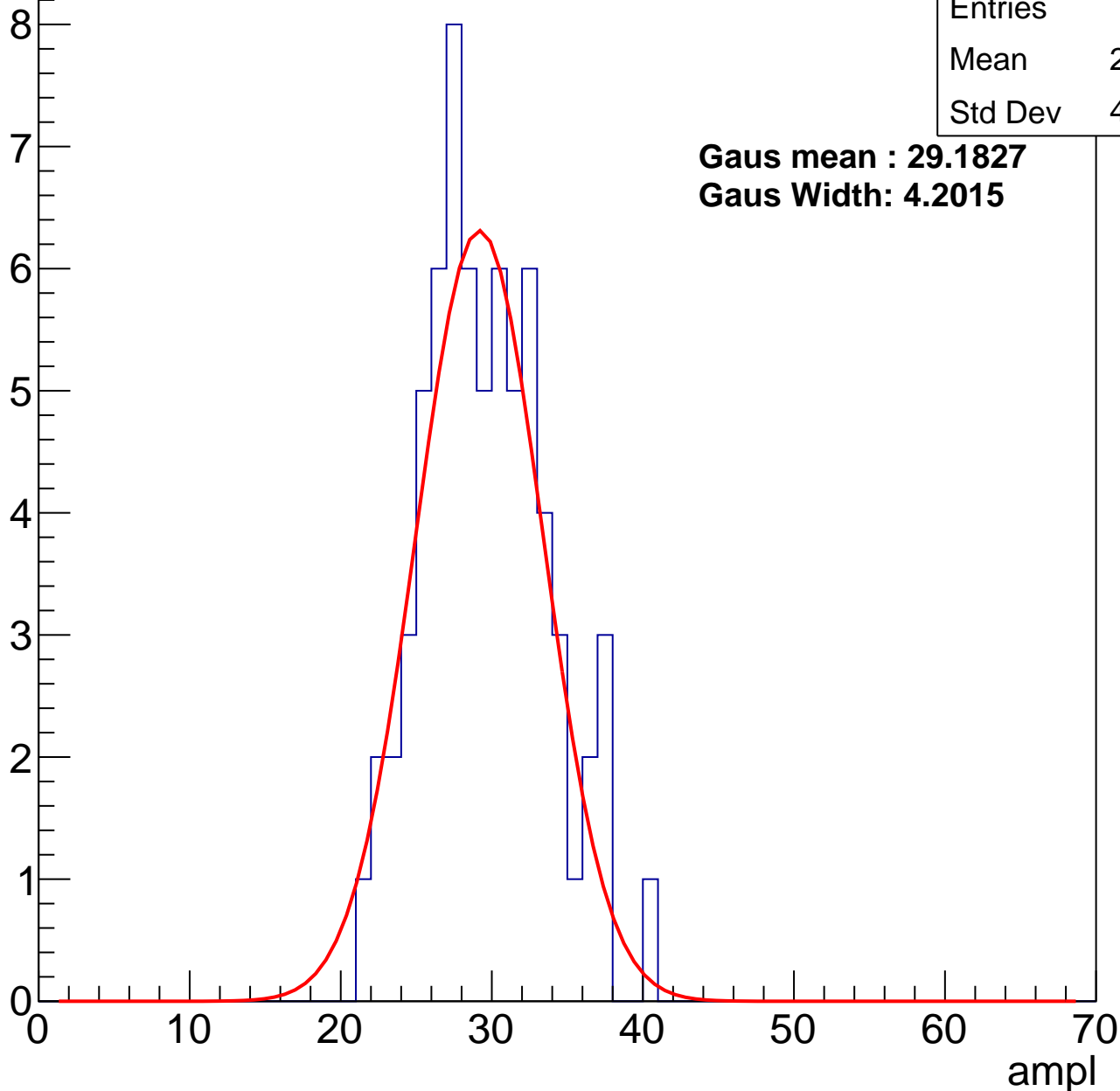
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.16
Std Dev	4.074

Gaus mean : 29.1827

Gaus Width: 4.2015



B1L102S, U19-ch124, adc1

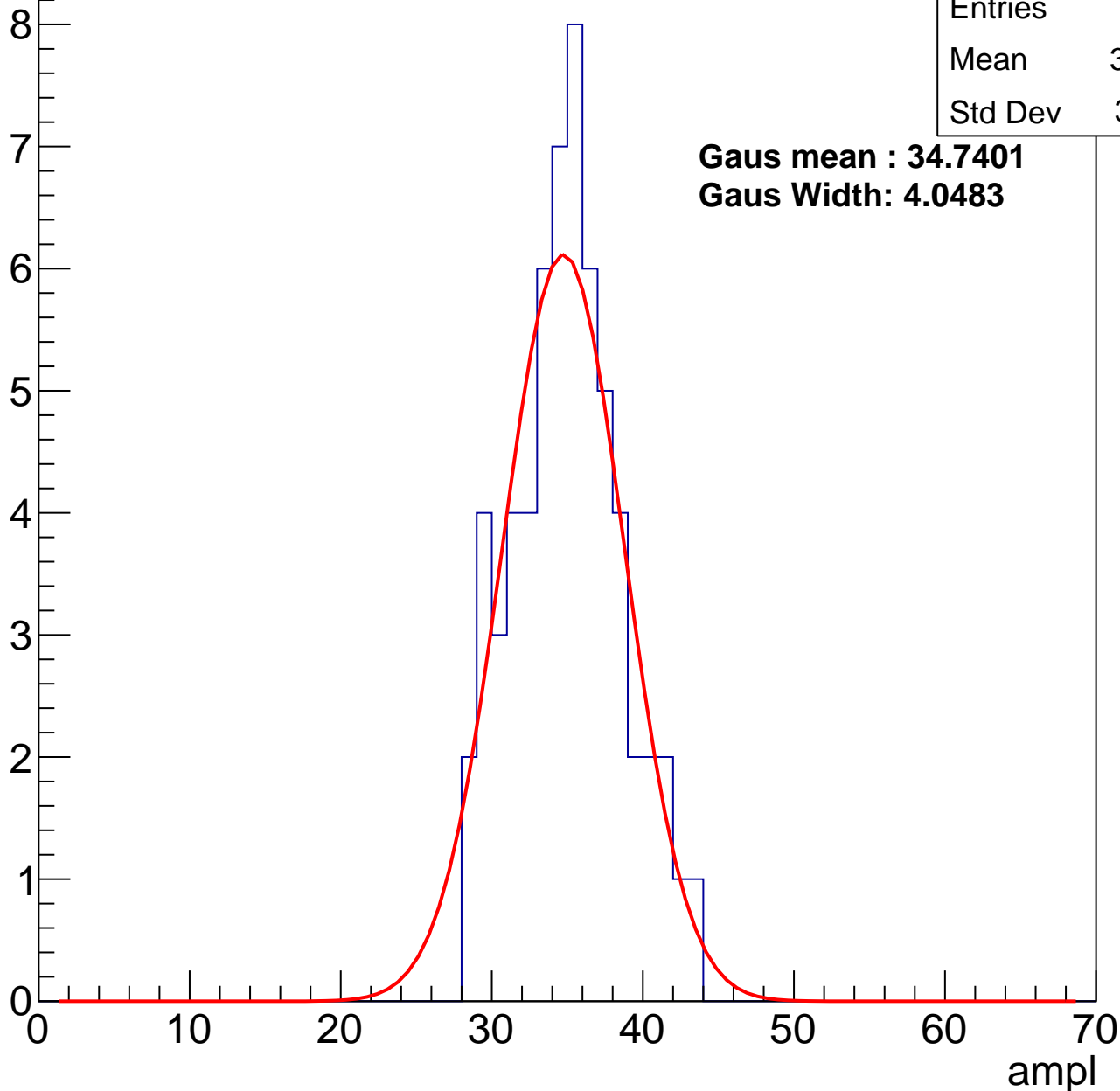
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.56
Std Dev	3.541

Gaus mean : 34.7401

Gaus Width: 4.0483



B1L102S, U19-ch124, adc2

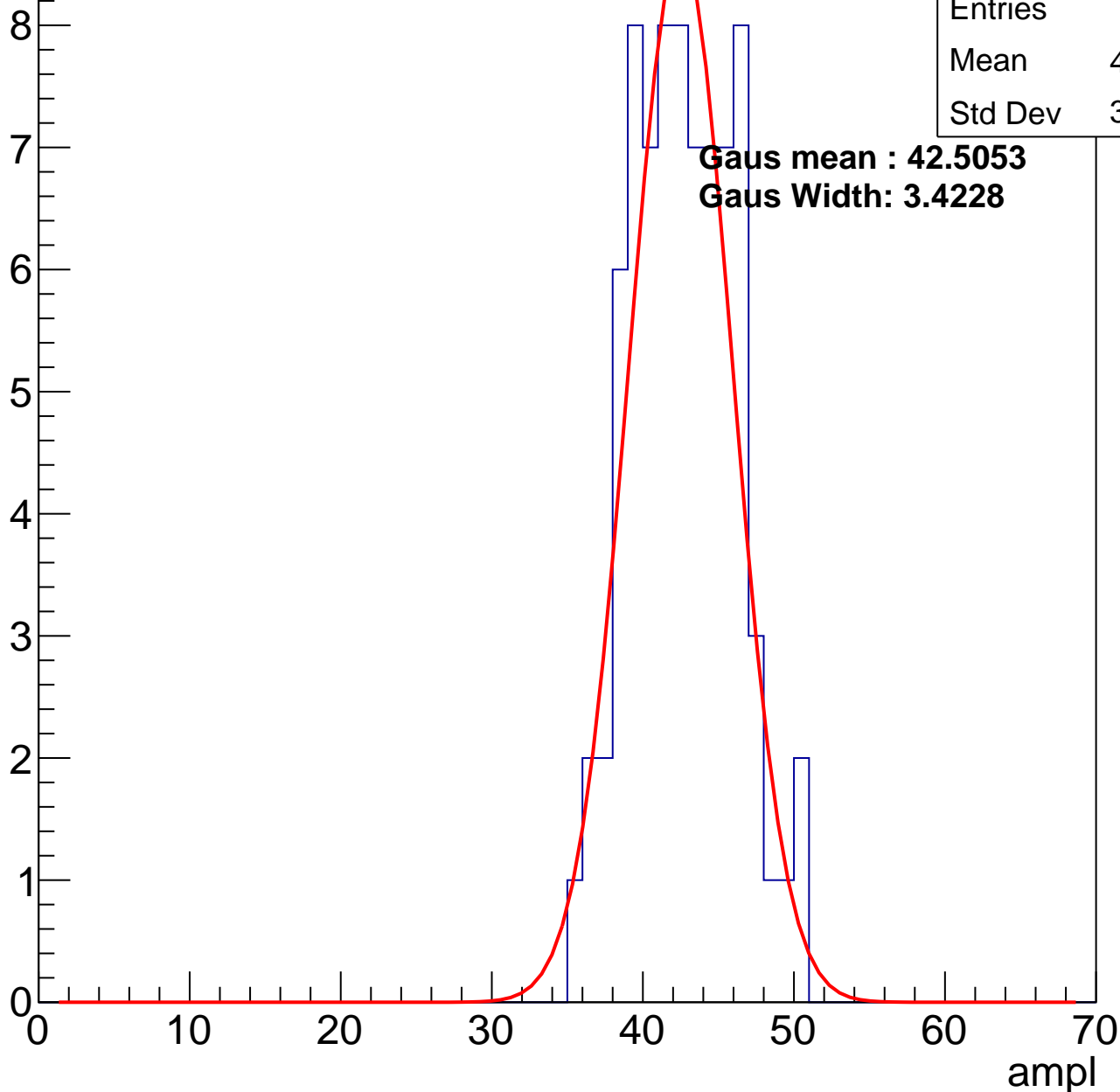
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.24
Std Dev	3.367

Gaus mean : 42.5053

Gaus Width: 3.4228

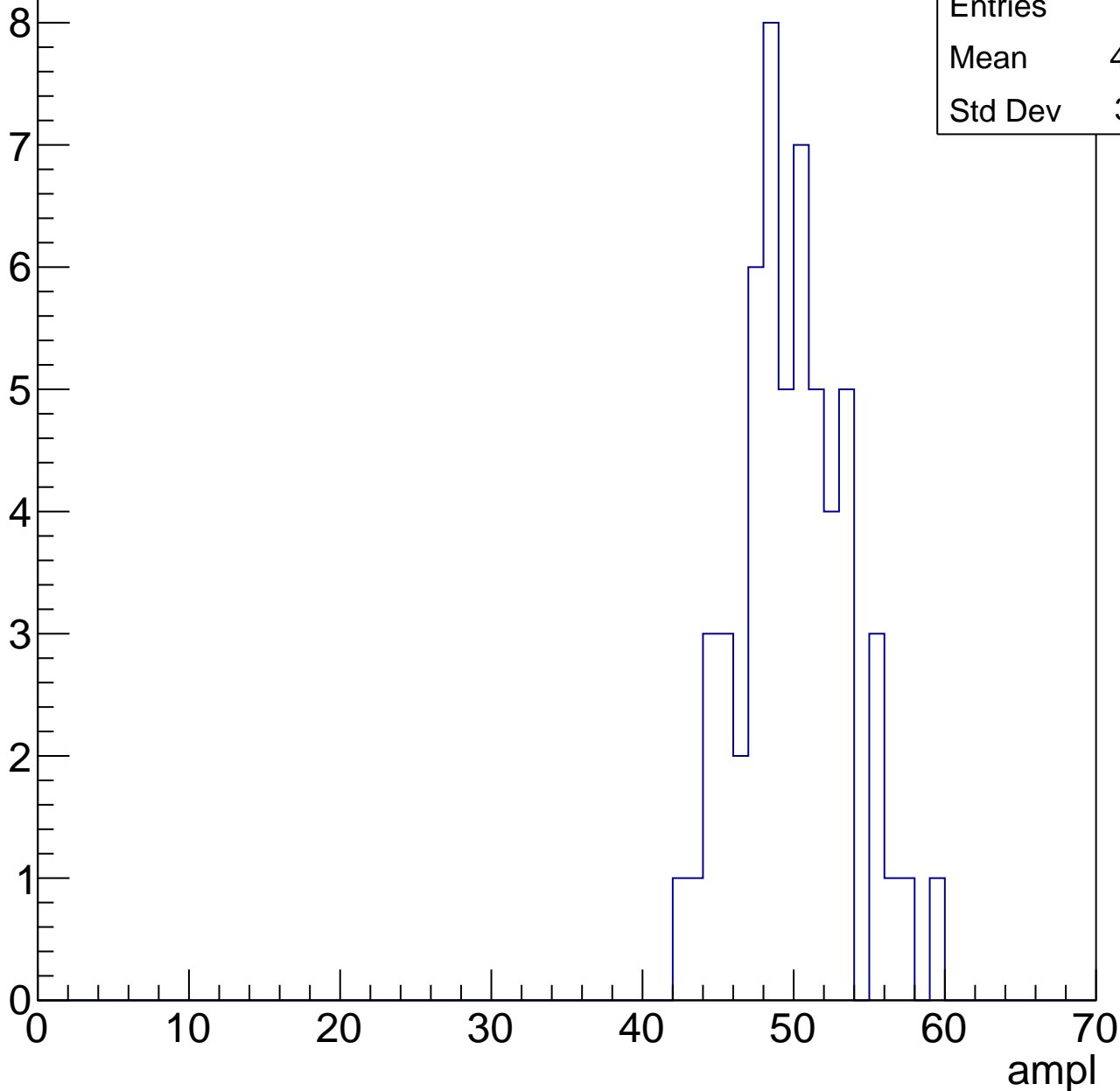


B1L102S, U19-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.46
Std Dev	3.561

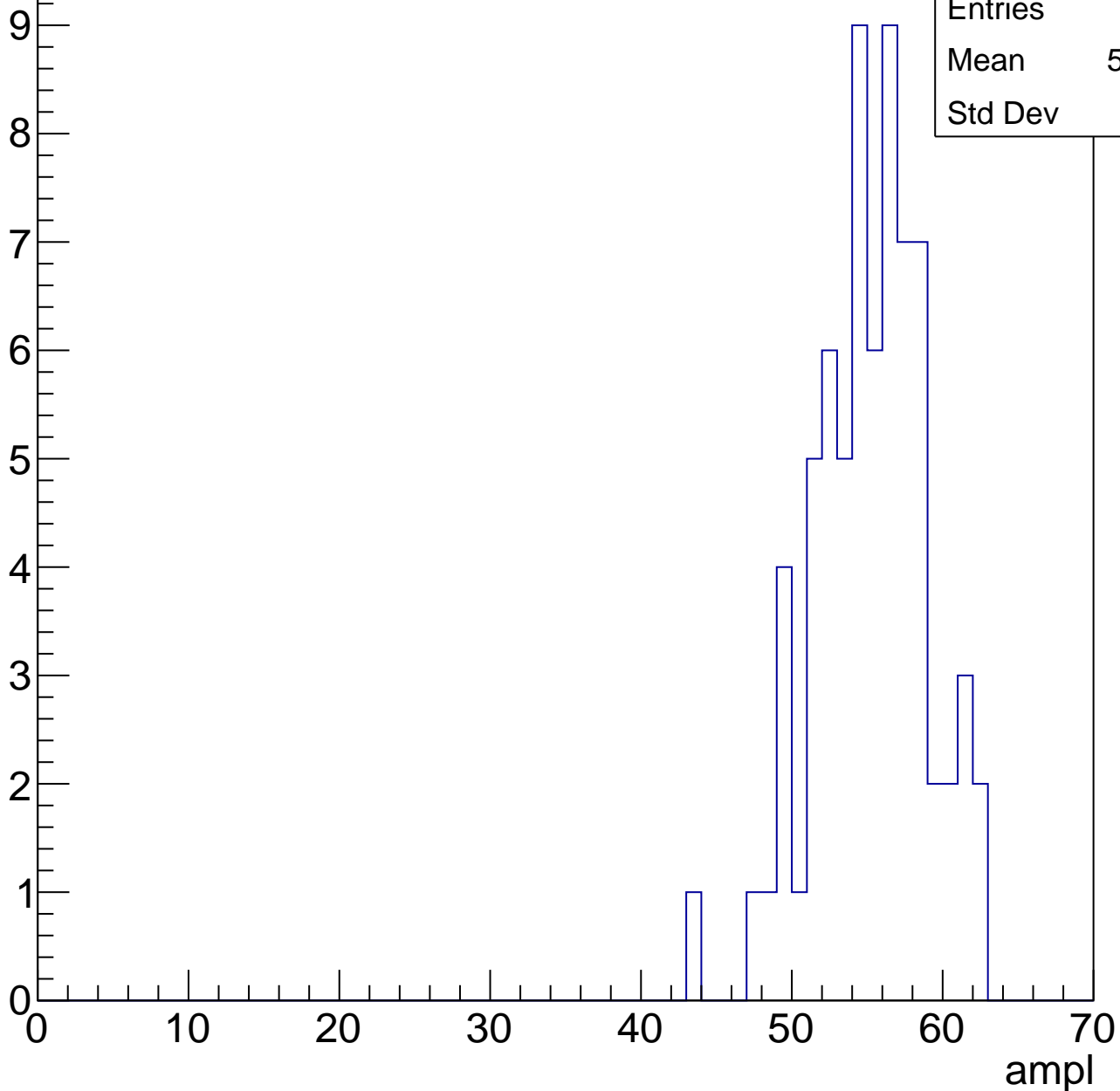


B1L102S, U19-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.73
Std Dev	3.7

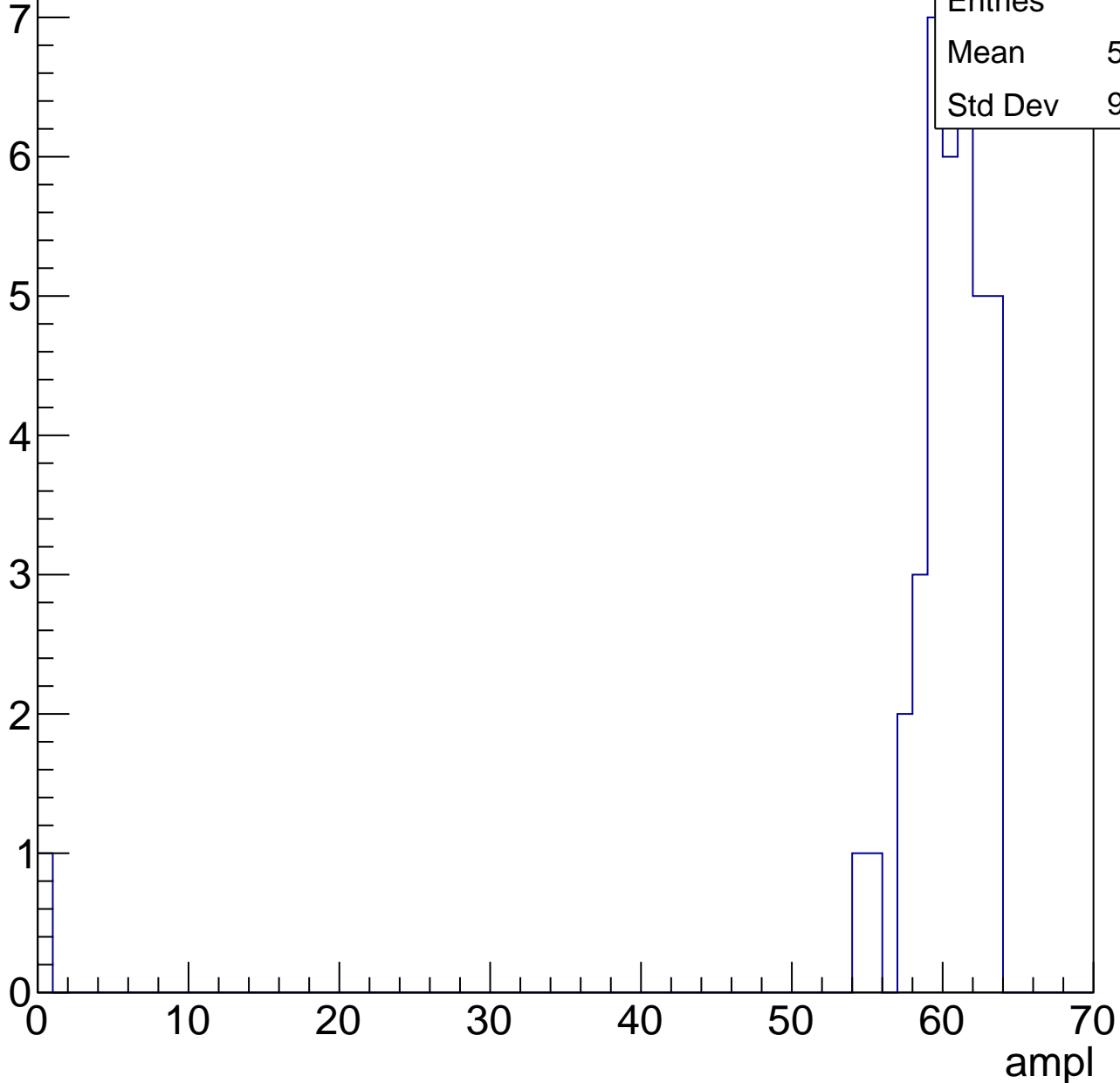


B1L102S, U19-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.47
Std Dev	9.843

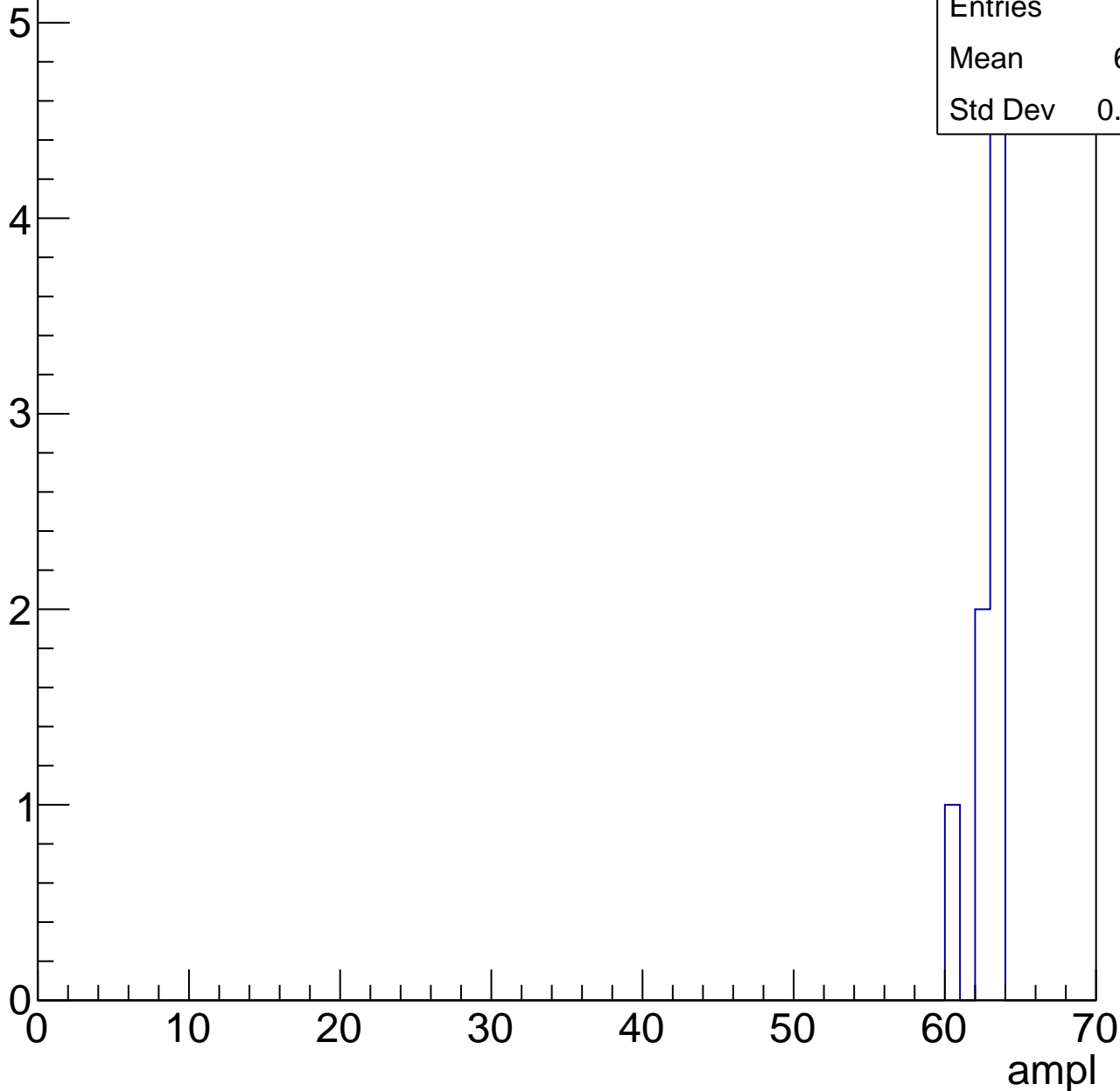


B1L102S, U19-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

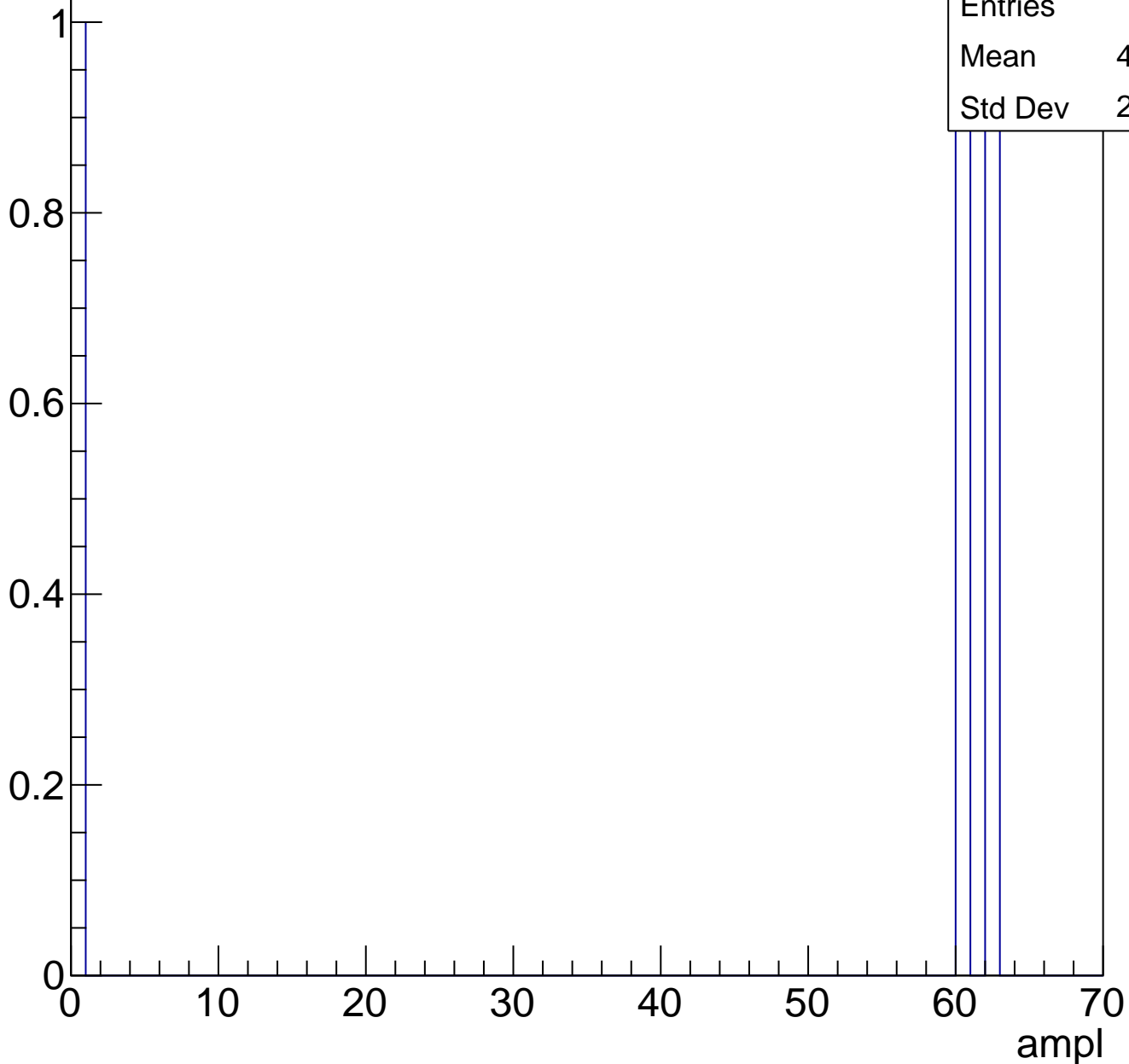
Entries	8
Mean	62.38
Std Dev	0.9922



B1L102S, U19-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch125, adc0

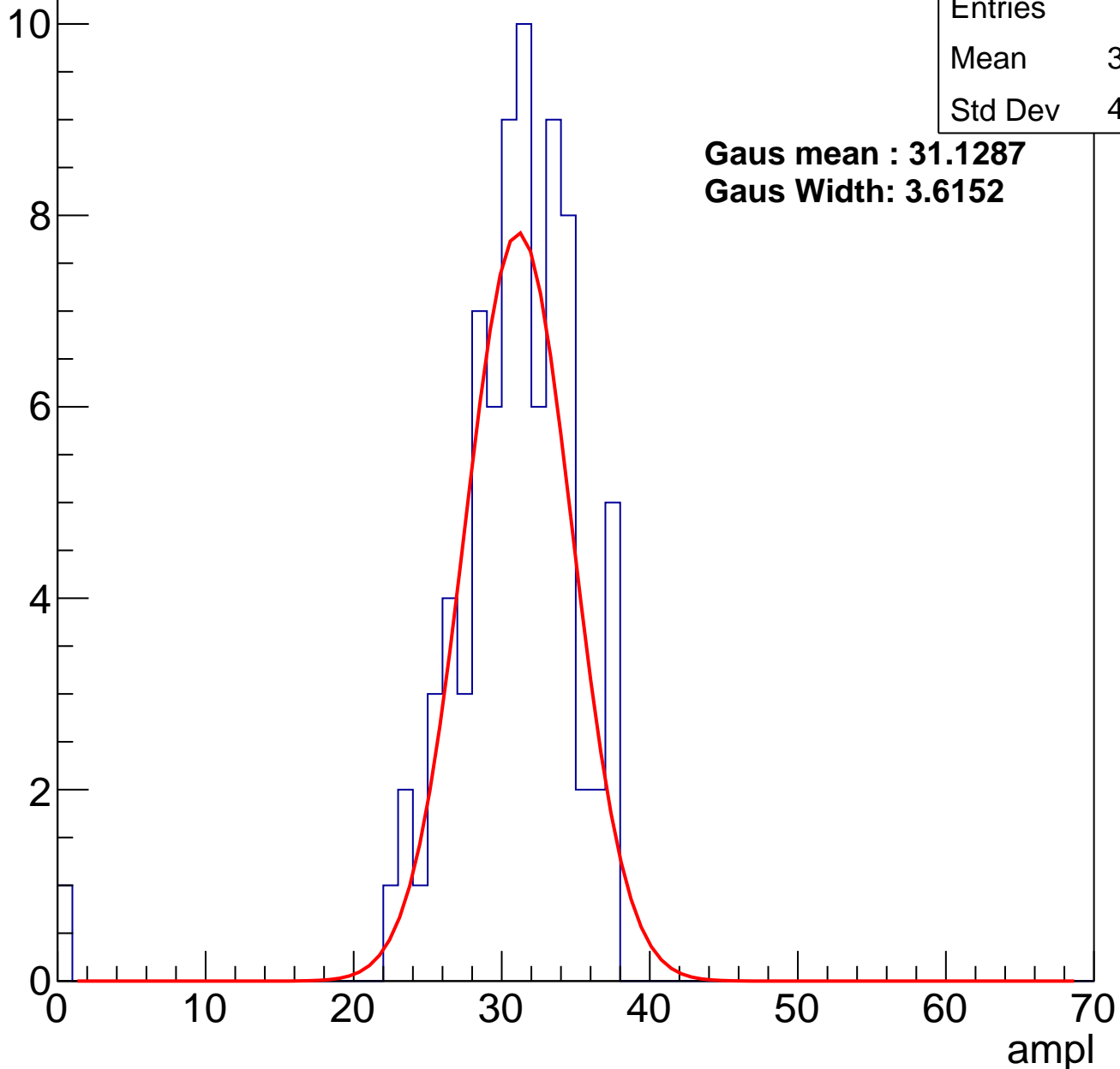
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	30.25
Std Dev	4.908

Gaus mean : 31.1287

Gaus Width: 3.6152

Entry



B1L102S, U19-ch125, adc1

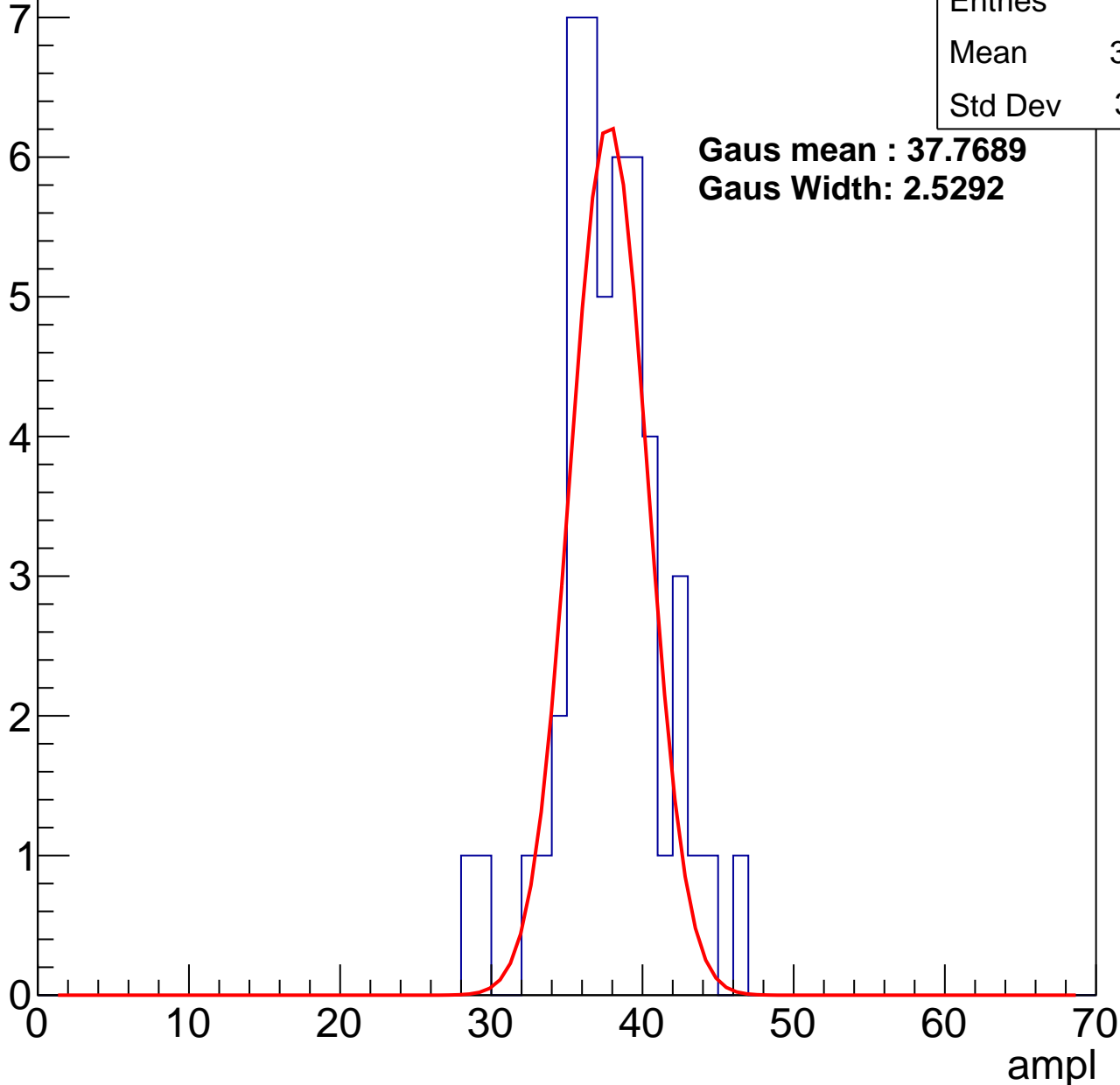
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	37.38
Std Dev	3.401

Gaus mean : 37.7689

Gaus Width: 2.5292



B1L102S, U19-ch125, adc2

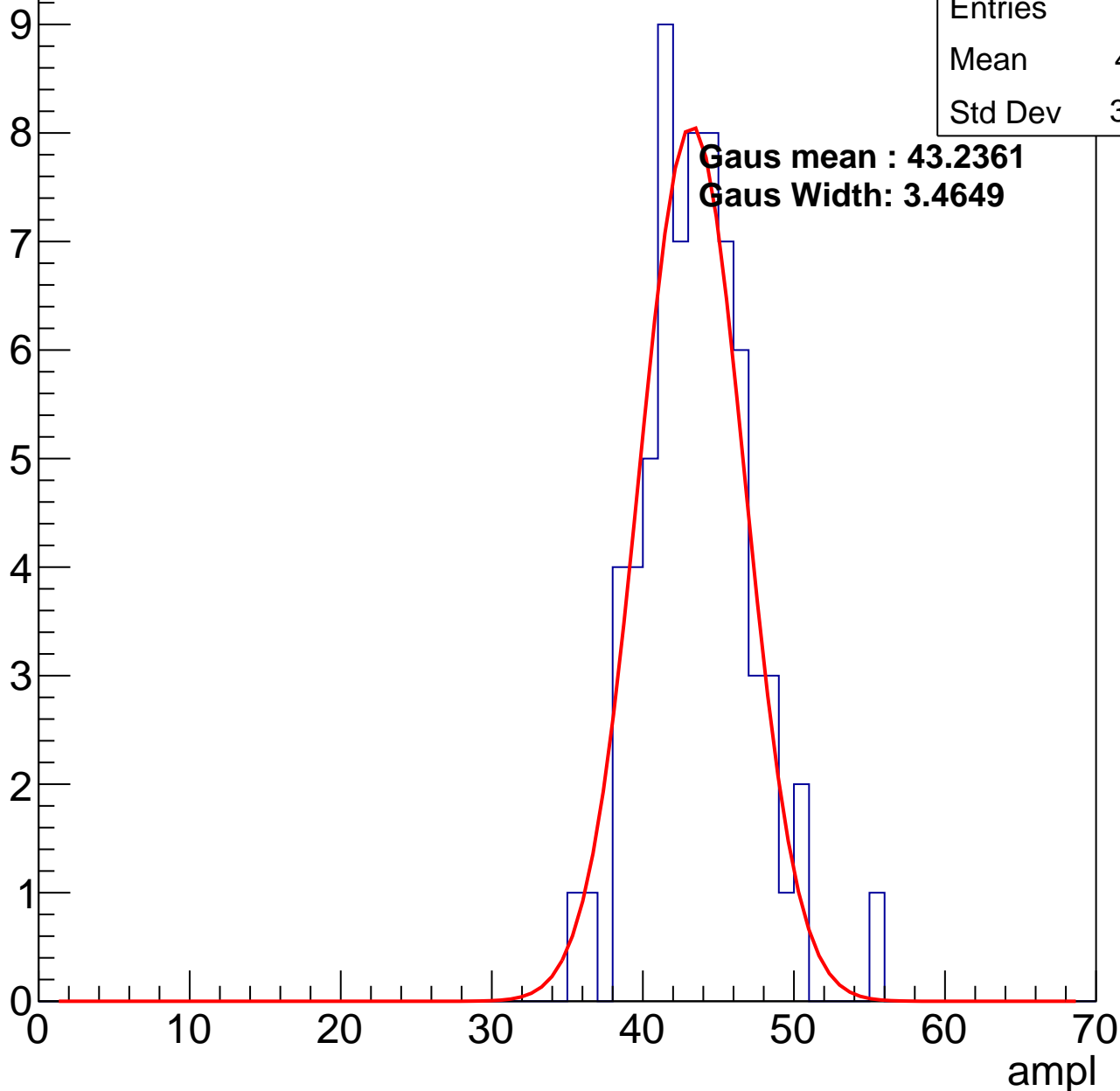
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.11
Std Dev	3.495

Gaus mean : 43.2361

Gaus Width: 3.4649

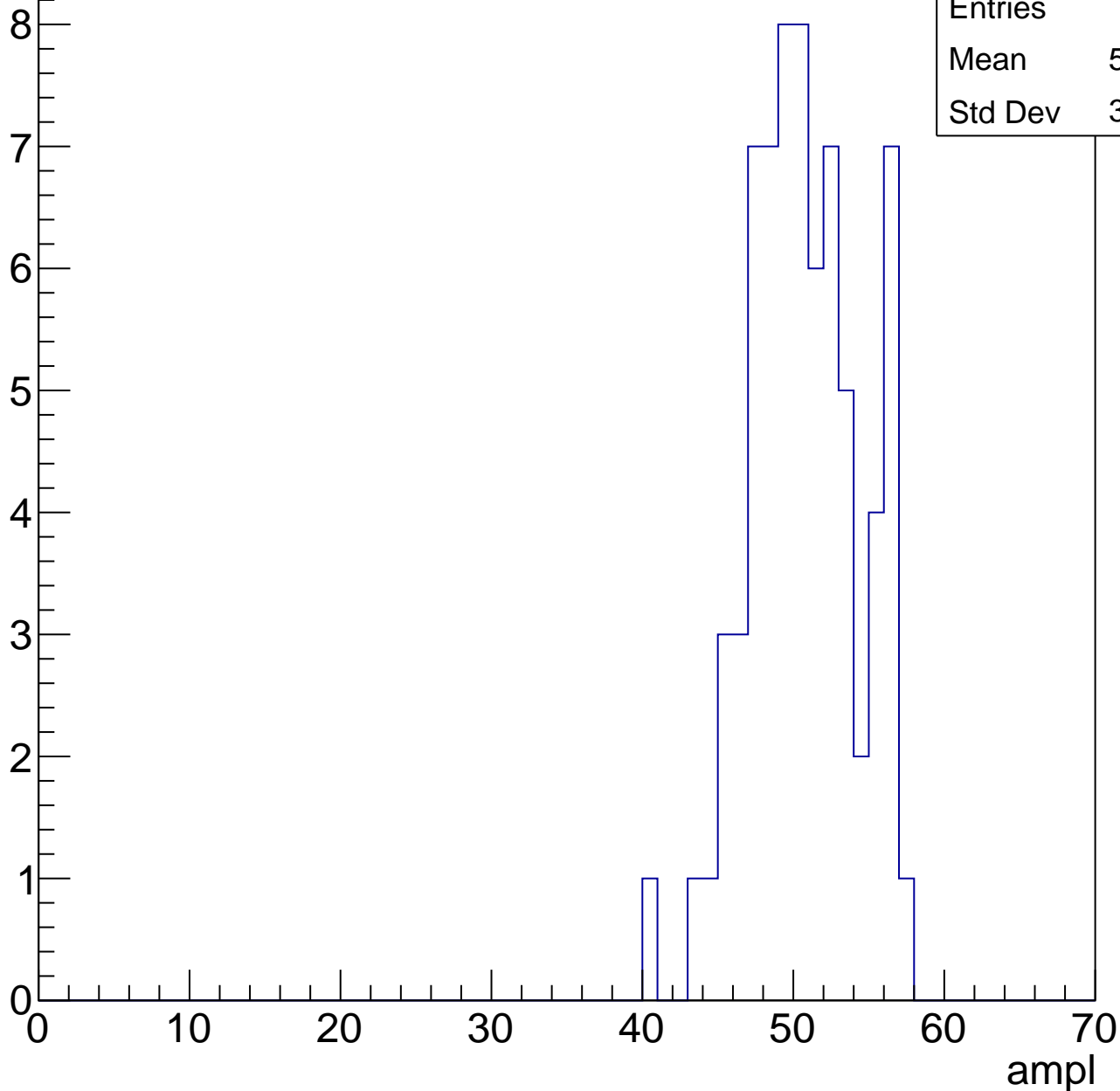


B1L102S, U19-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.27
Std Dev	3.588

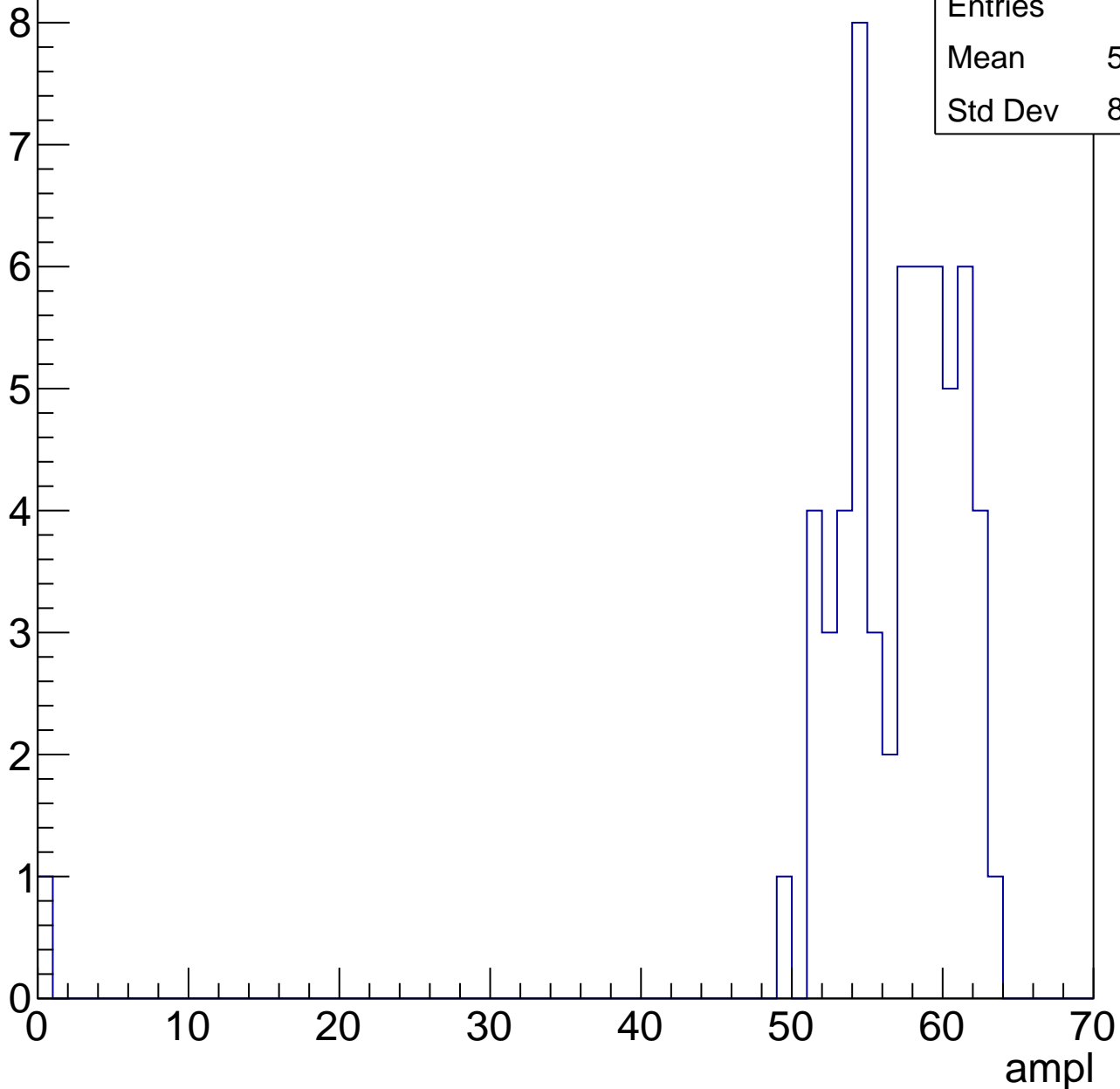


B1L102S, U19-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.85
Std Dev	8.072

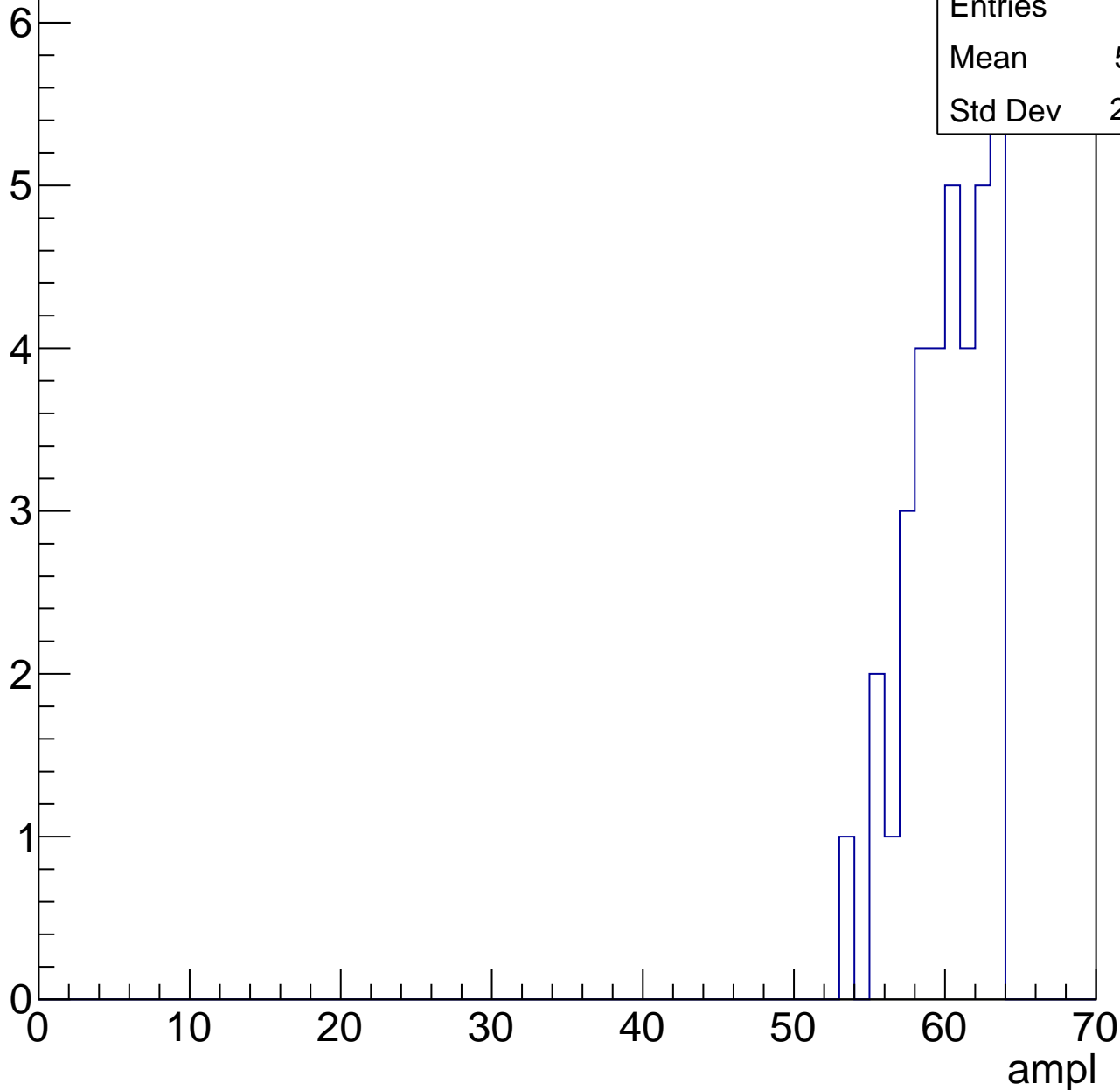


B1L102S, U19-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.71
Std Dev	2.603



B1L102S, U19-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U19-ch126, adc0

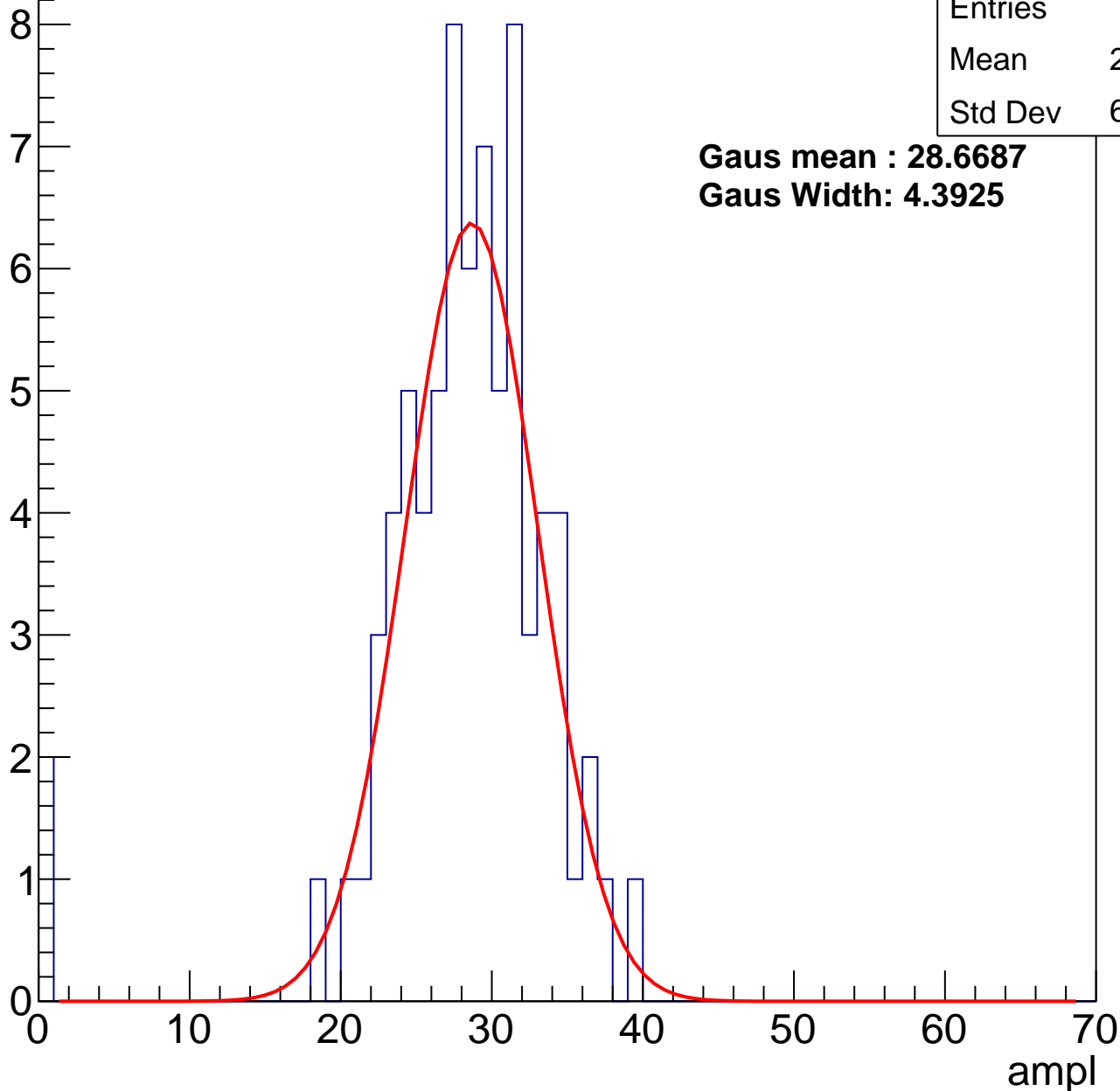
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.62
Std Dev	6.169

Gaus mean : 28.6687

Gaus Width: 4.3925



B1L102S, U19-ch126, adc1

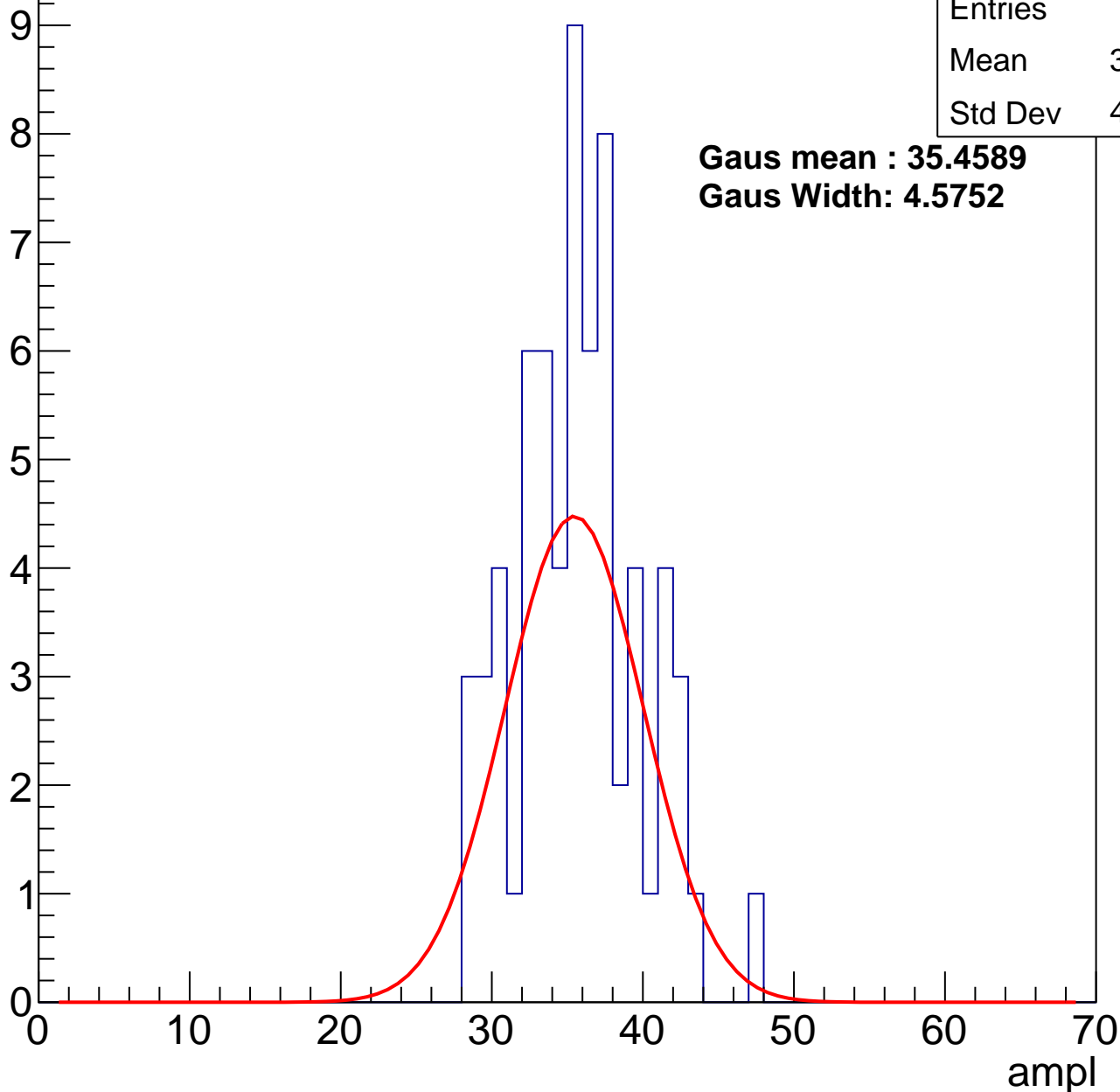
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.26
Std Dev	4.069

Gaus mean : 35.4589

Gaus Width: 4.5752



B1L102S, U19-ch126, adc2

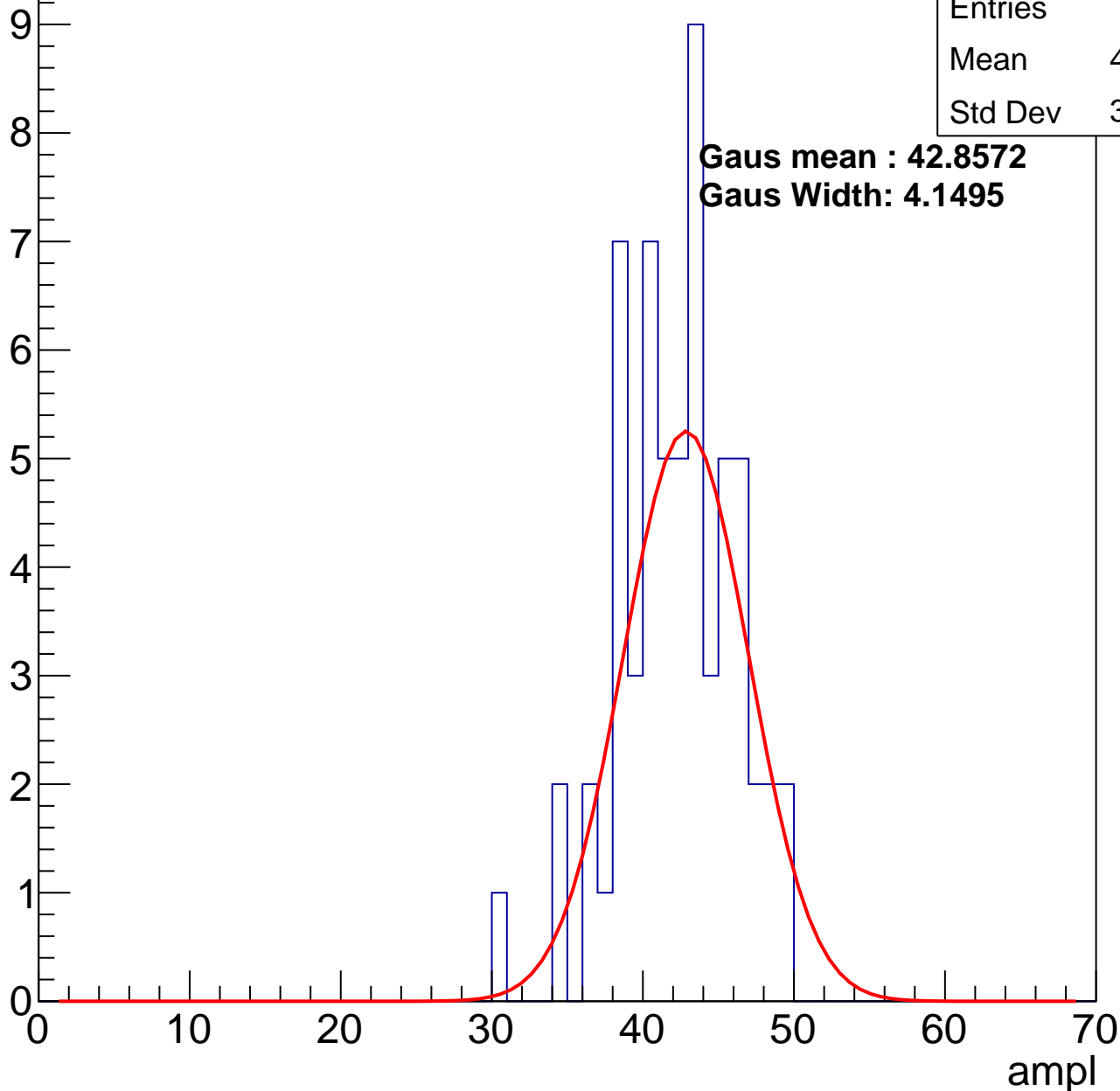
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.75
Std Dev	3.848

Gaus mean : 42.8572

Gaus Width: 4.1495

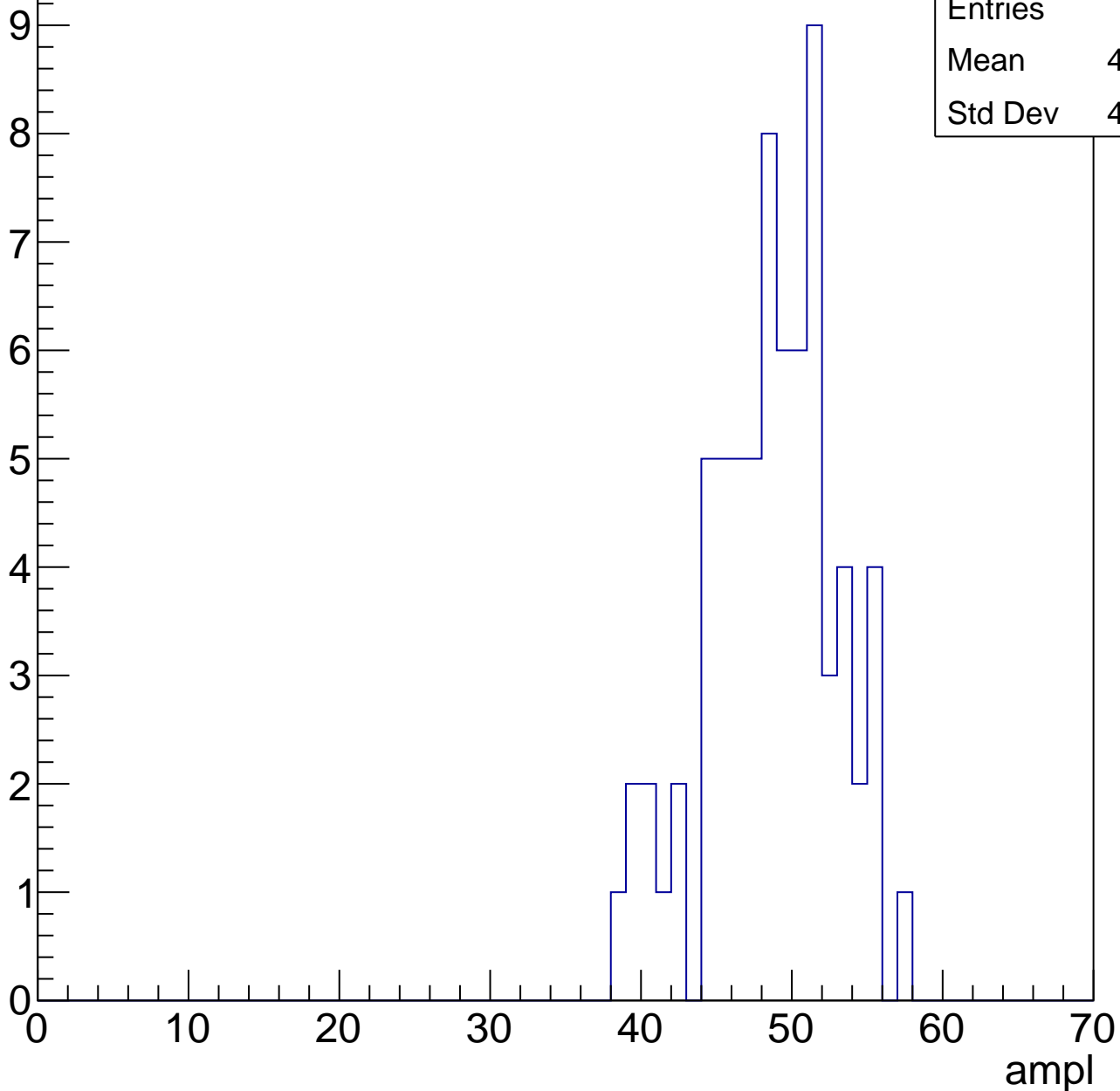


B1L102S, U19-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.18
Std Dev	4.224

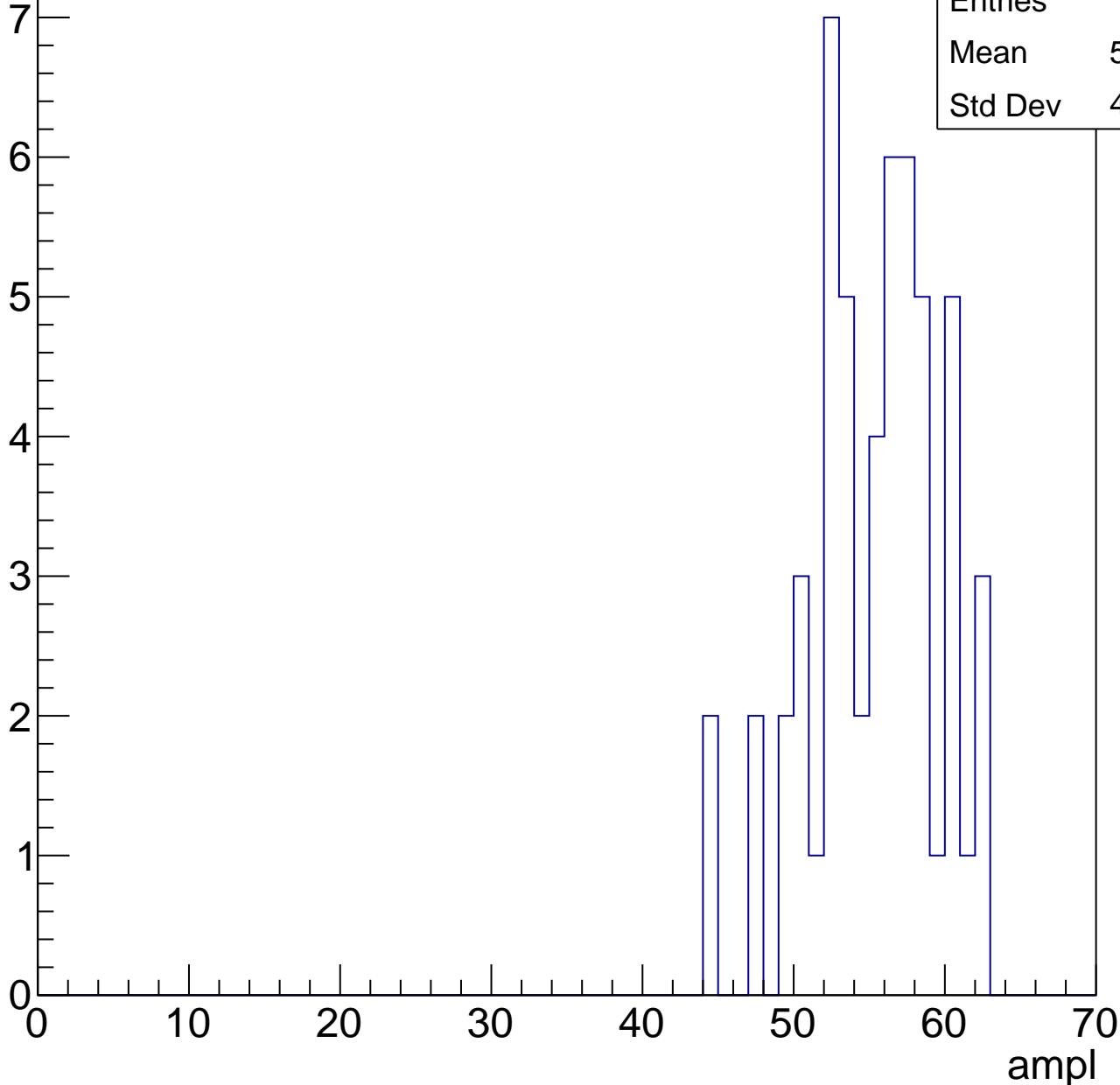


B1L102S, U19-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.76
Std Dev	4.294

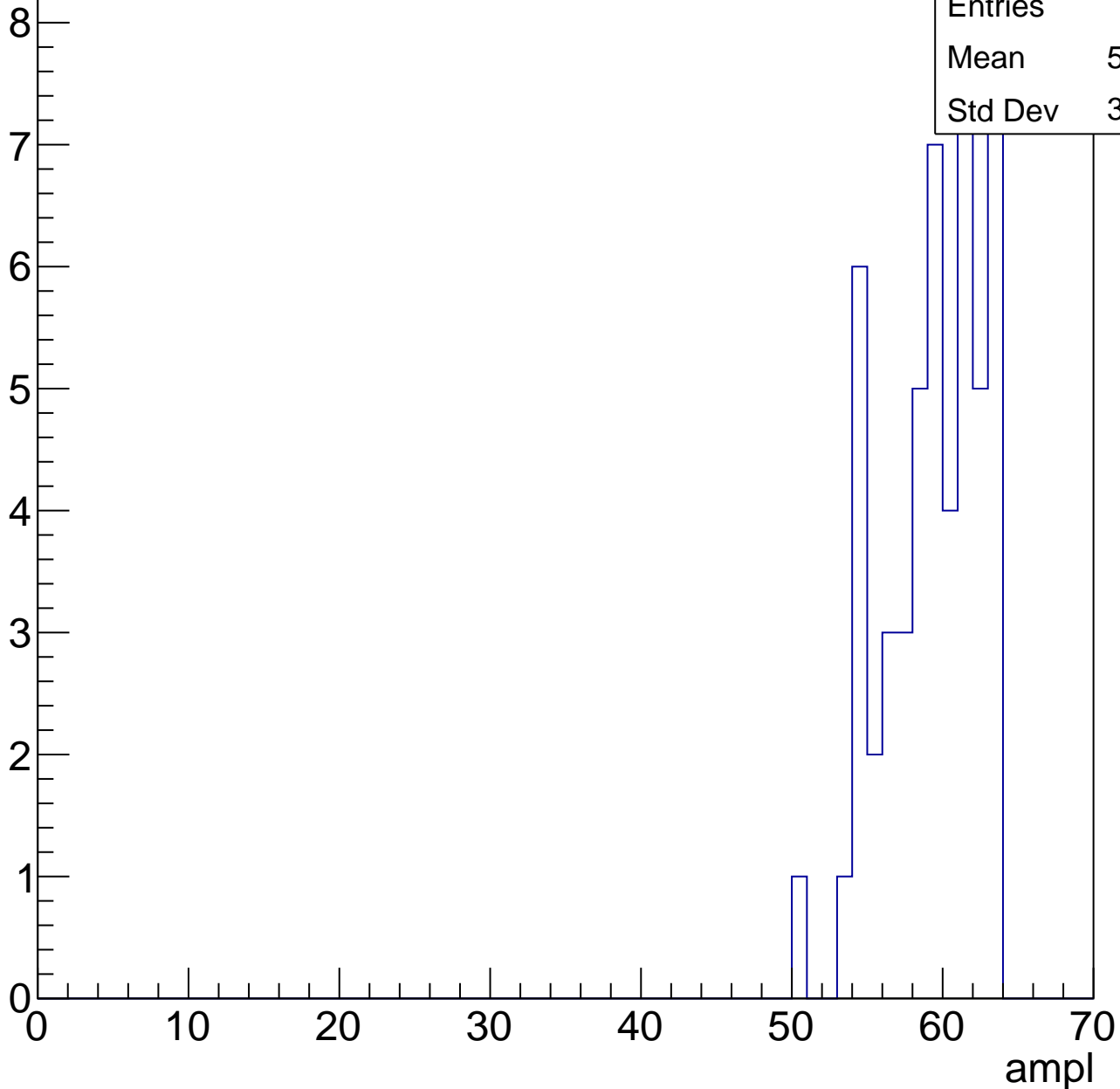


B1L102S, U19-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.89
Std Dev	3.225



B1L102S, U19-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	54.5
Std Dev	18.28

ampl

B1L102S, U19-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U19-ch127, adc0

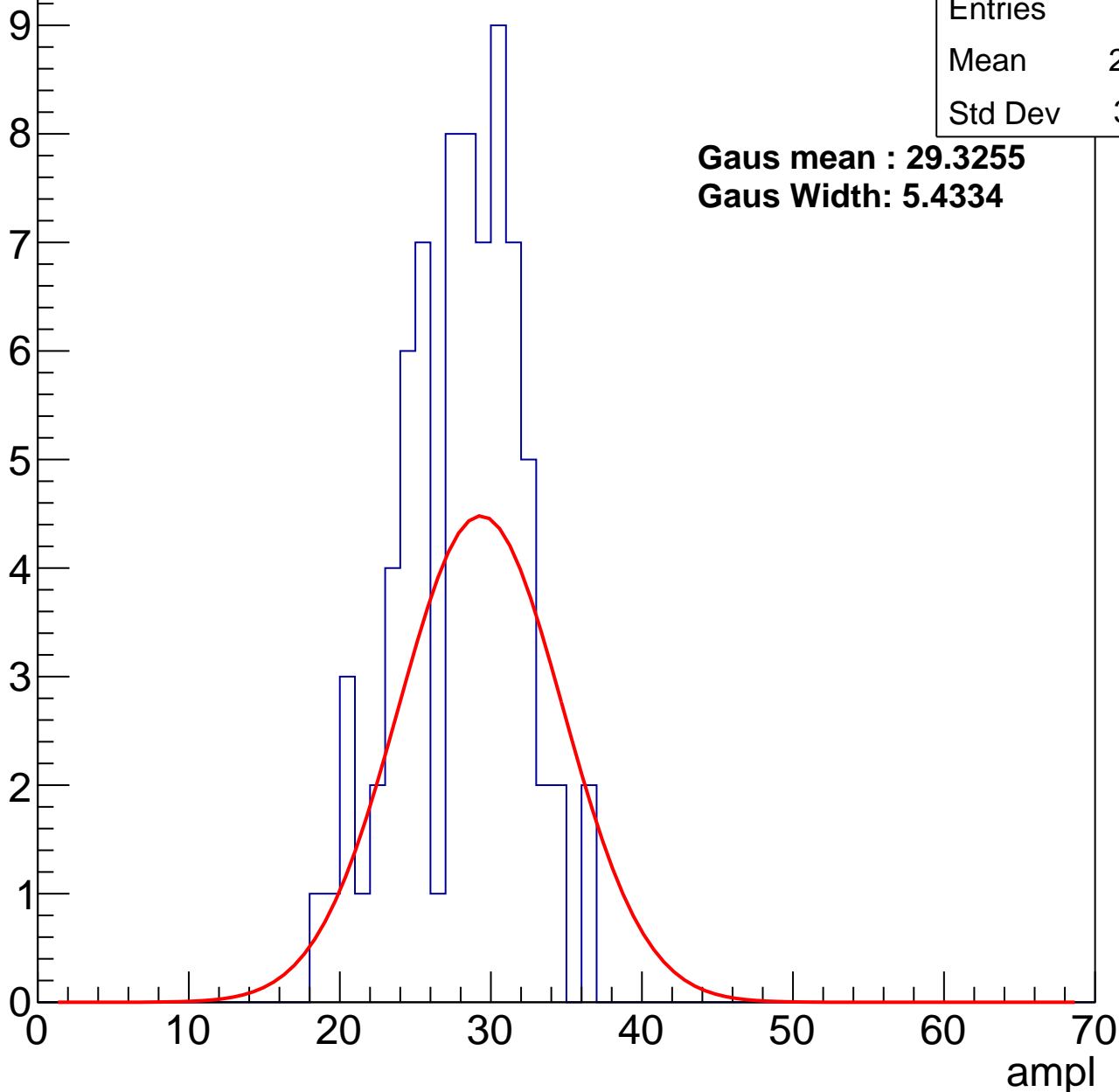
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.57
Std Dev	3.951

Gaus mean : 29.3255

Gaus Width: 5.4334



B1L102S, U19-ch127, adc1

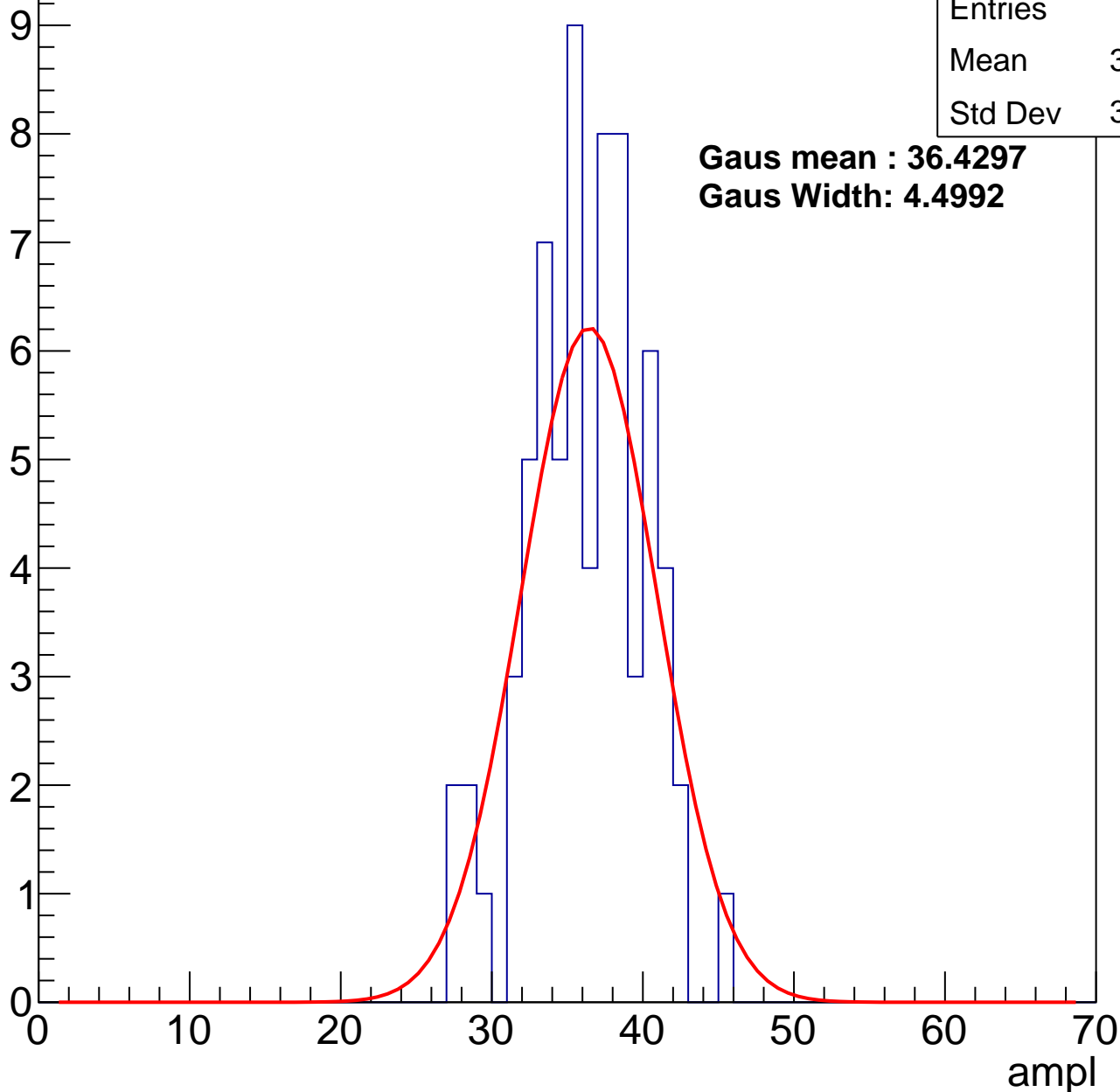
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.74
Std Dev	3.775

Gaus mean : 36.4297

Gaus Width: 4.4992



B1L102S, U19-ch127, adc2

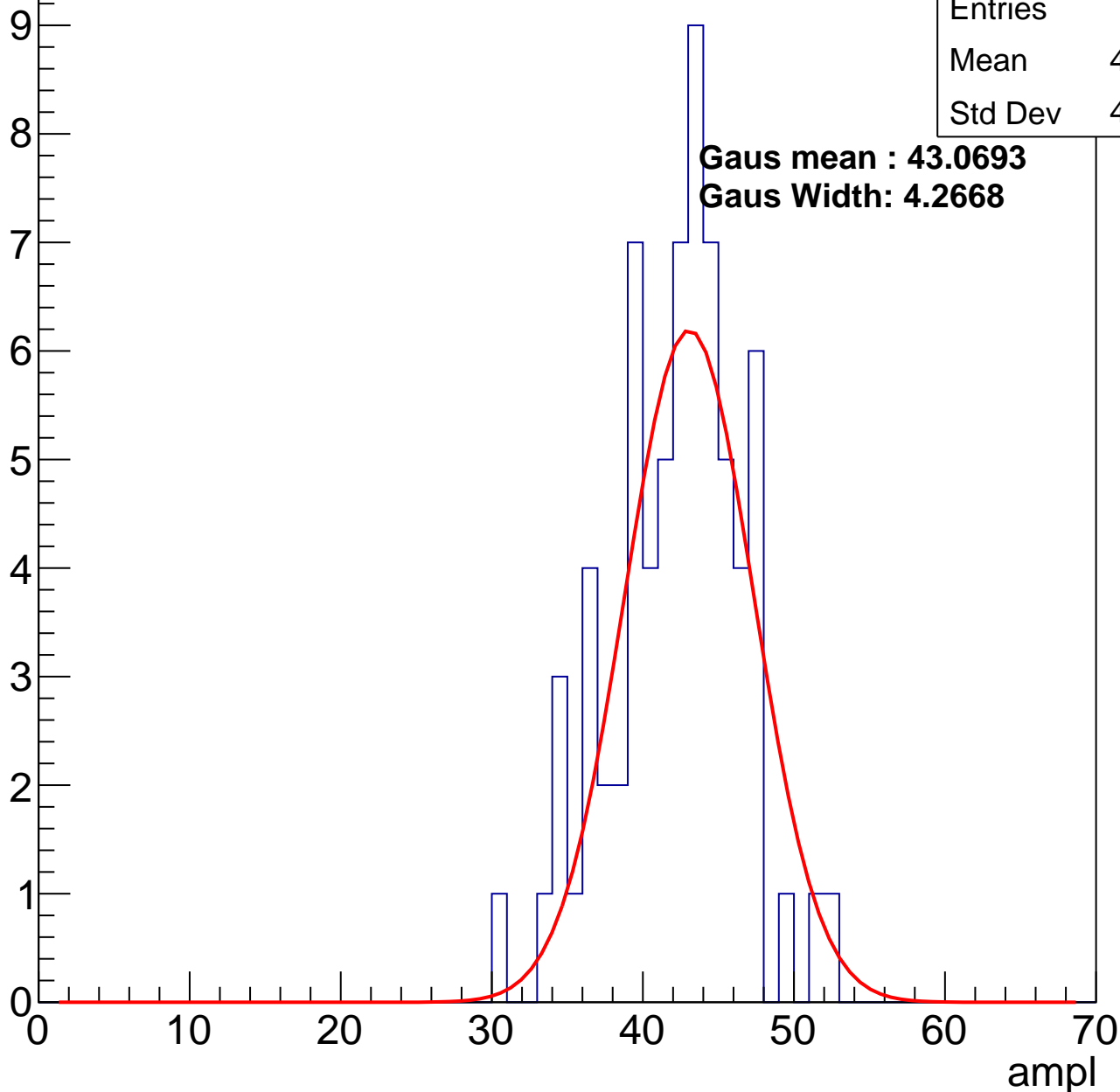
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.75
Std Dev	4.275

Gaus mean : 43.0693

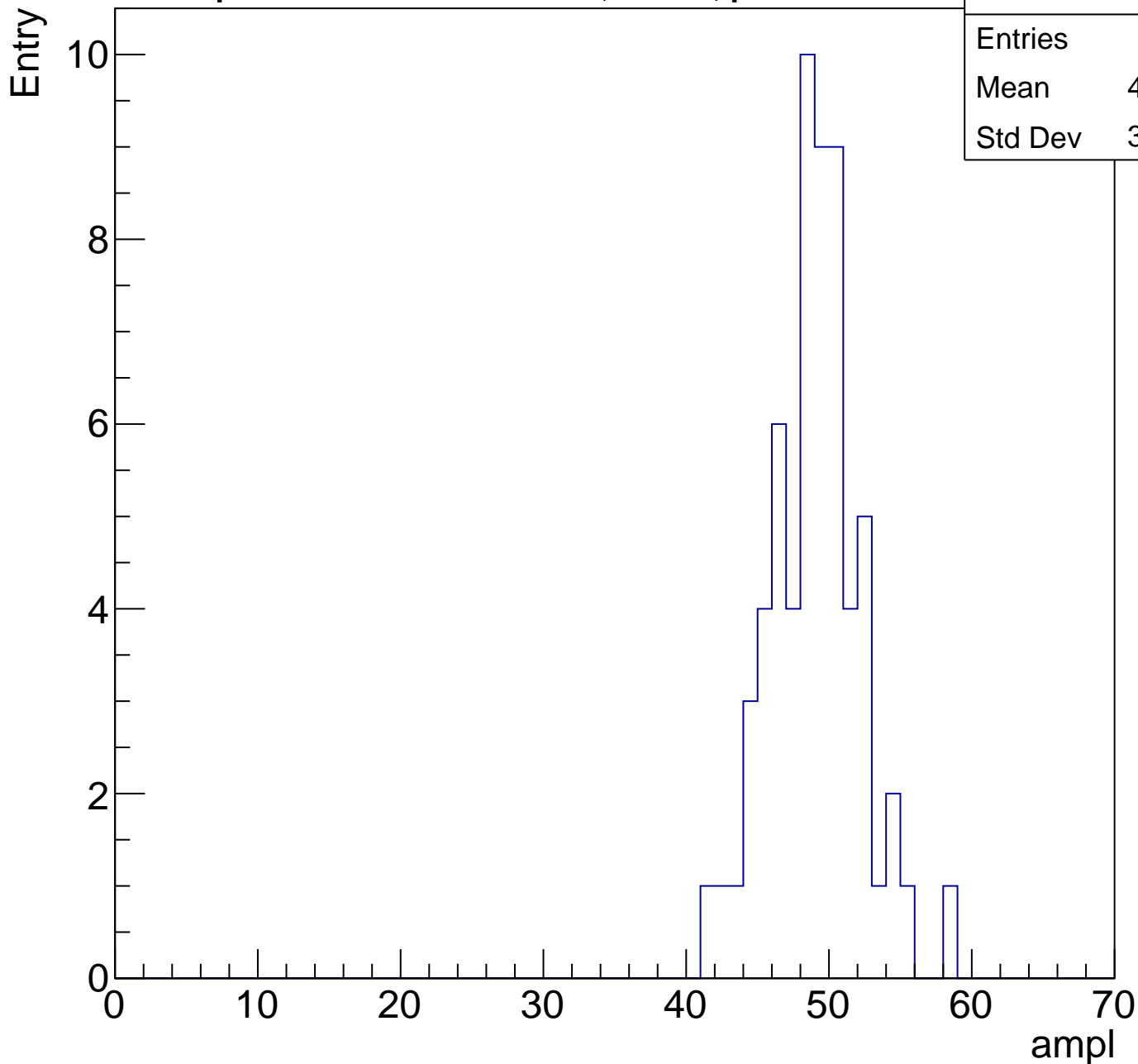
Gaus Width: 4.2668



B1L102S, U19-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	48.56
Std Dev	3.135

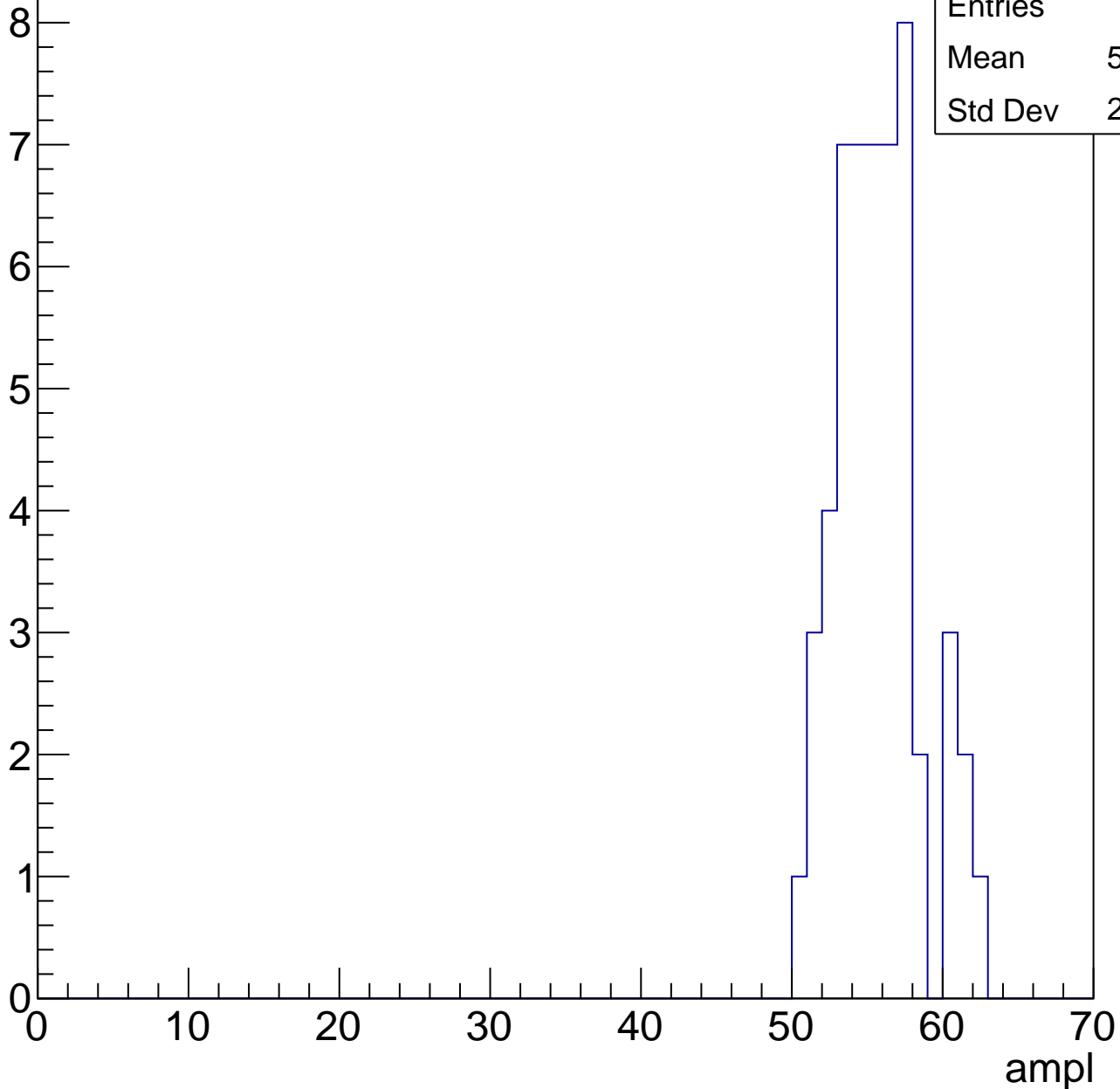


B1L102S, U19-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.25
Std Dev	2.752

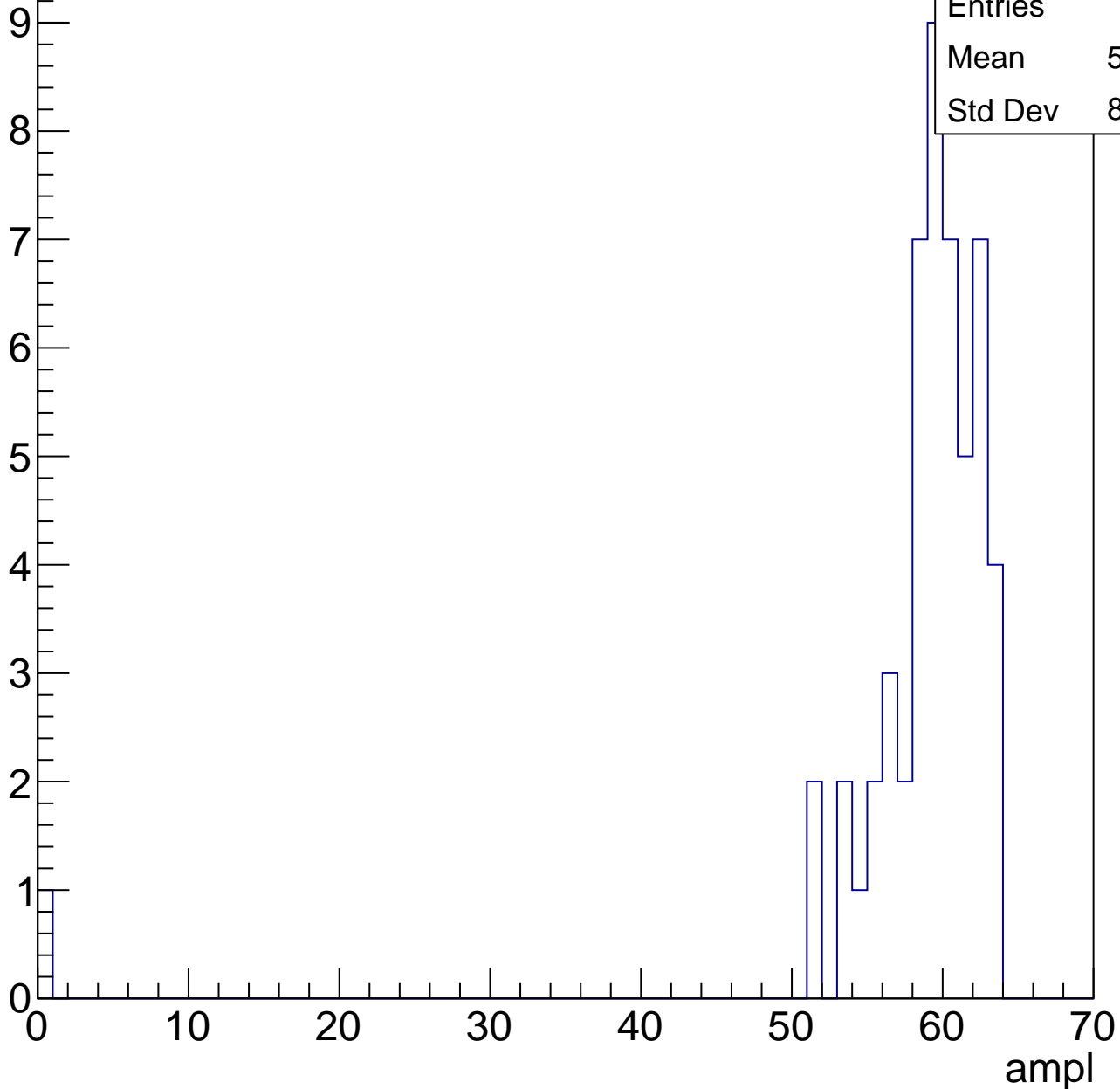


B1L102S, U19-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.73
Std Dev	8.605

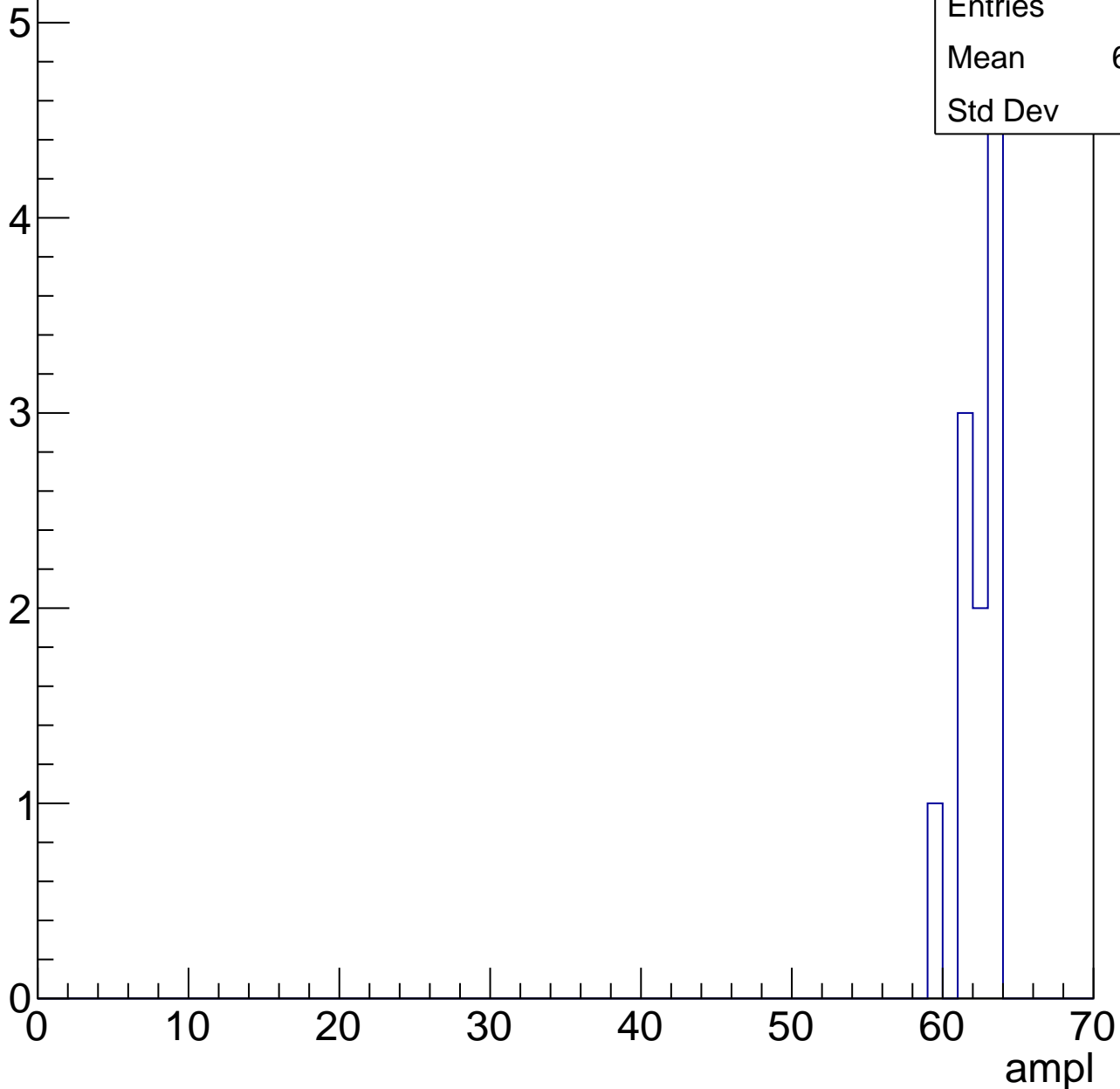


B1L102S, U19-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.91
Std Dev	1.24



B1L102S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

