

B1L000S, U21-ch0

calib_packv5_042523_0143.root, FC#3, port B3

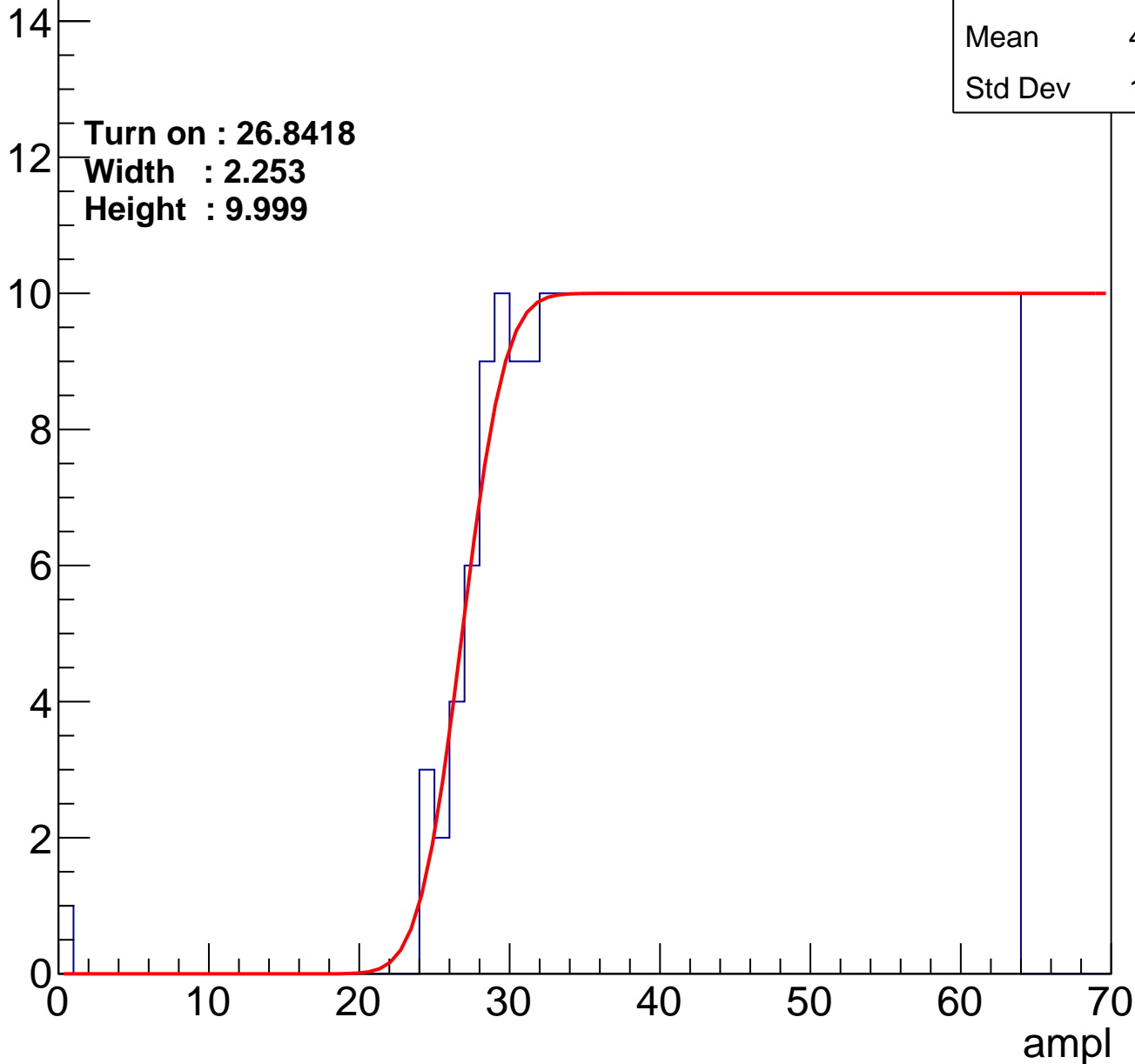
Entry

Entries	373
Mean	44.72
Std Dev	11.07

Turn on : 26.8418

Width : 2.253

Height : 9.999



B1L000S, U21-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.82
Std Dev	11.06

Turn on : 27.3998

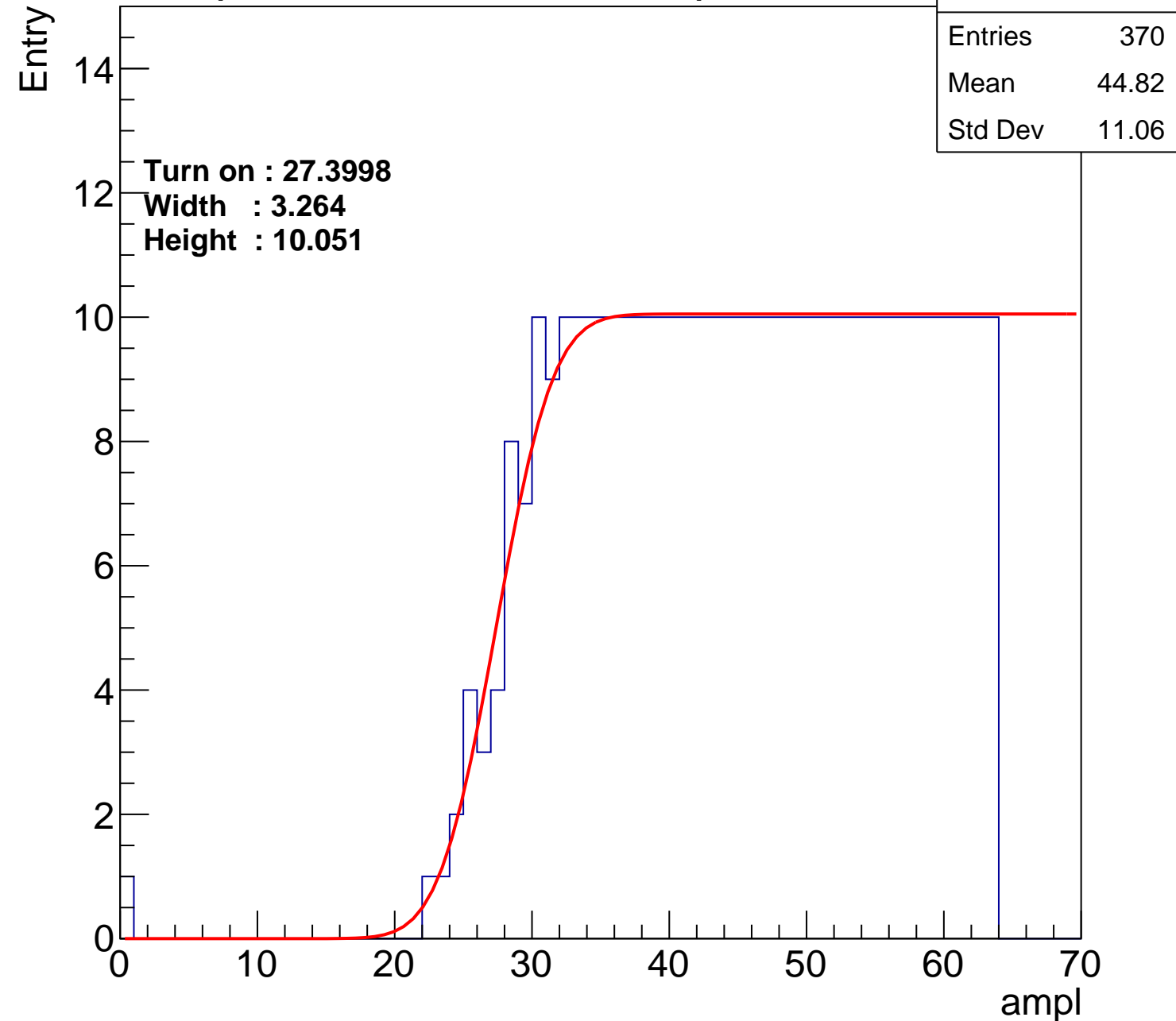
Width : 3.264

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.4
Std Dev	11.41

Turn on : 26.9392

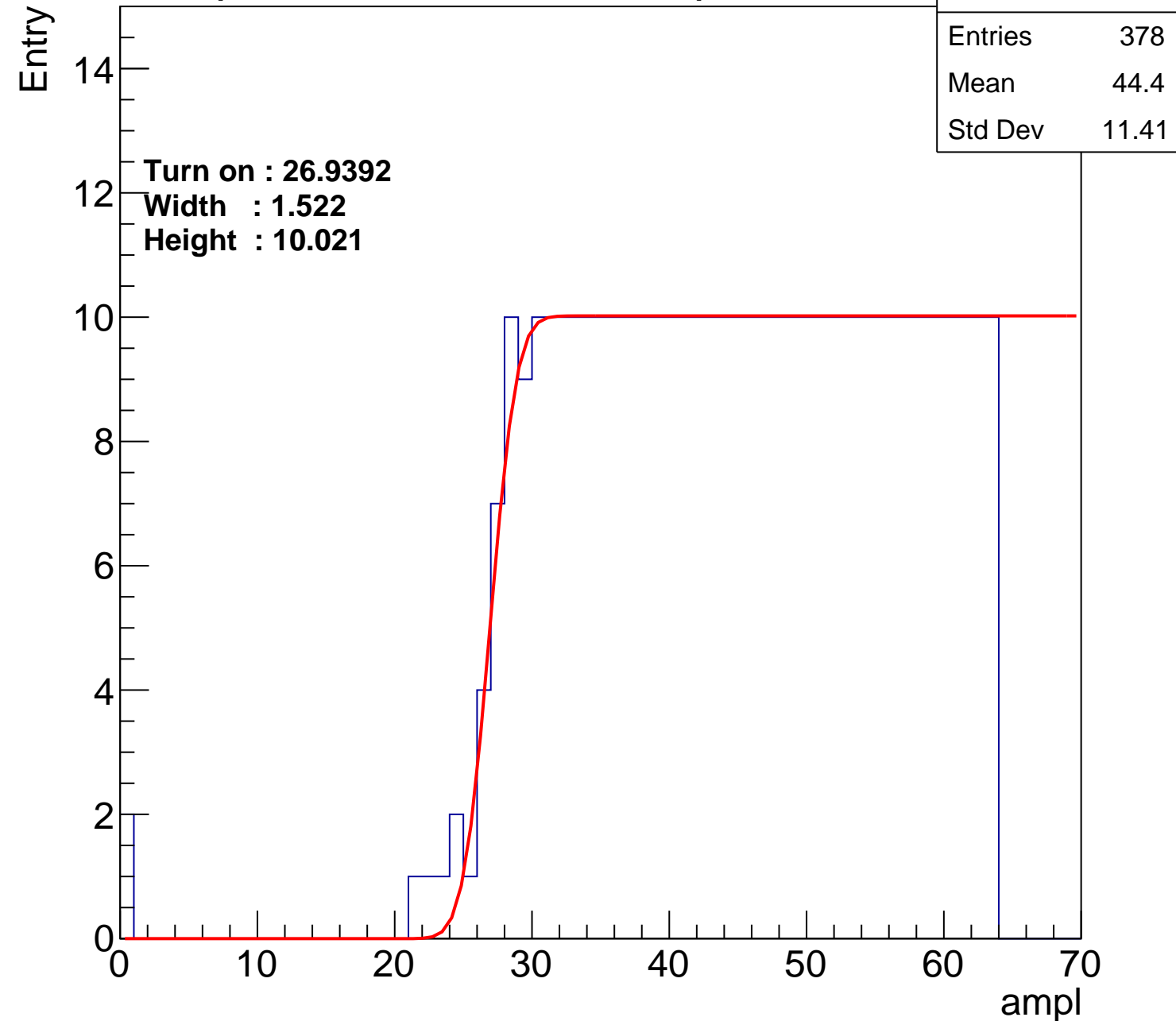
Width : 1.522

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.04
Std Dev	11.07

Turn on : 27.7756

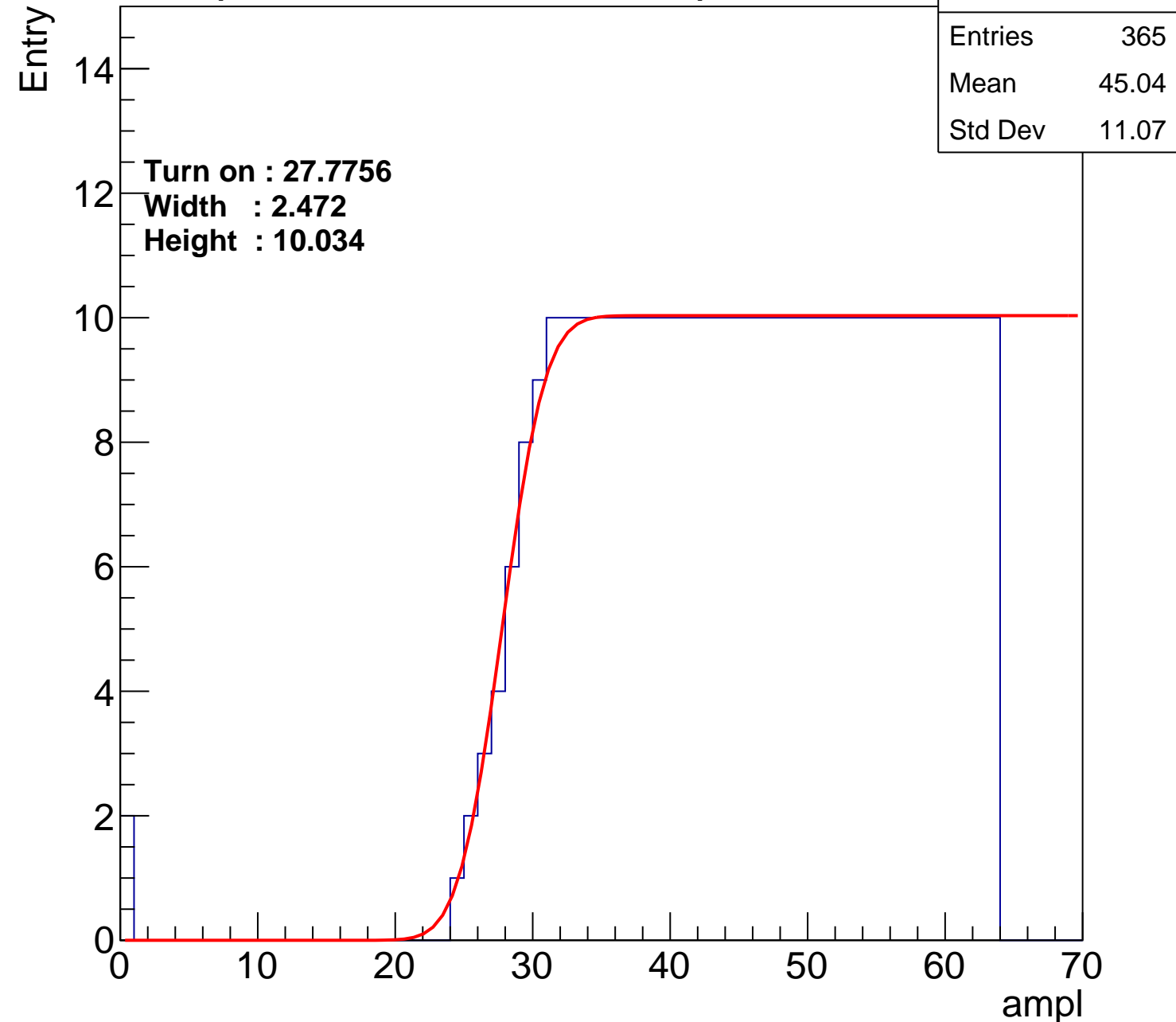
Width : 2.472

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.51
Std Dev	11.32

Turn on : 26.6178

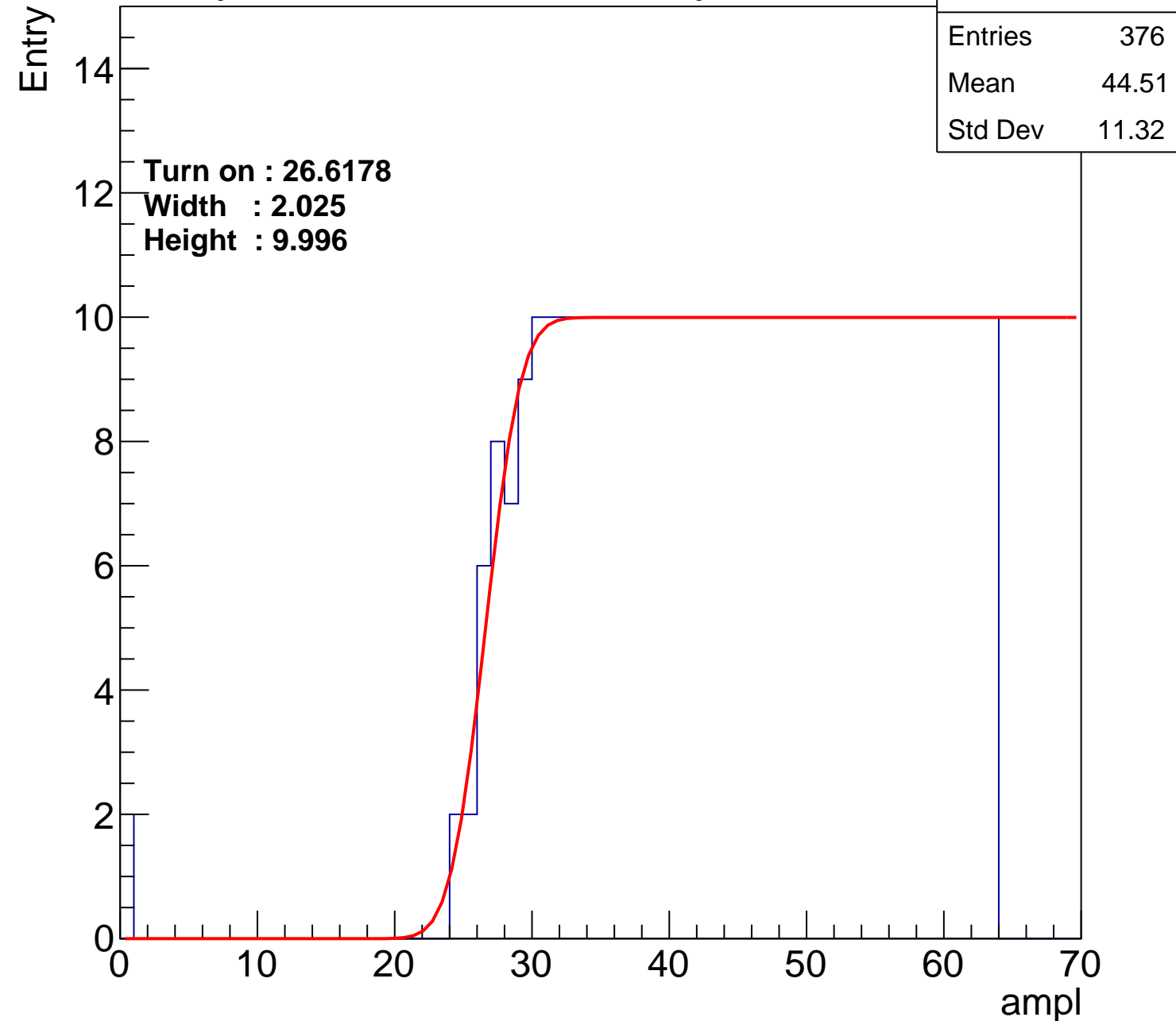
Width : 2.025

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.91
Std Dev	11.22

Turn on : 27.9934

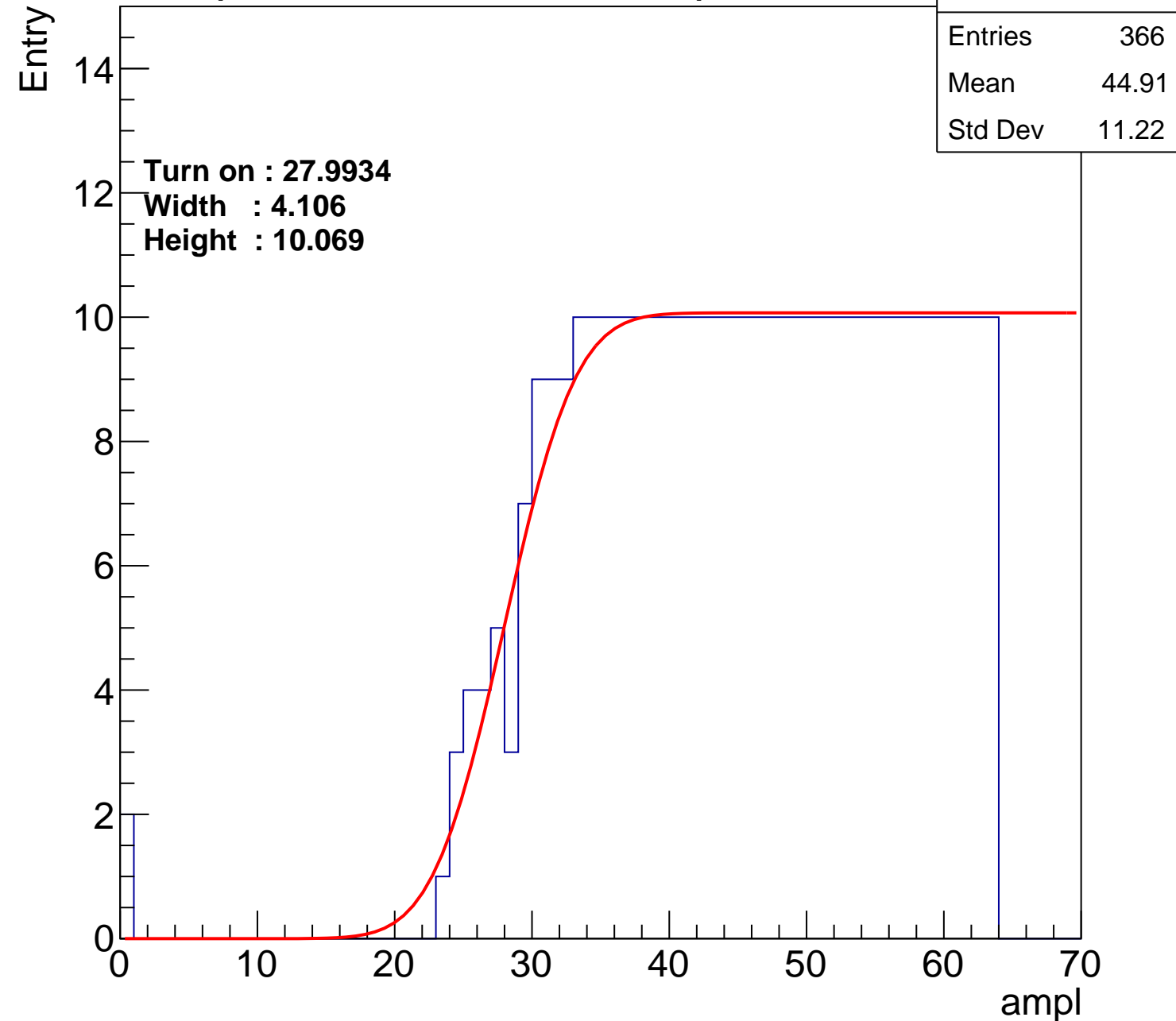
Width : 4.106

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.7927

Width : 3.544

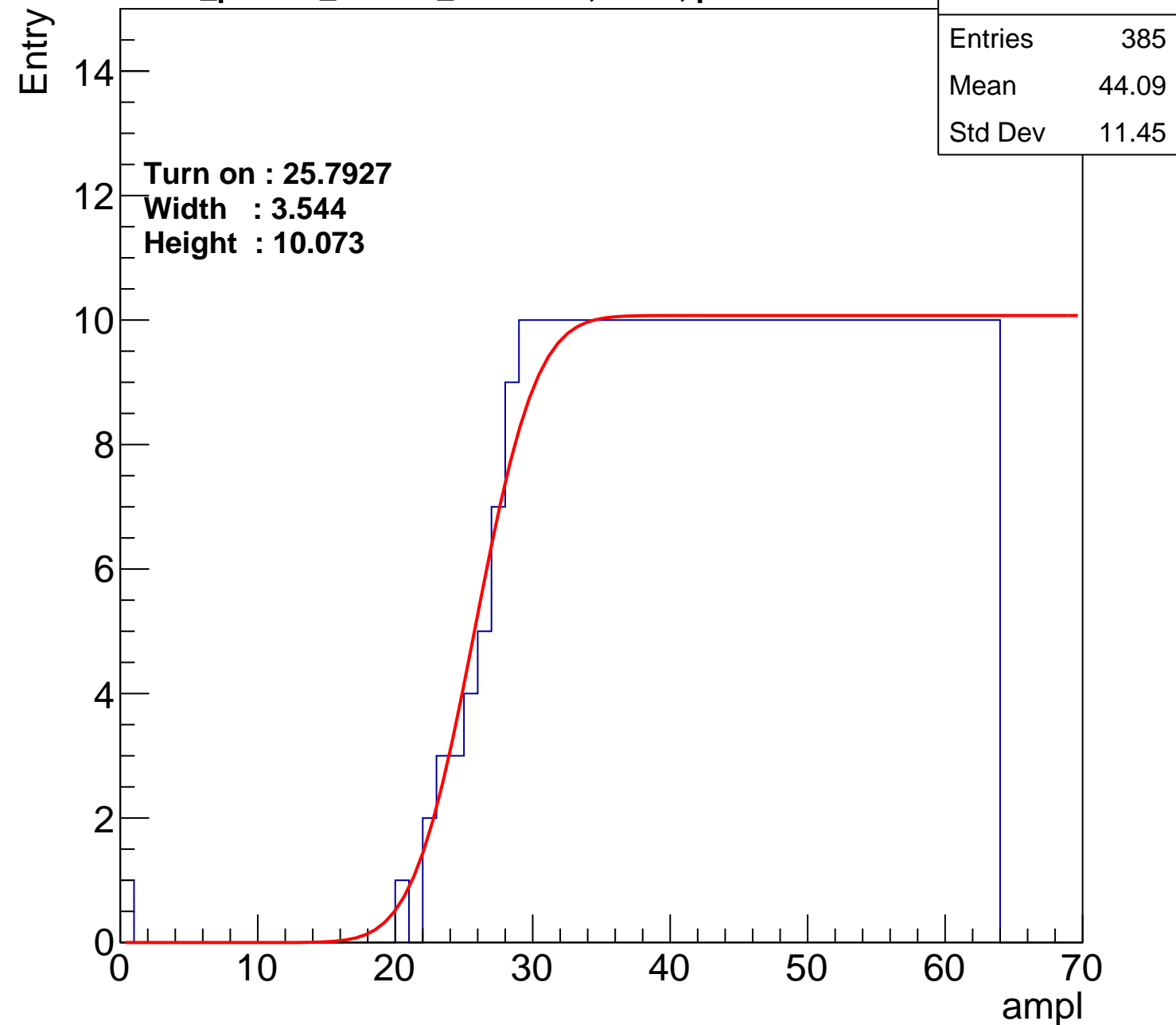
Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 24.8909

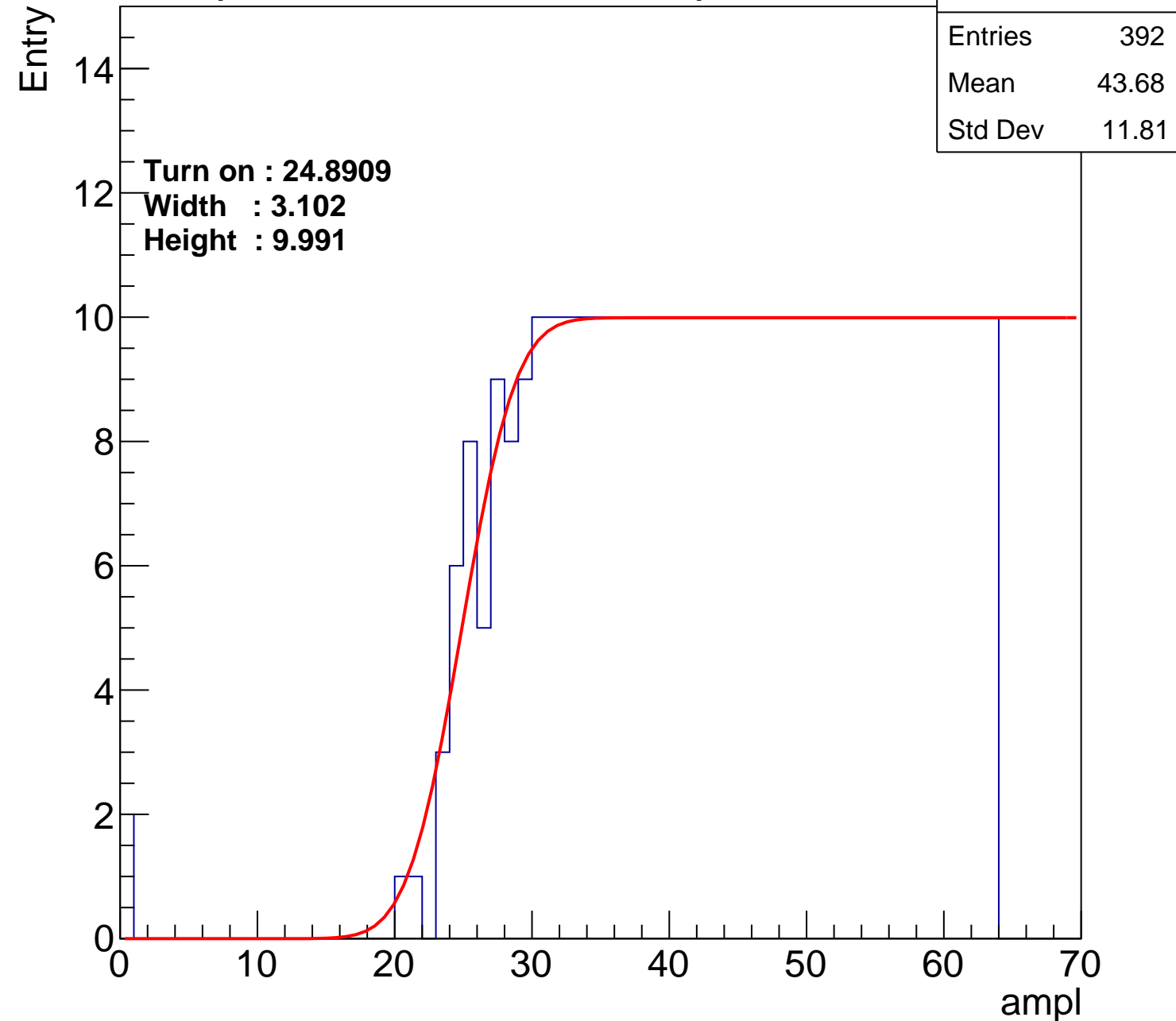
Width : 3.102

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.8878

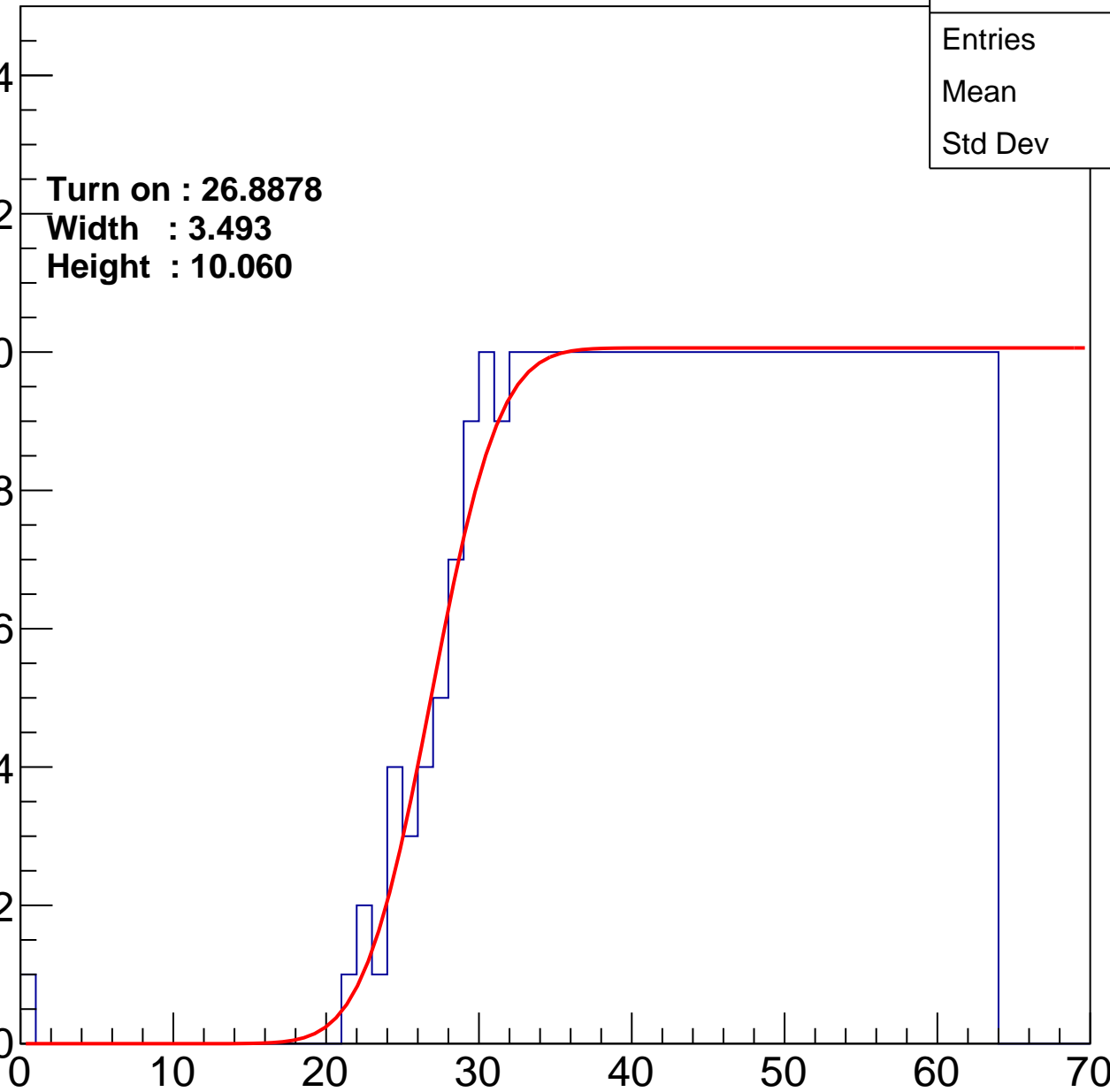
Width : 3.493

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.7338

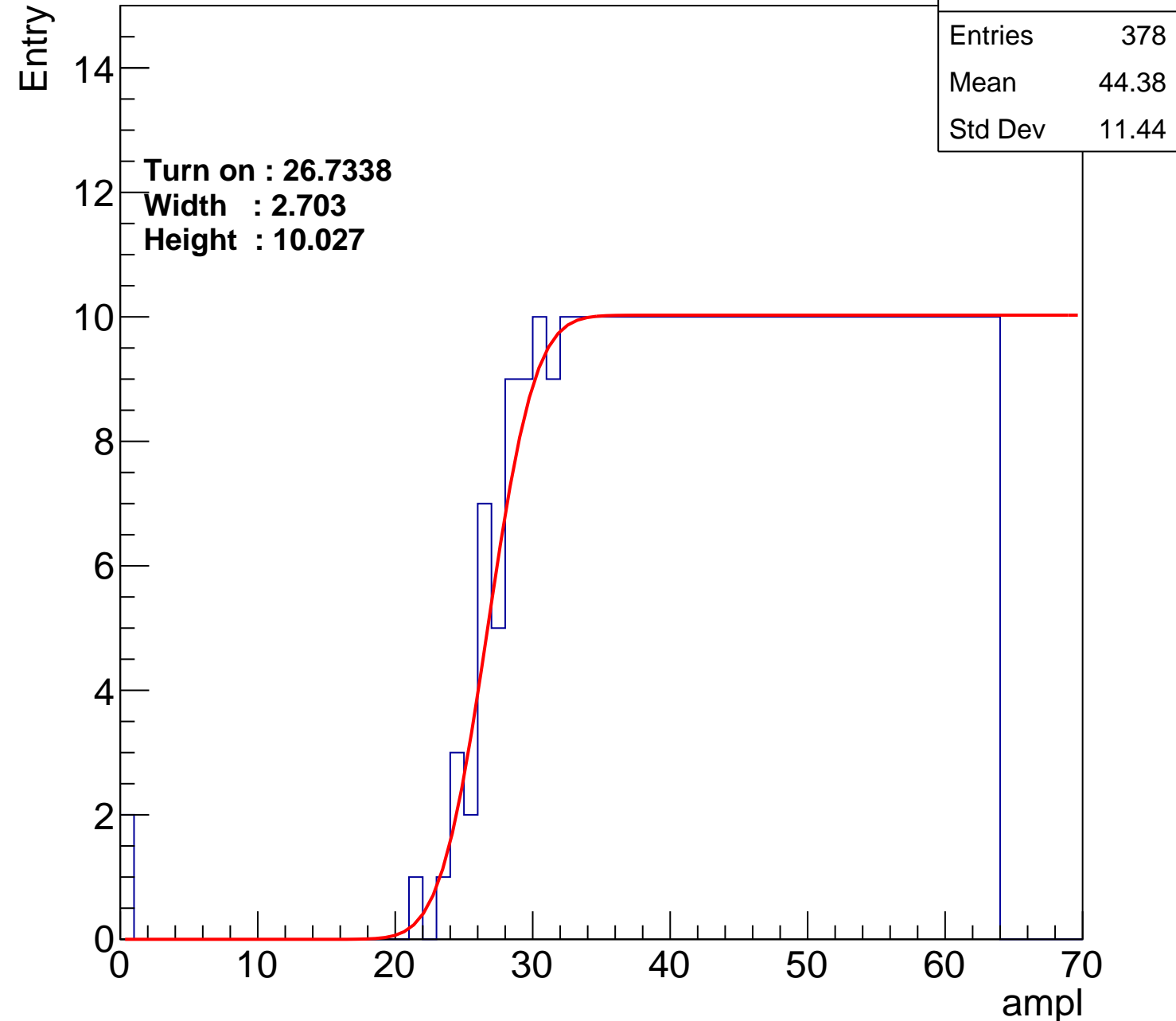
Width : 2.703

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch10

calib_packv5_042523_0143.root, FC#3, port B3

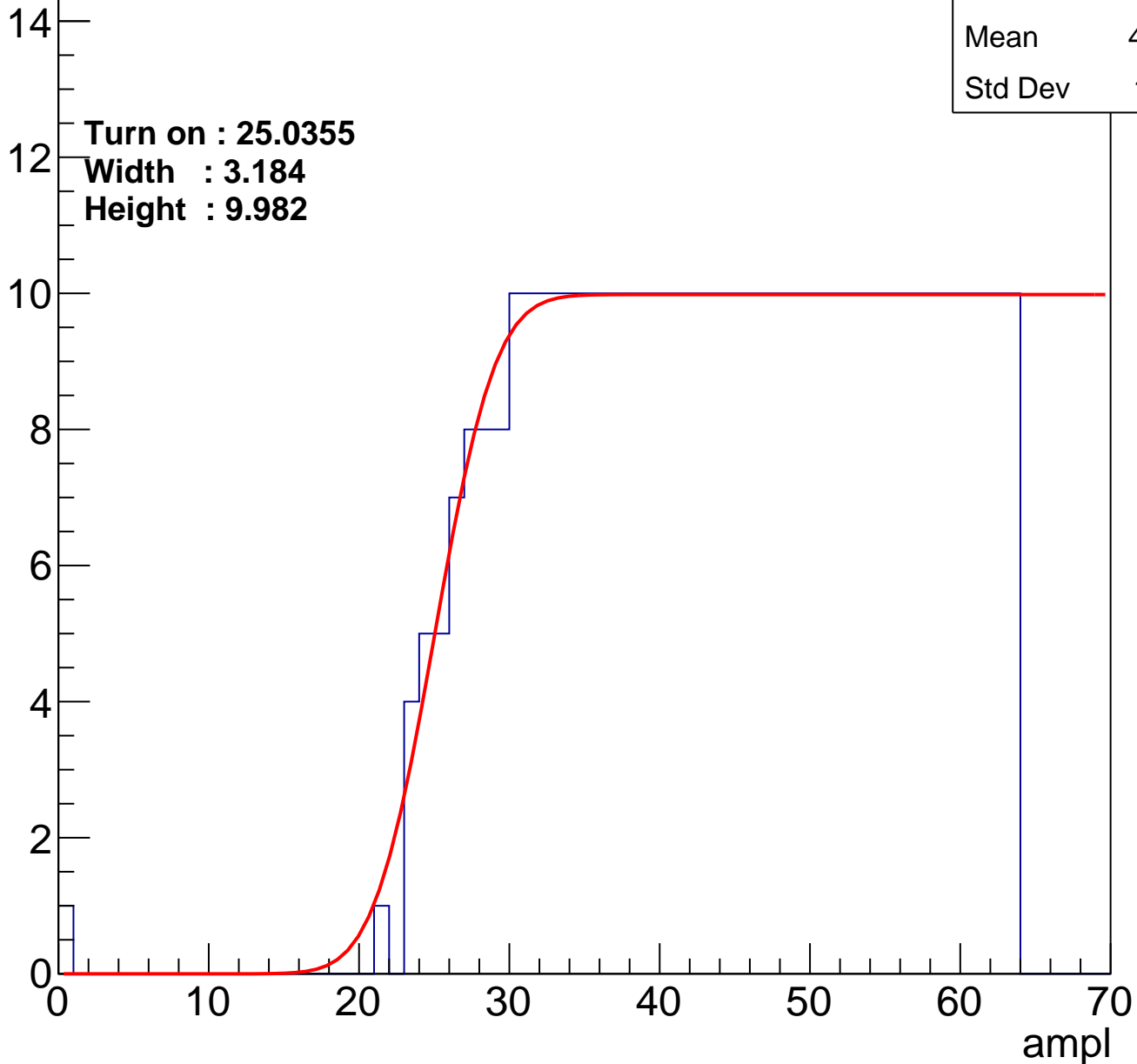
Entries	387
Mean	43.98
Std Dev	11.51

Turn on : 25.0355

Width : 3.184

Height : 9.982

Entry



B1L000S, U21-ch11

calib_packv5_042523_0143.root, FC#3, port B3

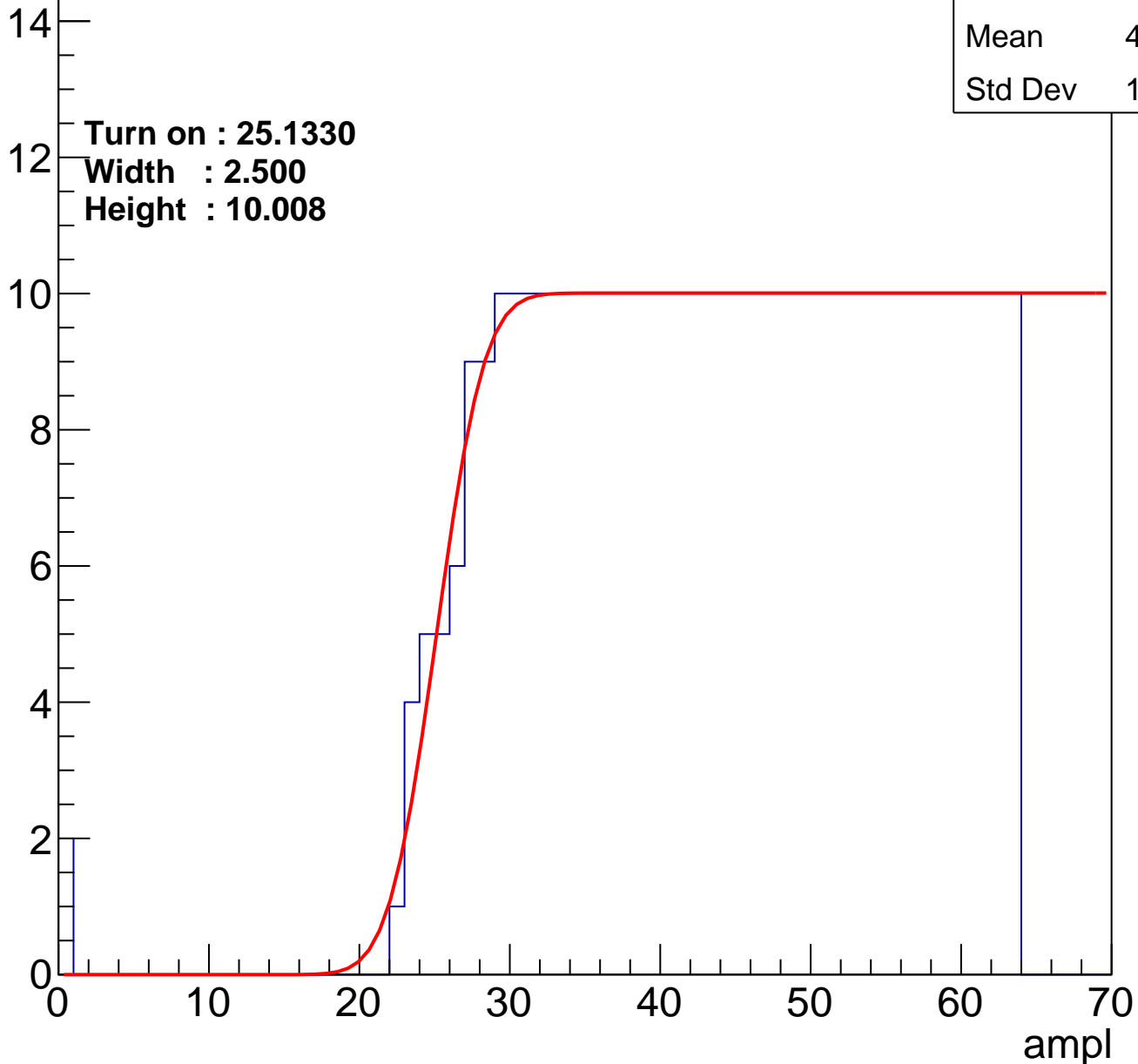
Entries	391
Mean	43.76
Std Dev	11.74

Turn on : 25.1330

Width : 2.500

Height : 10.008

Entry



B1L000S, U21-ch12

calib_packv5_042523_0143.root, FC#3, port B3

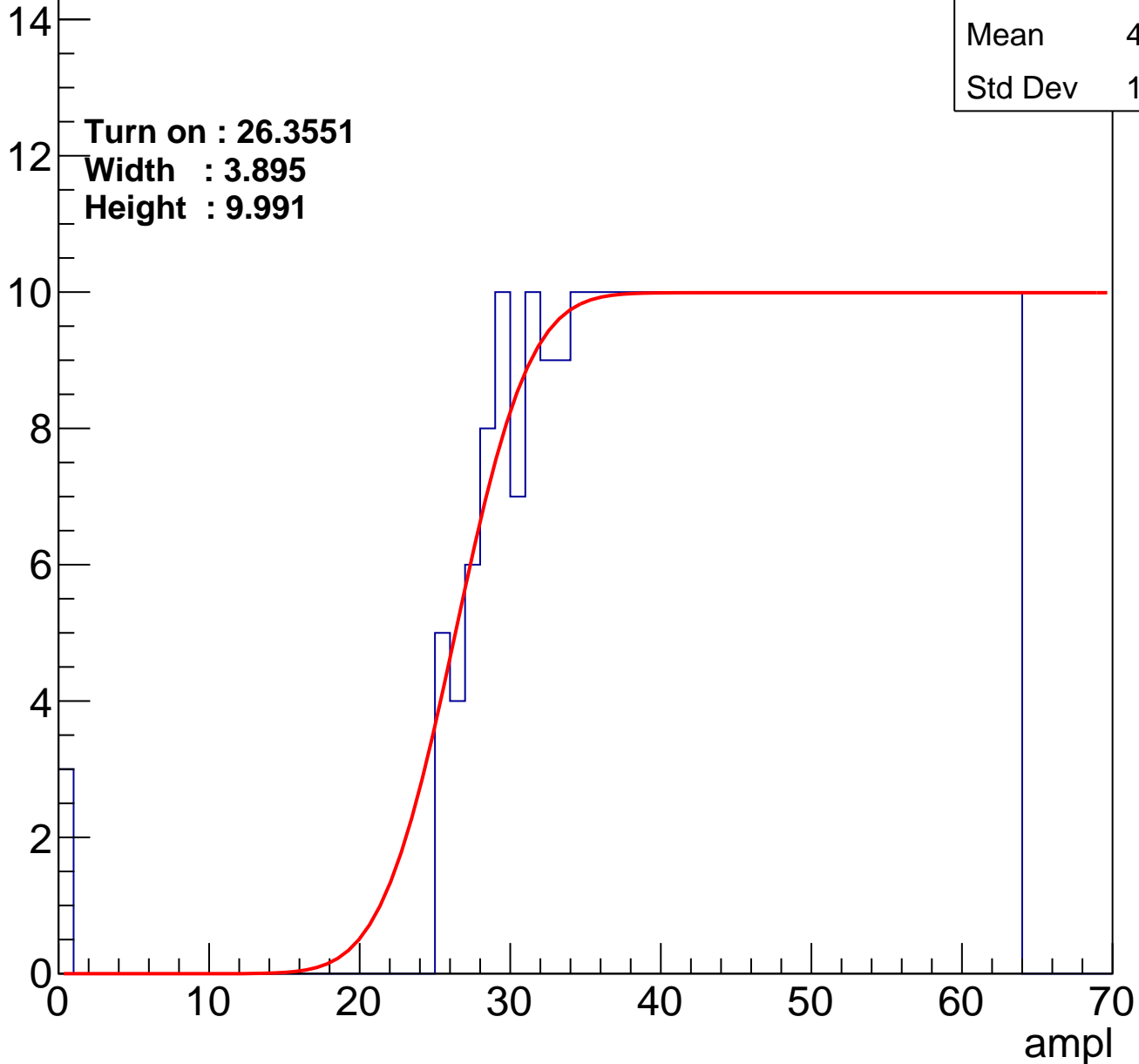
Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 26.3551

Width : 3.895

Height : 9.991

Entry



B1L000S, U21-ch13

calib_packv5_042523_0143.root, FC#3, port B3

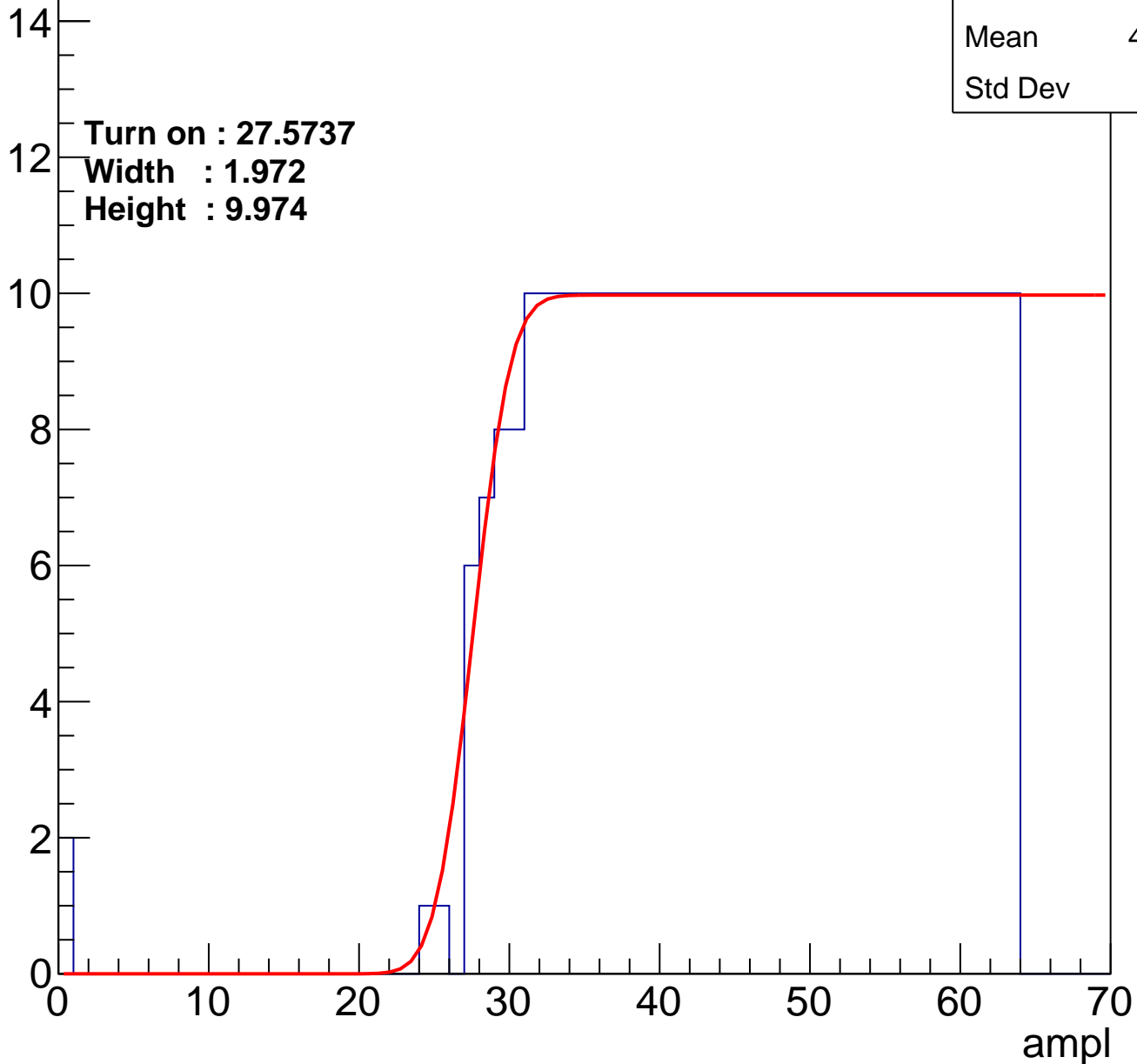
Entries	363
Mean	45.15
Std Dev	11

Turn on : 27.5737

Width : 1.972

Height : 9.974

Entry



B1L000S, U21-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	408
Mean	42.86
Std Dev	12.33

Turn on : 23.6329

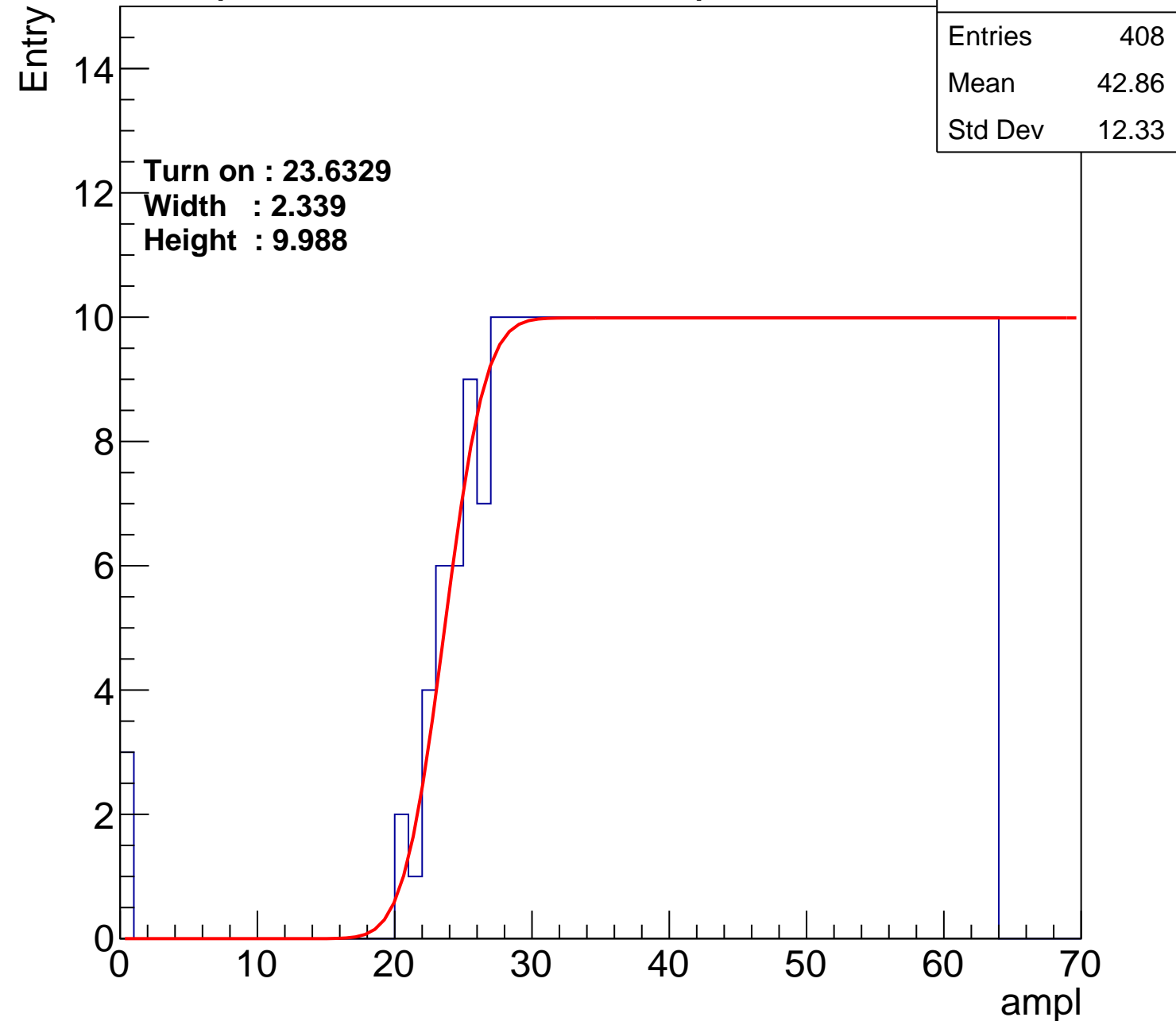
Width : 2.339

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.3265

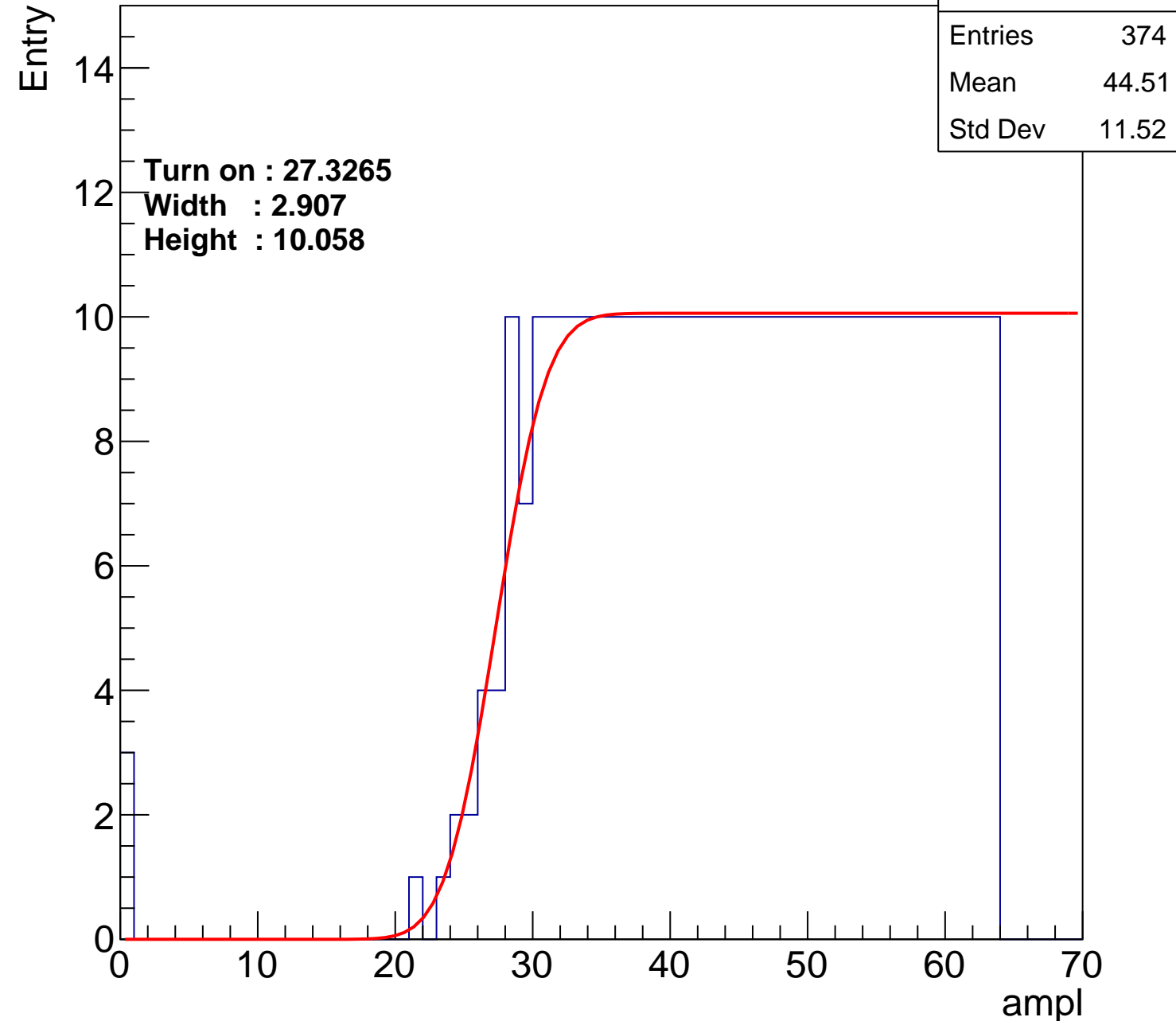
Width : 2.907

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.28
Std Dev	11.86

Turn on : 23.9248

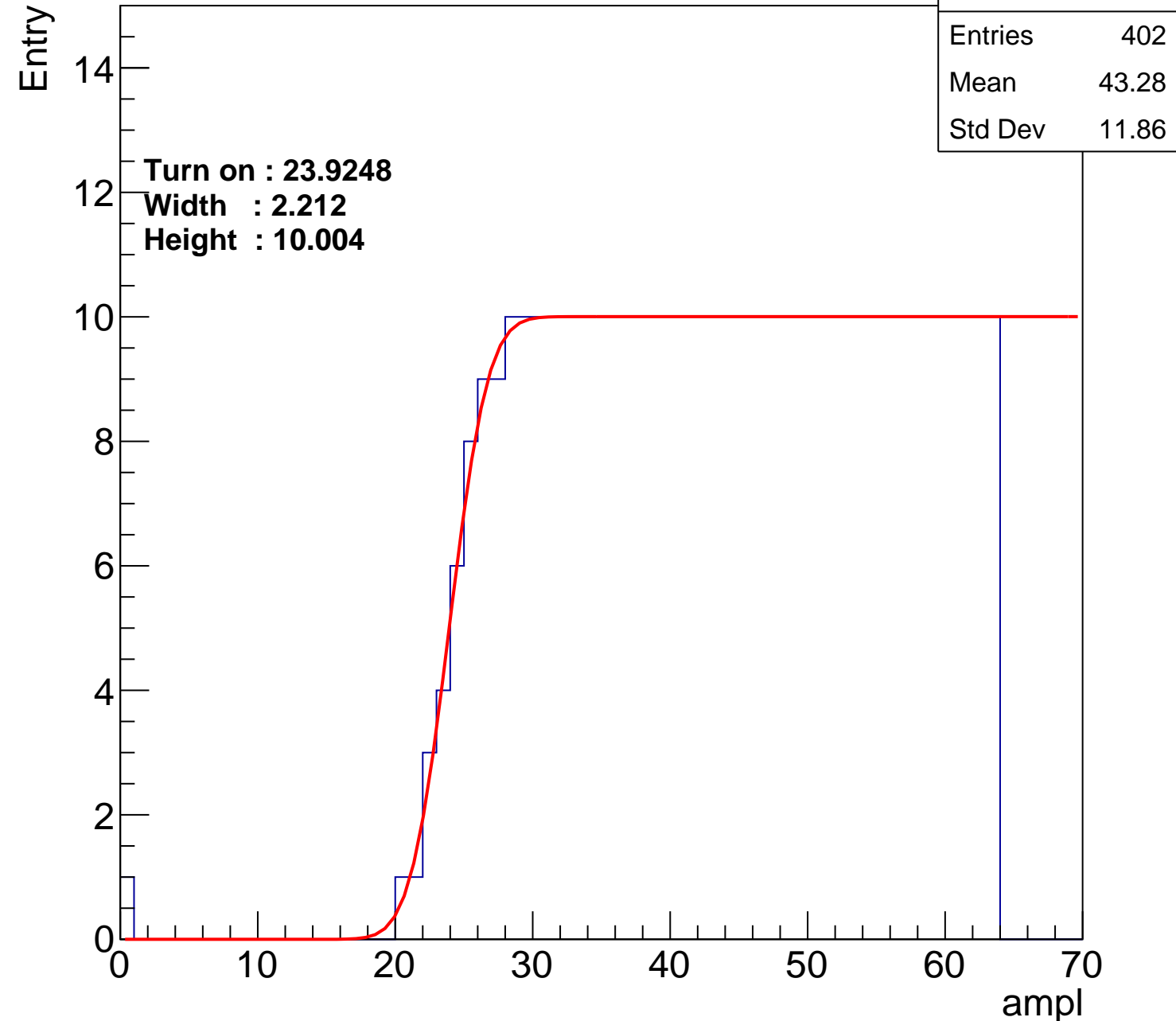
Width : 2.212

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.5492

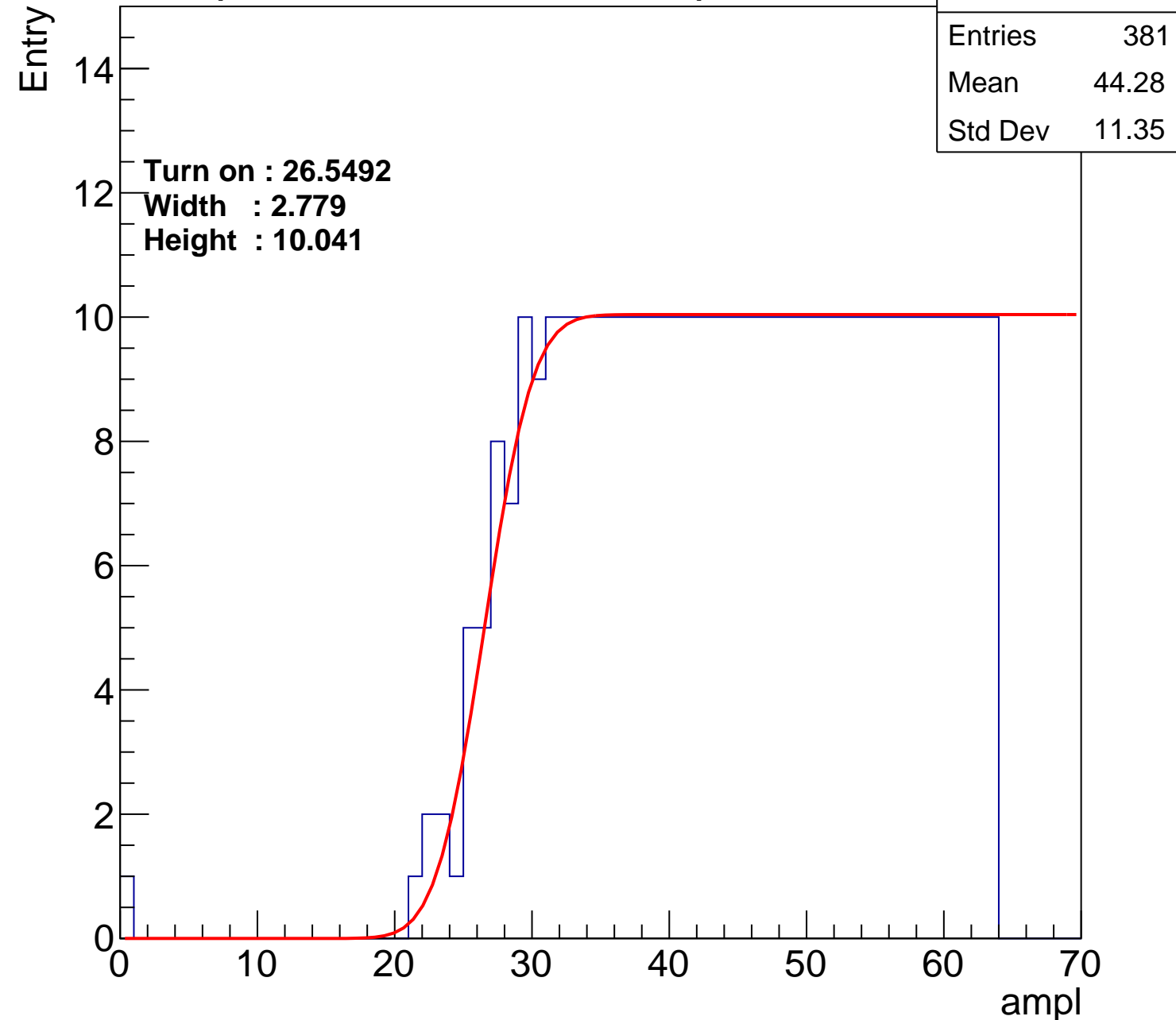
Width : 2.779

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.97
Std Dev	11.62

Turn on : 25.7193

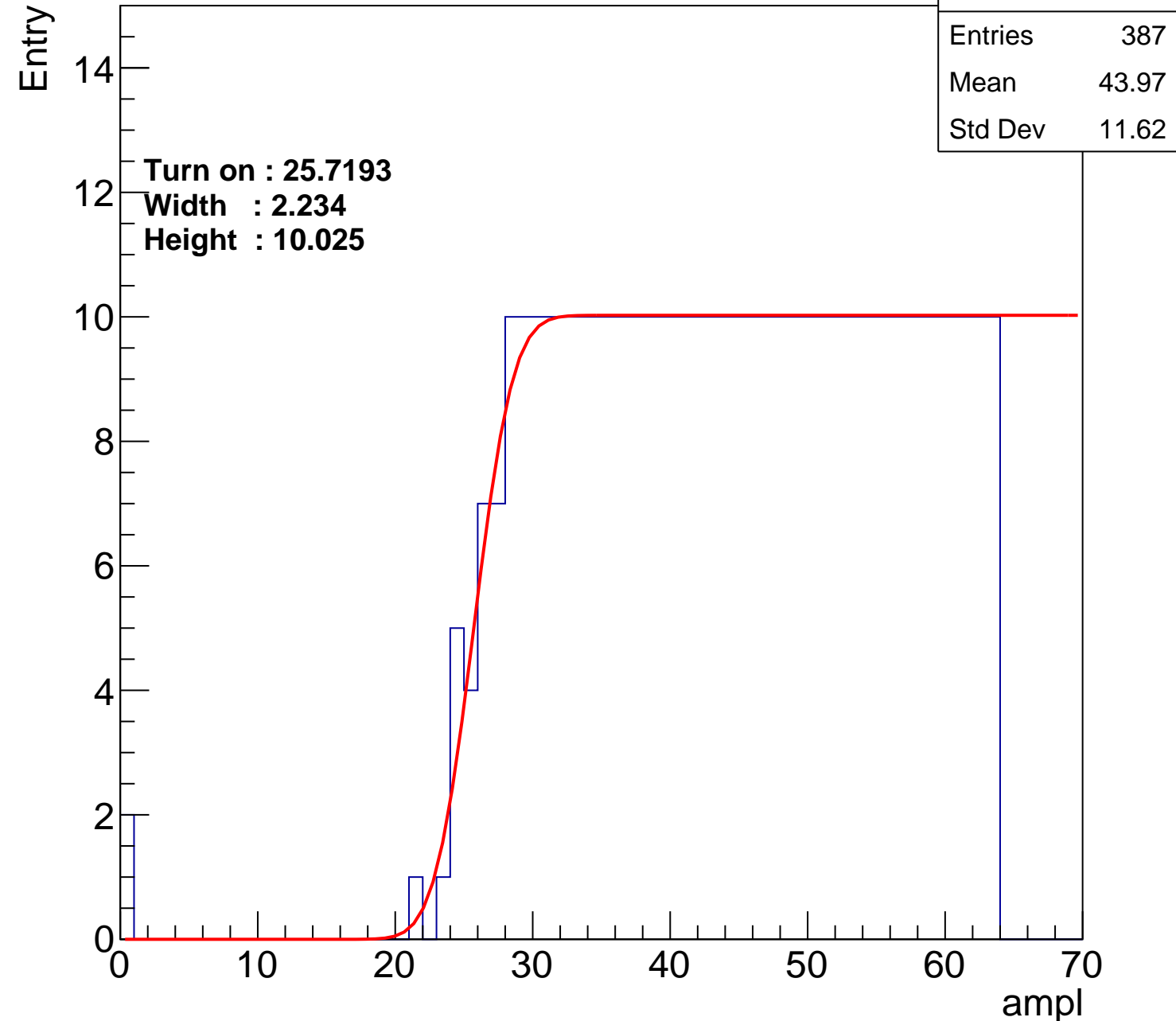
Width : 2.234

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch19

calib_packv5_042523_0143.root, FC#3, port B3

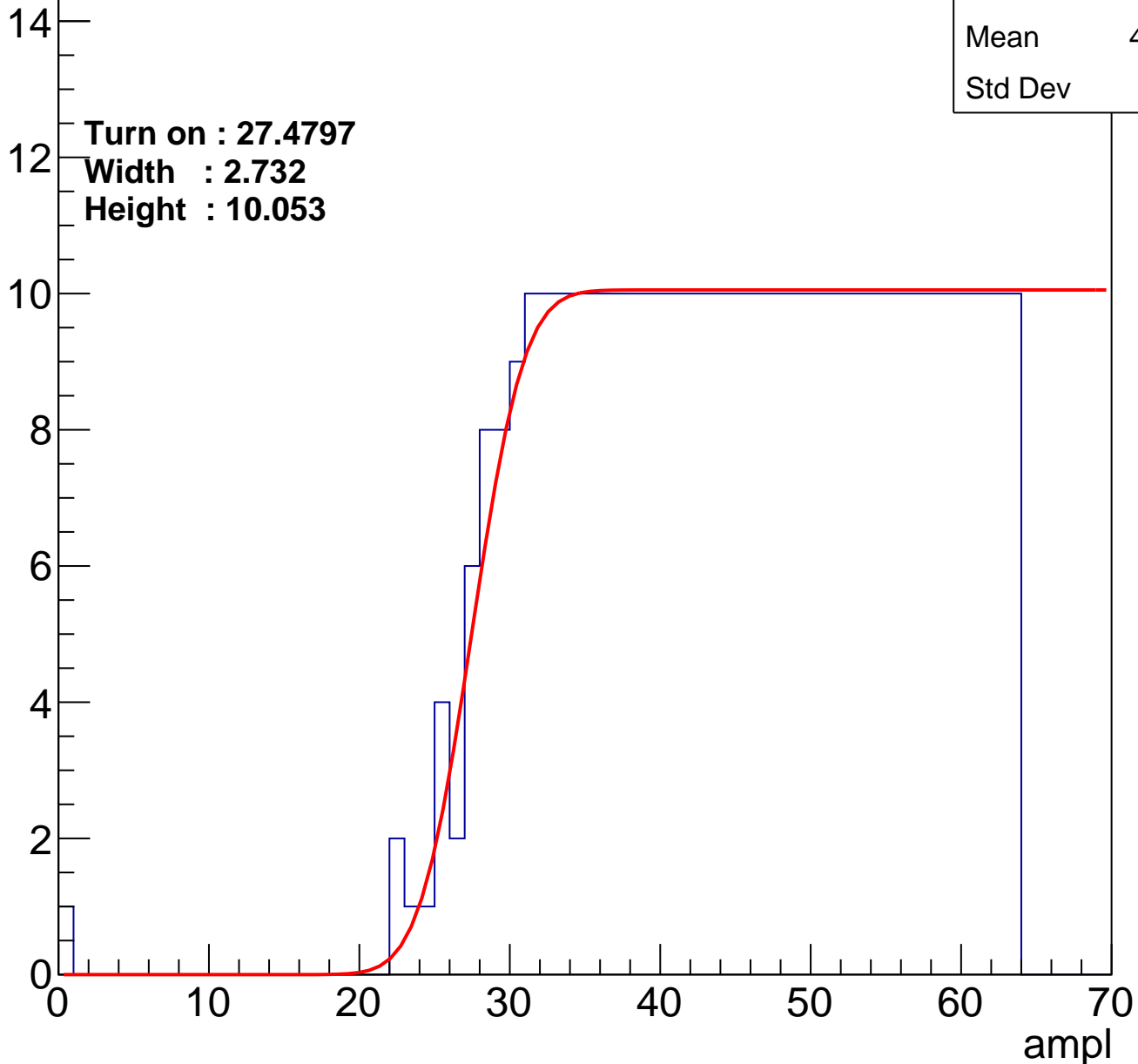
Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.4797

Width : 2.732

Height : 10.053

Entry



B1L000S, U21-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.36
Std Dev	11.95

Turn on : 24.4914

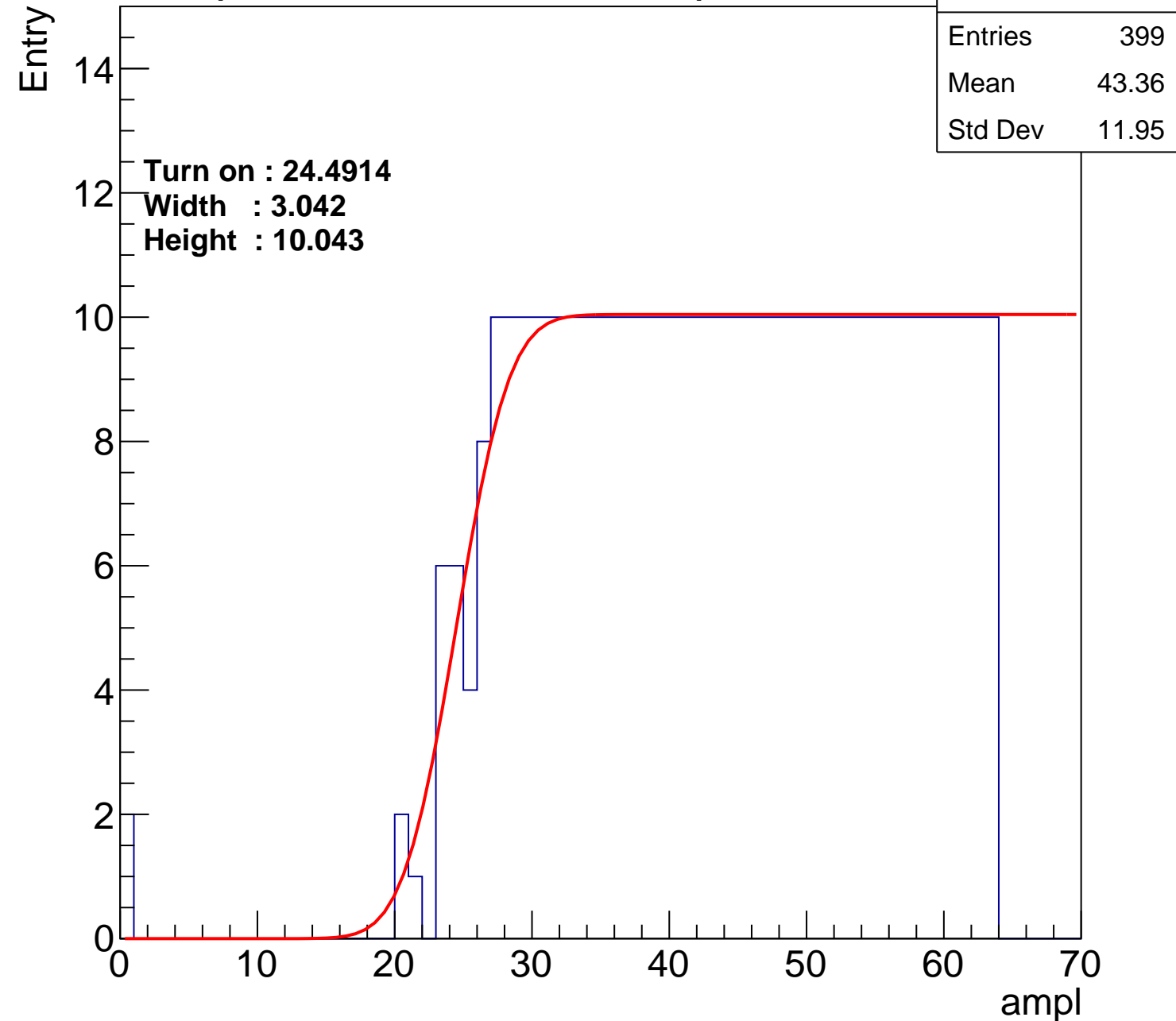
Width : 3.042

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch21

calib_packv5_042523_0143.root, FC#3, port B3

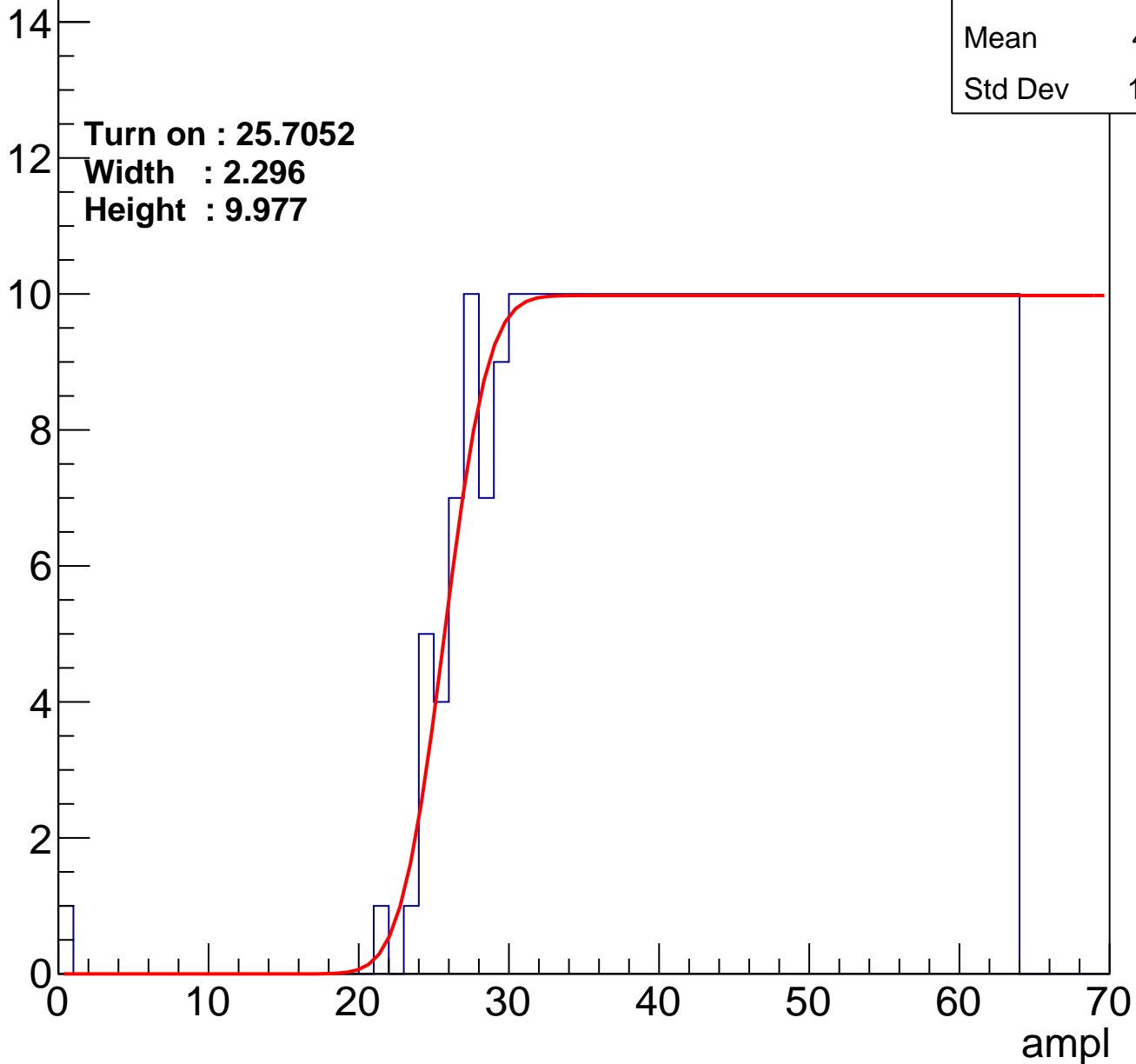
Entries	385
Mean	44.11
Std Dev	11.42

Turn on : 25.7052

Width : 2.296

Height : 9.977

Entry



B1L000S, U21-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.25
Std Dev	11.61

Turn on : 26.0789

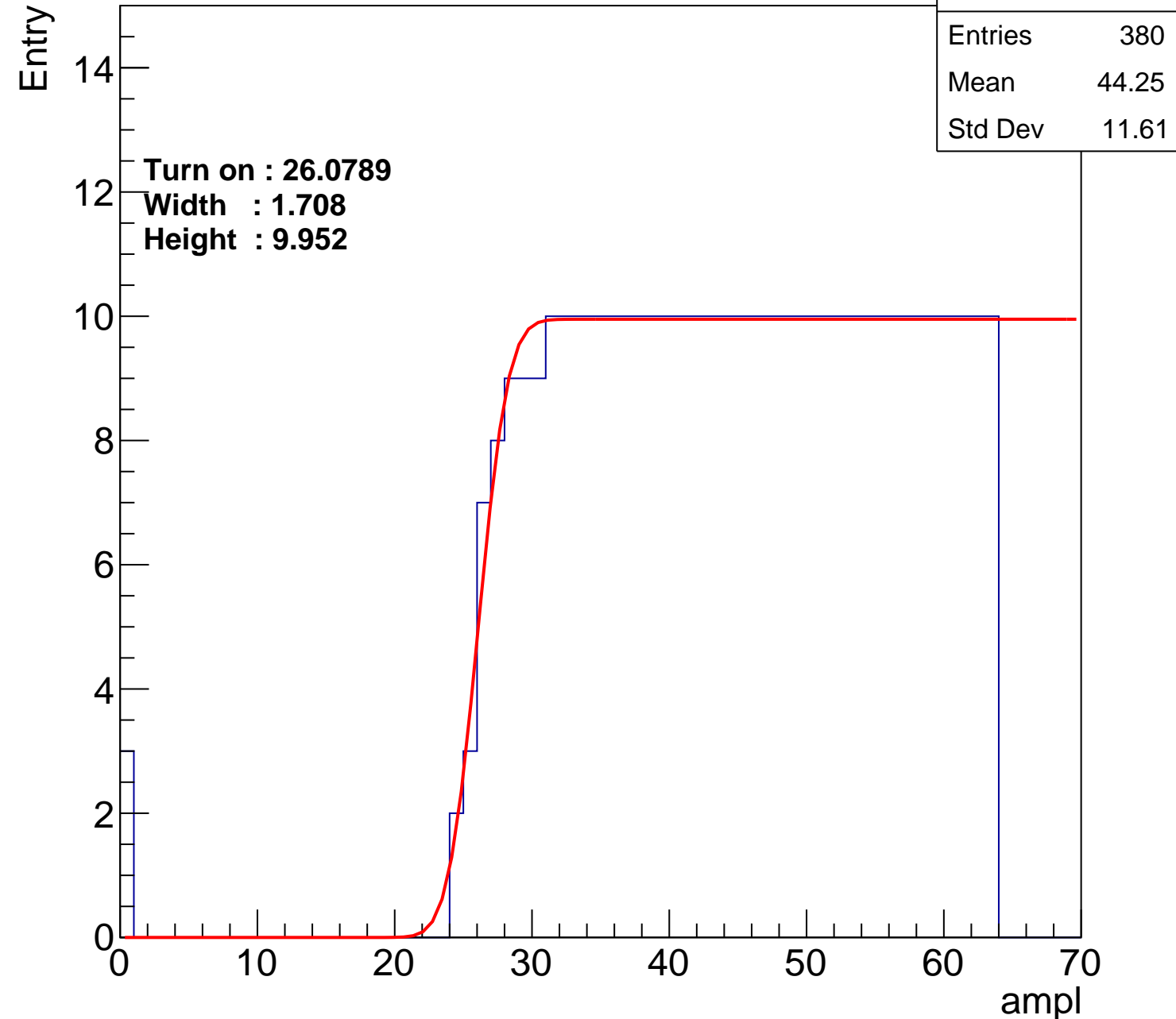
Width : 1.708

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	351
Mean	45.7
Std Dev	10.76

Turn on : 28.8933

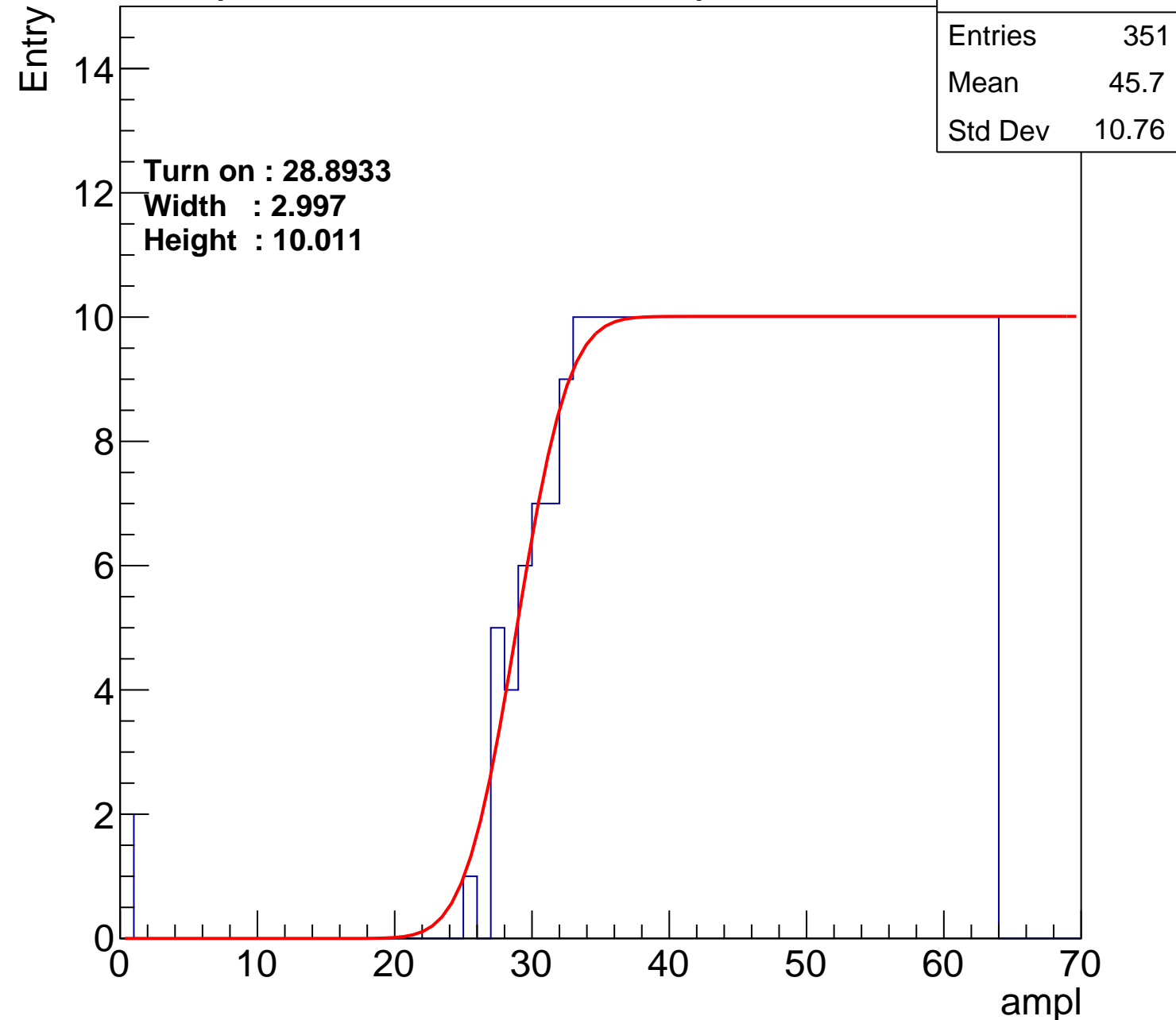
Width : 2.997

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.98
Std Dev	11.46

Turn on : 25.6734

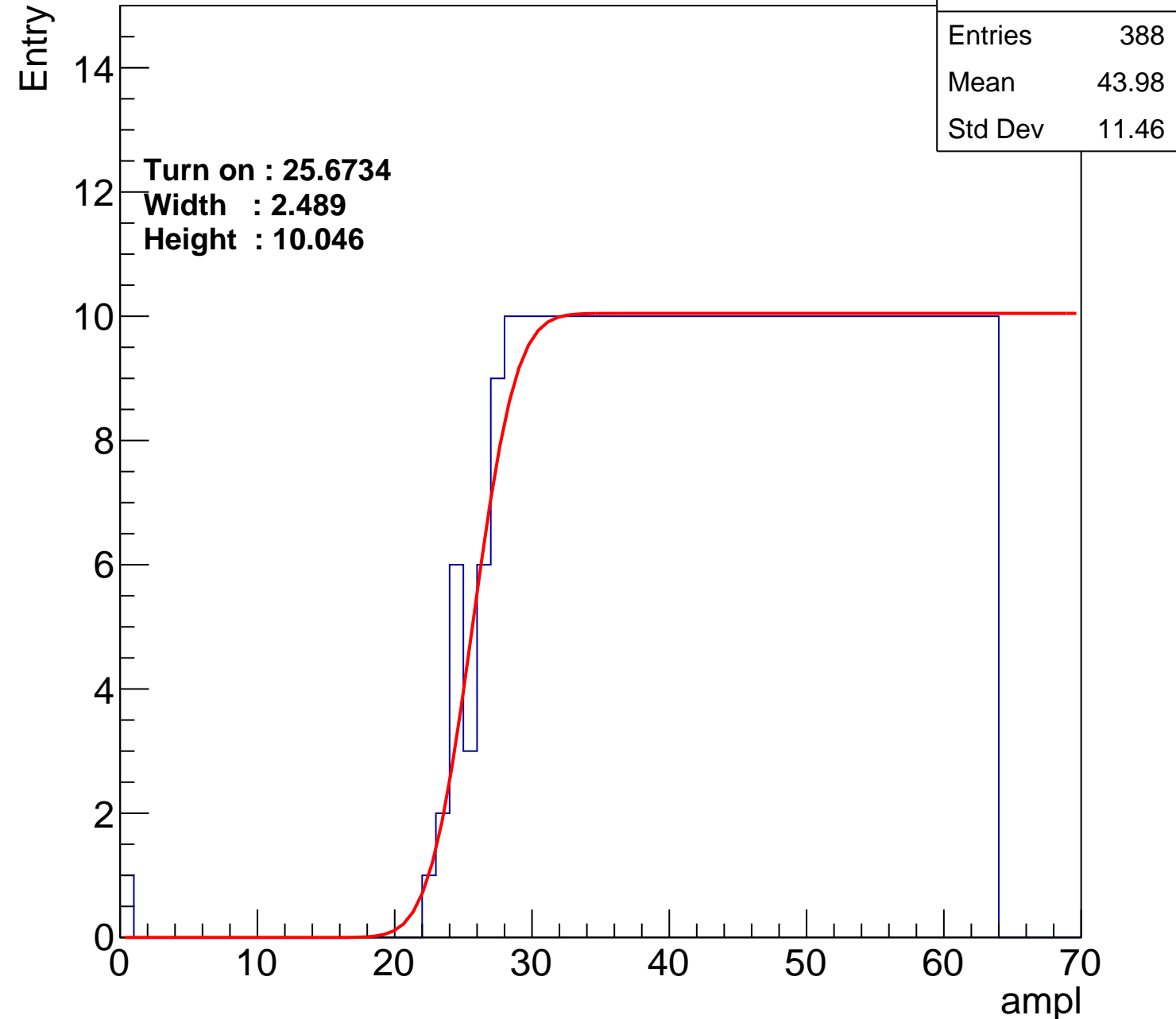
Width : 2.489

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch25

calib_packv5_042523_0143.root, FC#3, port B3

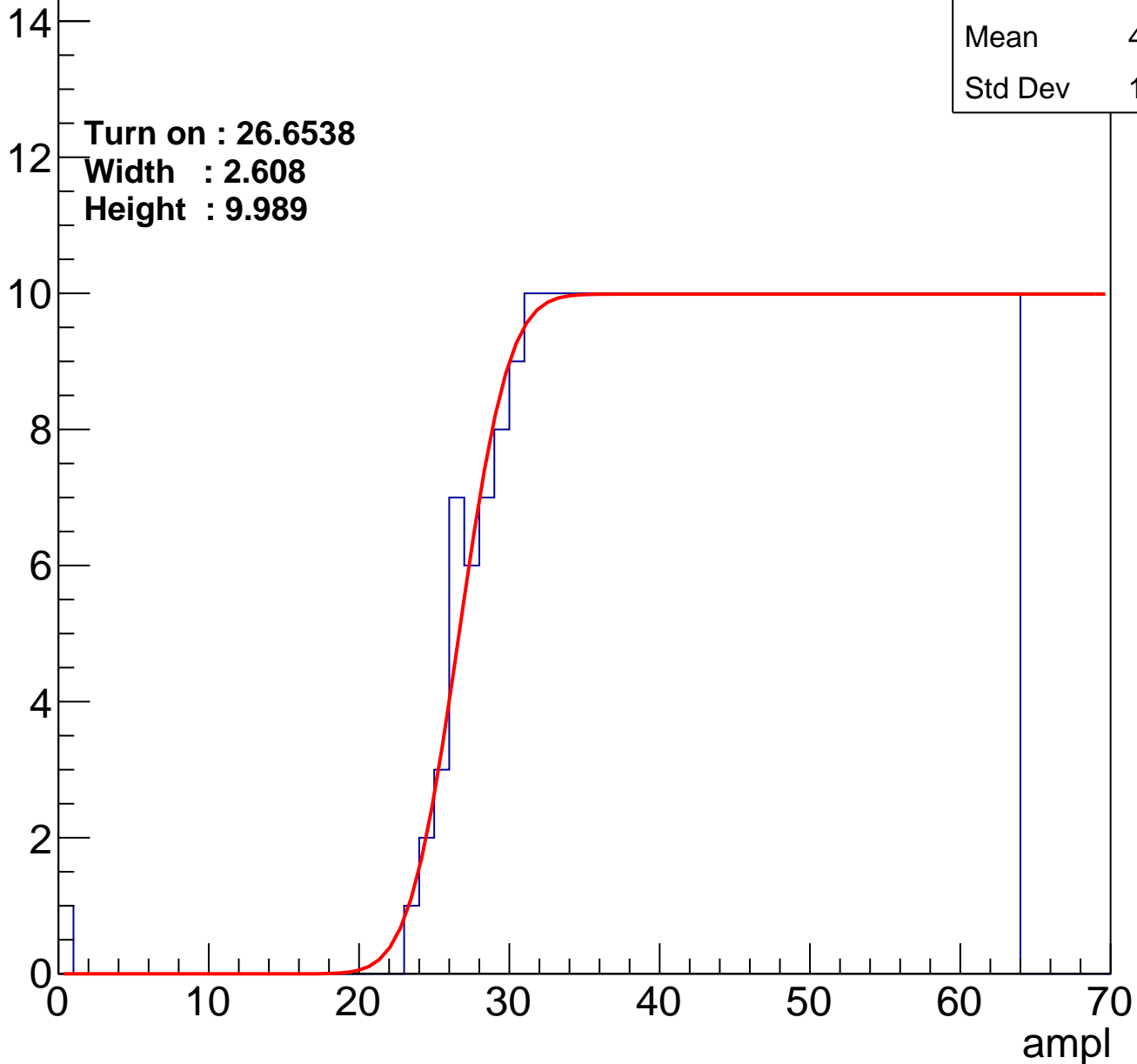
Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.6538

Width : 2.608

Height : 9.989

Entry



B1L000S, U21-ch26

calib_packv5_042523_0143.root, FC#3, port B3

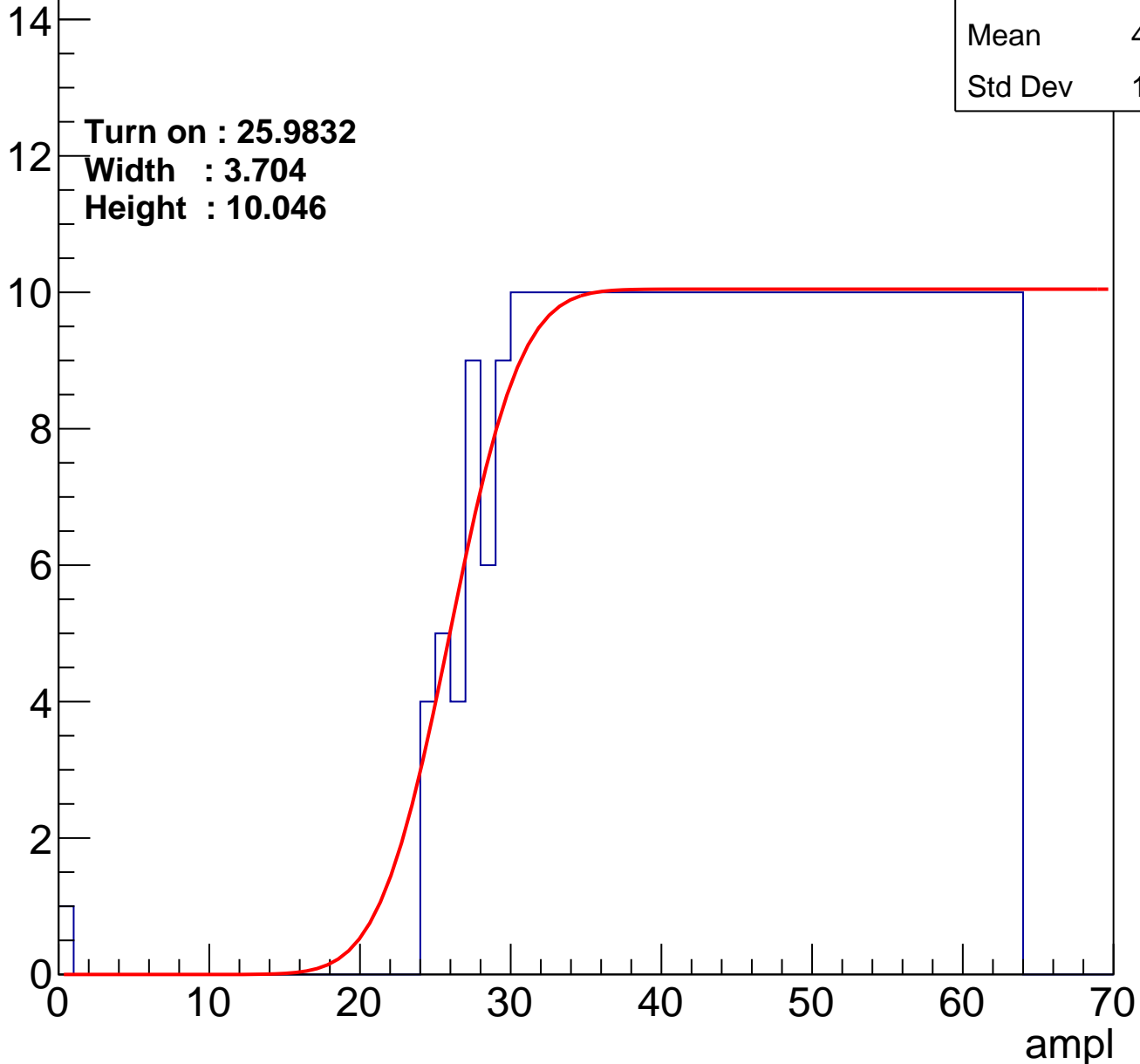
Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 25.9832

Width : 3.704

Height : 10.046

Entry



B1L000S, U21-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 26.7855

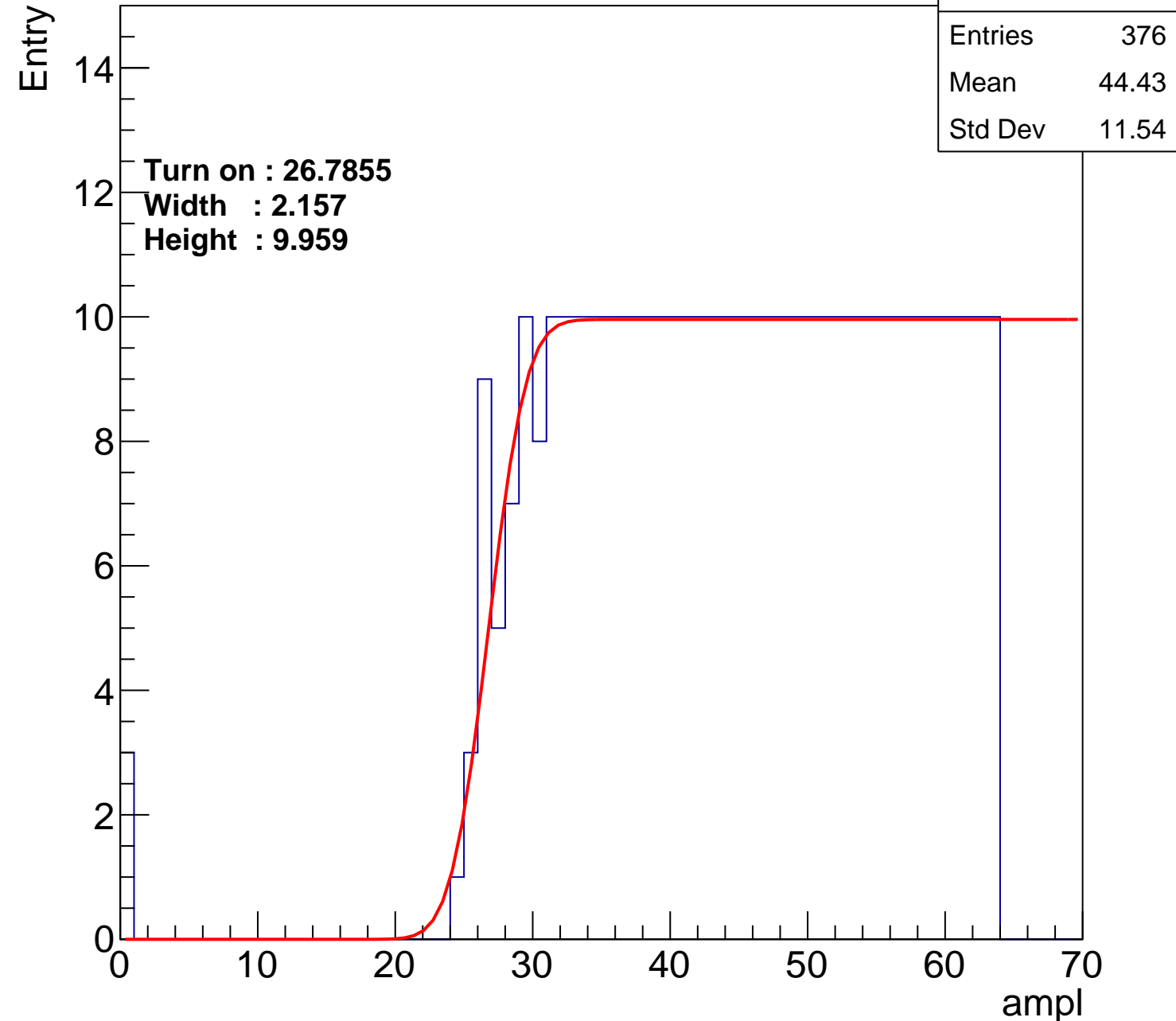
Width : 2.157

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.5768

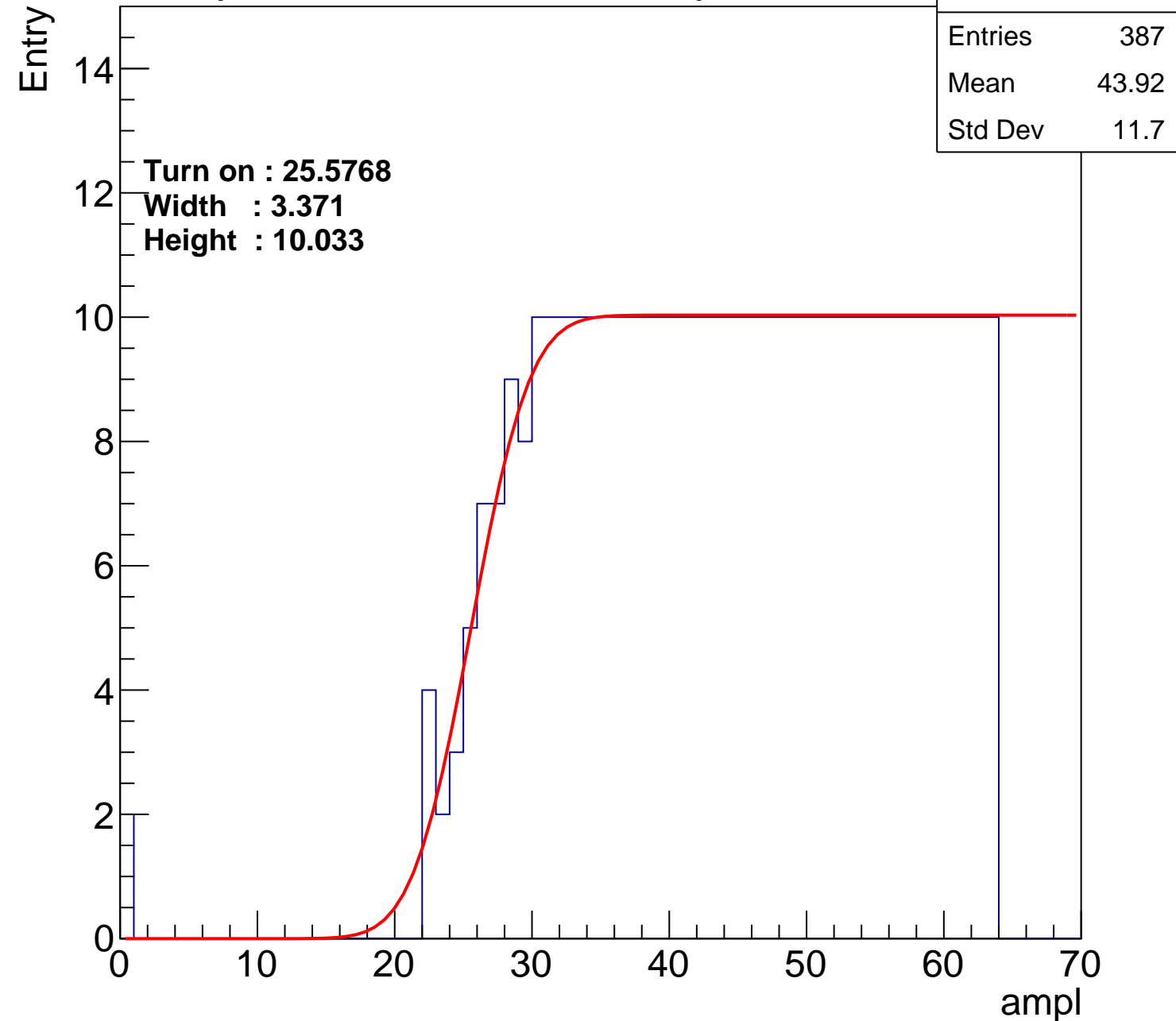
Width : 3.371

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 27.4002

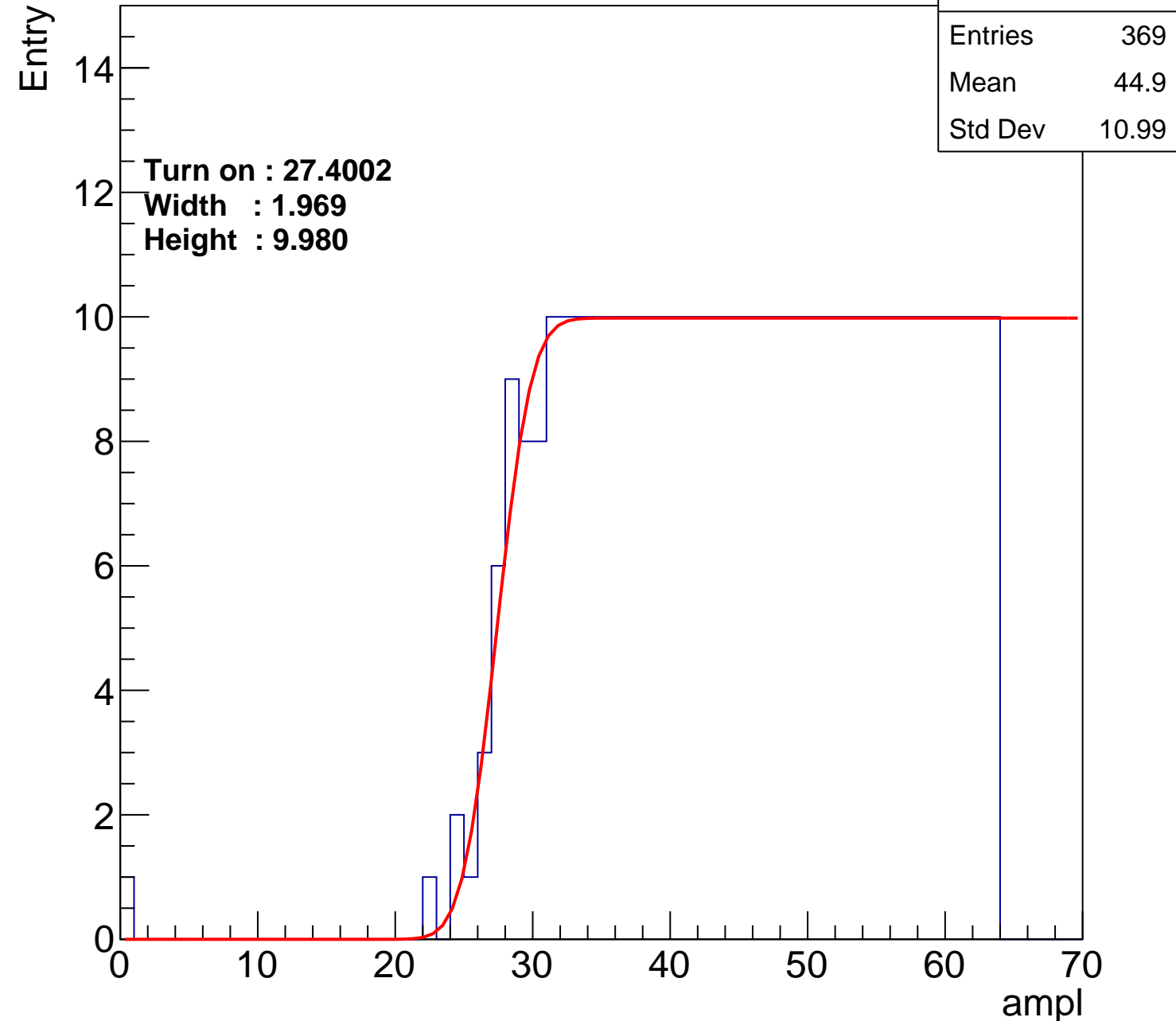
Width : 1.969

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.97
Std Dev	11.27

Turn on : 27.8508

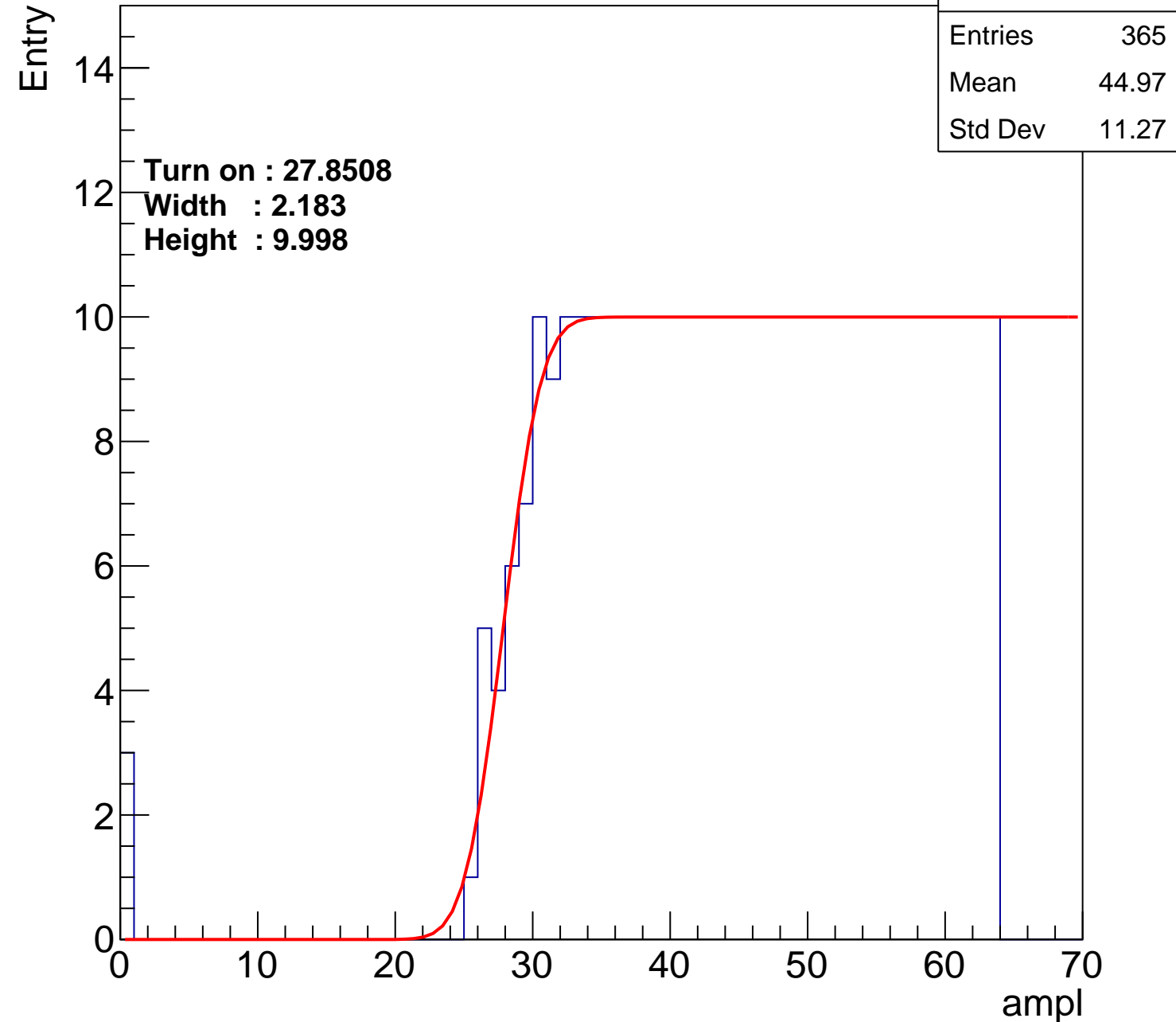
Width : 2.183

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.31
Std Dev	11.33

Turn on : 26.2557

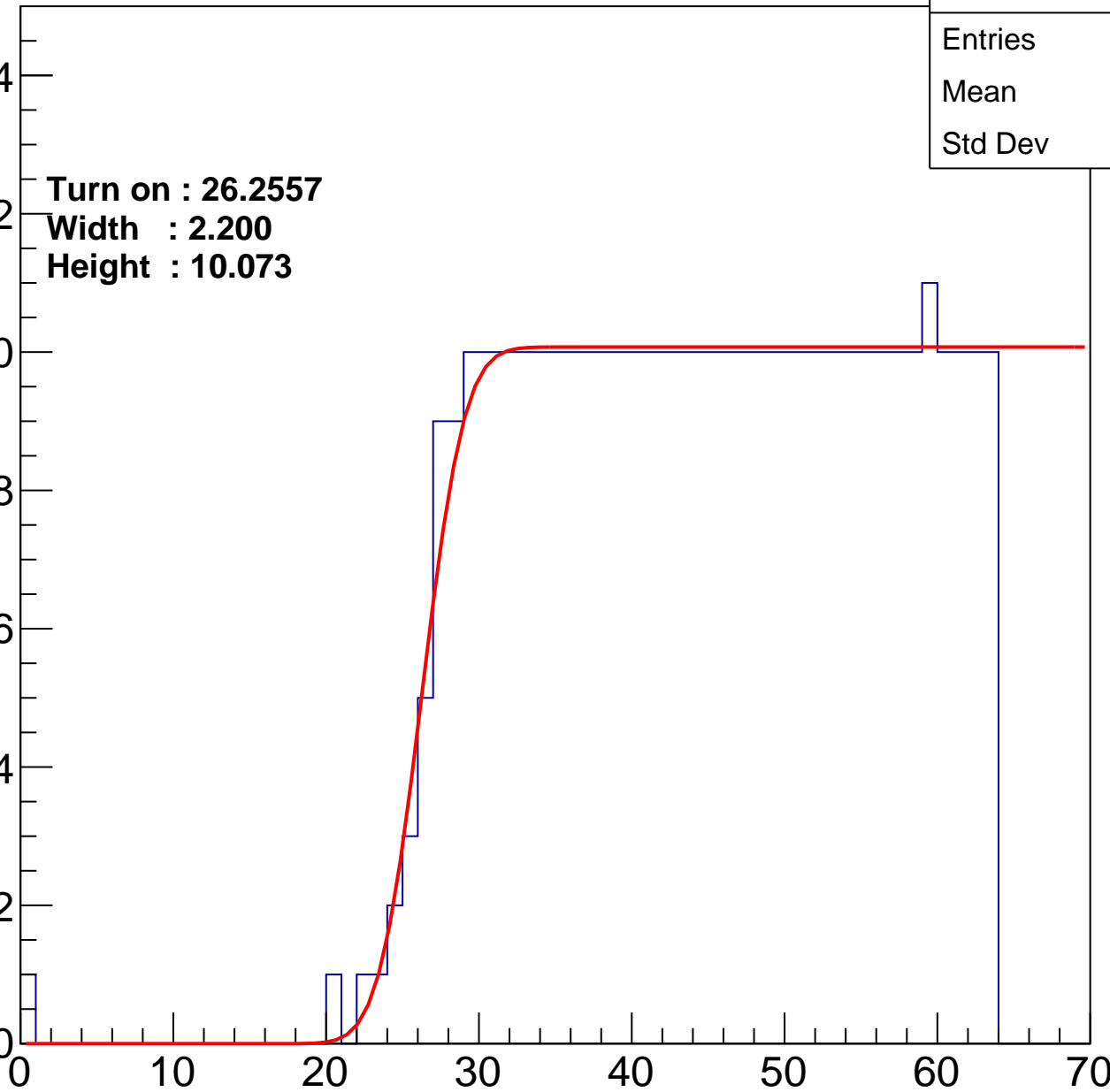
Width : 2.200

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.72
Std Dev	11.87

Turn on : 25.2426

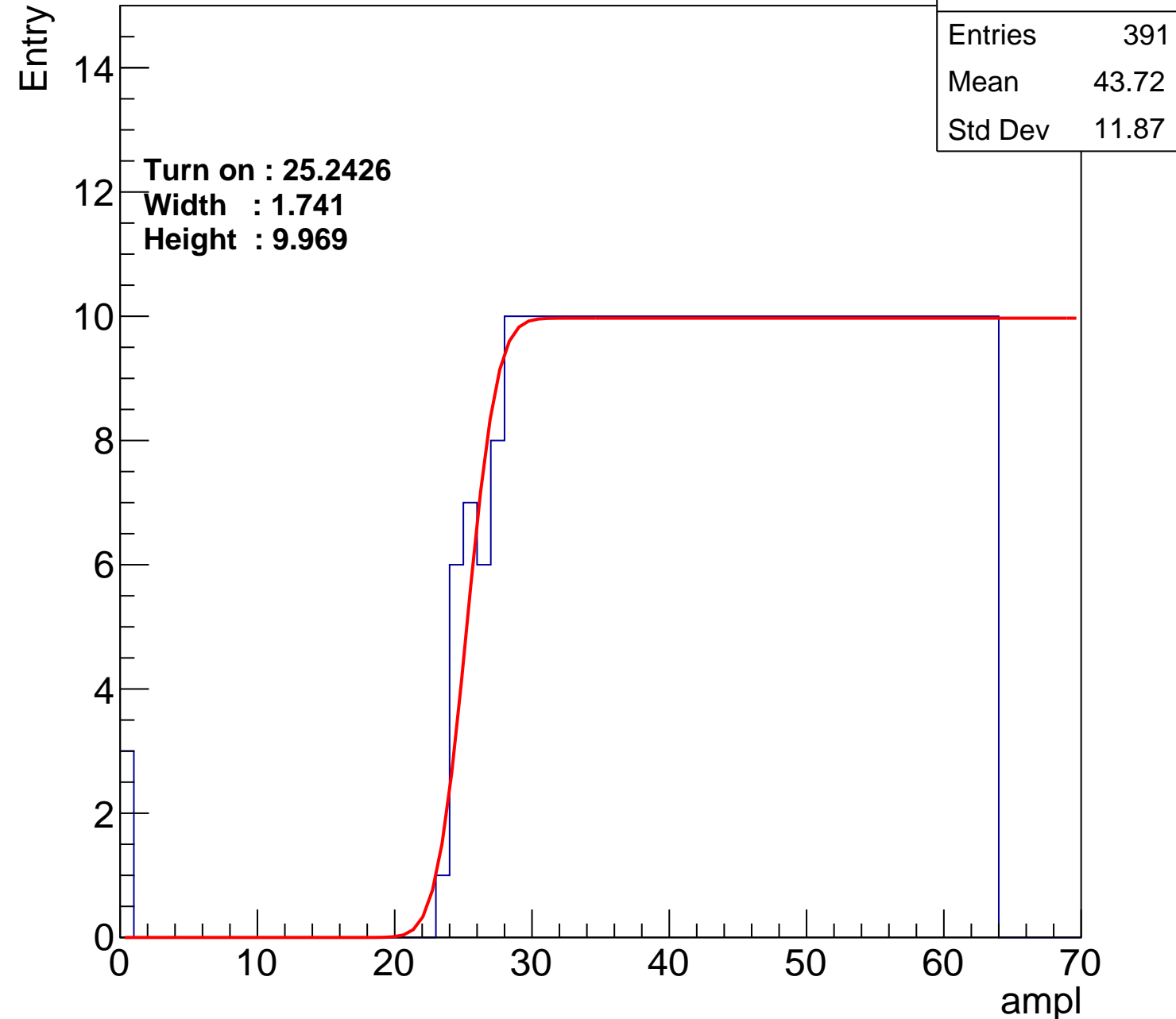
Width : 1.741

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.6003

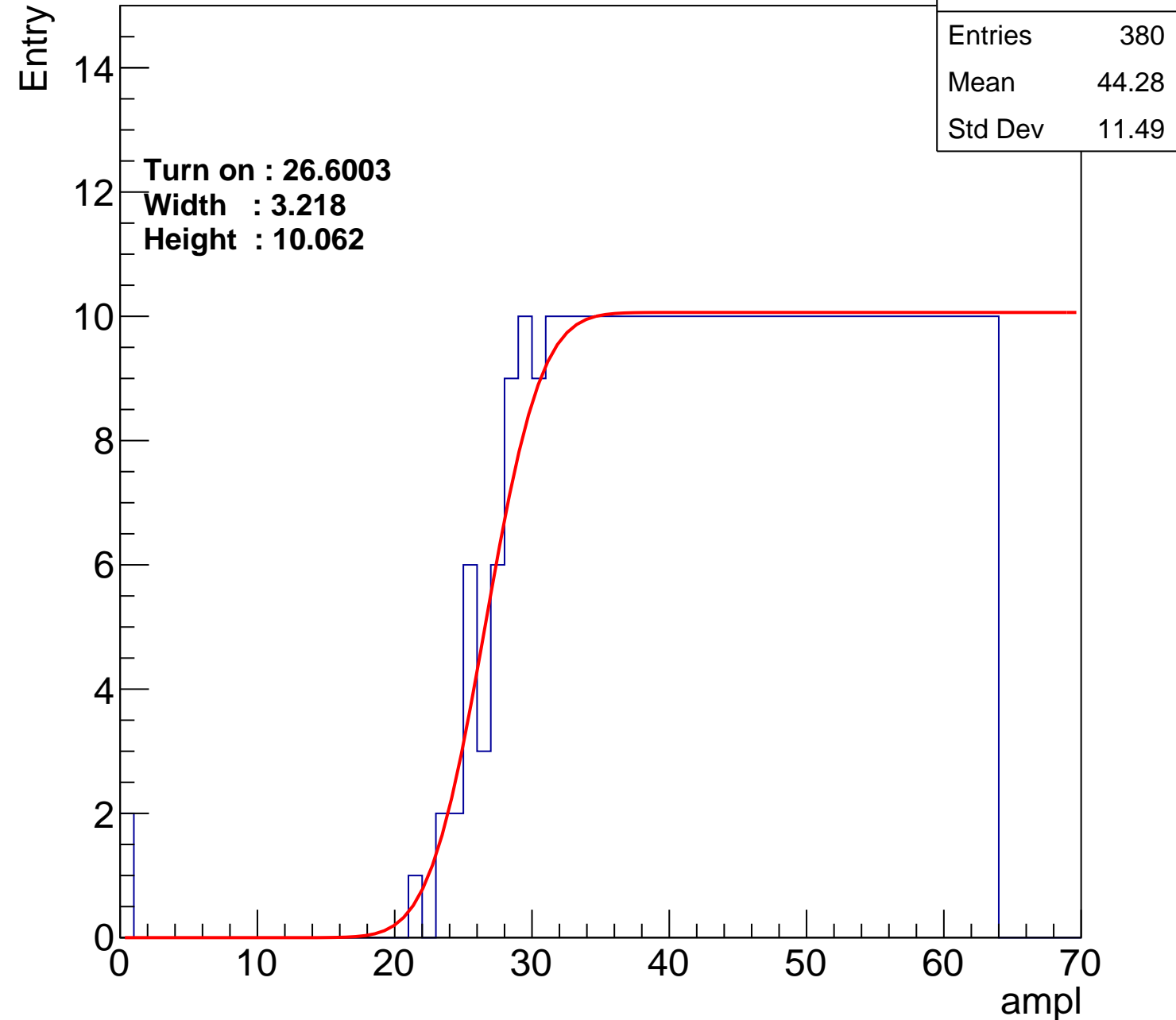
Width : 3.218

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.6908

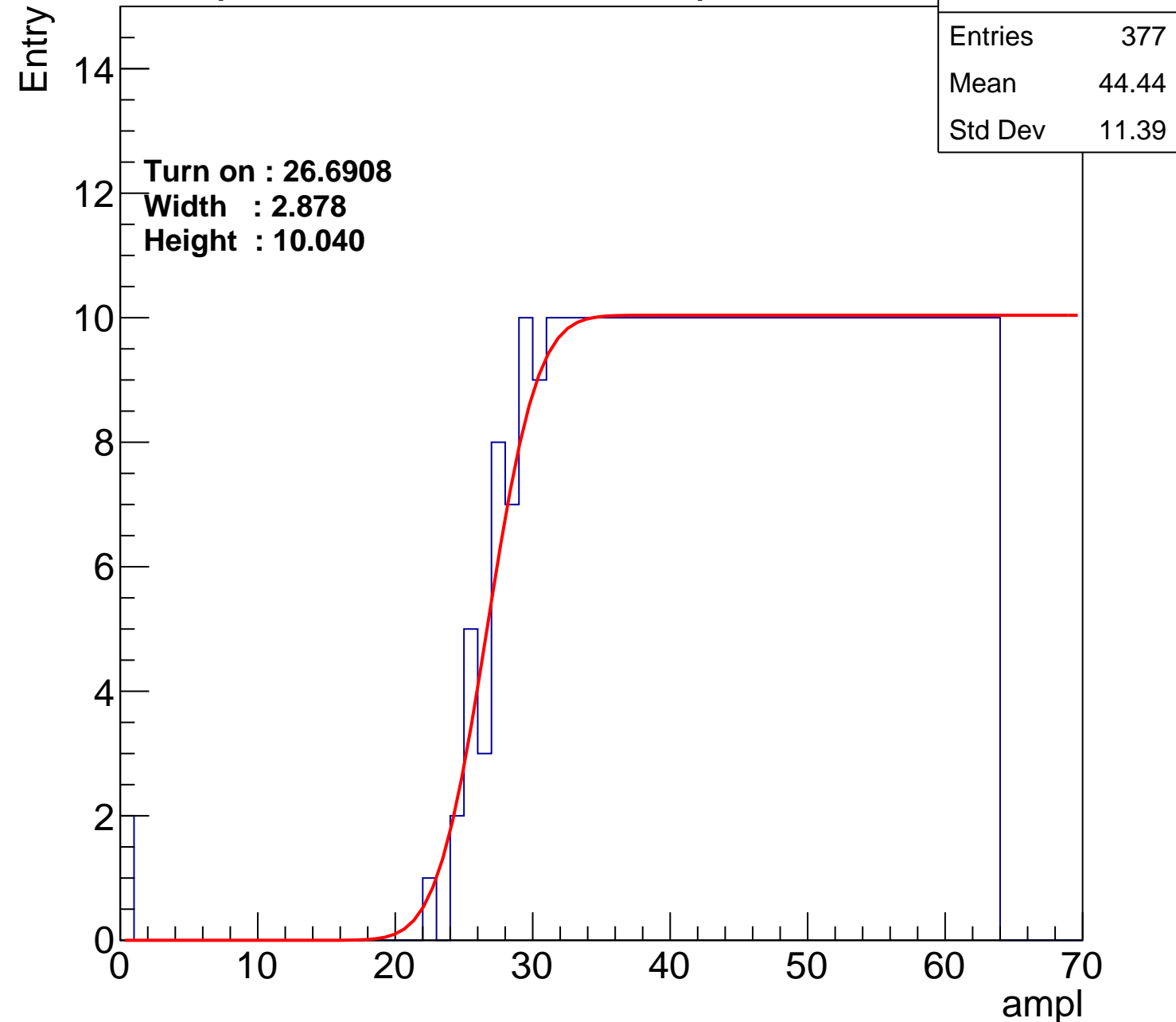
Width : 2.878

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch35

calib_packv5_042523_0143.root, FC#3, port B3

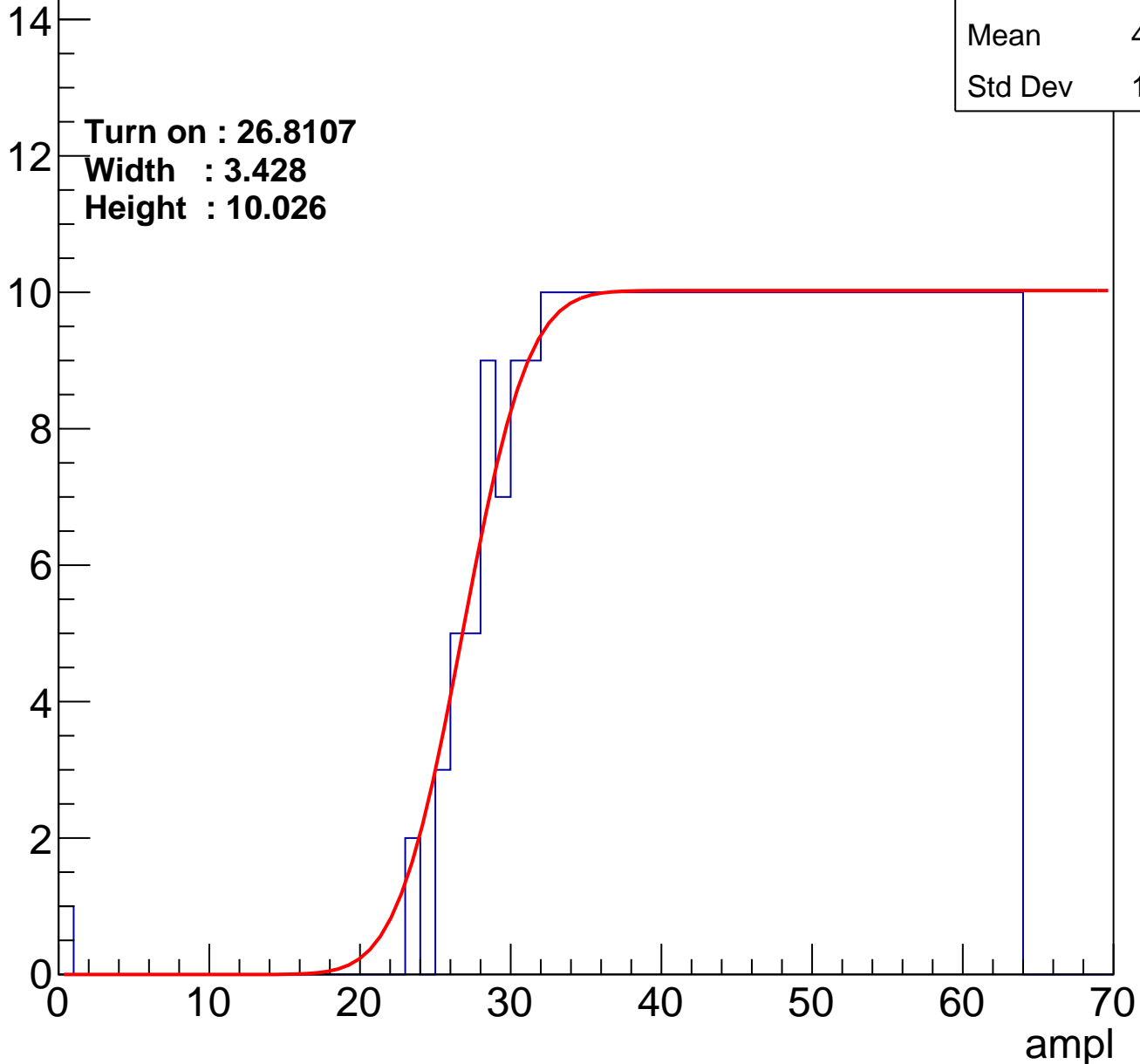
Entries	370
Mean	44.84
Std Dev	11.04

Turn on : 26.8107

Width : 3.428

Height : 10.026

Entry



B1L000S, U21-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.96
Std Dev	11.67

Turn on : 25.4156

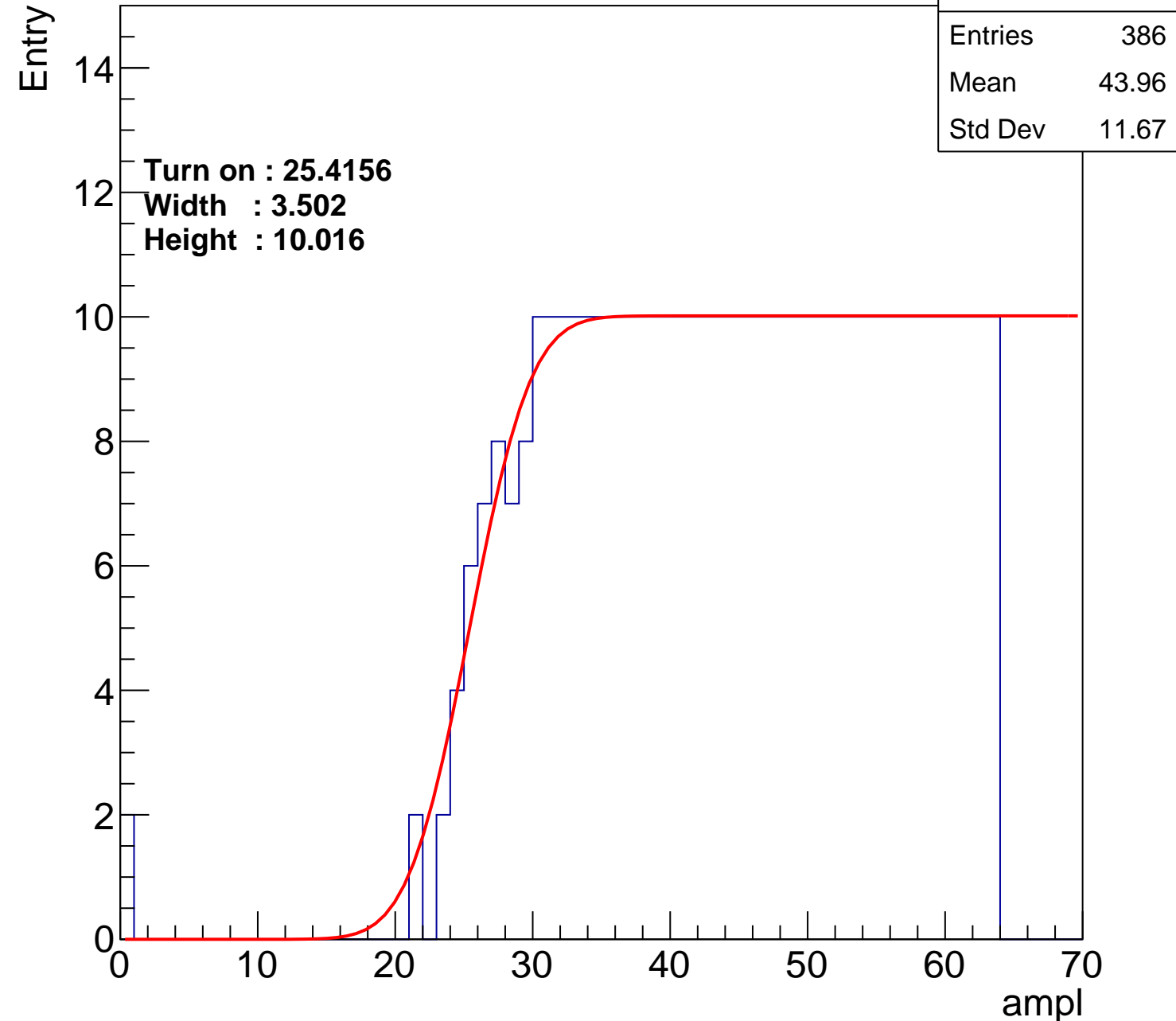
Width : 3.502

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.94
Std Dev	11.7

Turn on : 25.7839

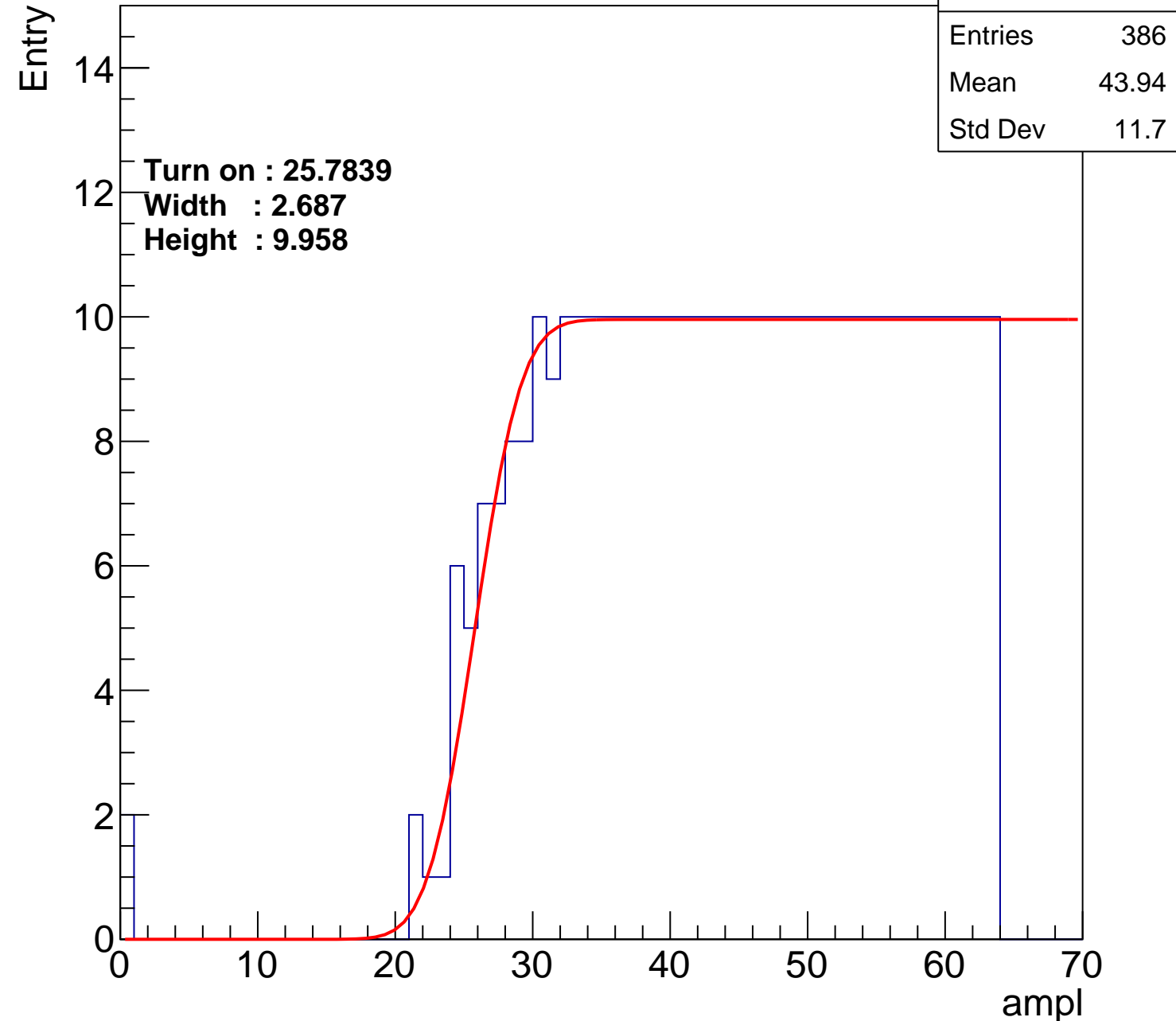
Width : 2.687

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.73
Std Dev	11.67

Turn on : 25.4232

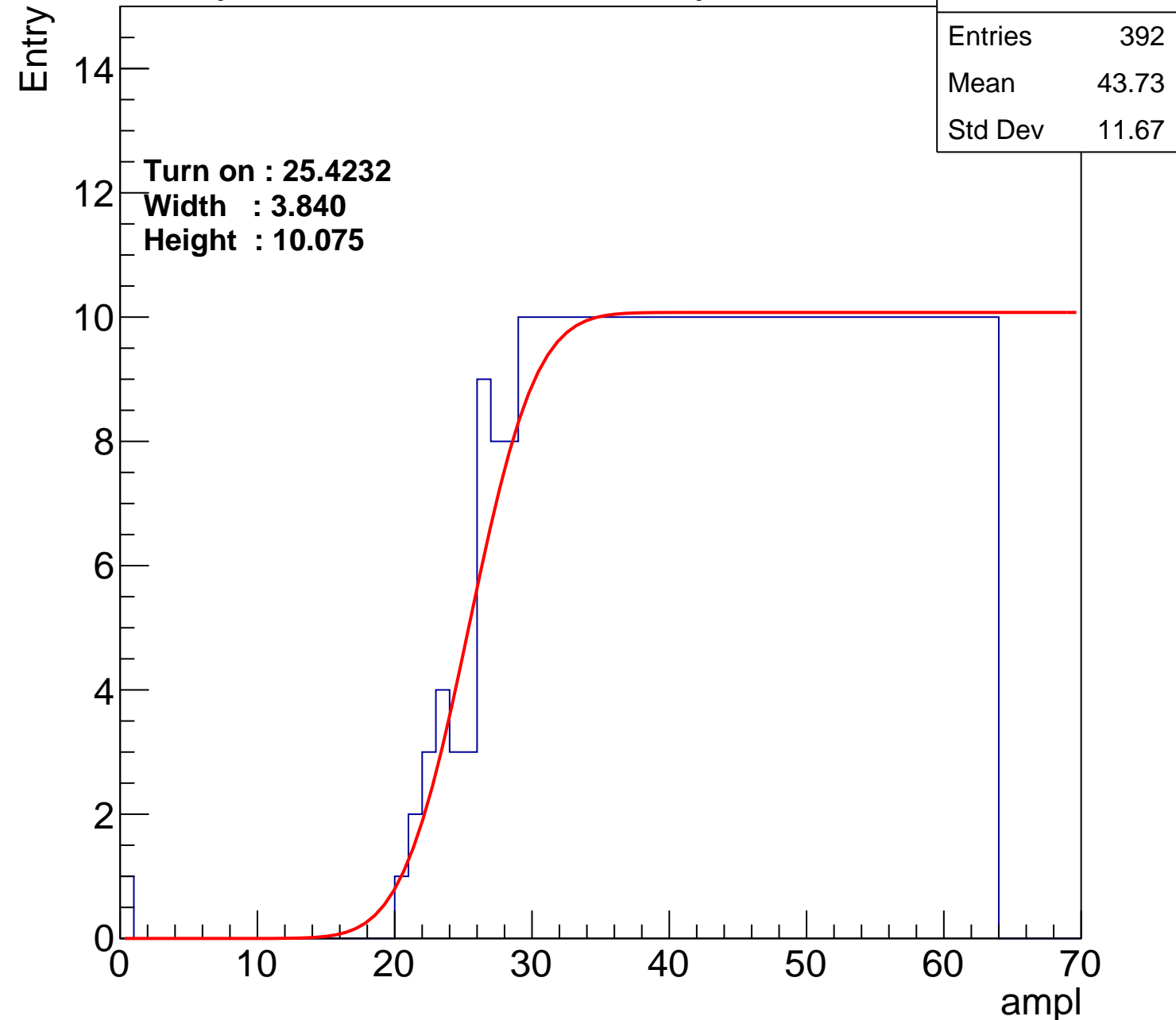
Width : 3.840

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.51
Std Dev	10.65

Turn on : 28.3635

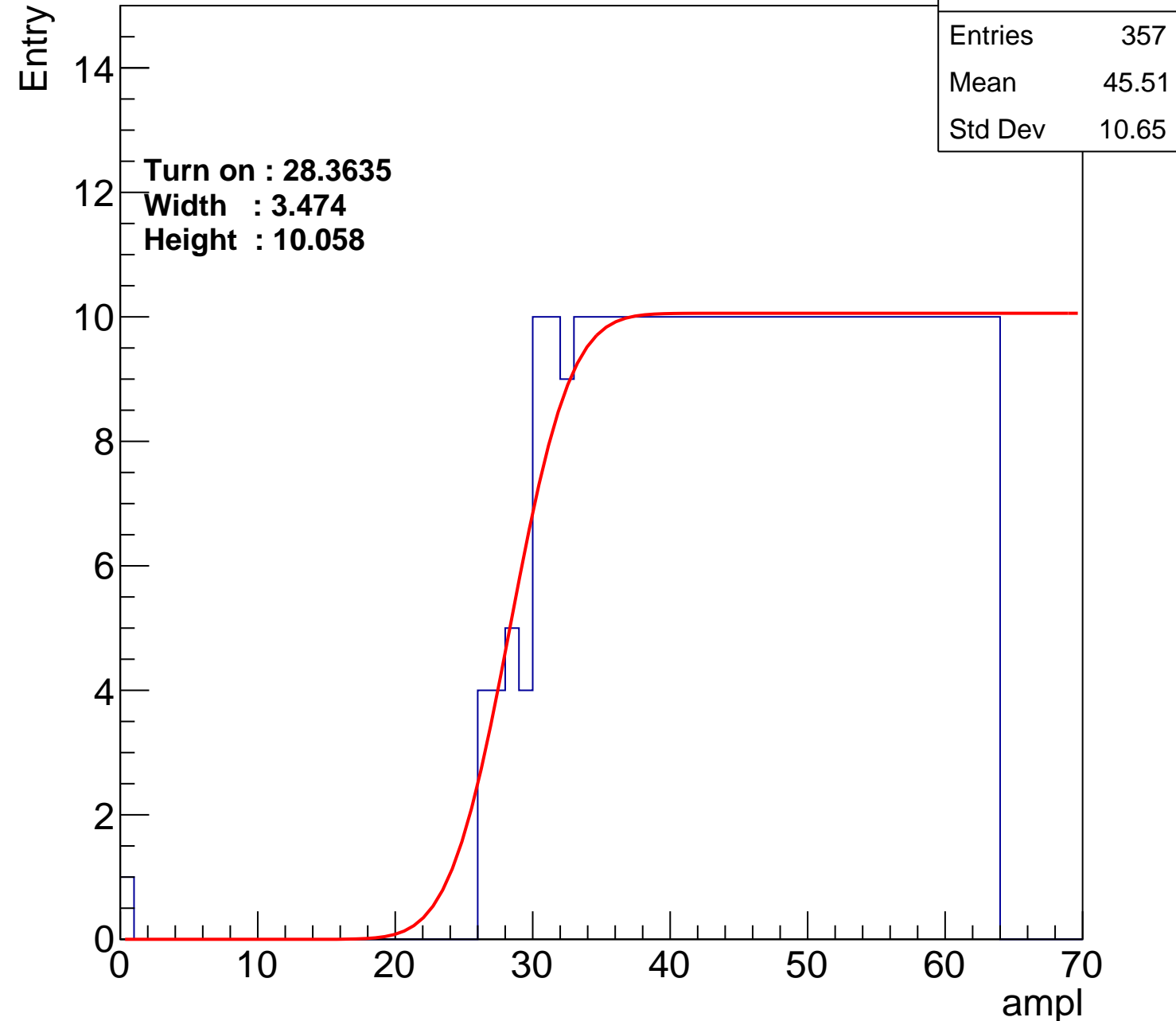
Width : 3.474

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.98
Std Dev	11.6

Turn on : 25.6132

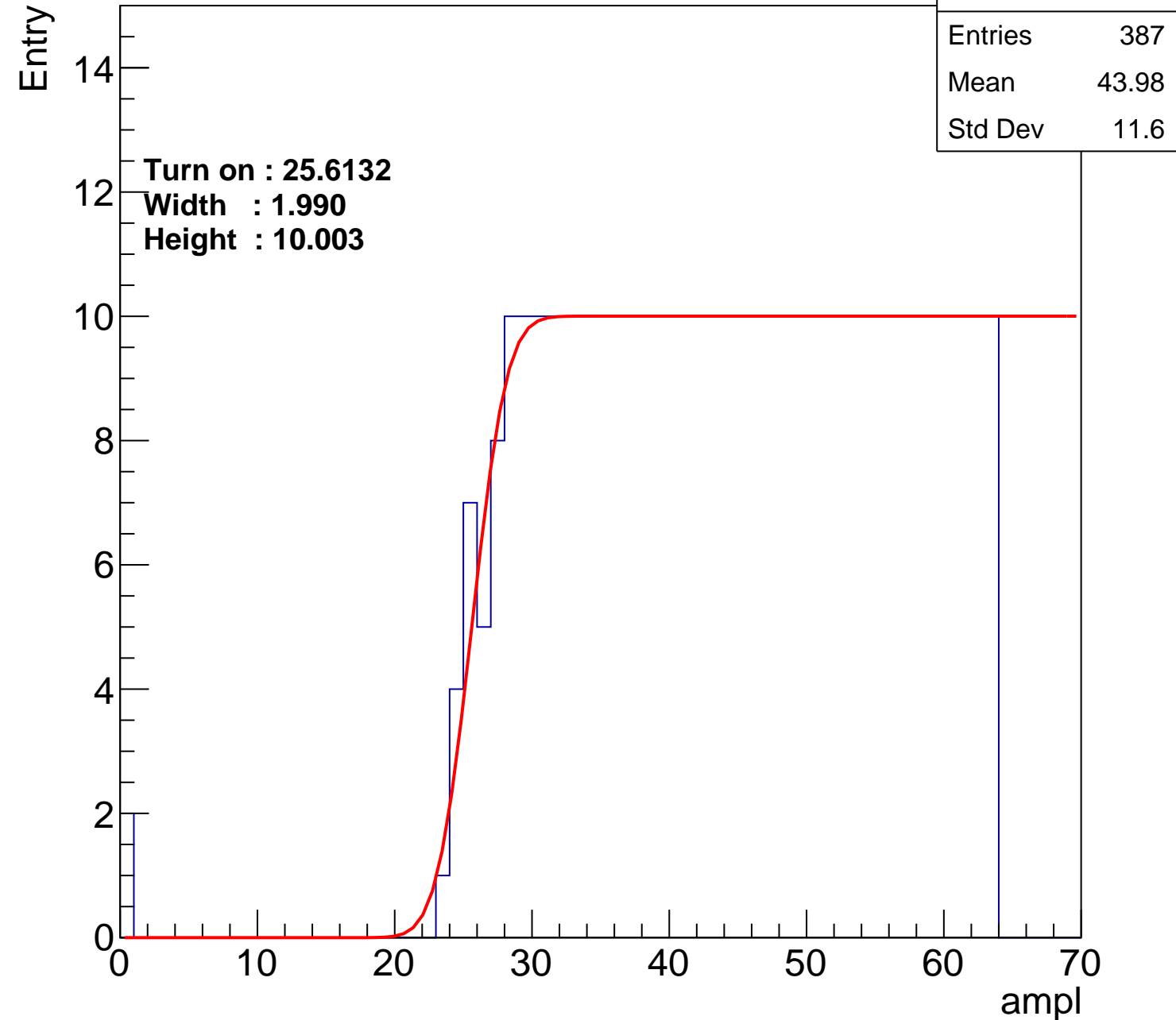
Width : 1.990

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch41

calib_packv5_042523_0143.root, FC#3, port B3

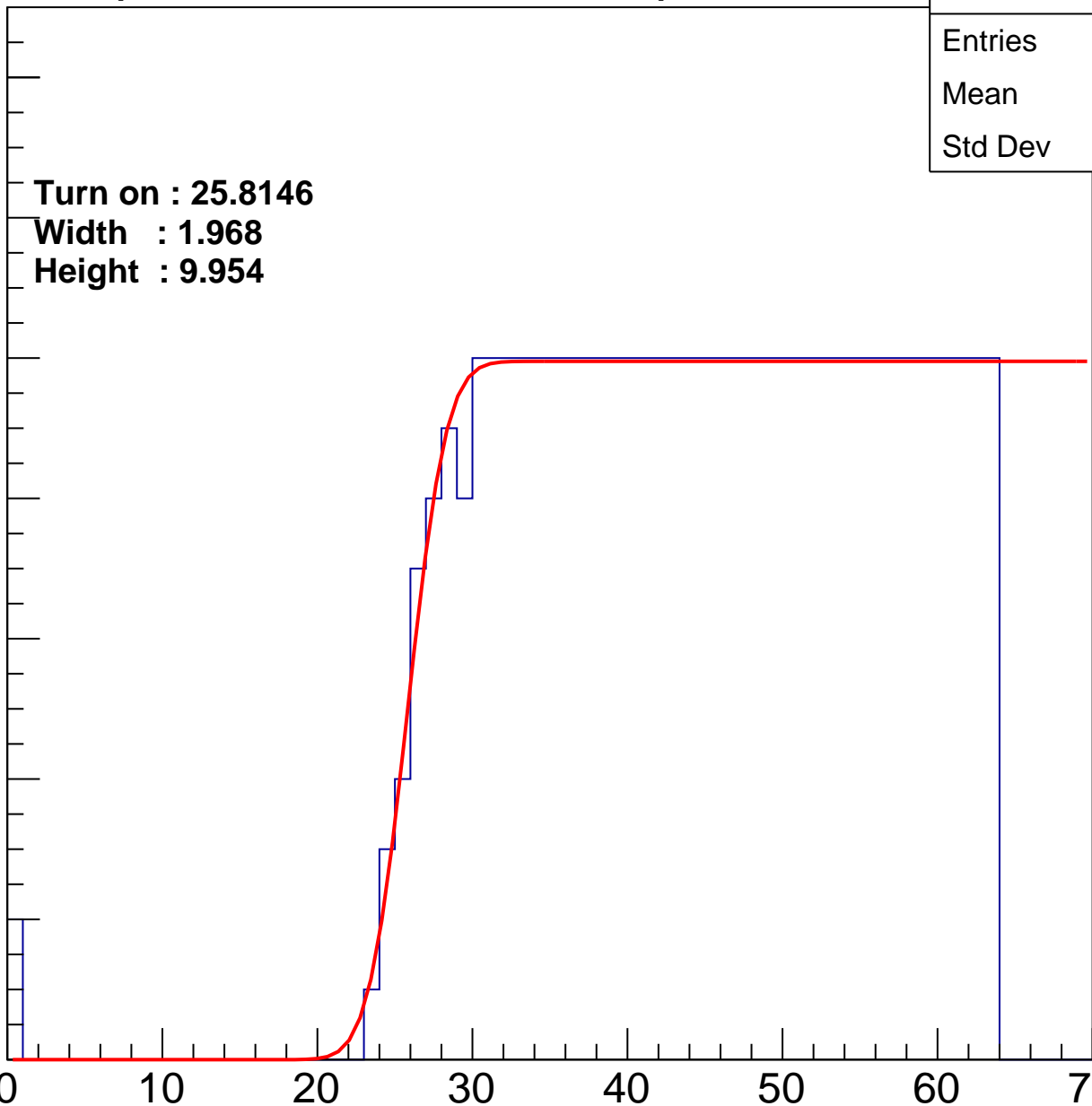
Entry

14
12
10
8
6
4
2
0

Turn on : 25.8146
Width : 1.968
Height : 9.954

Entries	382
Mean	44.21
Std Dev	11.49

ampl



B1L000S, U21-ch42

calib_packv5_042523_0143.root, FC#3, port B3

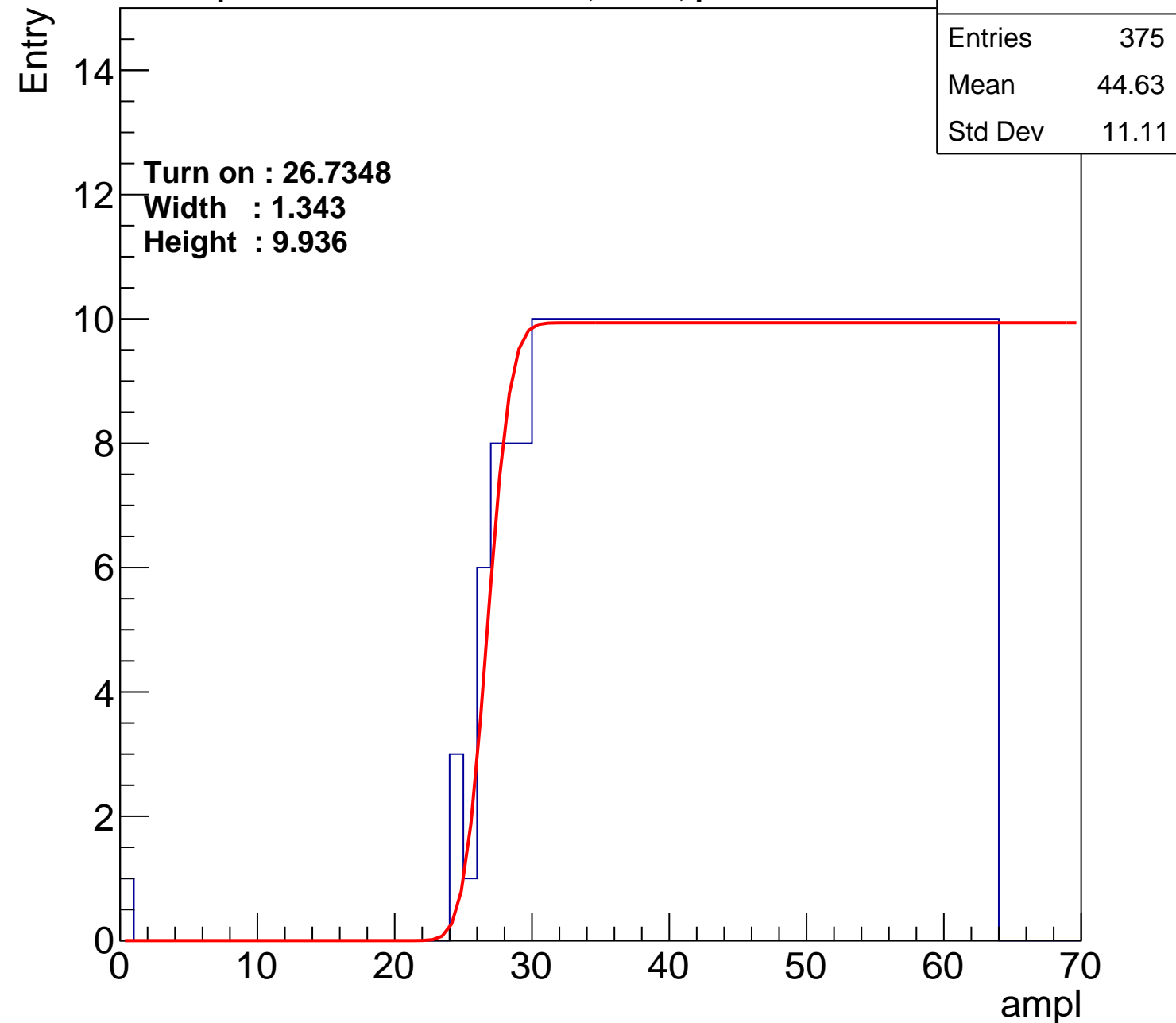
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7348
Width : 1.343
Height : 9.936

Entries	375
Mean	44.63
Std Dev	11.11

0 10 20 30 40 50 60 70
ampl



B1L000S, U21-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.14
Std Dev	11.21

Turn on : 27.5614

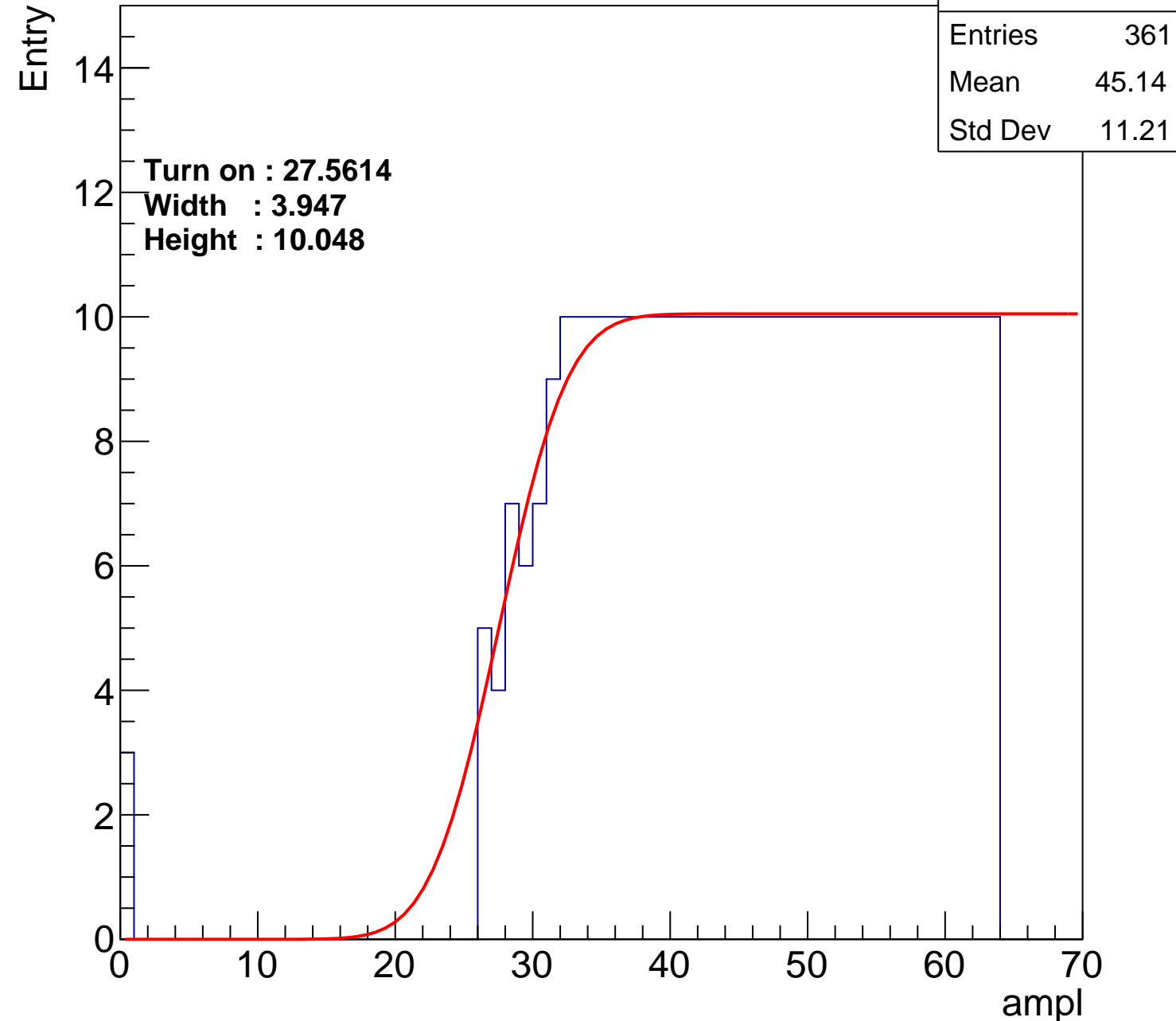
Width : 3.947

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch44

calib_packv5_042523_0143.root, FC#3, port B3

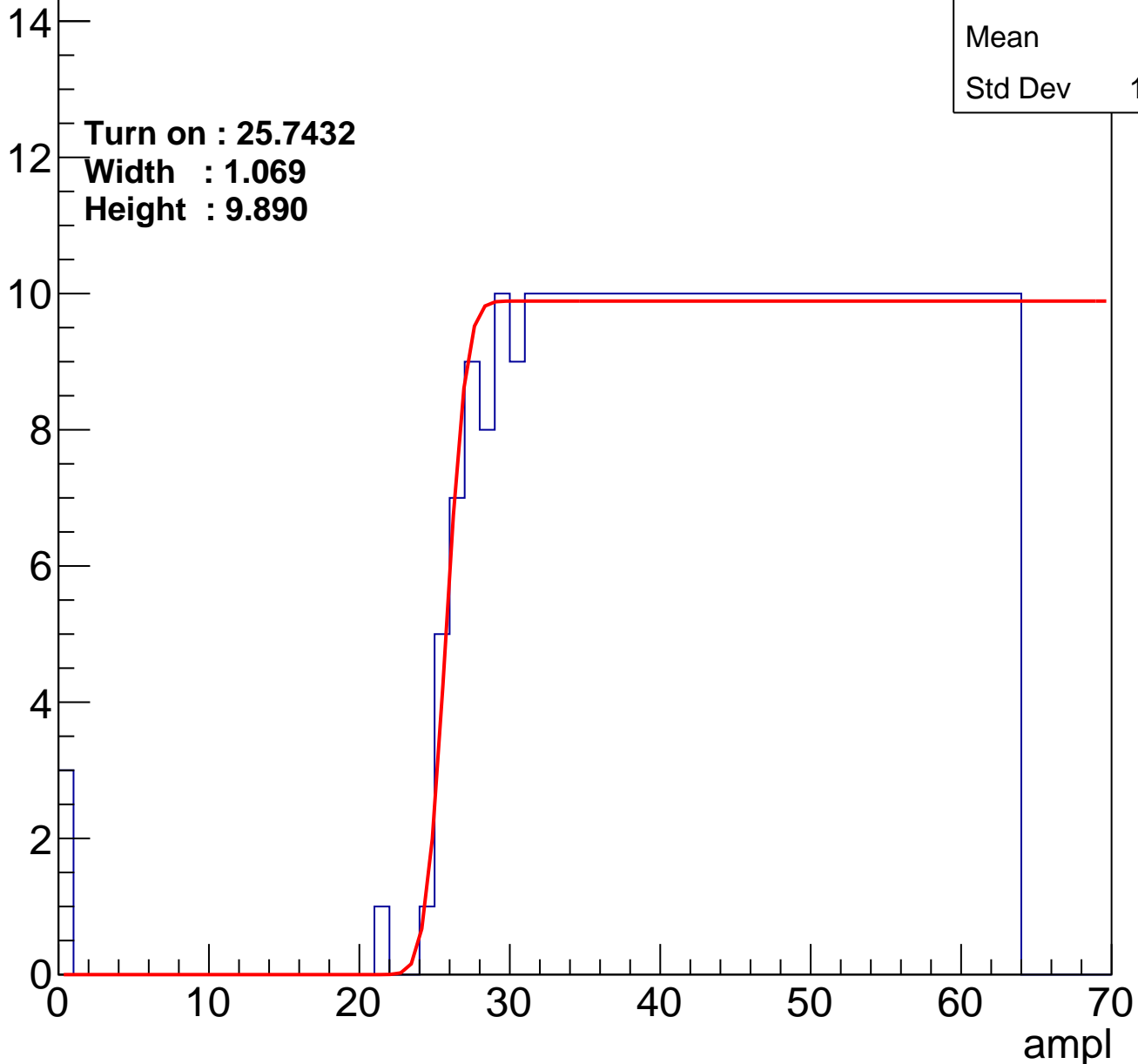
Entries	383
Mean	44.1
Std Dev	11.69

Turn on : 25.7432

Width : 1.069

Height : 9.890

Entry



B1L000S, U21-ch45

calib_packv5_042523_0143.root, FC#3, port B3

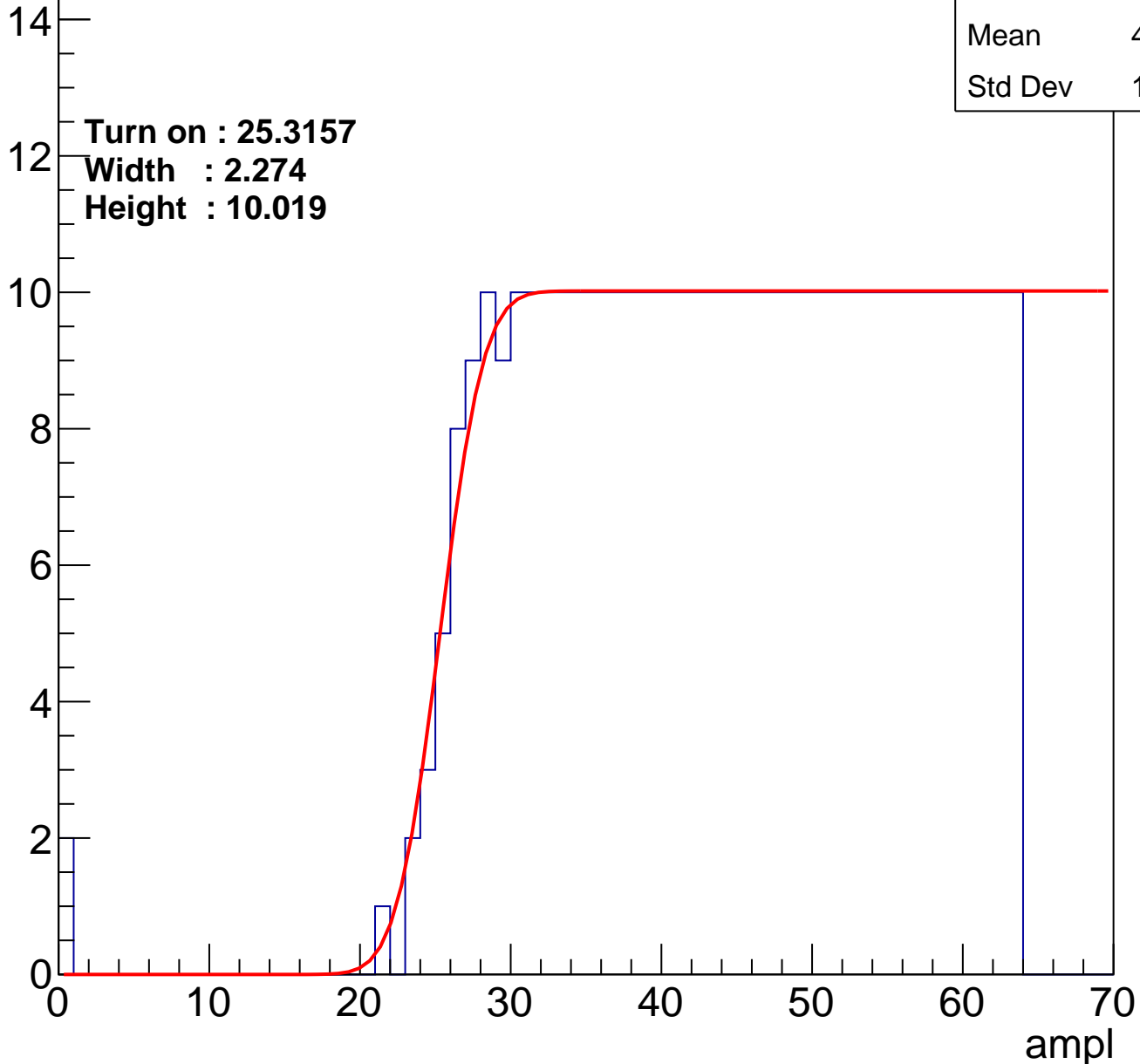
Entries	389
Mean	43.87
Std Dev	11.66

Turn on : 25.3157

Width : 2.274

Height : 10.019

Entry



B1L000S, U21-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 26.8428

Width : 3.061

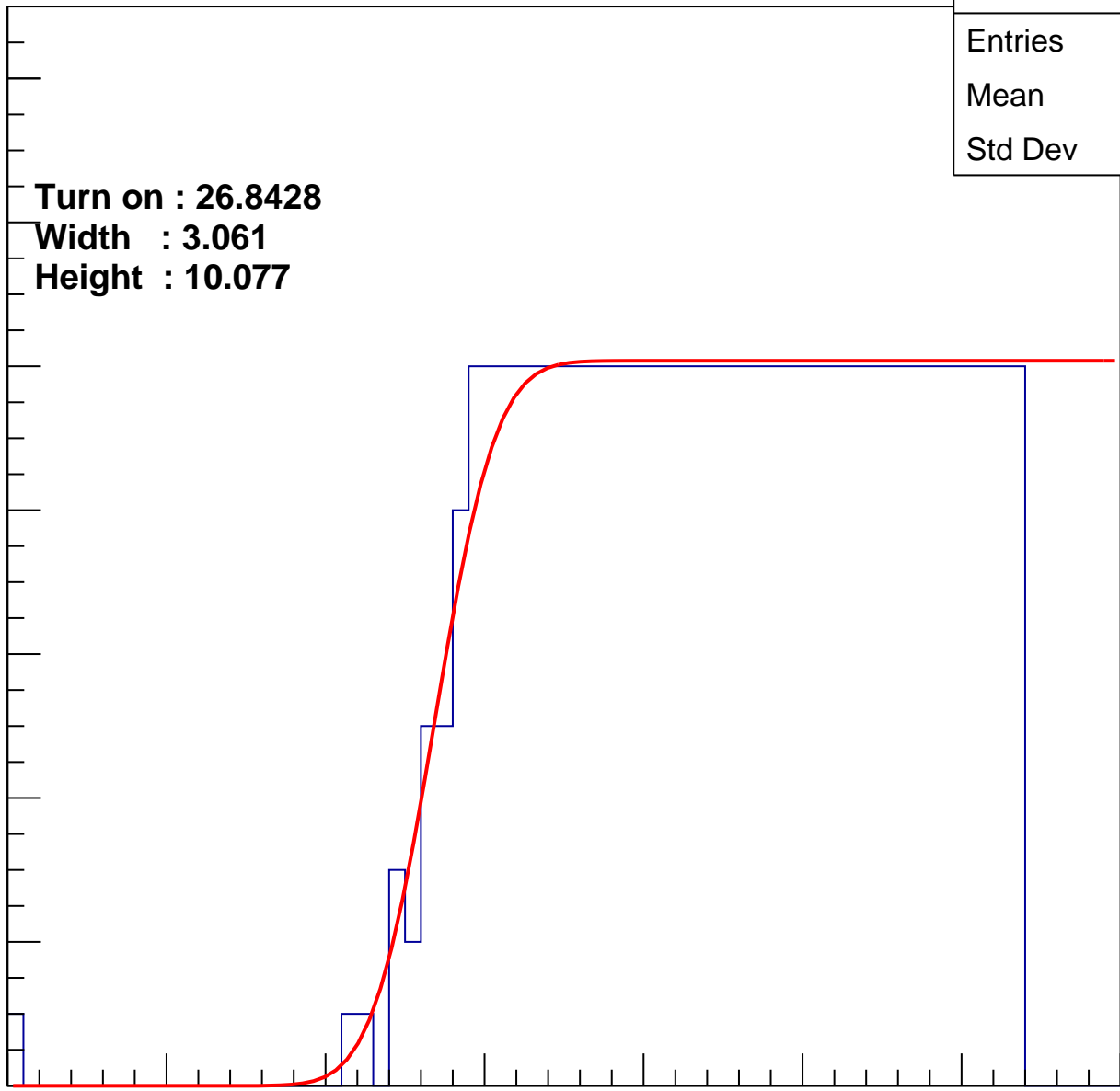
Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.2589

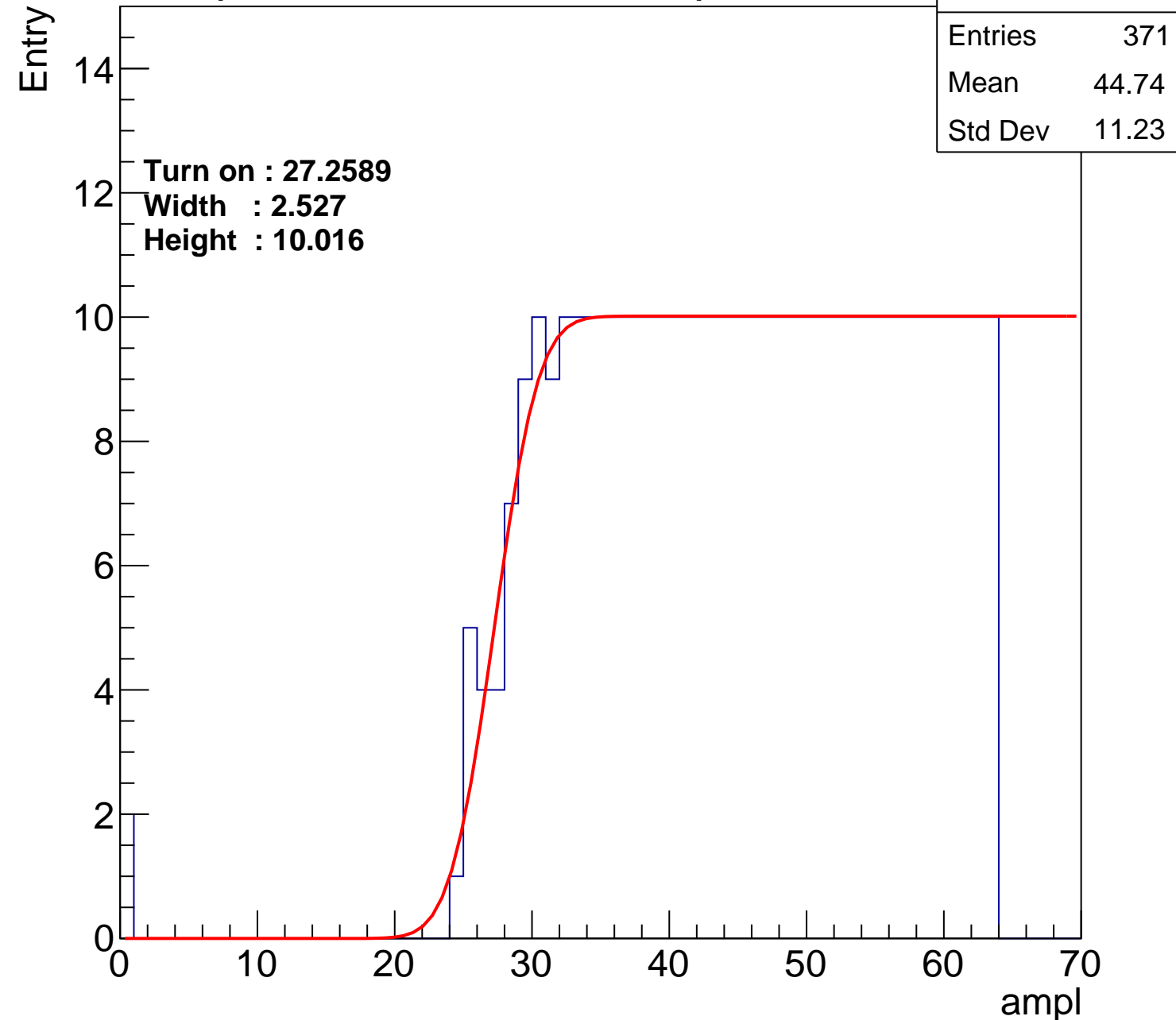
Width : 2.527

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch48

calib_packv5_042523_0143.root, FC#3, port B3

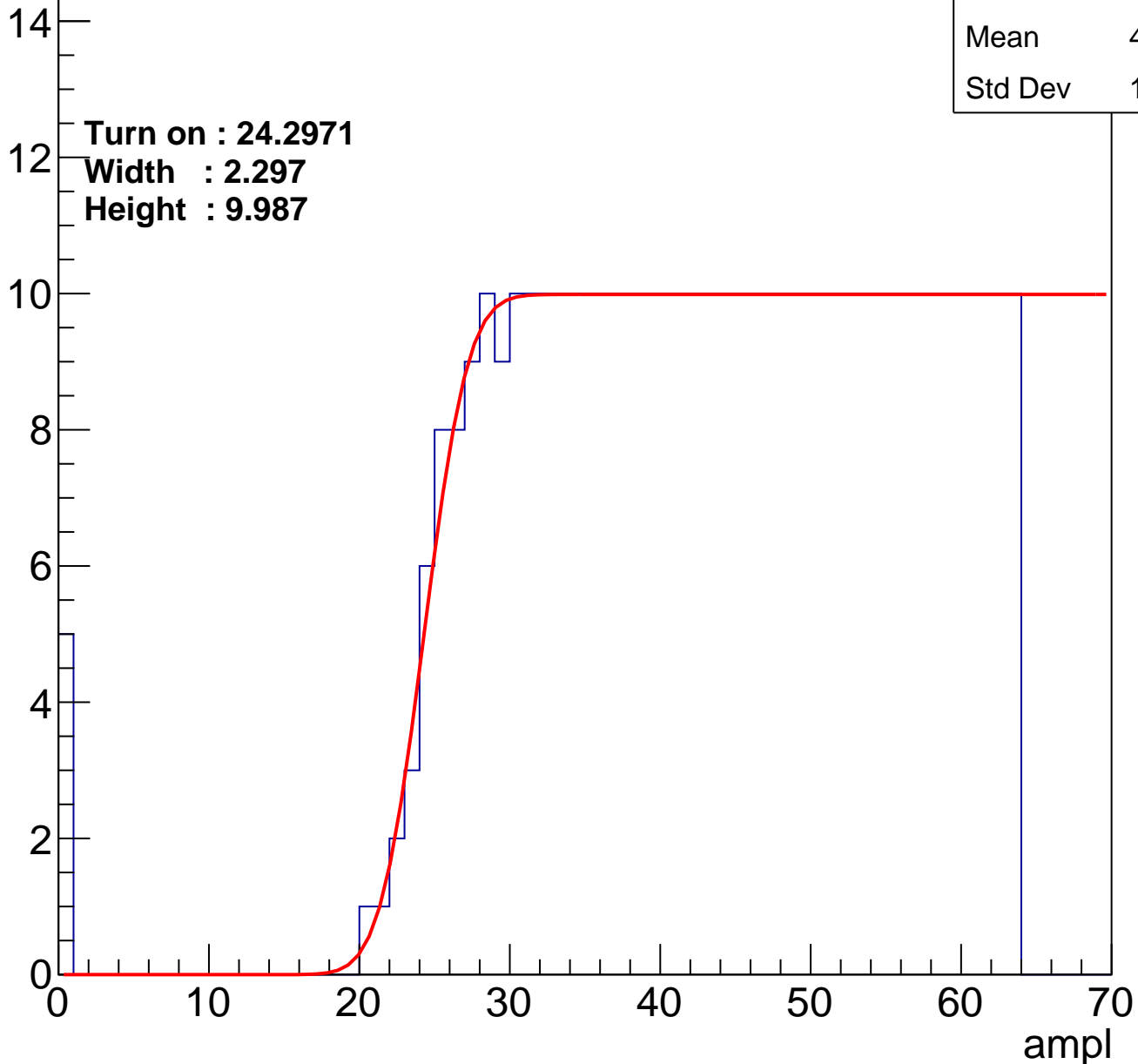
Entries	402
Mean	43.03
Std Dev	12.48

Turn on : 24.2971

Width : 2.297

Height : 9.987

Entry



B1L000S, U21-ch49

calib_packv5_042523_0143.root, FC#3, port B3

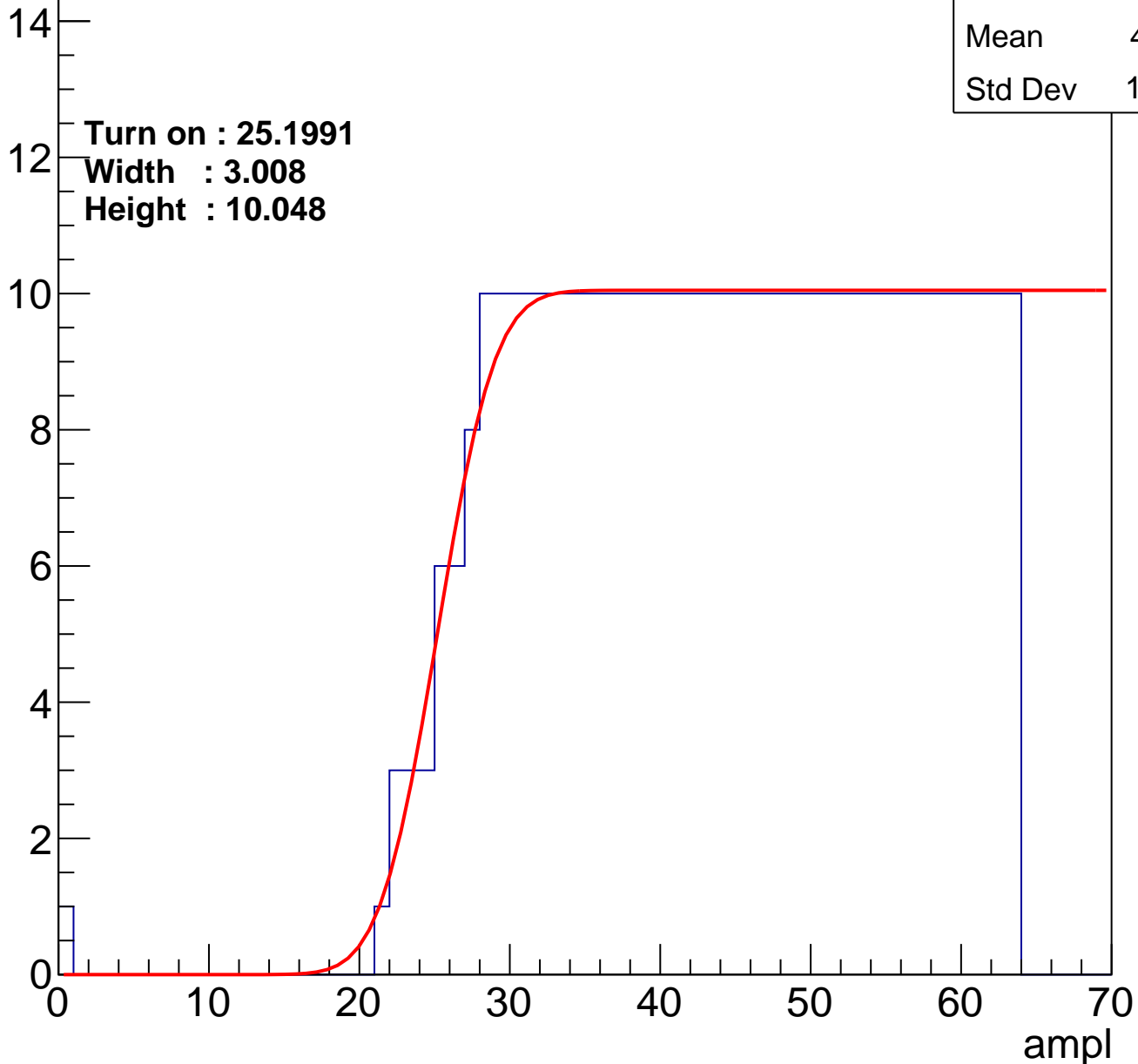
Entries	391
Mean	43.81
Std Dev	11.59

Turn on : 25.1991

Width : 3.008

Height : 10.048

Entry



B1L000S, U21-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.75
Std Dev	11.76

Turn on : 25.1552

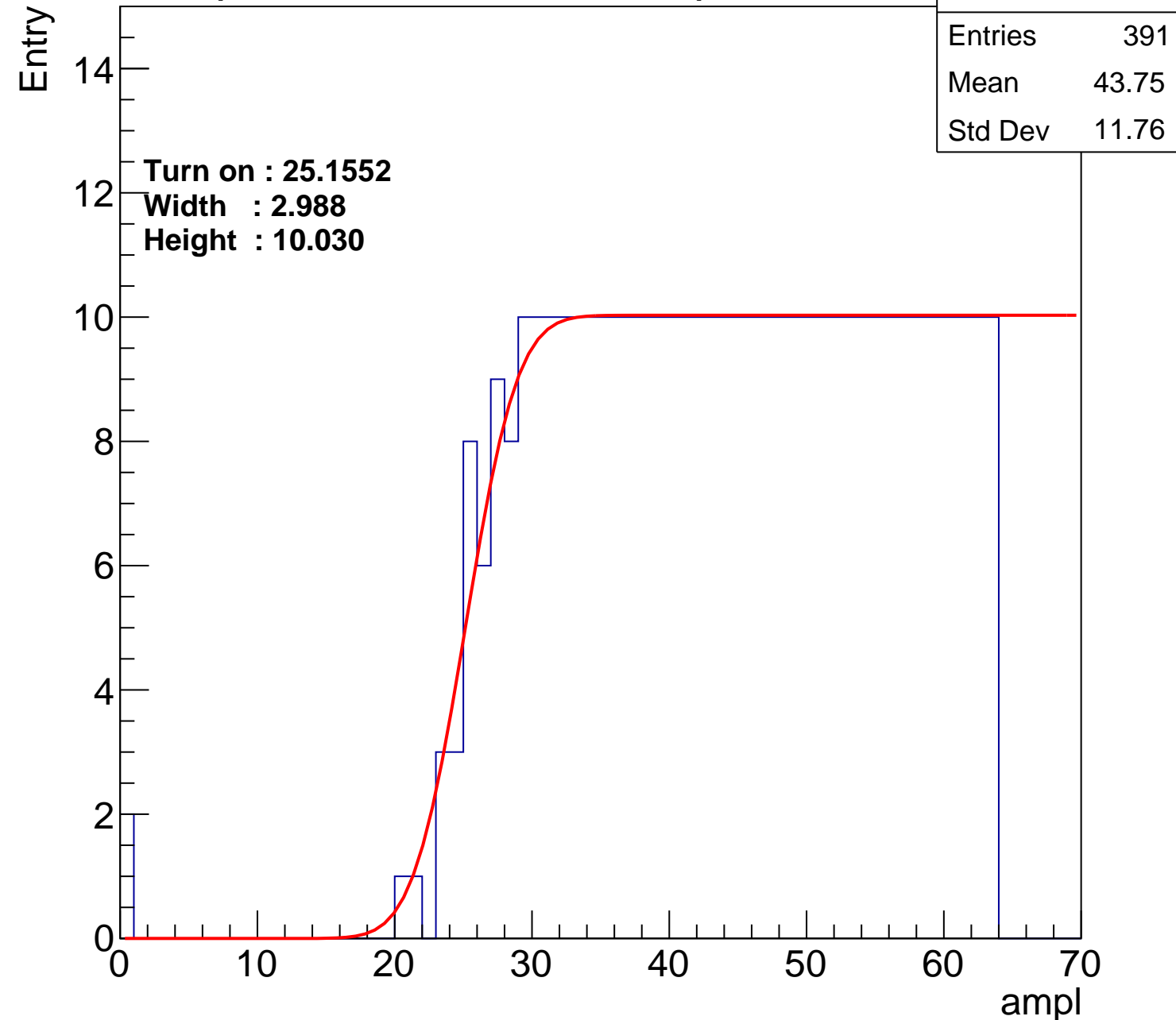
Width : 2.988

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch51

calib_packv5_042523_0143.root, FC#3, port B3

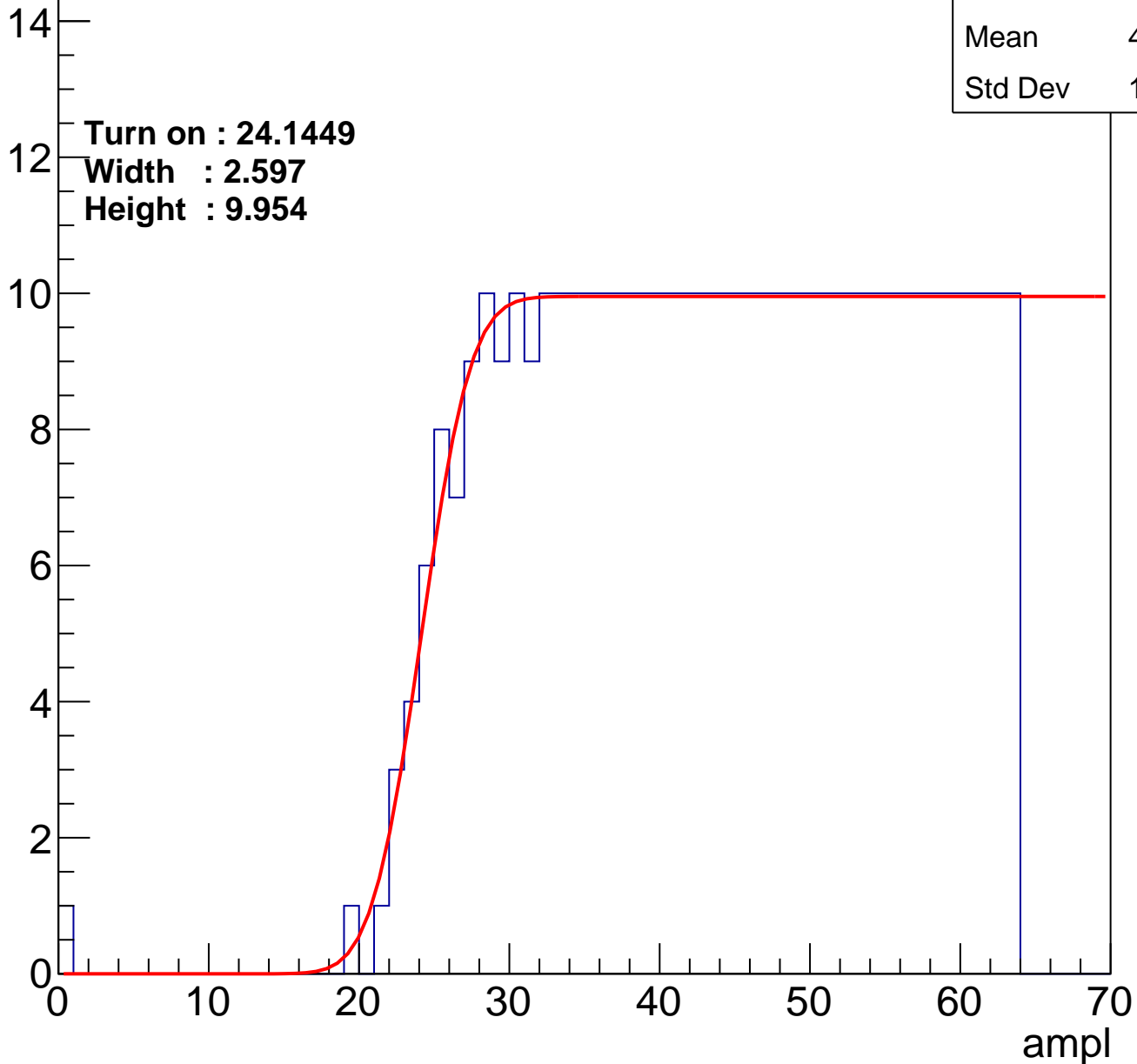
Entries	398
Mean	43.43
Std Dev	11.82

Turn on : 24.1449

Width : 2.597

Height : 9.954

Entry



B1L000S, U21-ch52

calib_packv5_042523_0143.root, FC#3, port B3

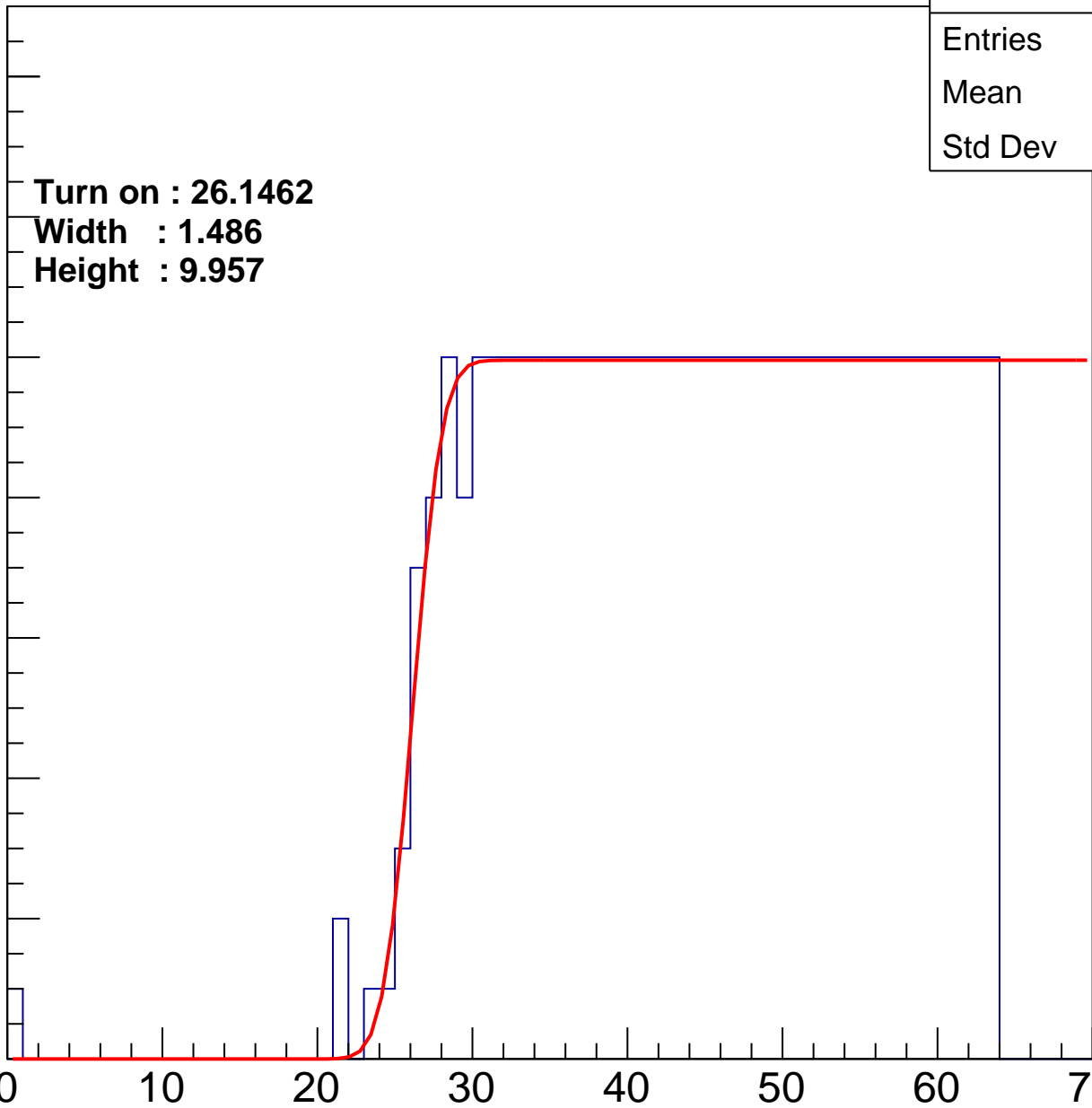
Entry

14
12
10
8
6
4
2
0

Turn on : 26.1462
Width : 1.486
Height : 9.957

Entries	381
Mean	44.31
Std Dev	11.3

ampl



B1L000S, U21-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.97
Std Dev	11.13

Turn on : 27.8828

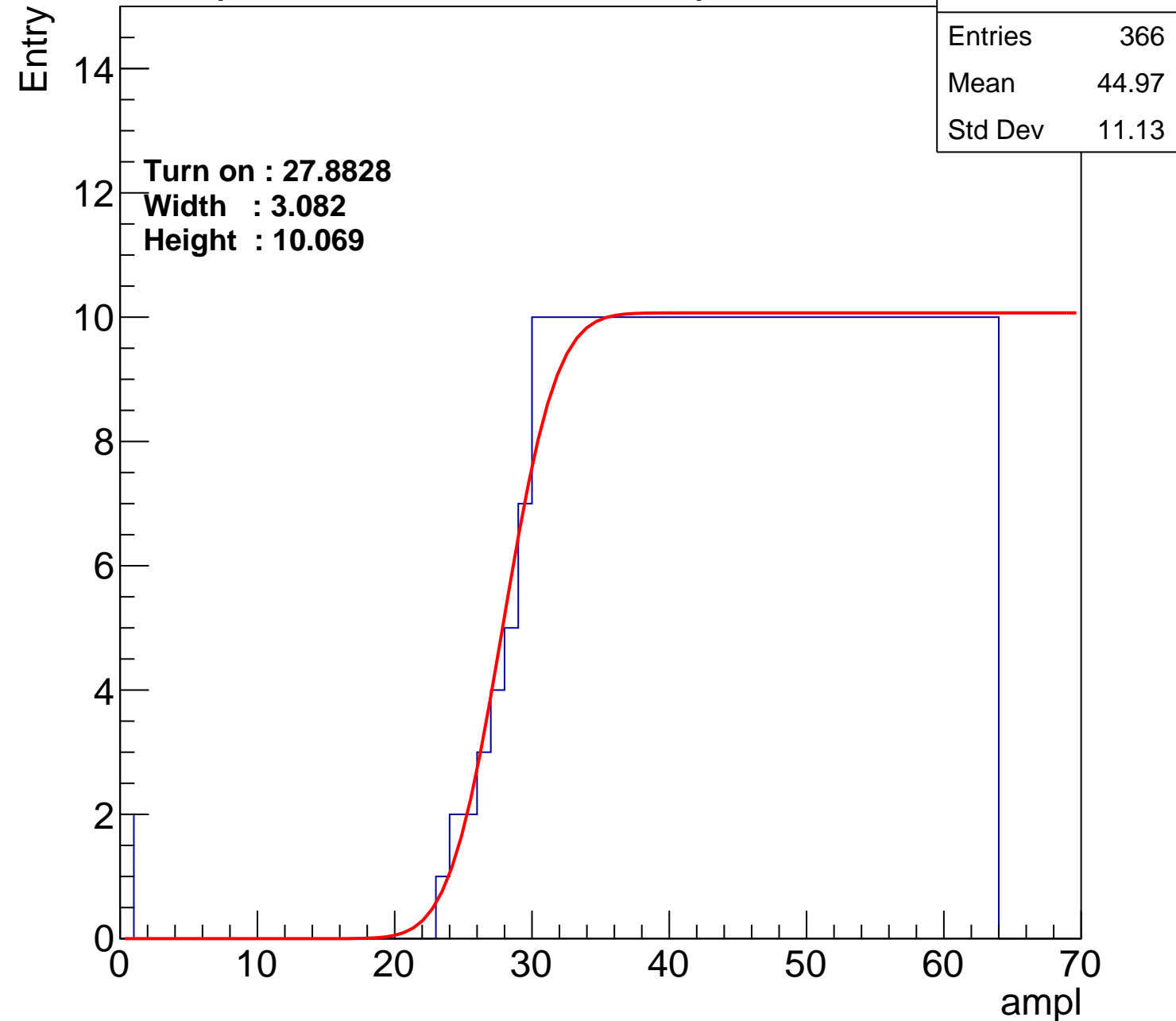
Width : 3.082

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch54

calib_packv5_042523_0143.root, FC#3, port B3

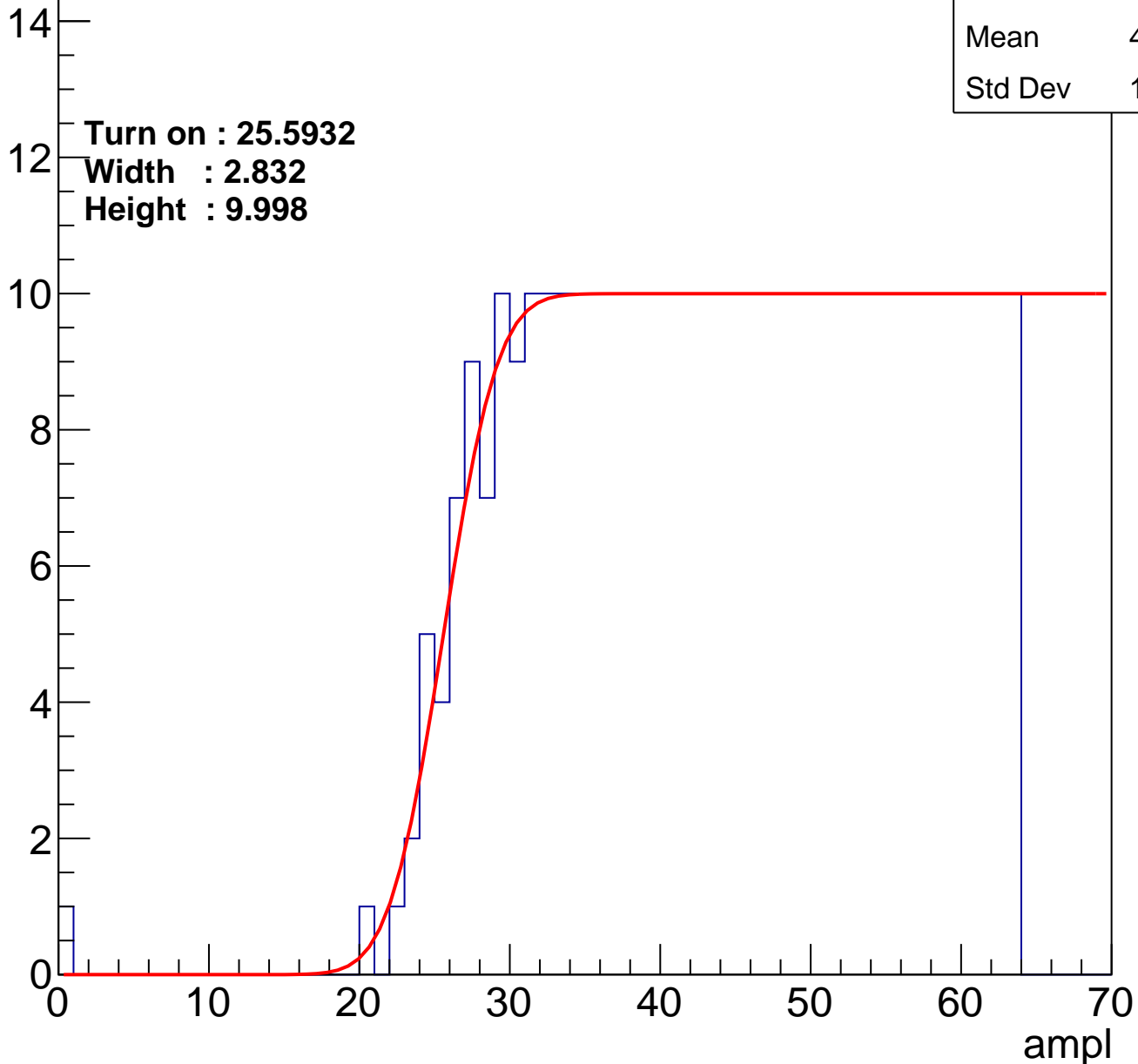
Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.5932

Width : 2.832

Height : 9.998

Entry



B1L000S, U21-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 28.1056

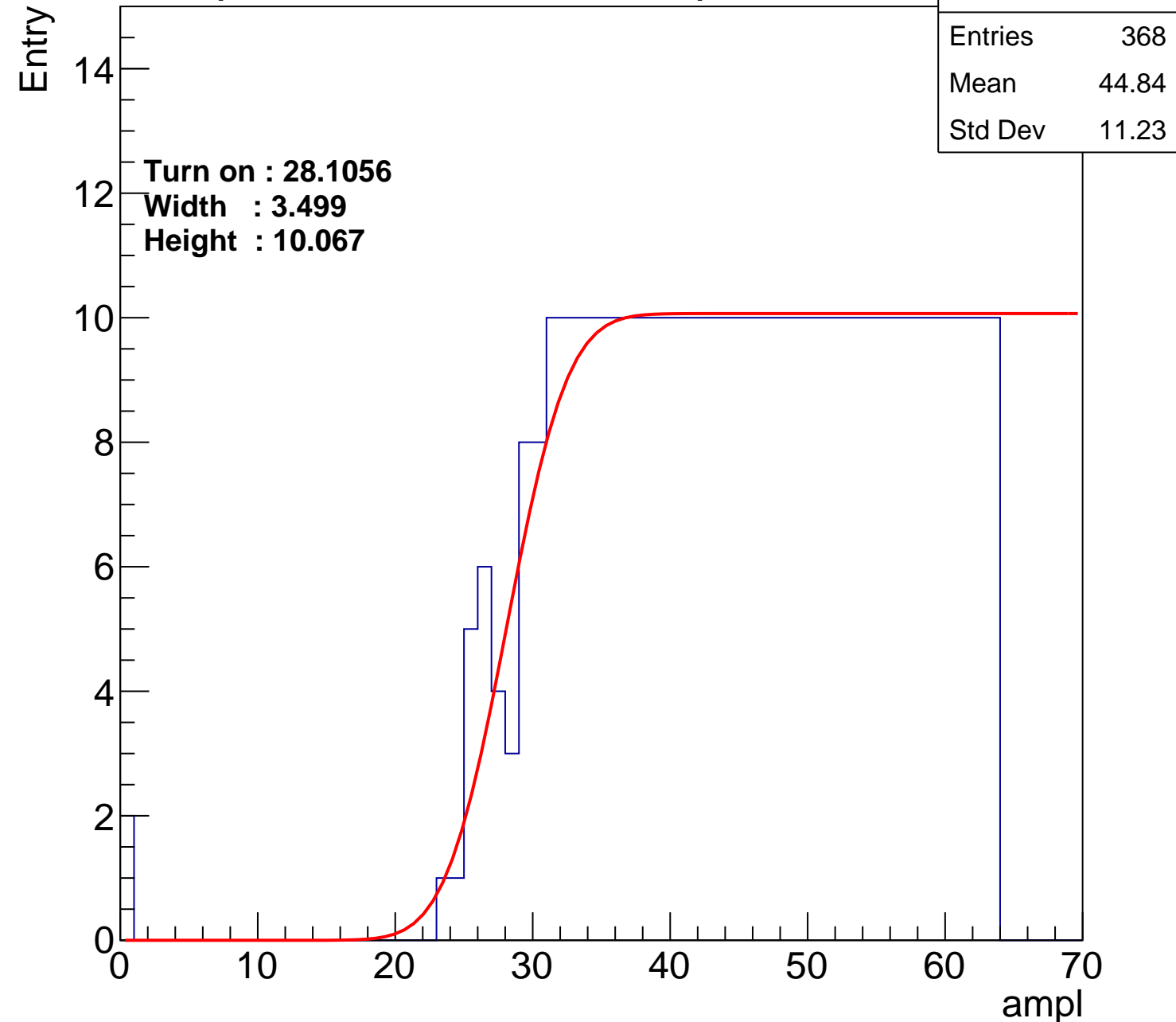
Width : 3.499

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch56

calib_packv5_042523_0143.root, FC#3, port B3

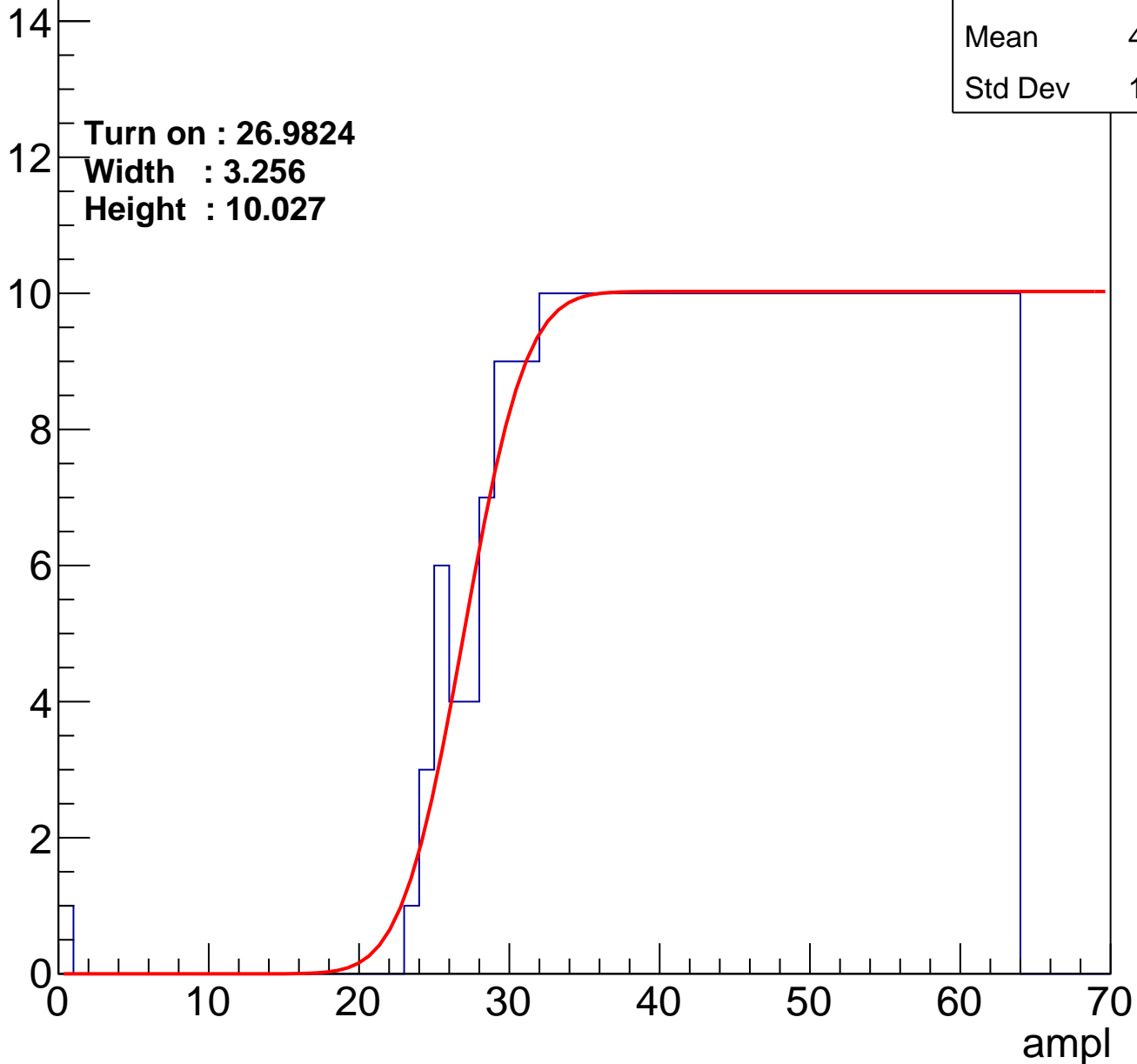
Entries	373
Mean	44.67
Std Dev	11.14

Turn on : 26.9824

Width : 3.256

Height : 10.027

Entry



B1L000S, U21-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 25.8695

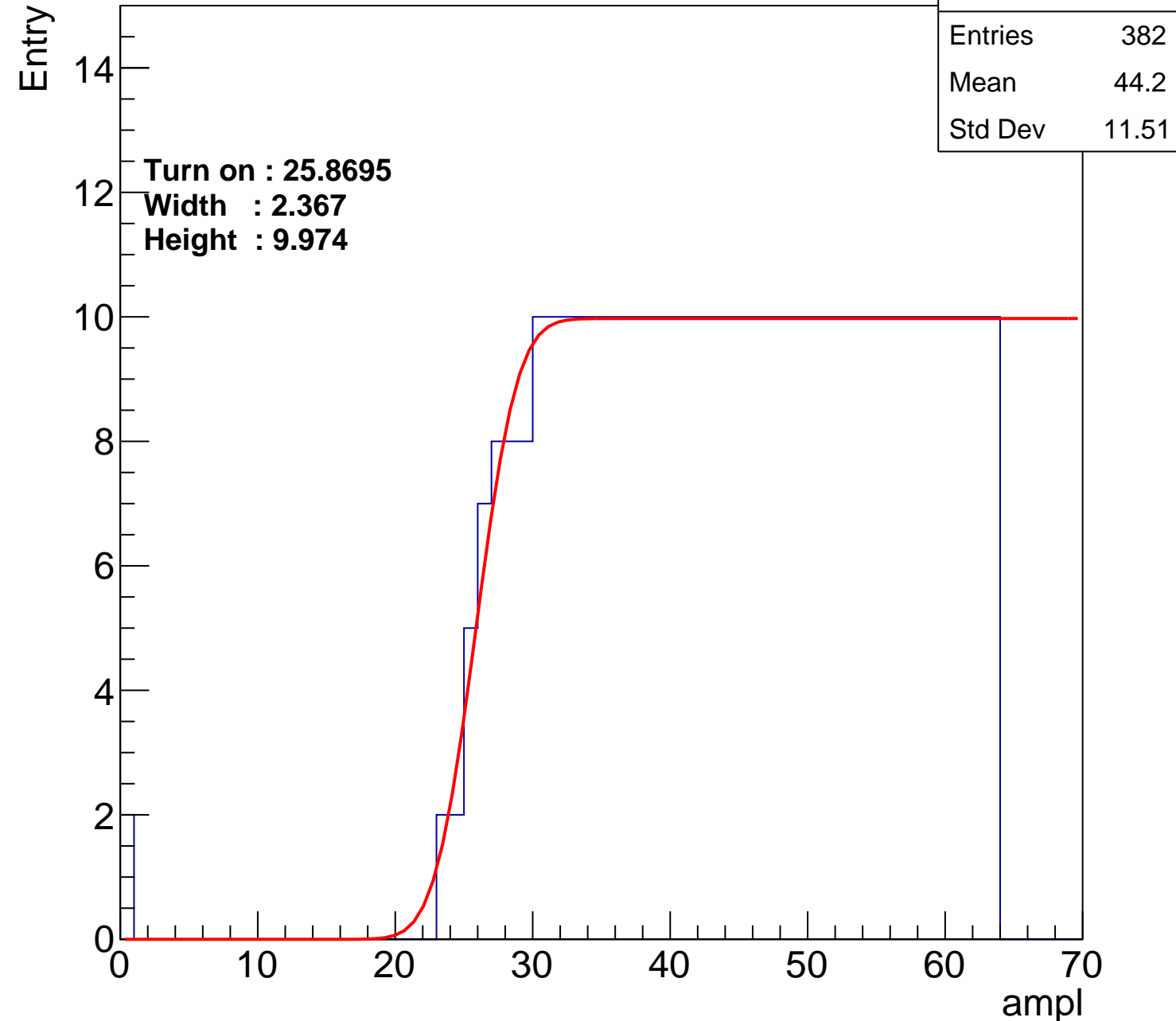
Width : 2.367

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch58

calib_packv5_042523_0143.root, FC#3, port B3

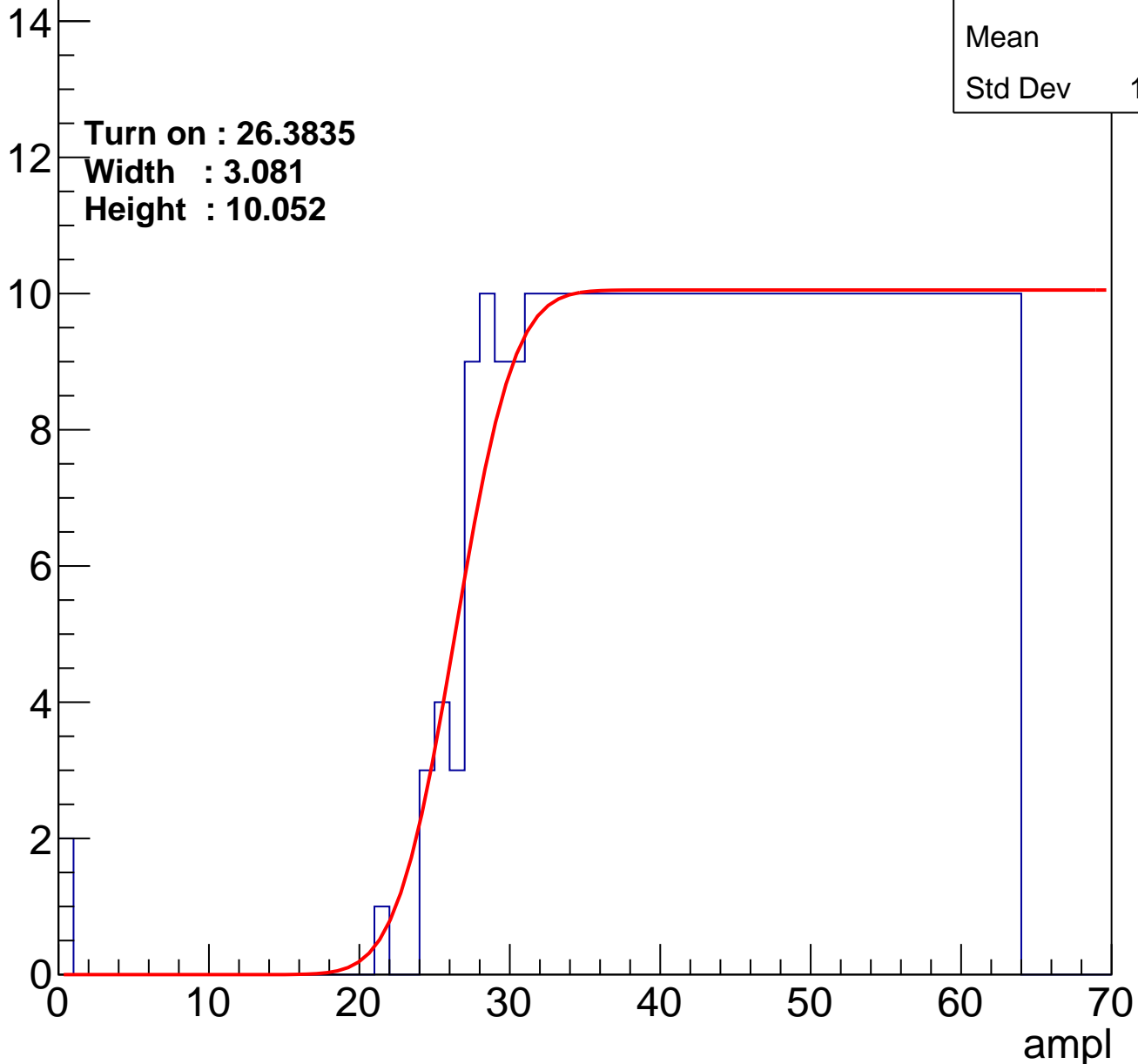
Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.3835

Width : 3.081

Height : 10.052

Entry



B1L000S, U21-ch59

calib_packv5_042523_0143.root, FC#3, port B3

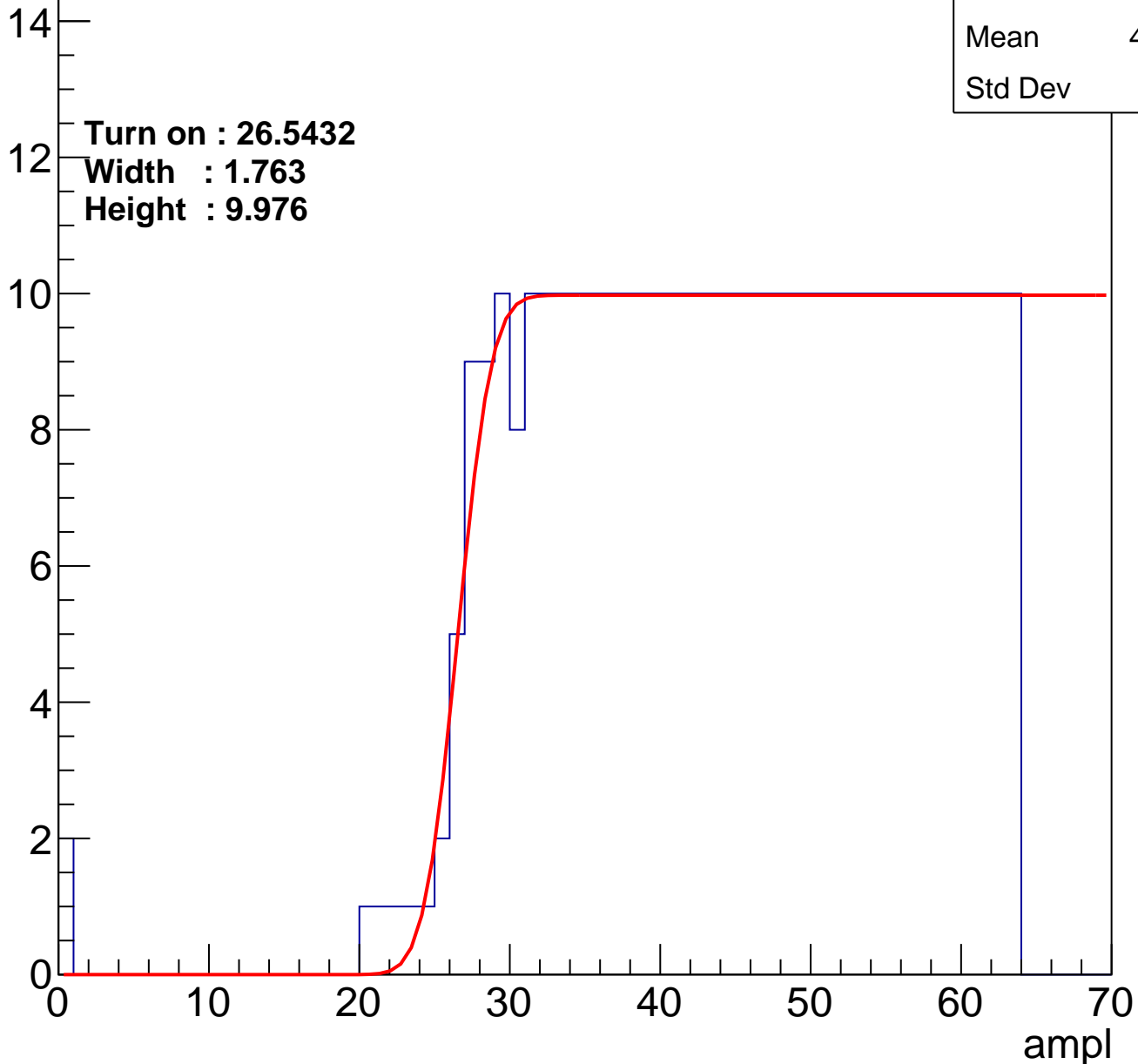
Entries	380
Mean	44.28
Std Dev	11.5

Turn on : 26.5432

Width : 1.763

Height : 9.976

Entry



B1L000S, U21-ch60

calib_packv5_042523_0143.root, FC#3, port B3

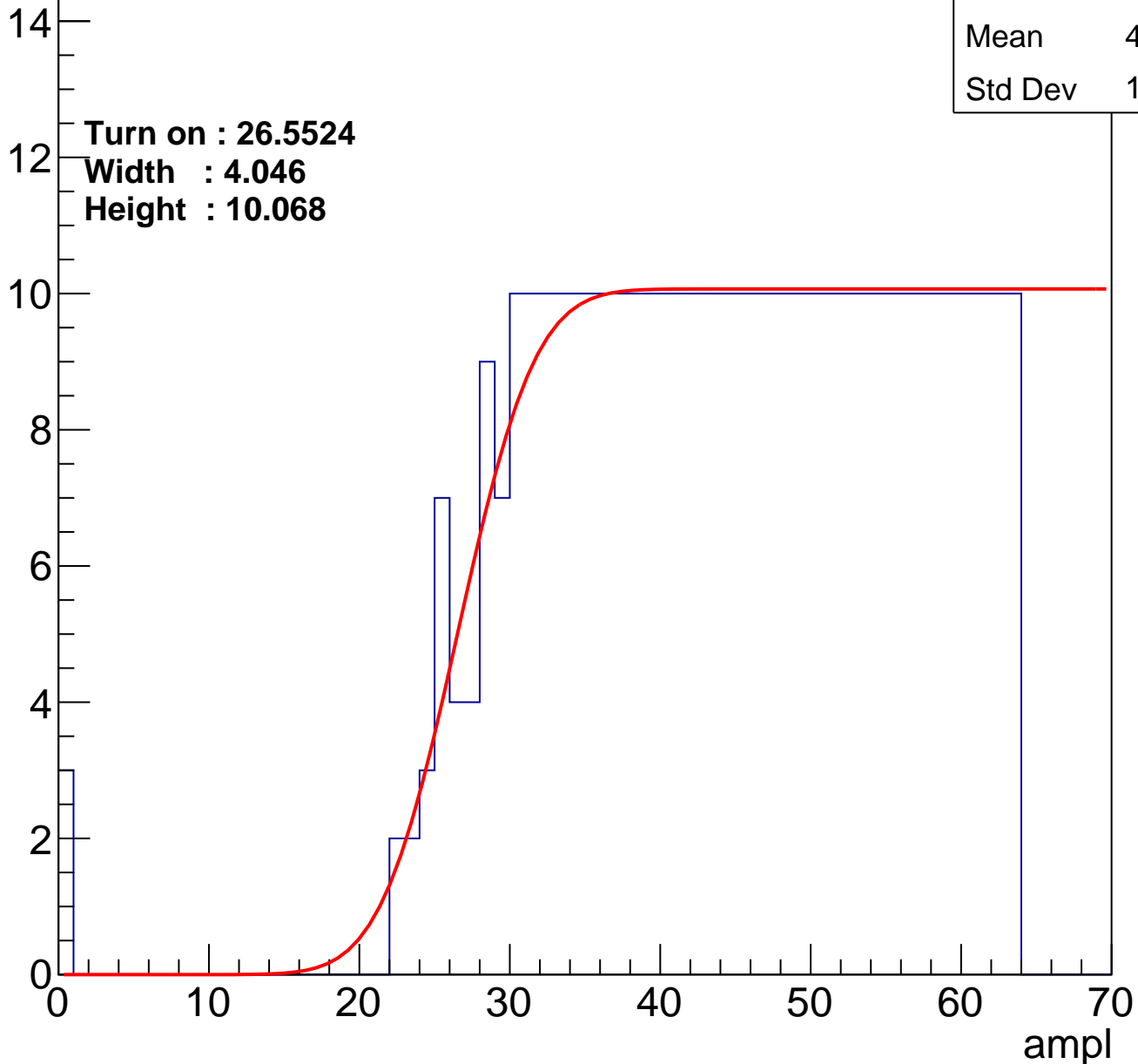
Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 26.5524

Width : 4.046

Height : 10.068

Entry



B1L000S, U21-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.15
Std Dev	11.46

Turn on : 26.3564

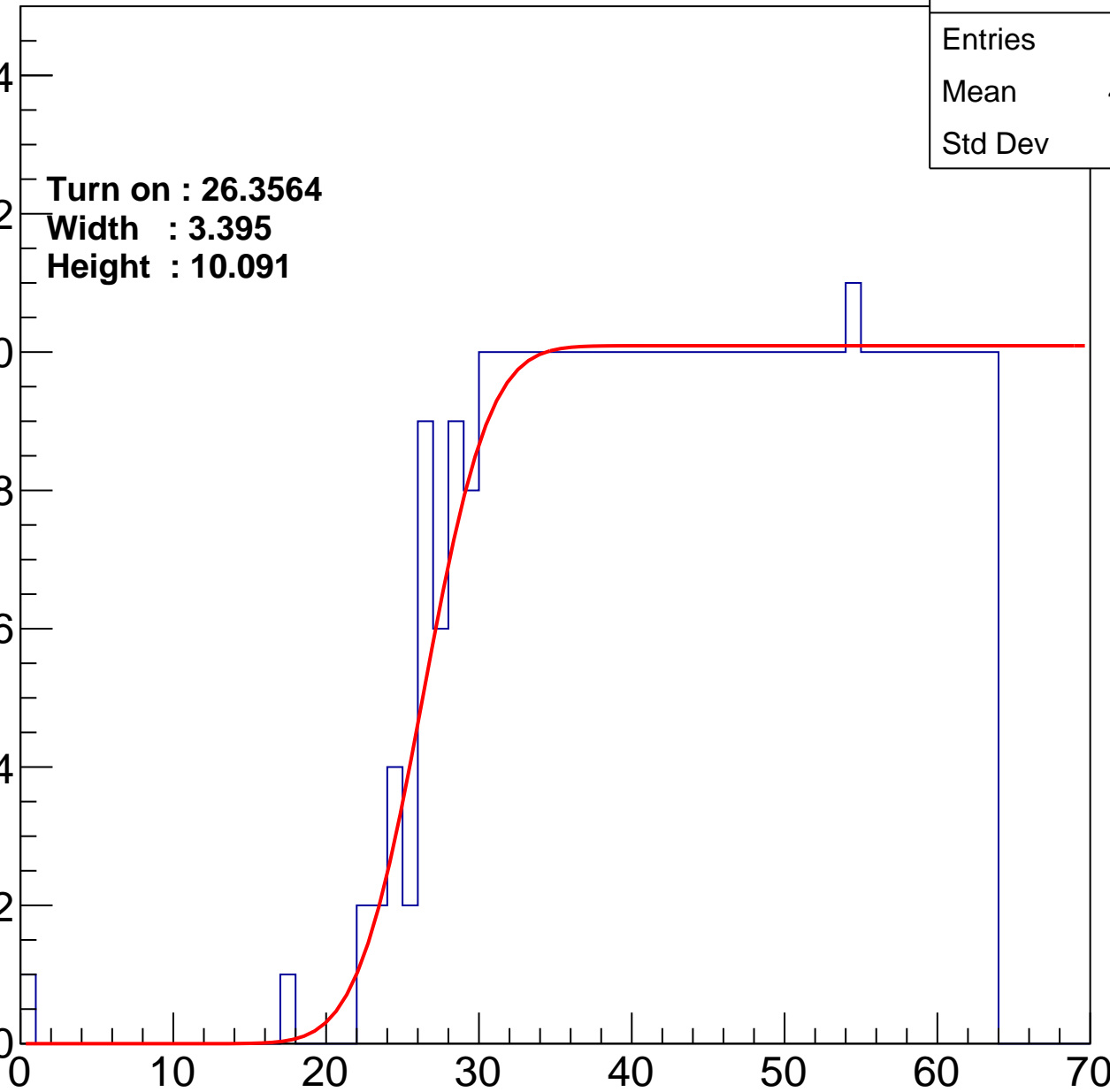
Width : 3.395

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.2020

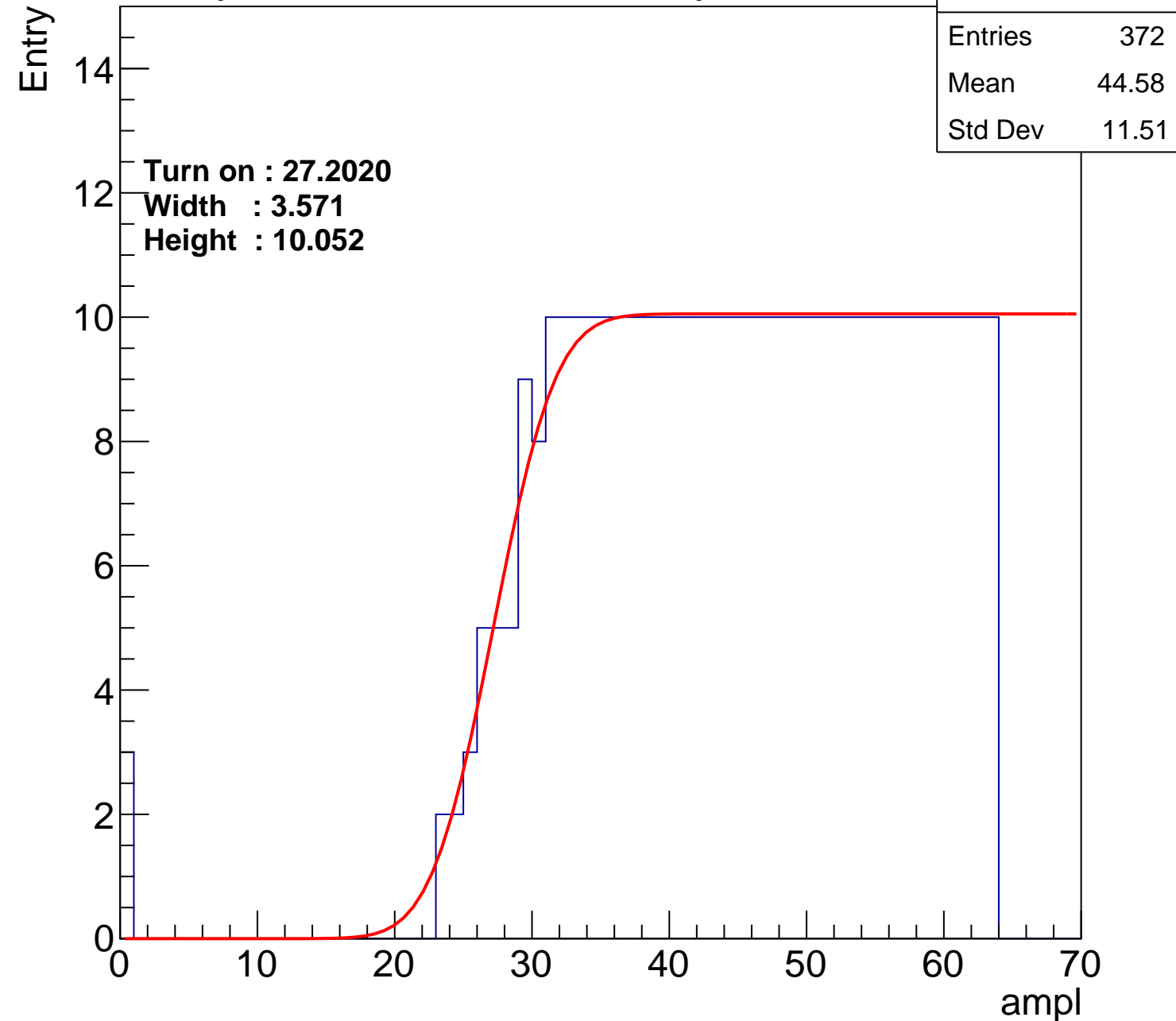
Width : 3.571

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch63

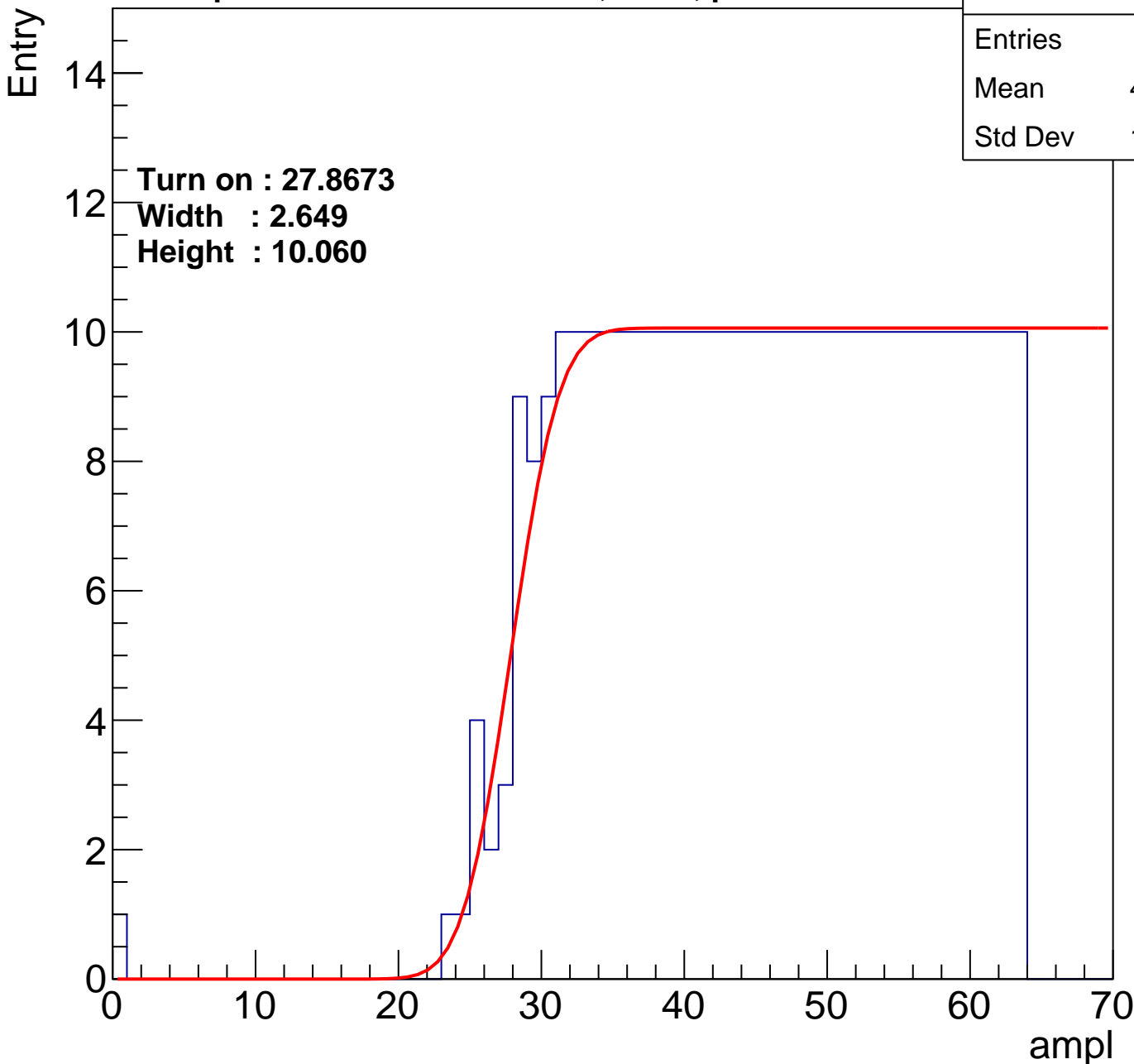
calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.8673

Width : 2.649

Height : 10.060



B1L000S, U21-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 26.1431

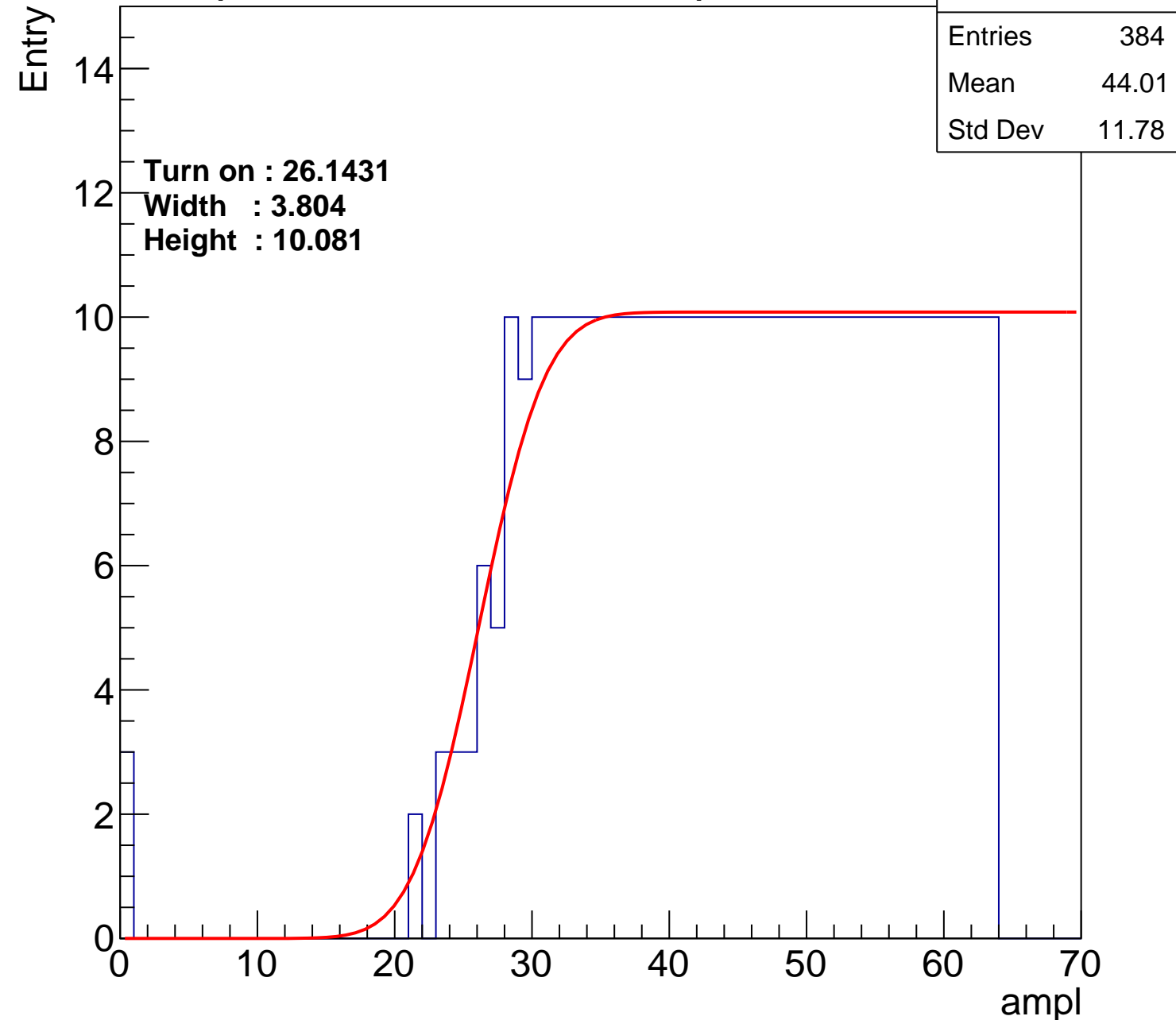
Width : 3.804

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.34
Std Dev	11.93

Turn on : 24.2397

Width : 0.592

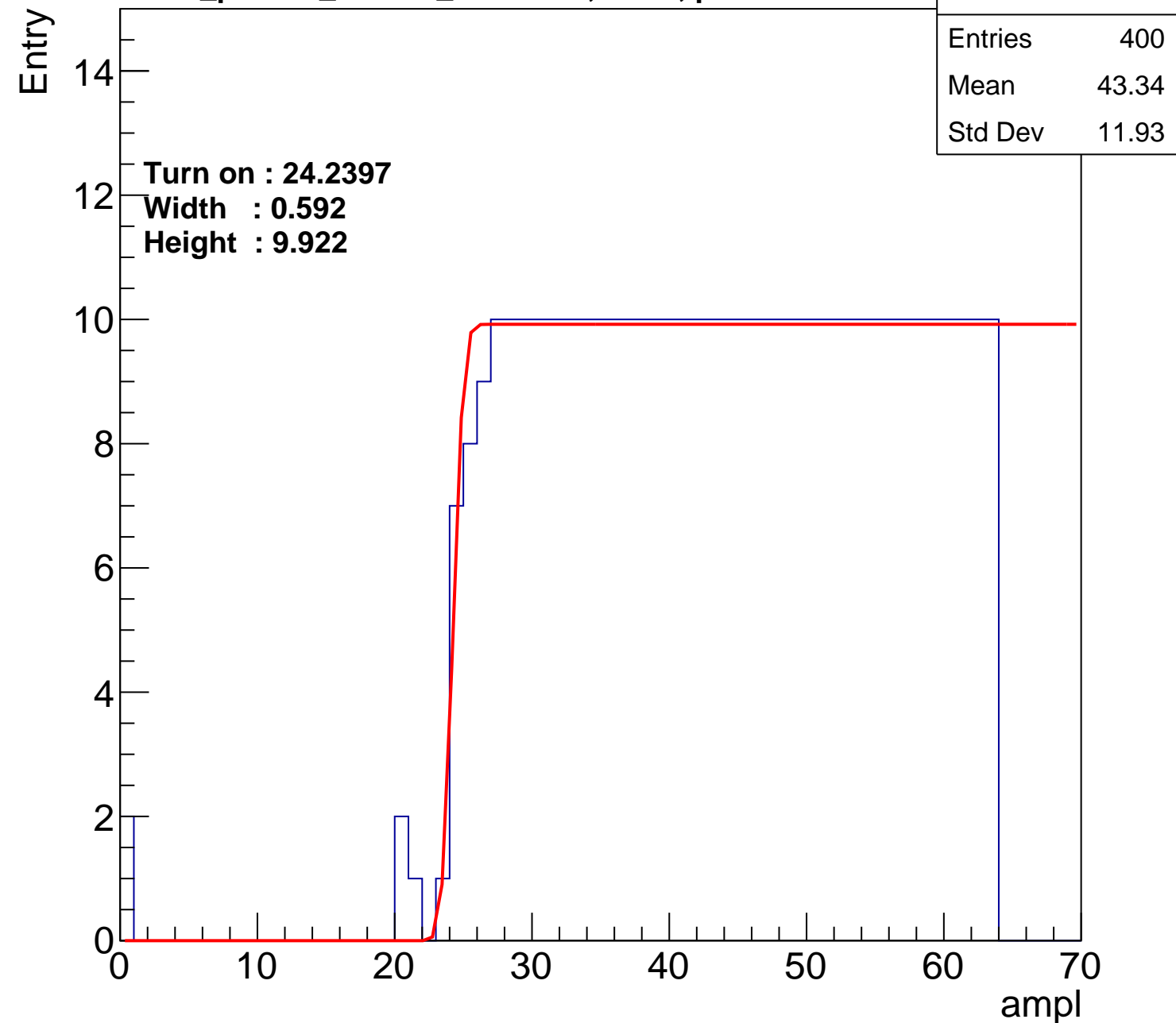
Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch66

calib_packv5_042523_0143.root, FC#3, port B3

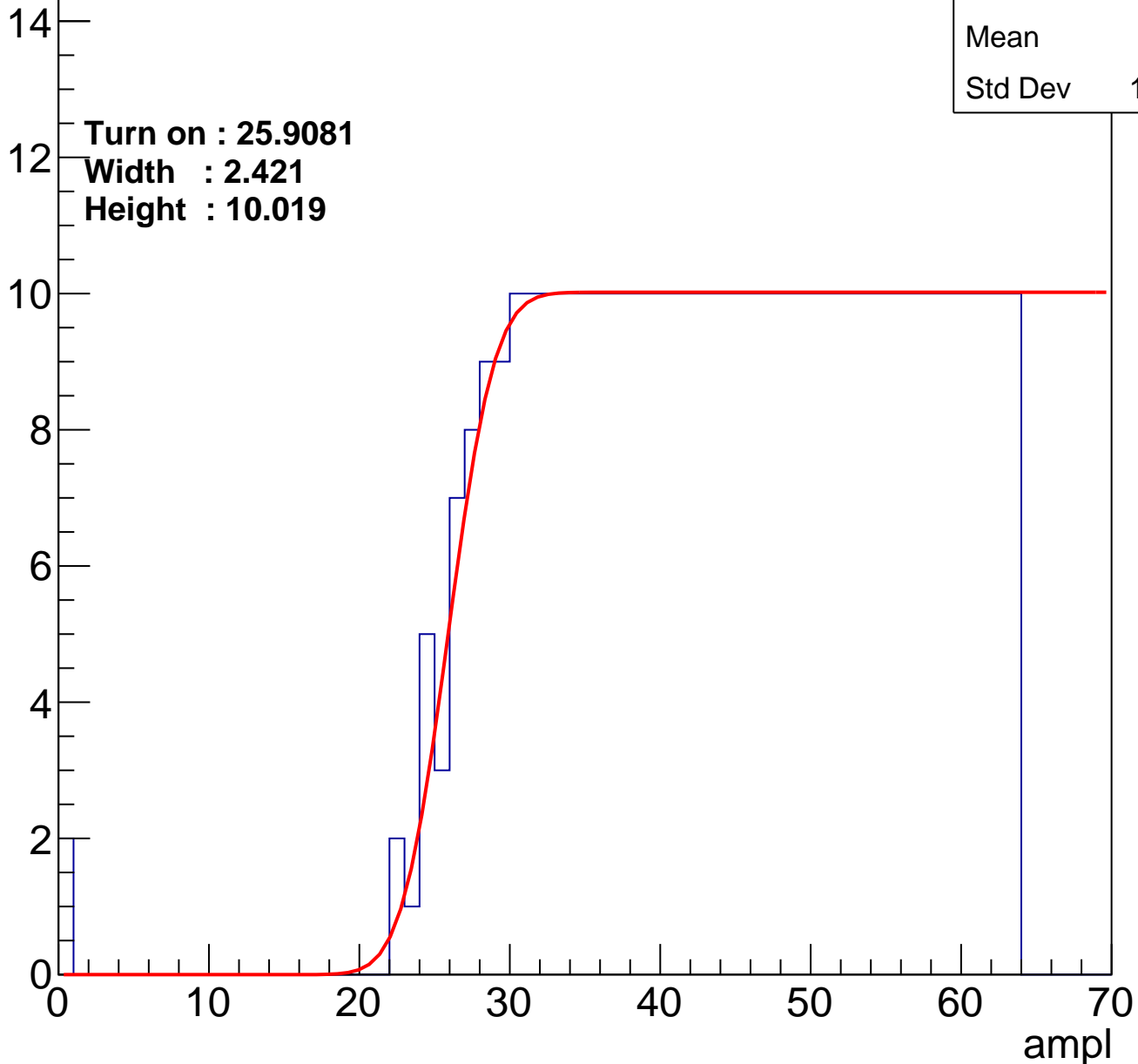
Entries	386
Mean	44
Std Dev	11.62

Turn on : 25.9081

Width : 2.421

Height : 10.019

Entry



B1L000S, U21-ch67

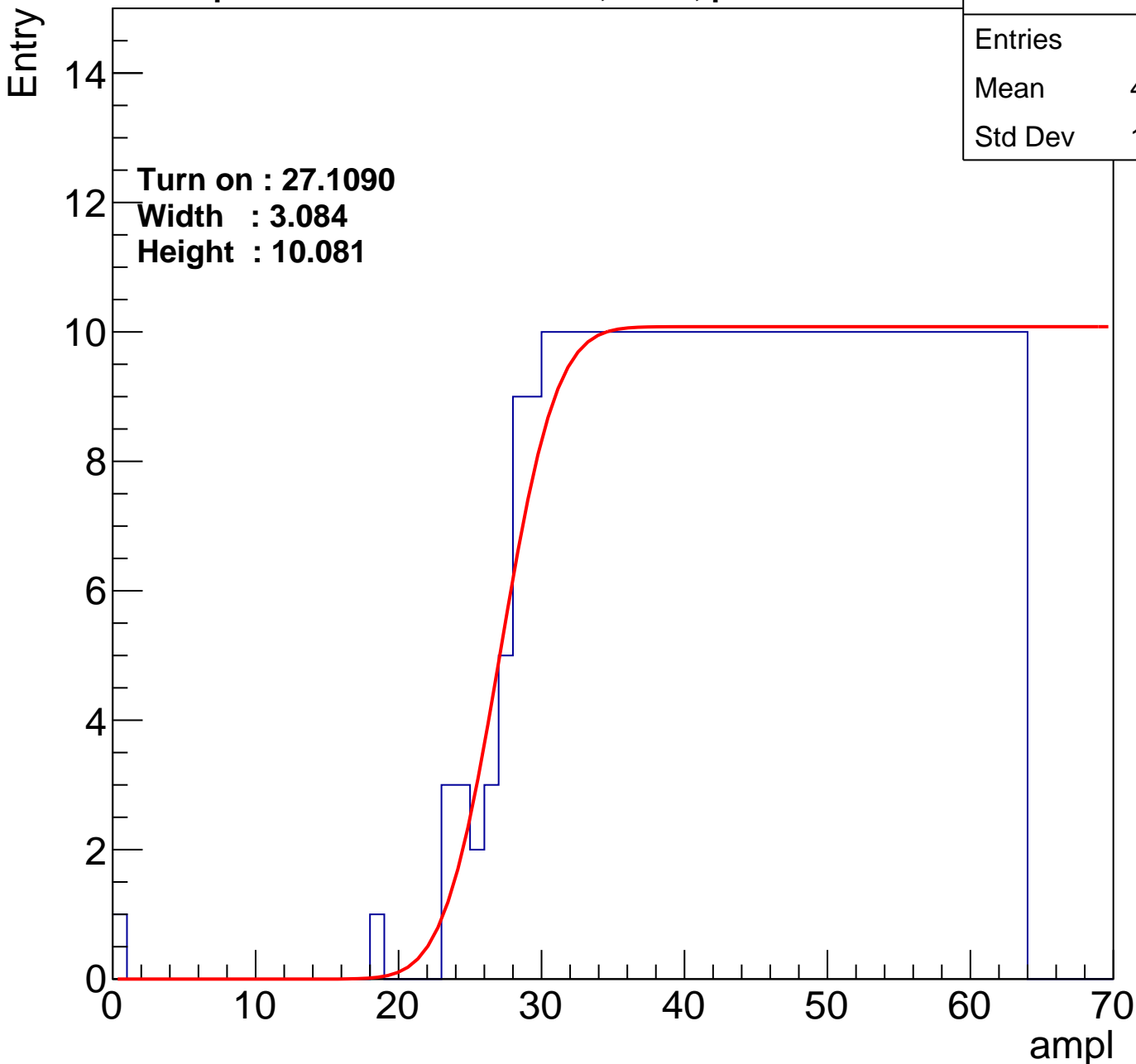
calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 27.1090

Width : 3.084

Height : 10.081



B1L000S, U21-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.49
Std Dev	11.24

Turn on : 26.5460

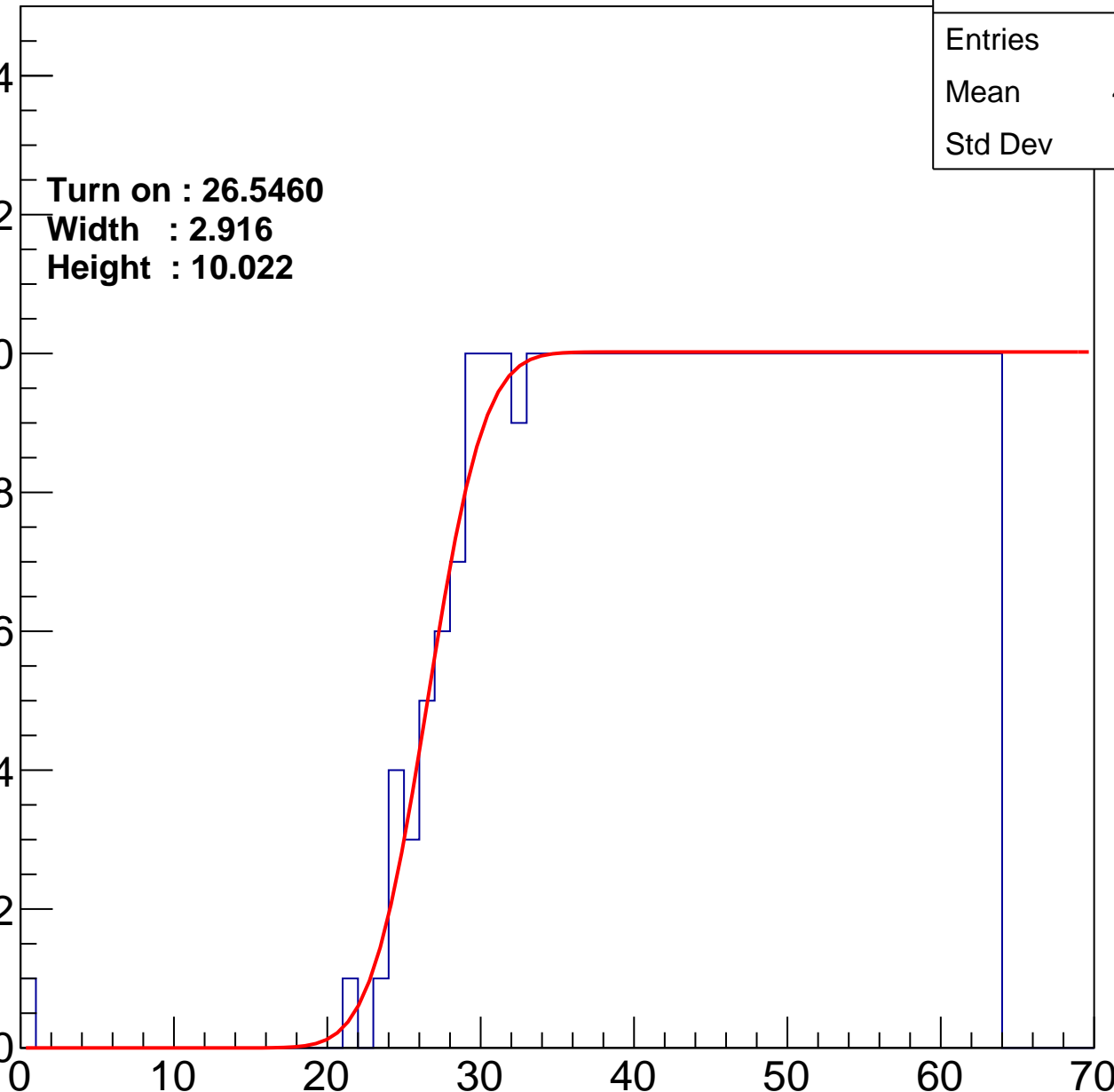
Width : 2.916

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.8167

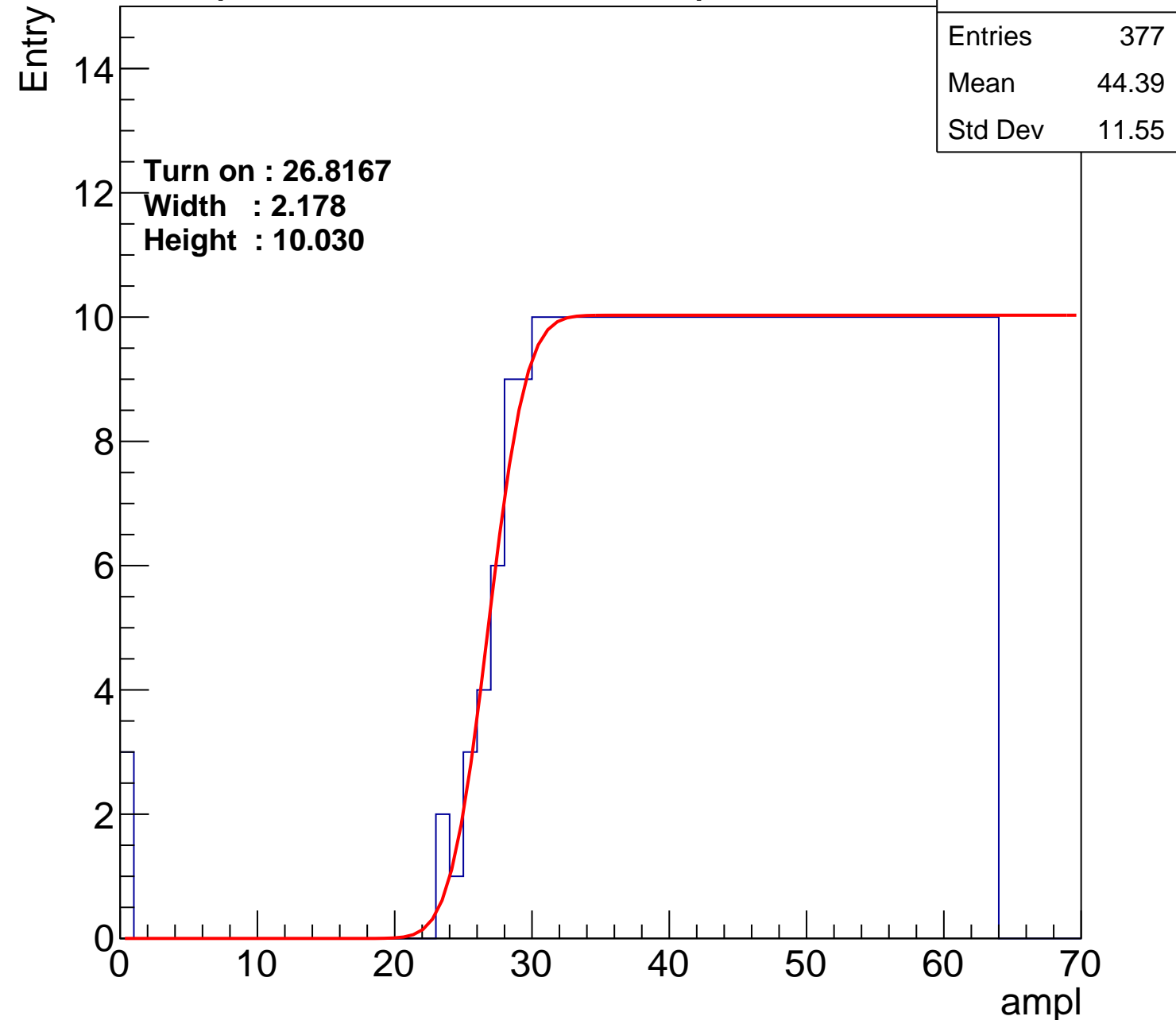
Width : 2.178

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.05
Std Dev	10.92

Turn on : 27.8162

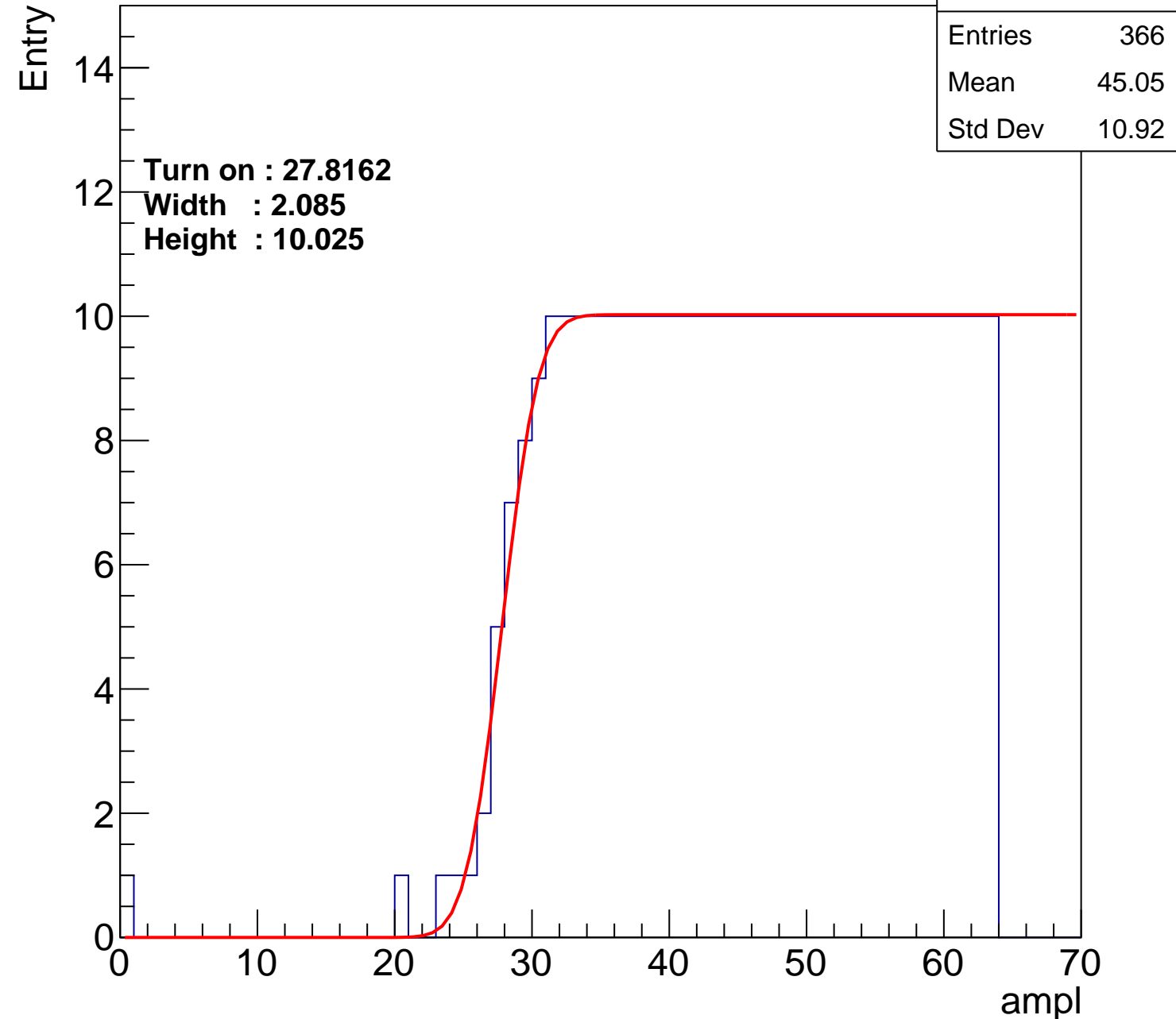
Width : 2.085

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.56
Std Dev	12.01

Turn on : 25.3704

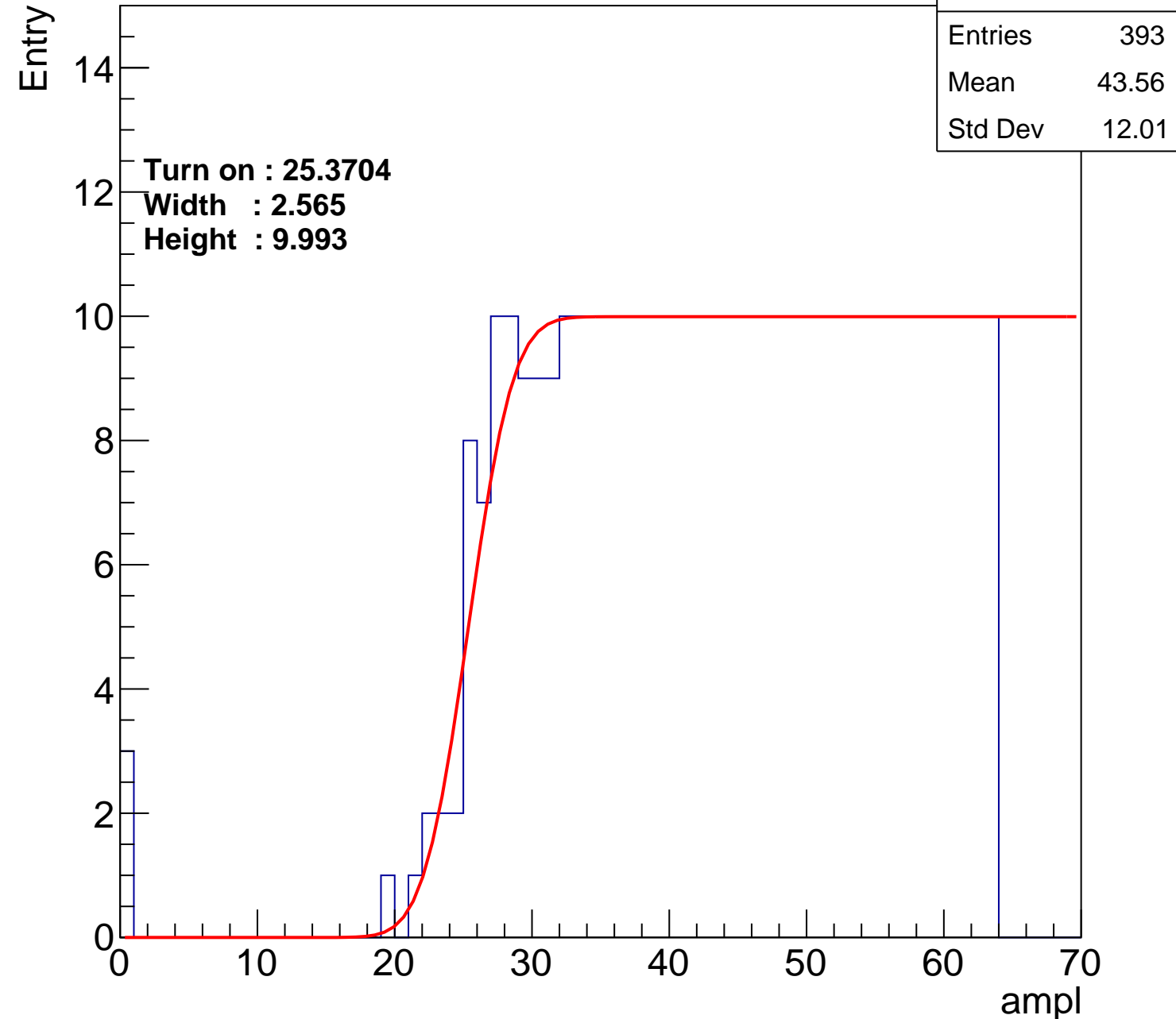
Width : 2.565

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.18
Std Dev	10.85

Turn on : 27.9140

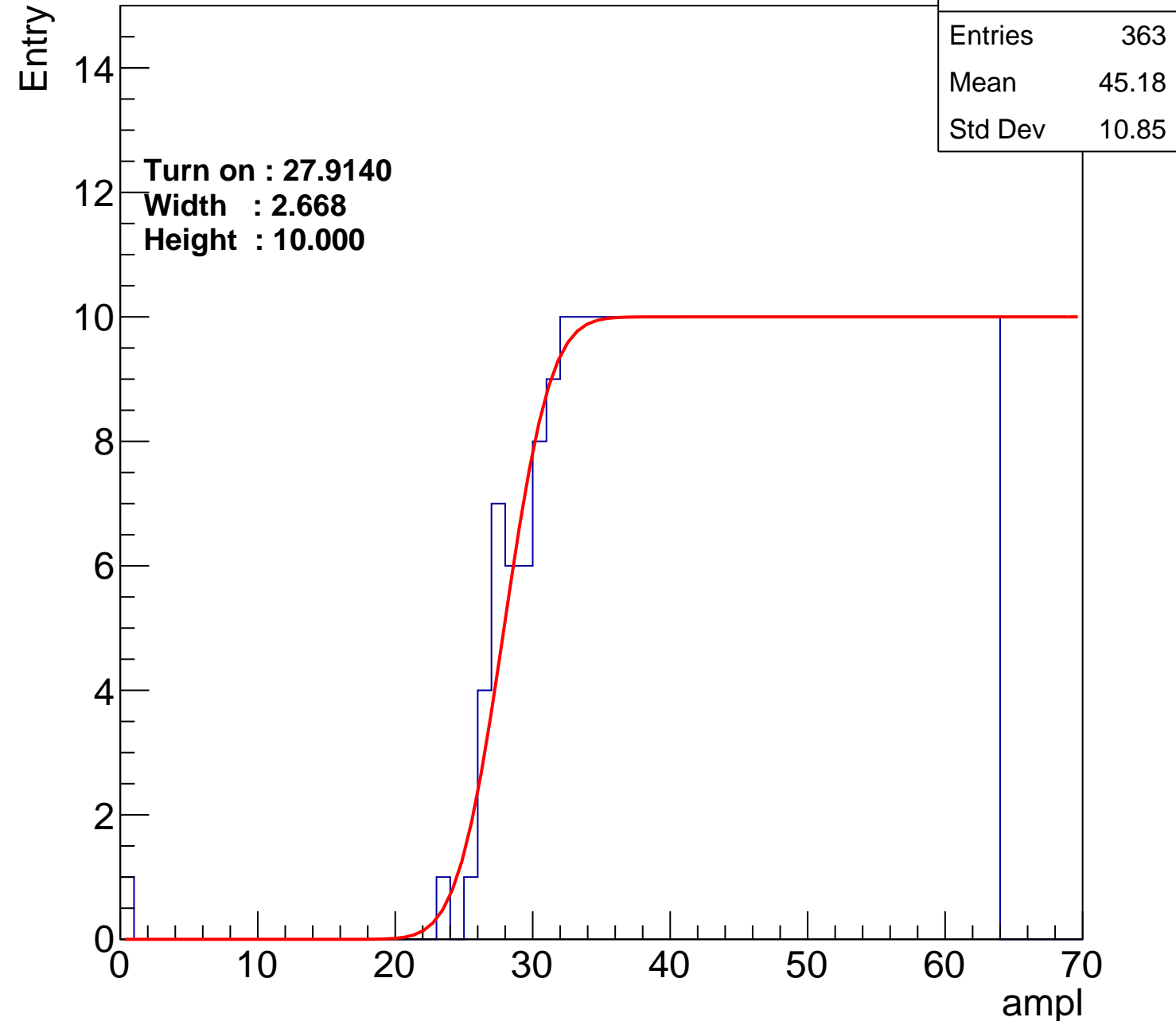
Width : 2.668

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.41
Std Dev	11.5

Turn on : 27.0686

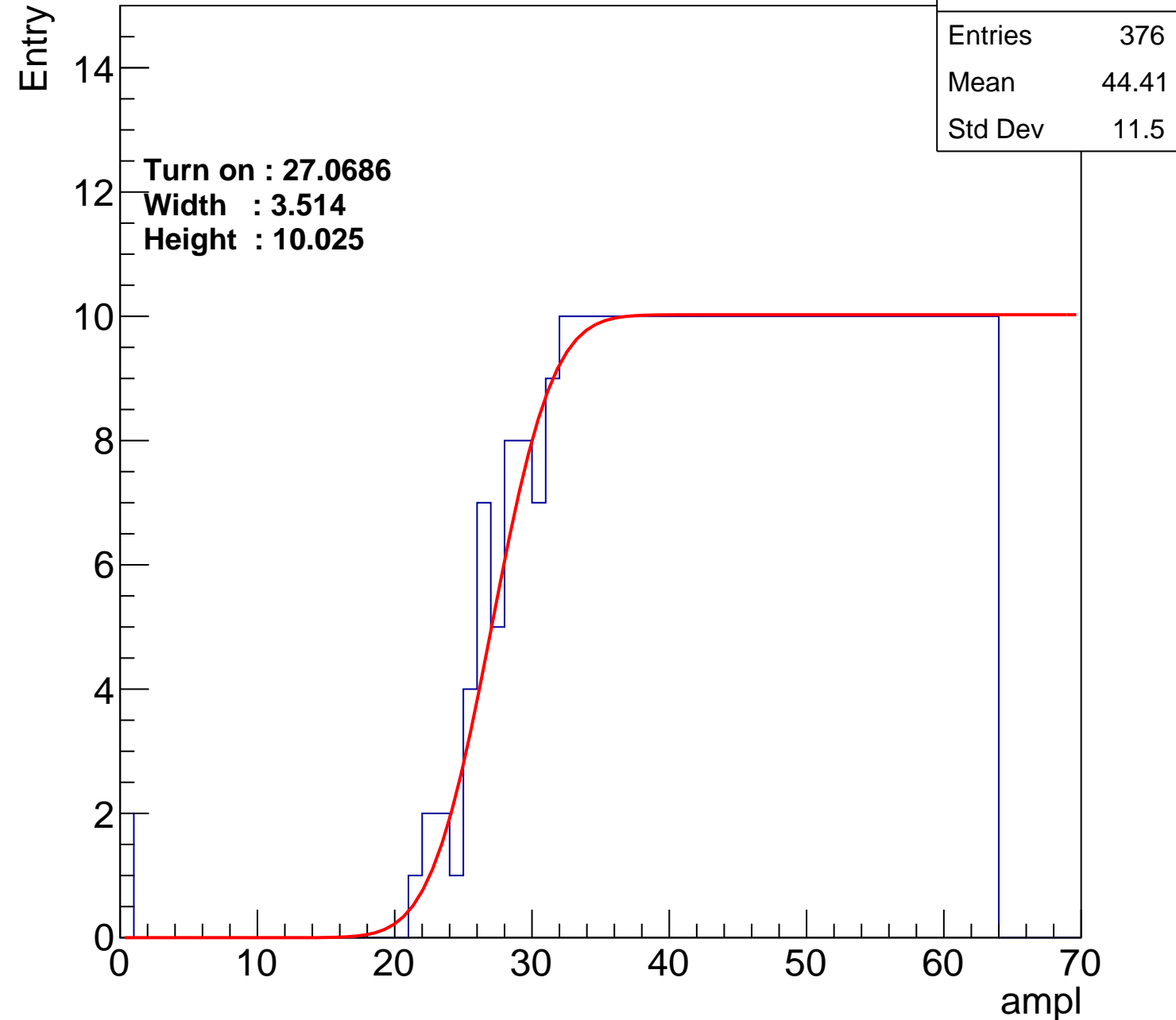
Width : 3.514

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch74

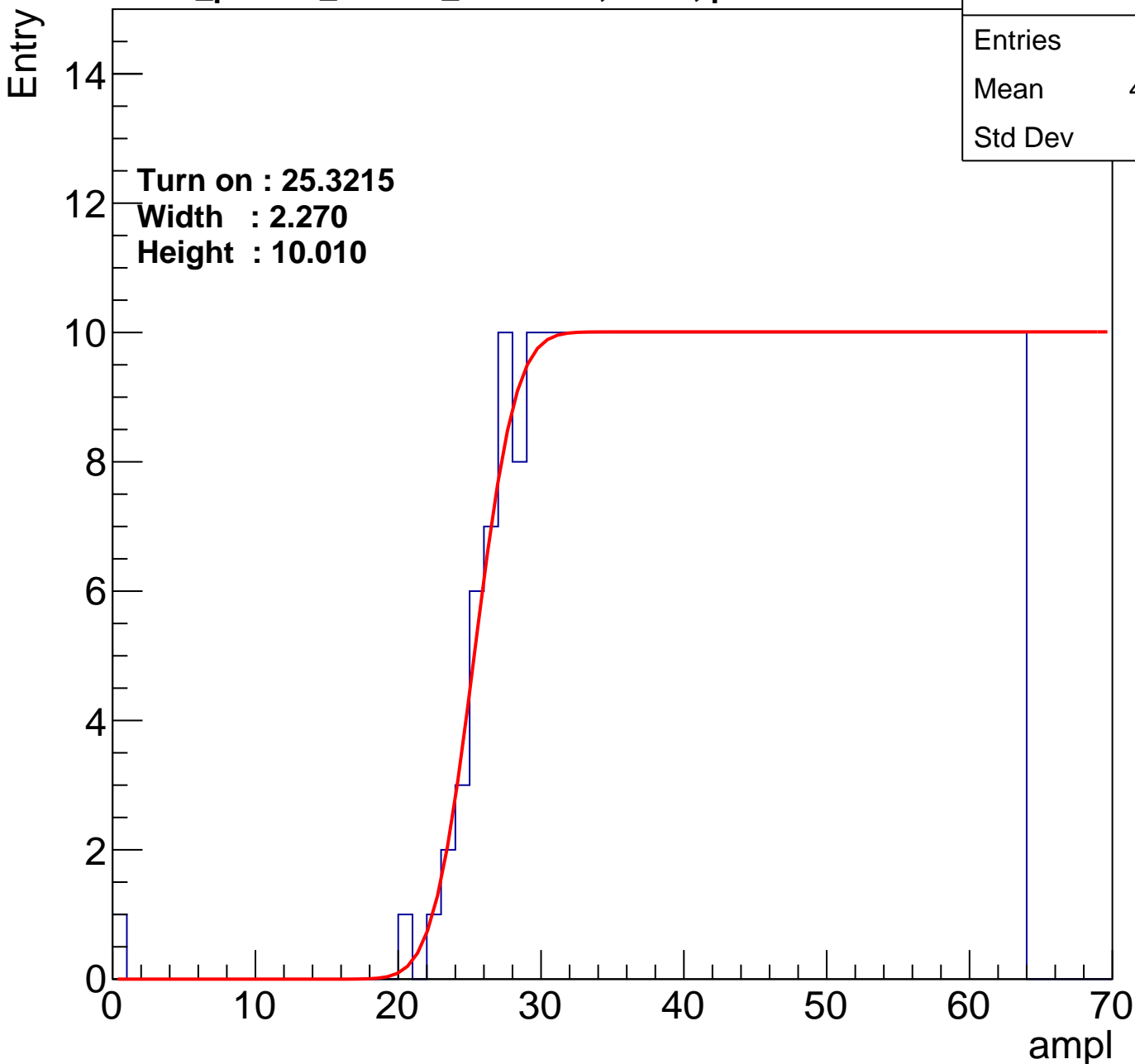
calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.92
Std Dev	11.51

Turn on : 25.3215

Width : 2.270

Height : 10.010



B1L000S, U21-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.2315

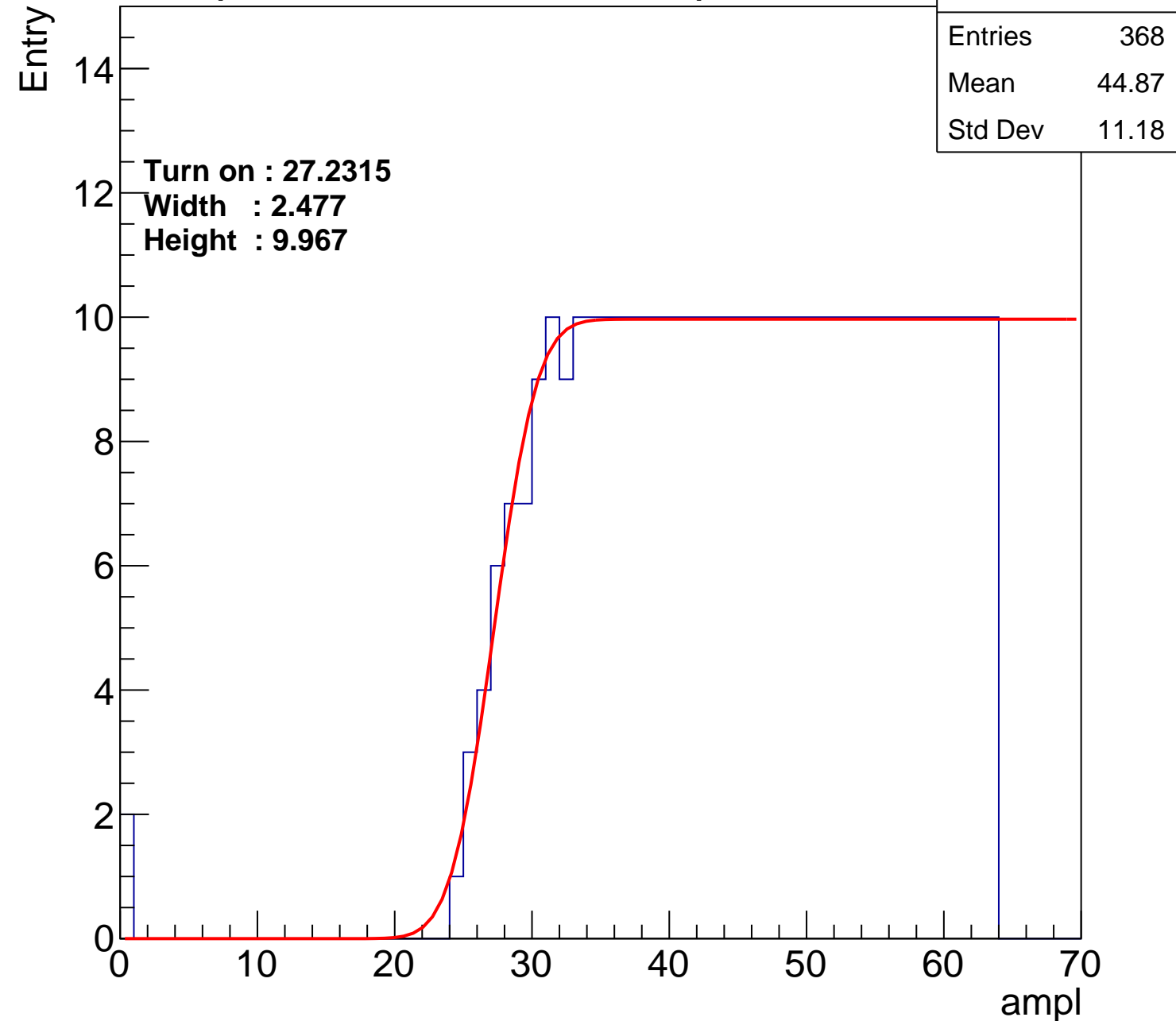
Width : 2.477

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch76

calib_packv5_042523_0143.root, FC#3, port B3

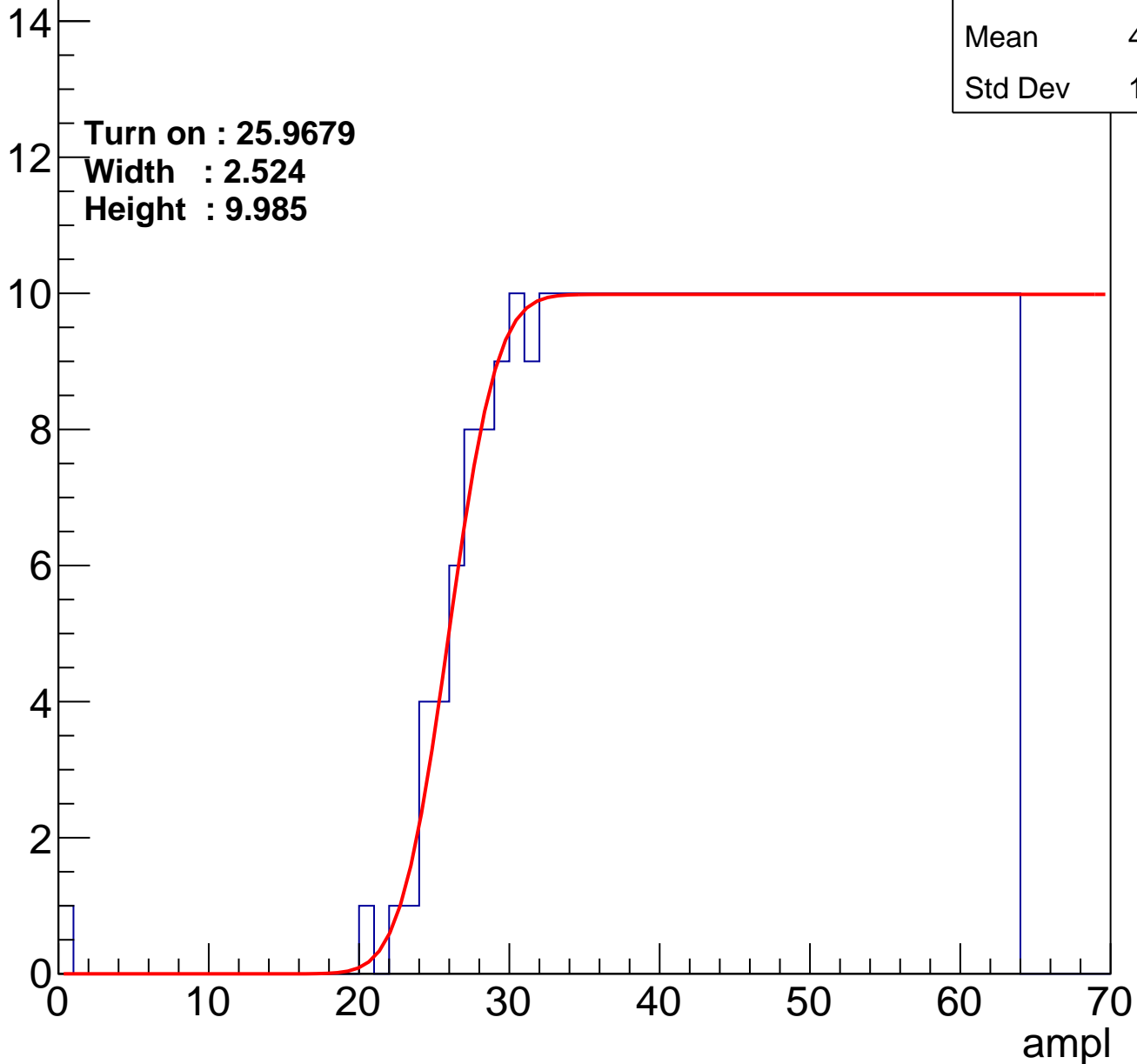
Entries	382
Mean	44.23
Std Dev	11.38

Turn on : 25.9679

Width : 2.524

Height : 9.985

Entry



B1L000S, U21-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	351
Mean	45.78
Std Dev	10.53

Turn on : 29.0572

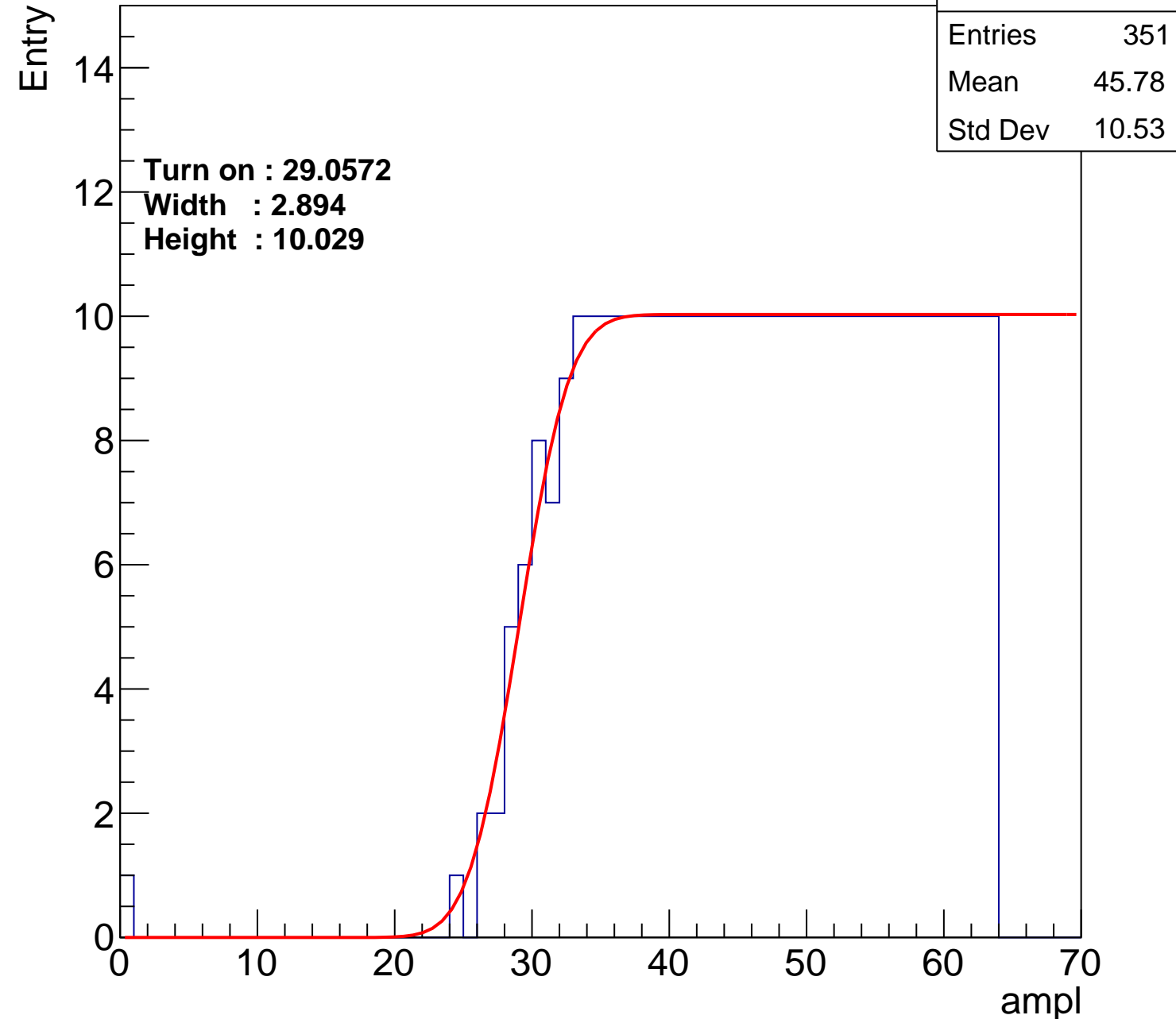
Width : 2.894

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 27.4457

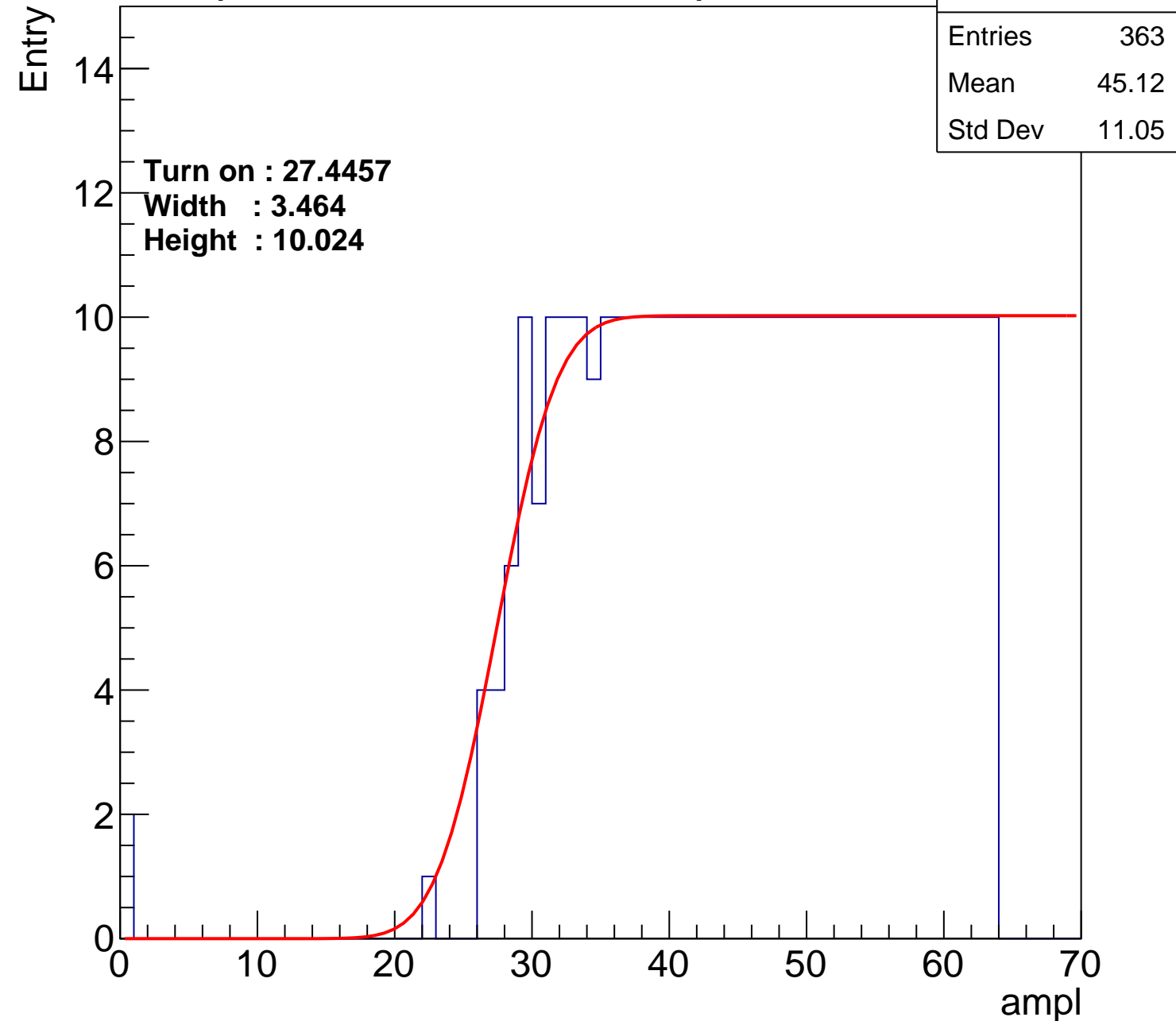
Width : 3.464

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch79

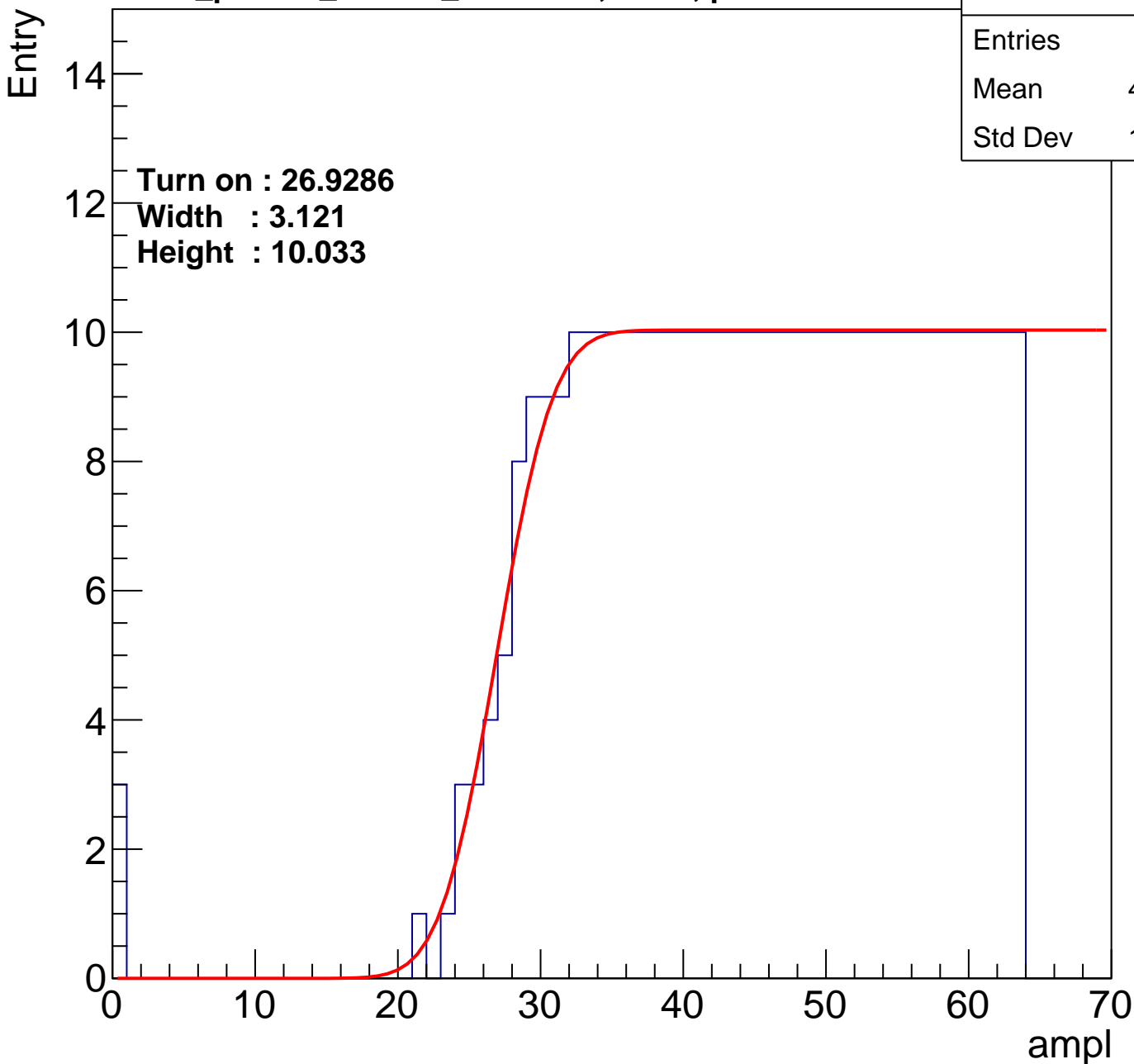
calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 26.9286

Width : 3.121

Height : 10.033



B1L000S, U21-ch80

calib_packv5_042523_0143.root, FC#3, port B3

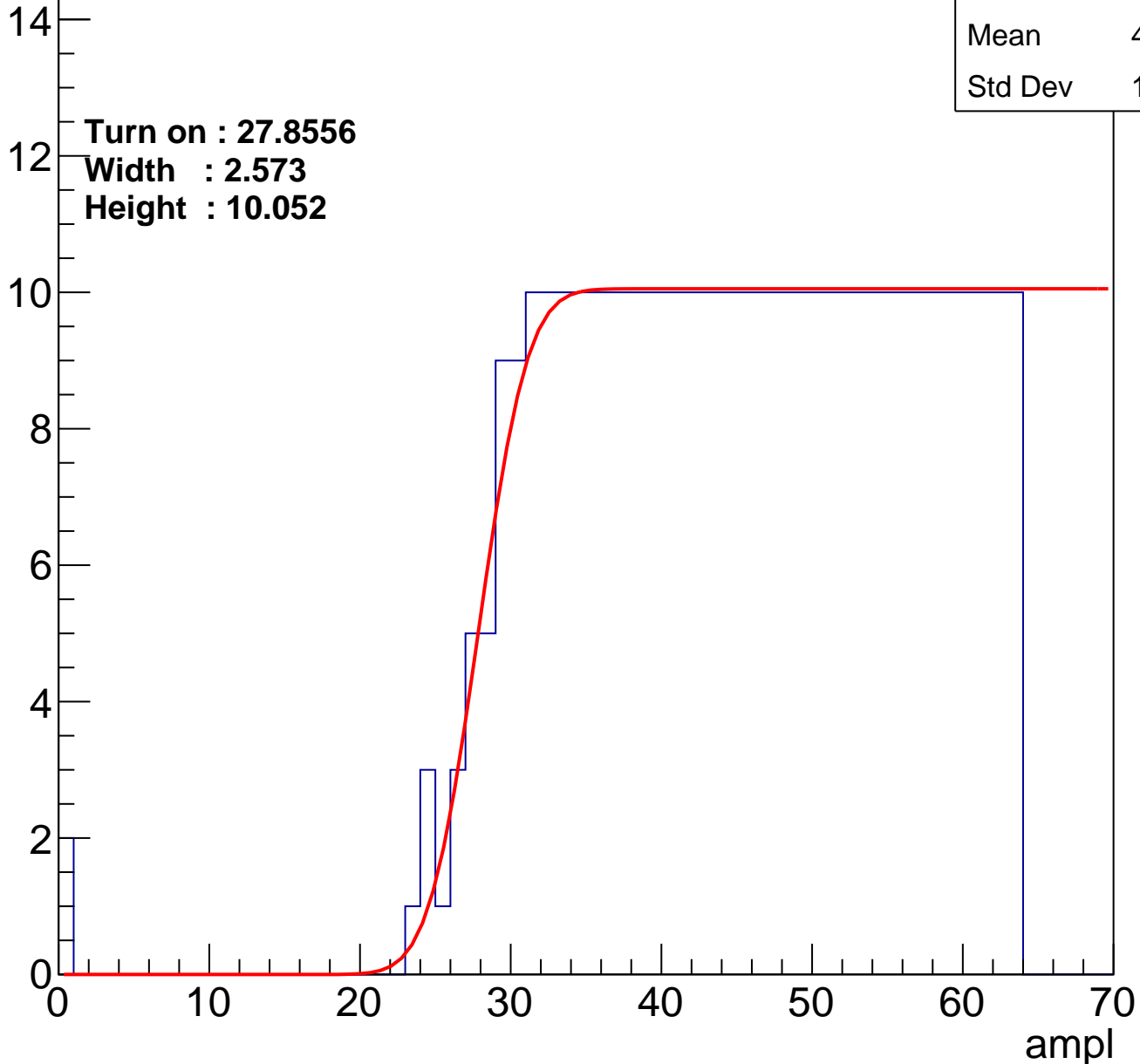
Entries	368
Mean	44.88
Std Dev	11.18

Turn on : 27.8556

Width : 2.573

Height : 10.052

Entry



B1L000S, U21-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.2412

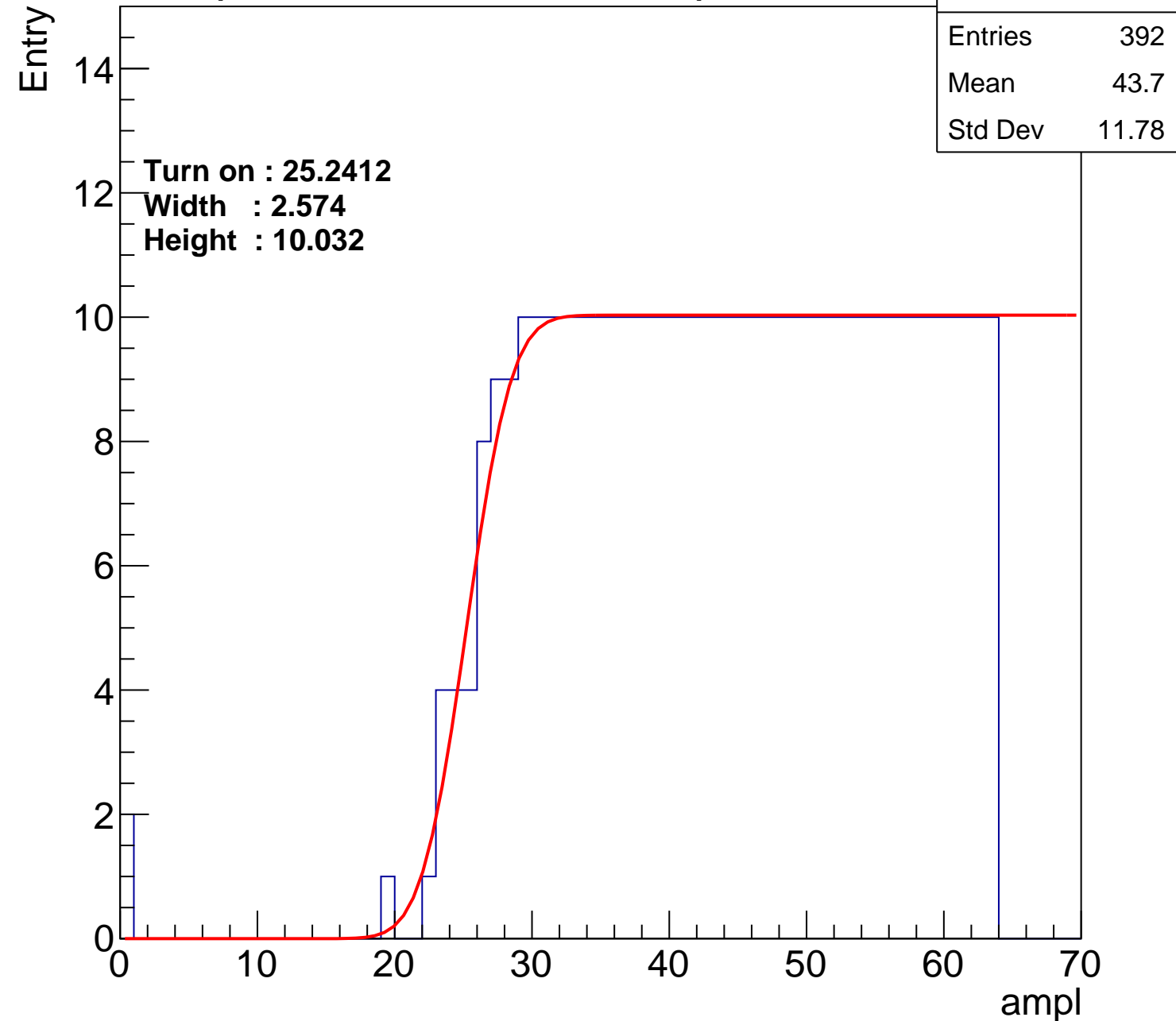
Width : 2.574

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.3036

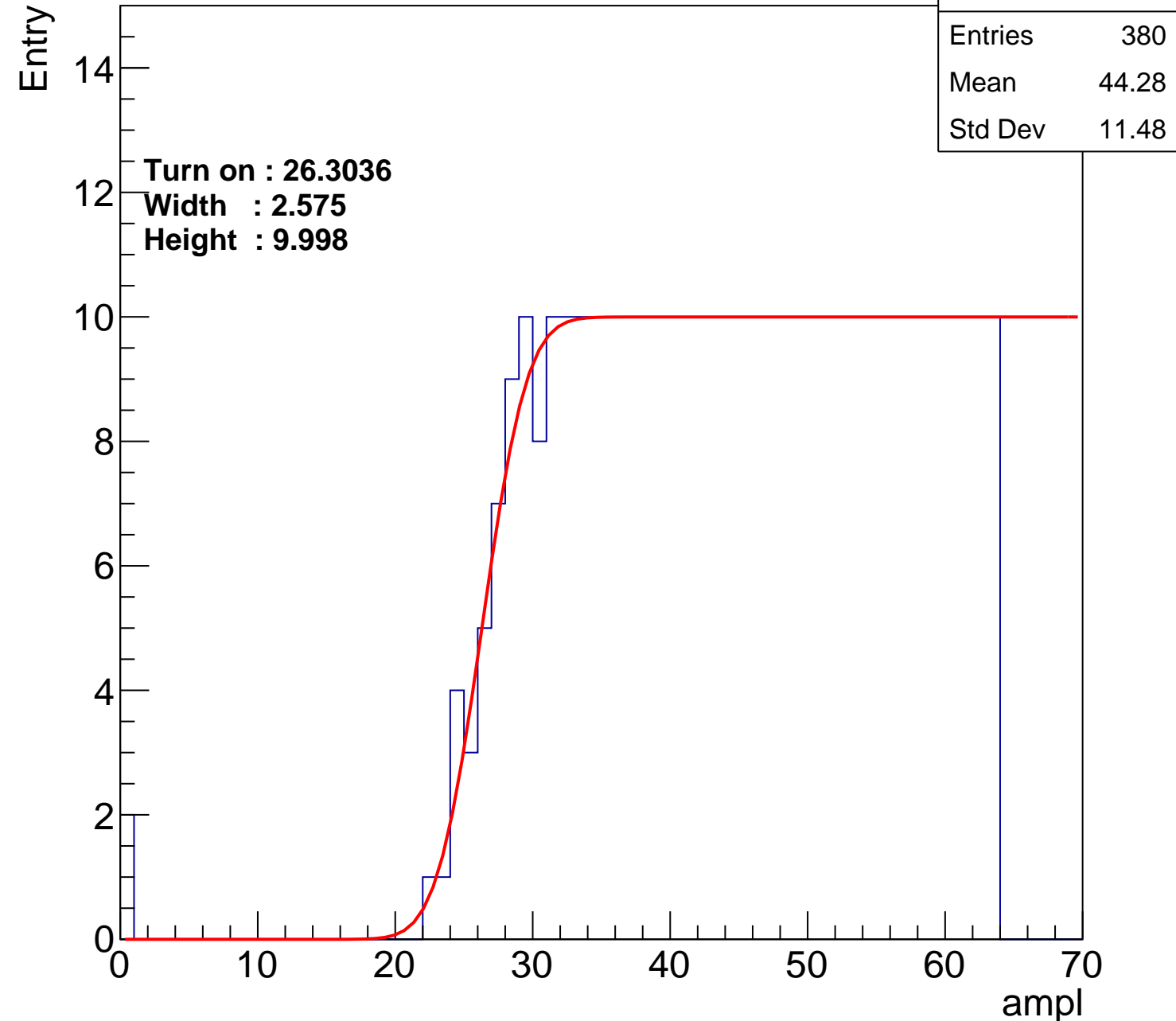
Width : 2.575

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.82
Std Dev	11.69

Turn on : 24.8759

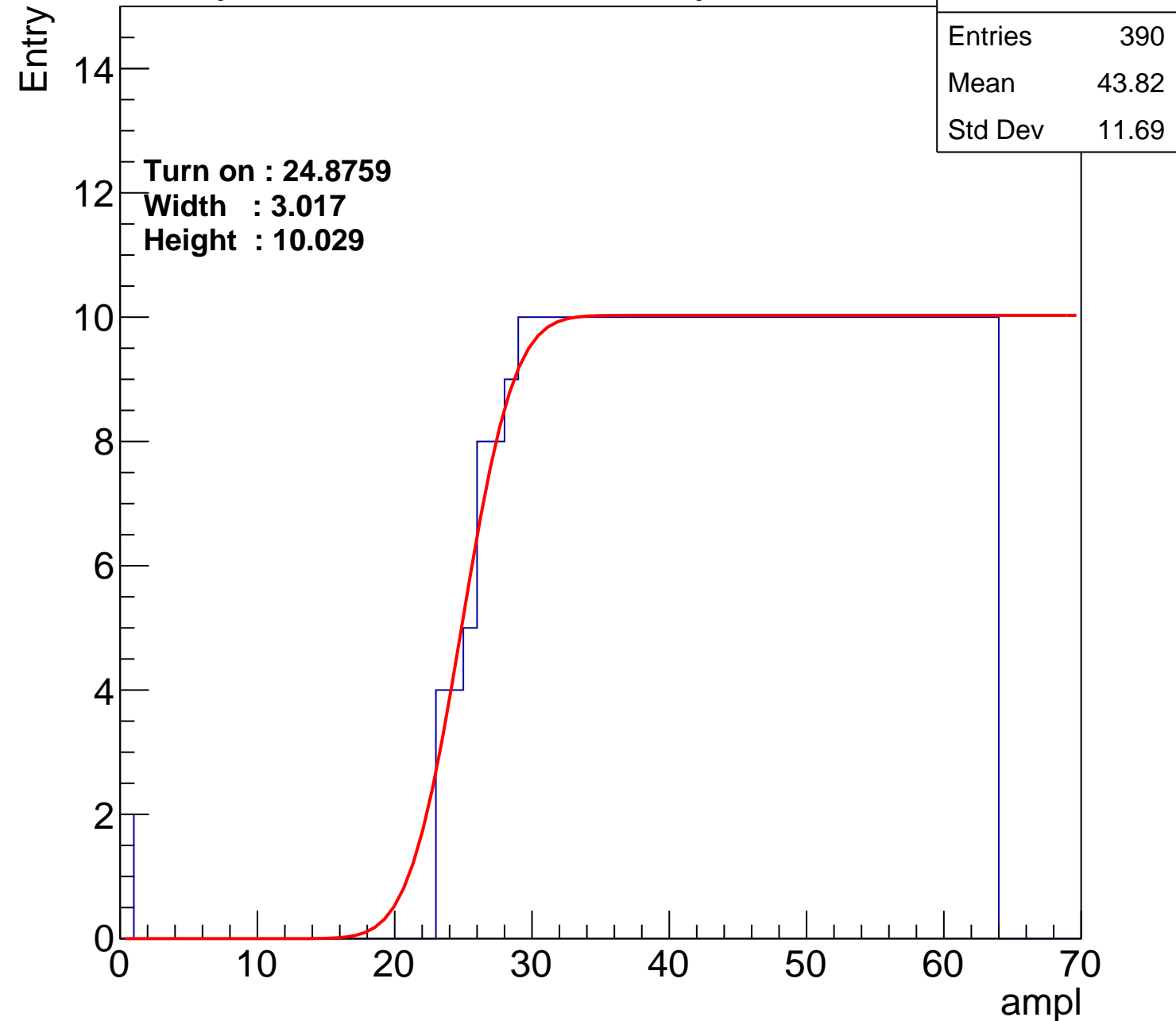
Width : 3.017

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44
Std Dev	11.8

Turn on : 26.3280

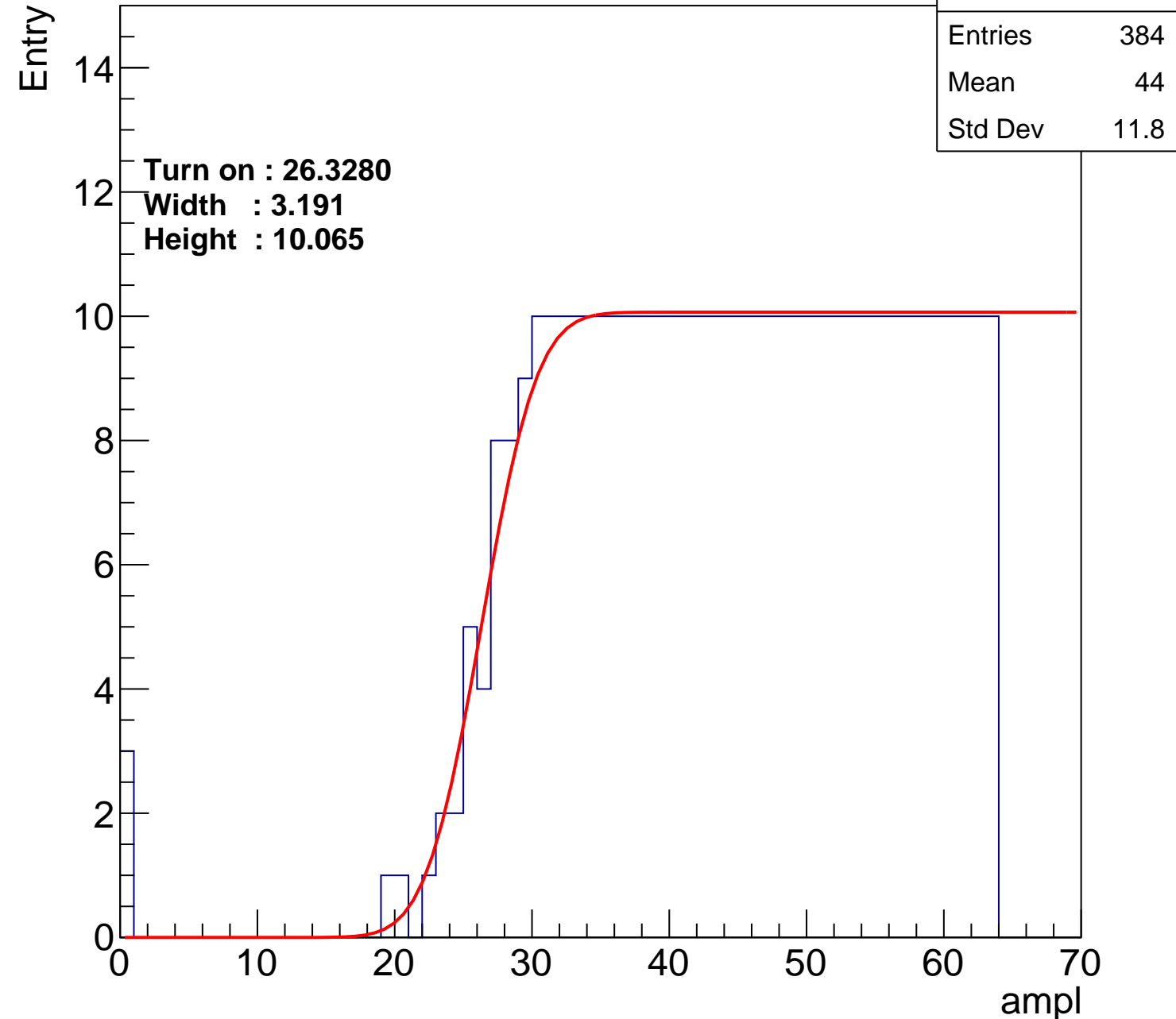
Width : 3.191

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.2354

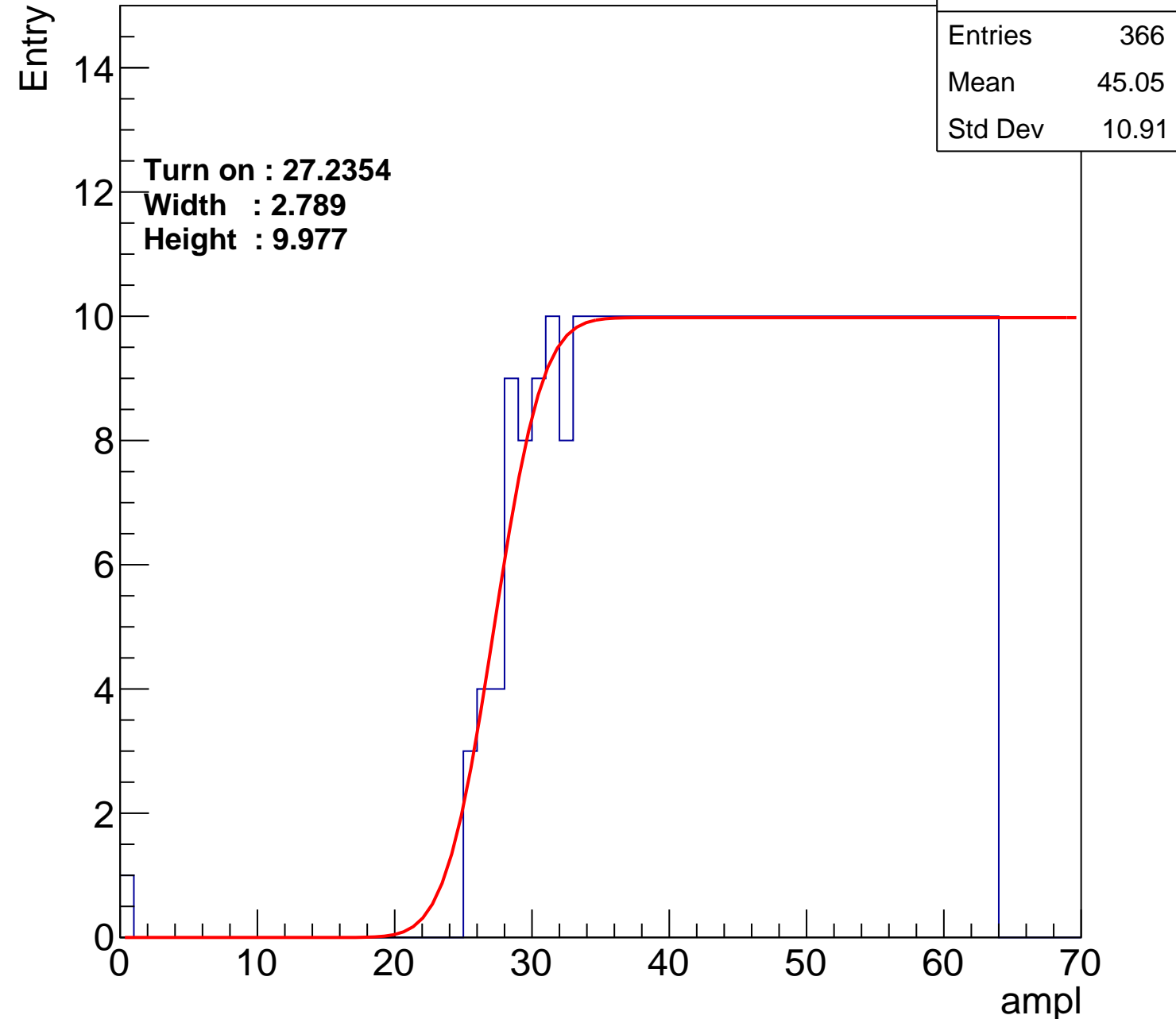
Width : 2.789

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.4
Std Dev	11.82

Turn on : 23.9484

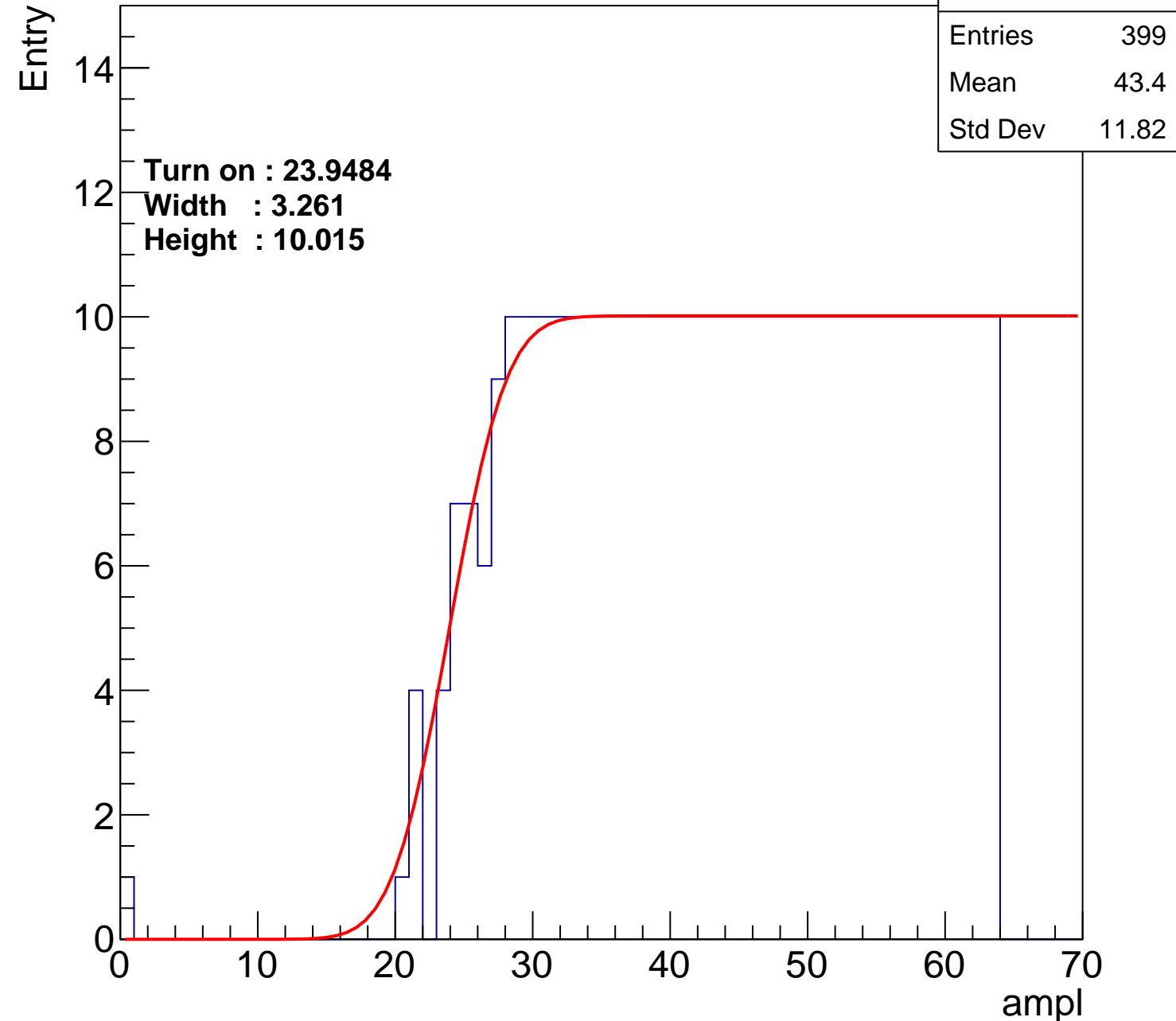
Width : 3.261

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 26.5865

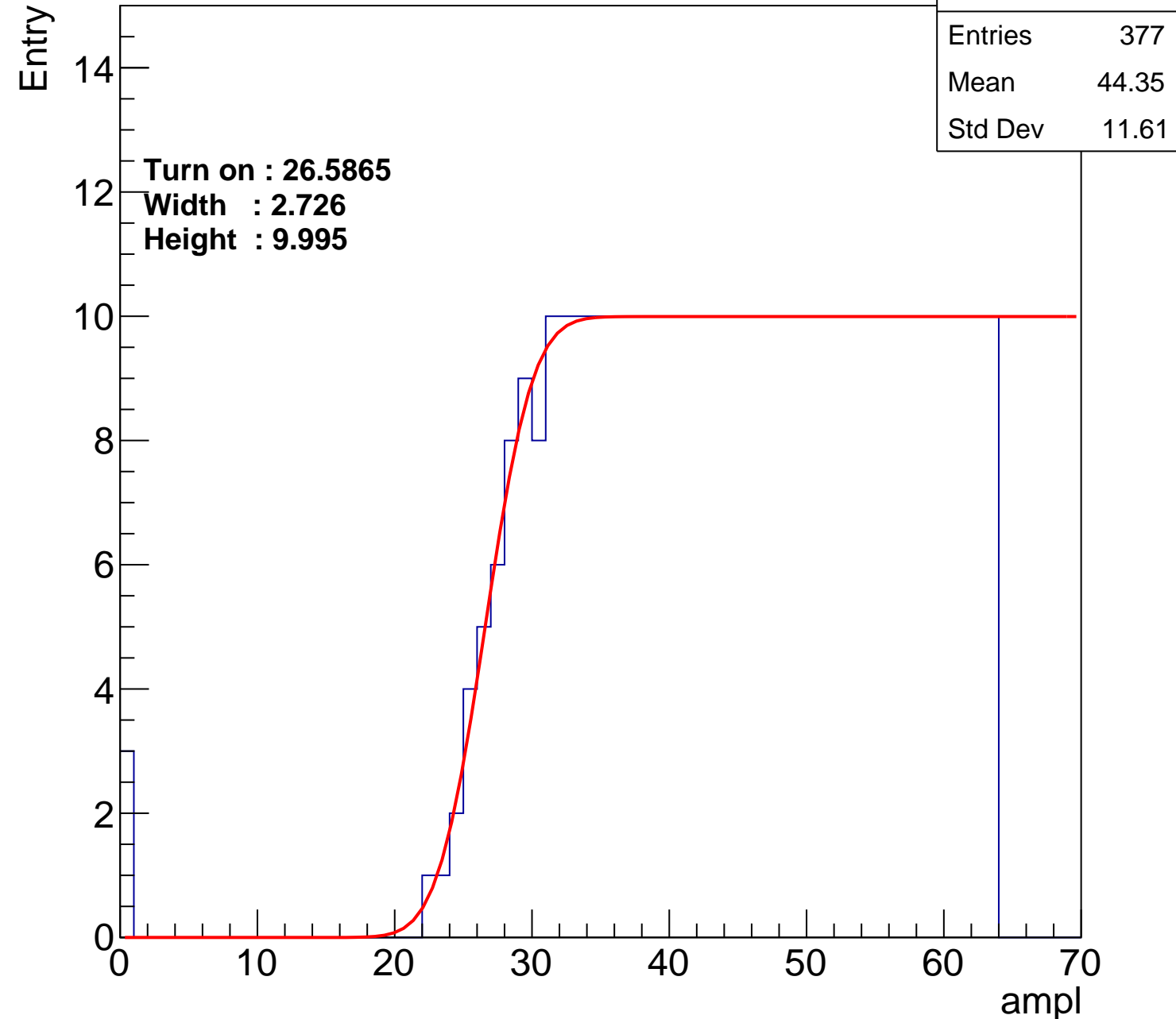
Width : 2.726

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.34
Std Dev	12.1

Turn on : 24.8291

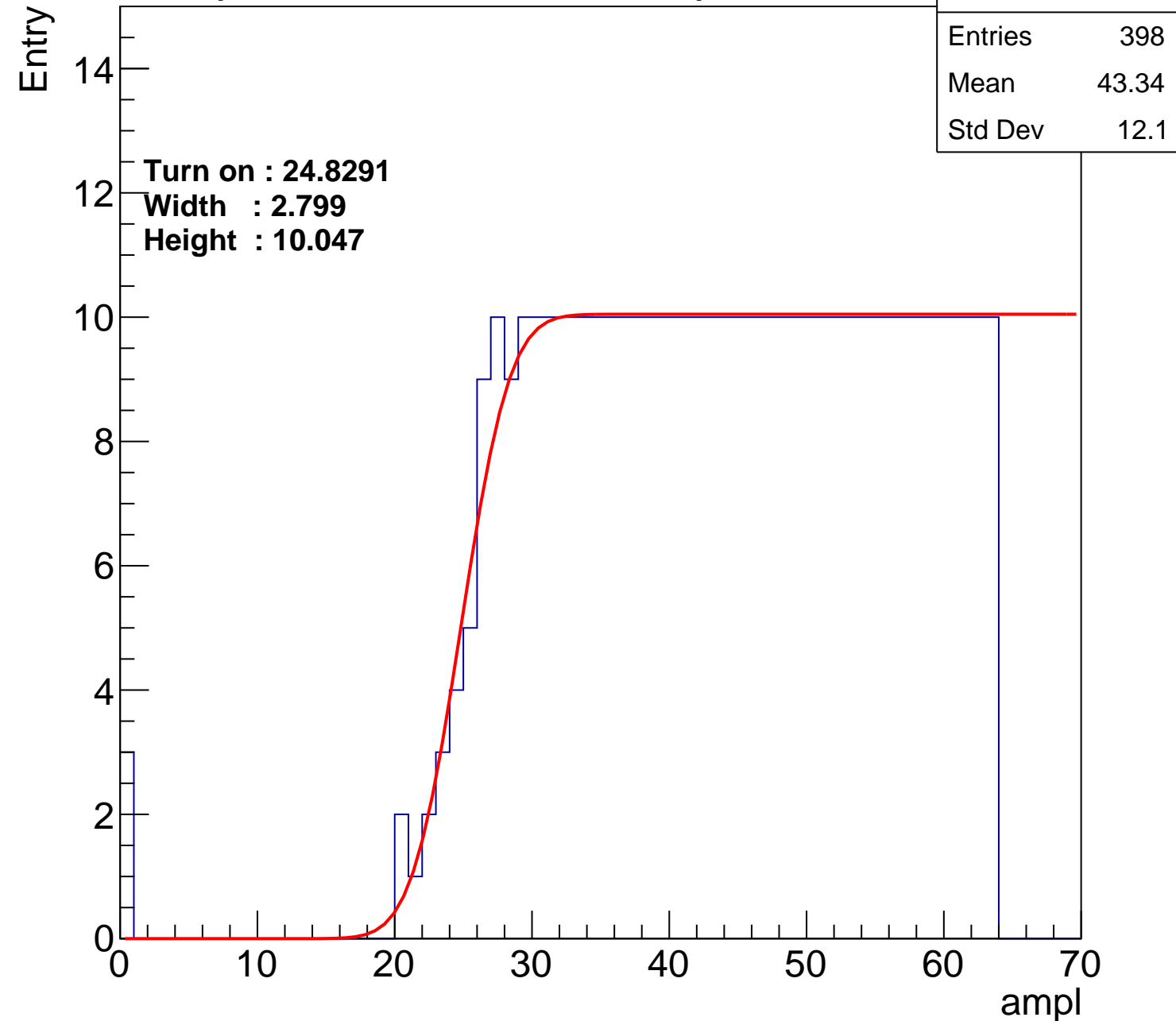
Width : 2.799

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.2569

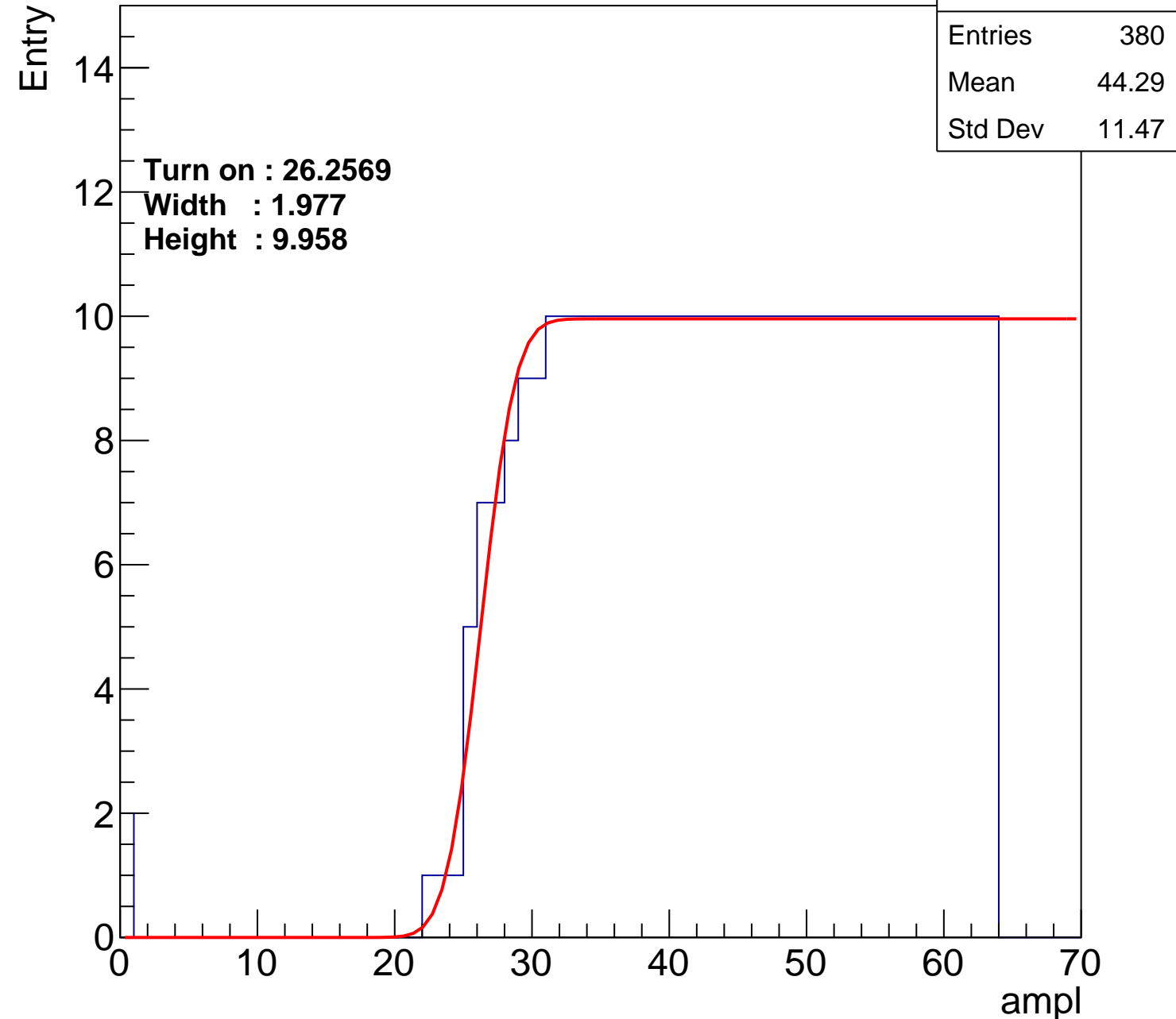
Width : 1.977

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch90

calib_packv5_042523_0143.root, FC#3, port B3

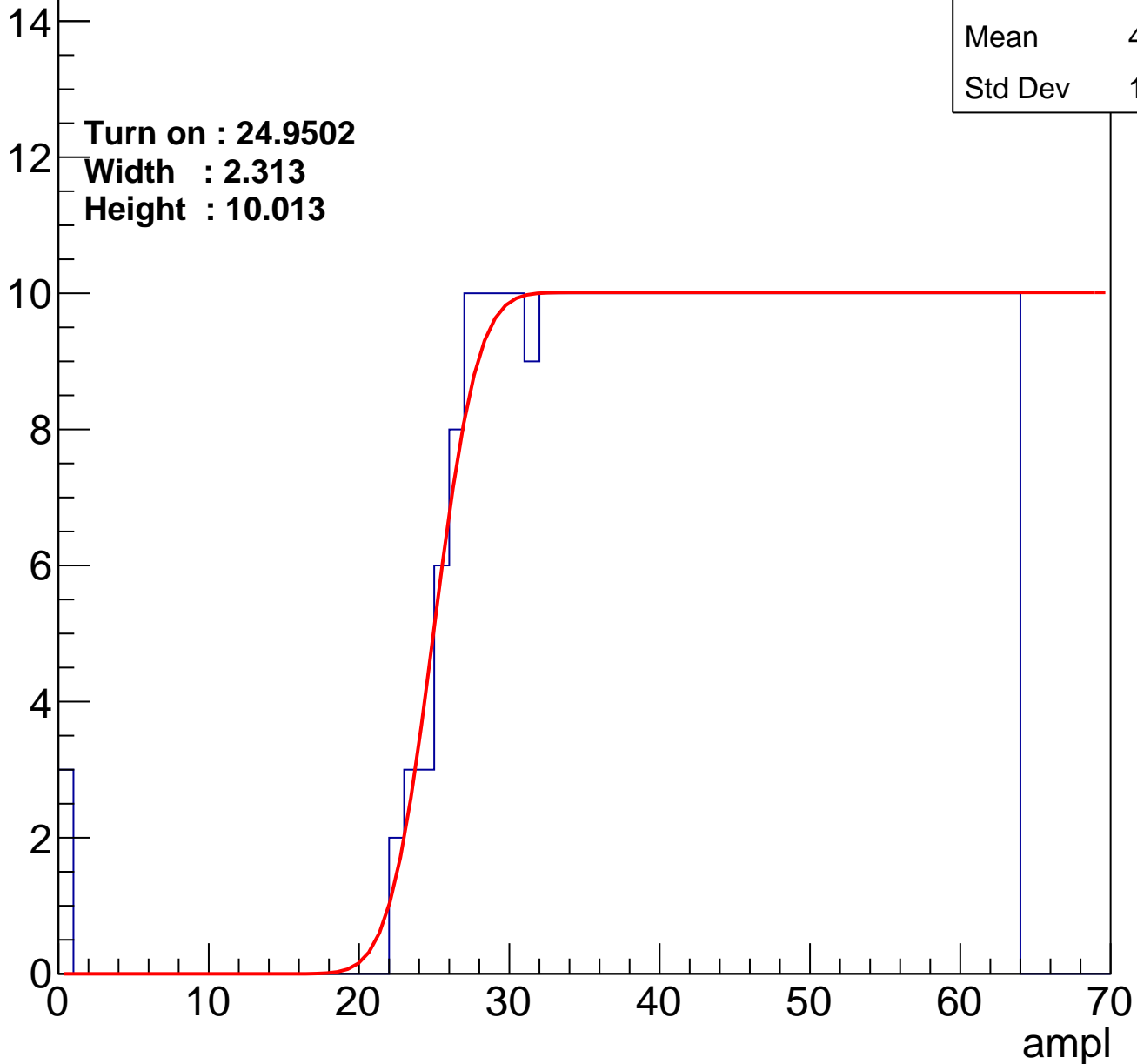
Entries	394
Mean	43.56
Std Dev	11.96

Turn on : 24.9502

Width : 2.313

Height : 10.013

Entry



B1L000S, U21-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.2461

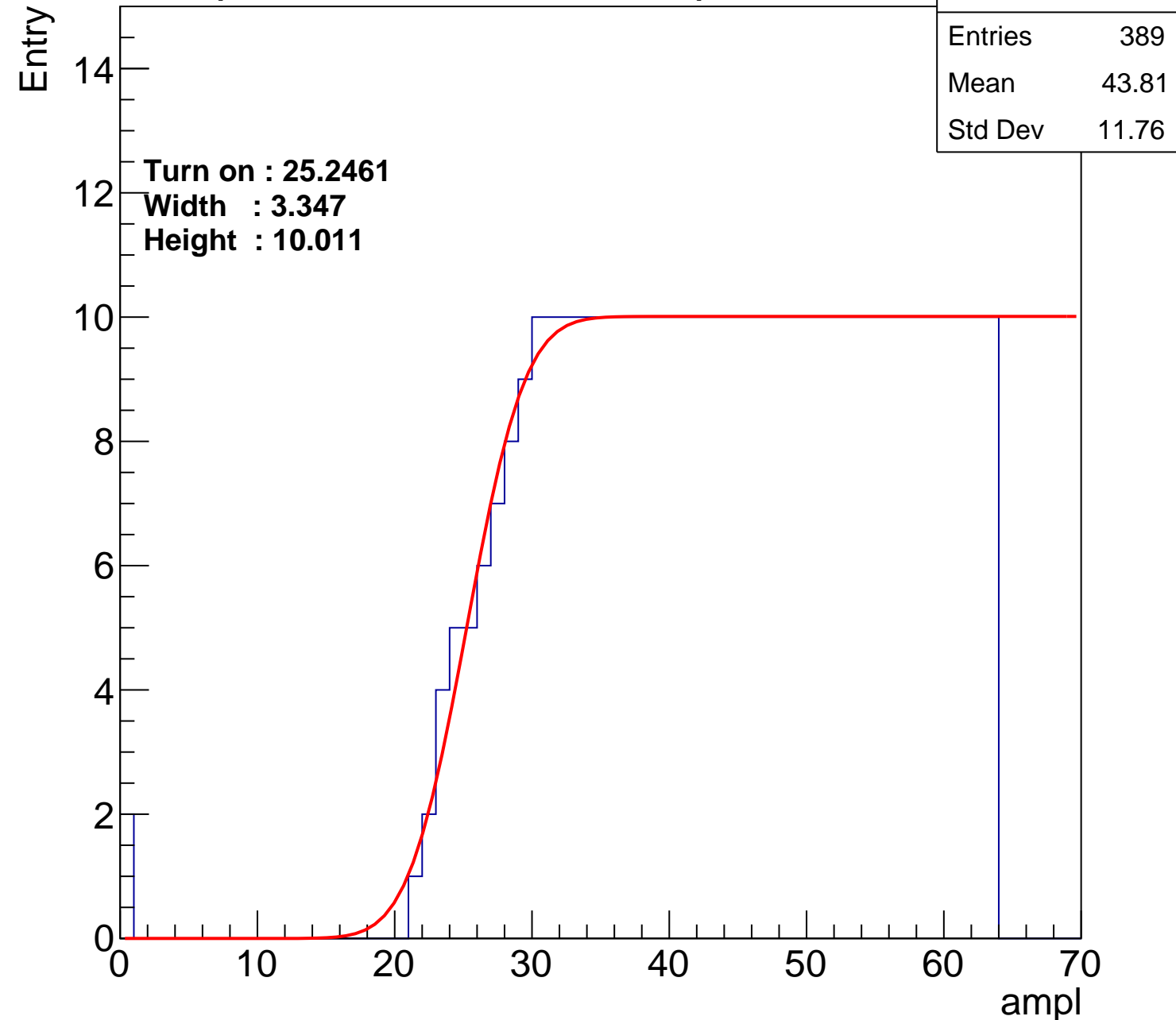
Width : 3.347

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.91
Std Dev	11.78

Turn on : 25.7121

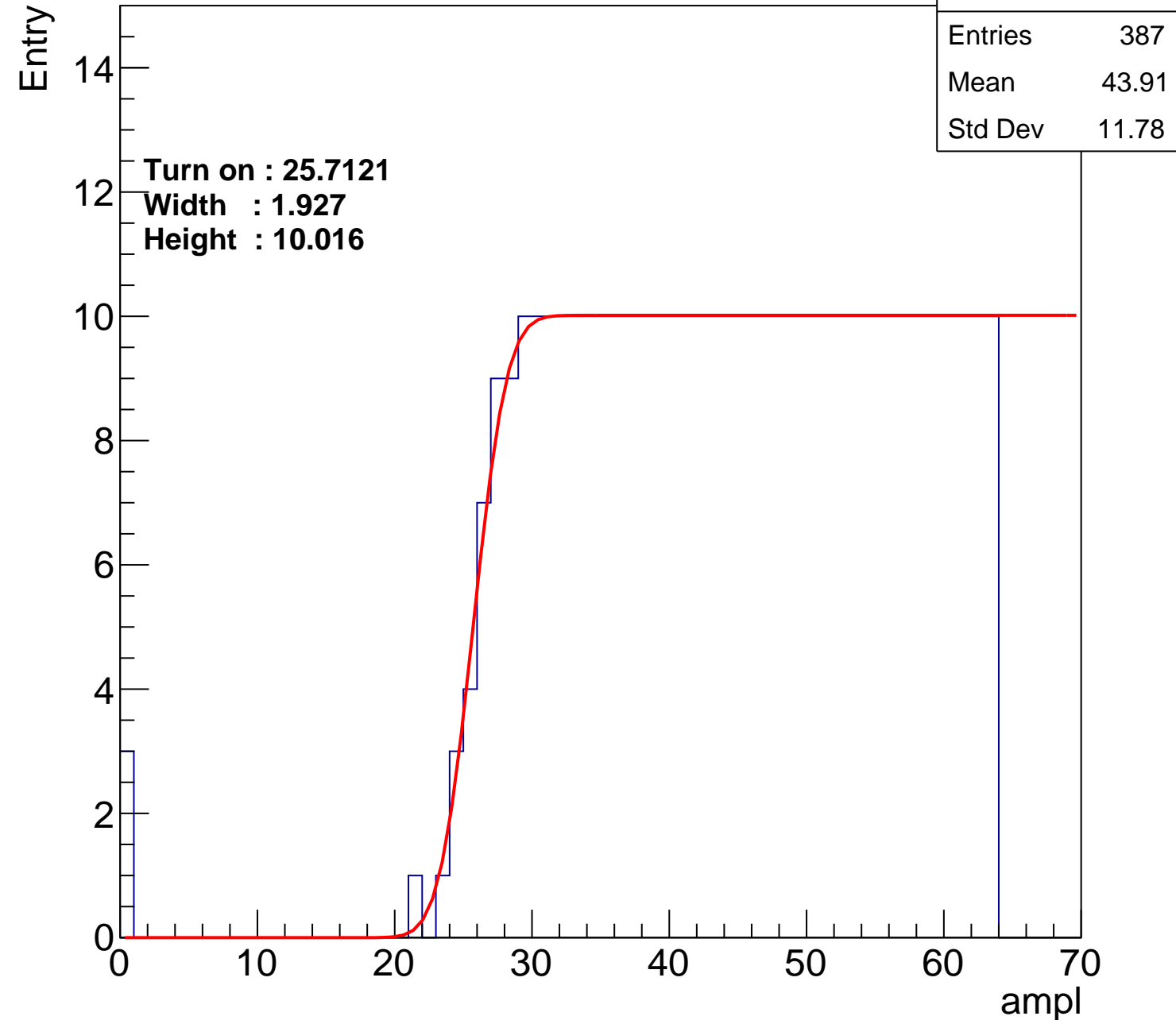
Width : 1.927

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.78
Std Dev	11.59

Turn on : 24.7320

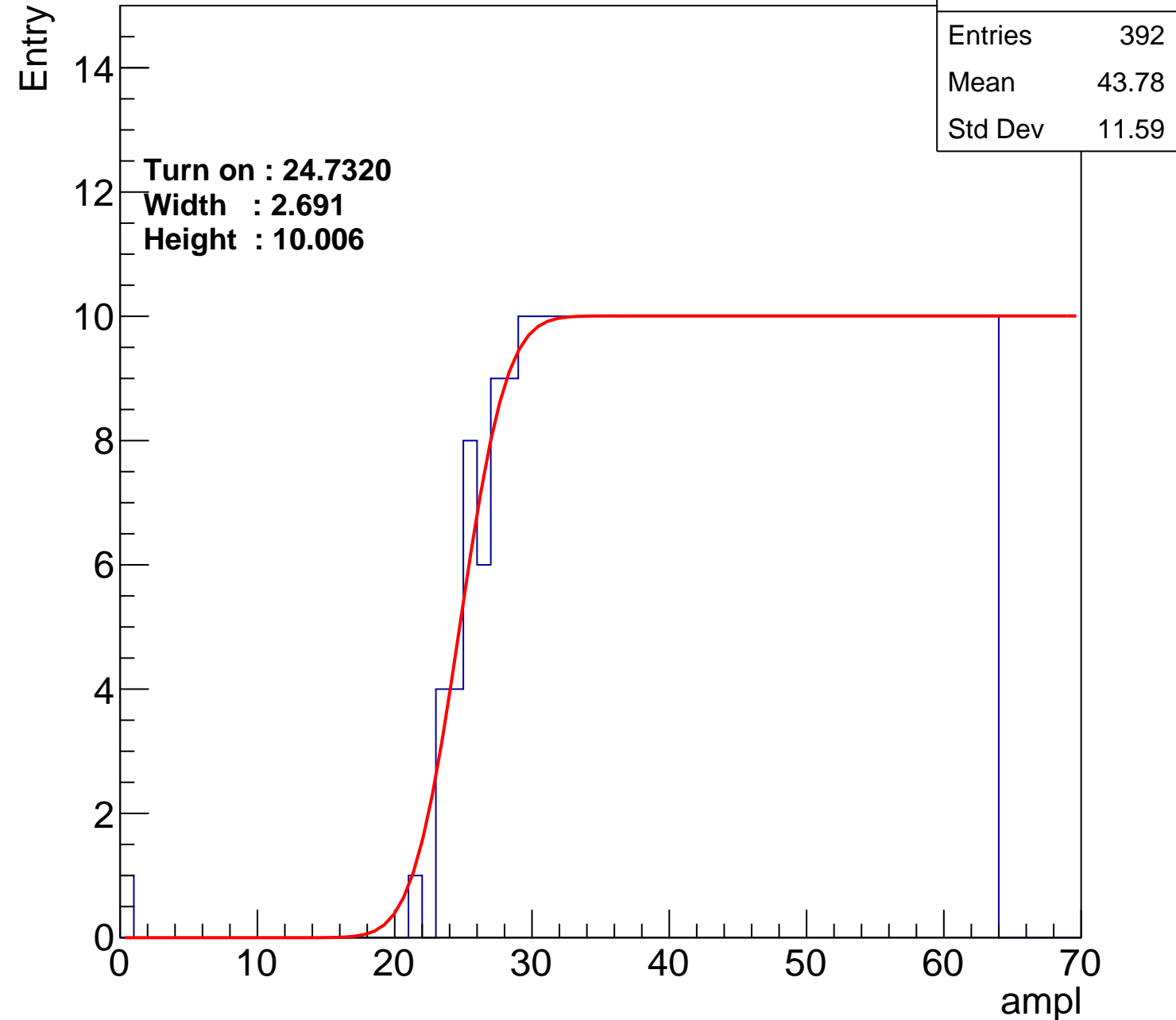
Width : 2.691

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.7490

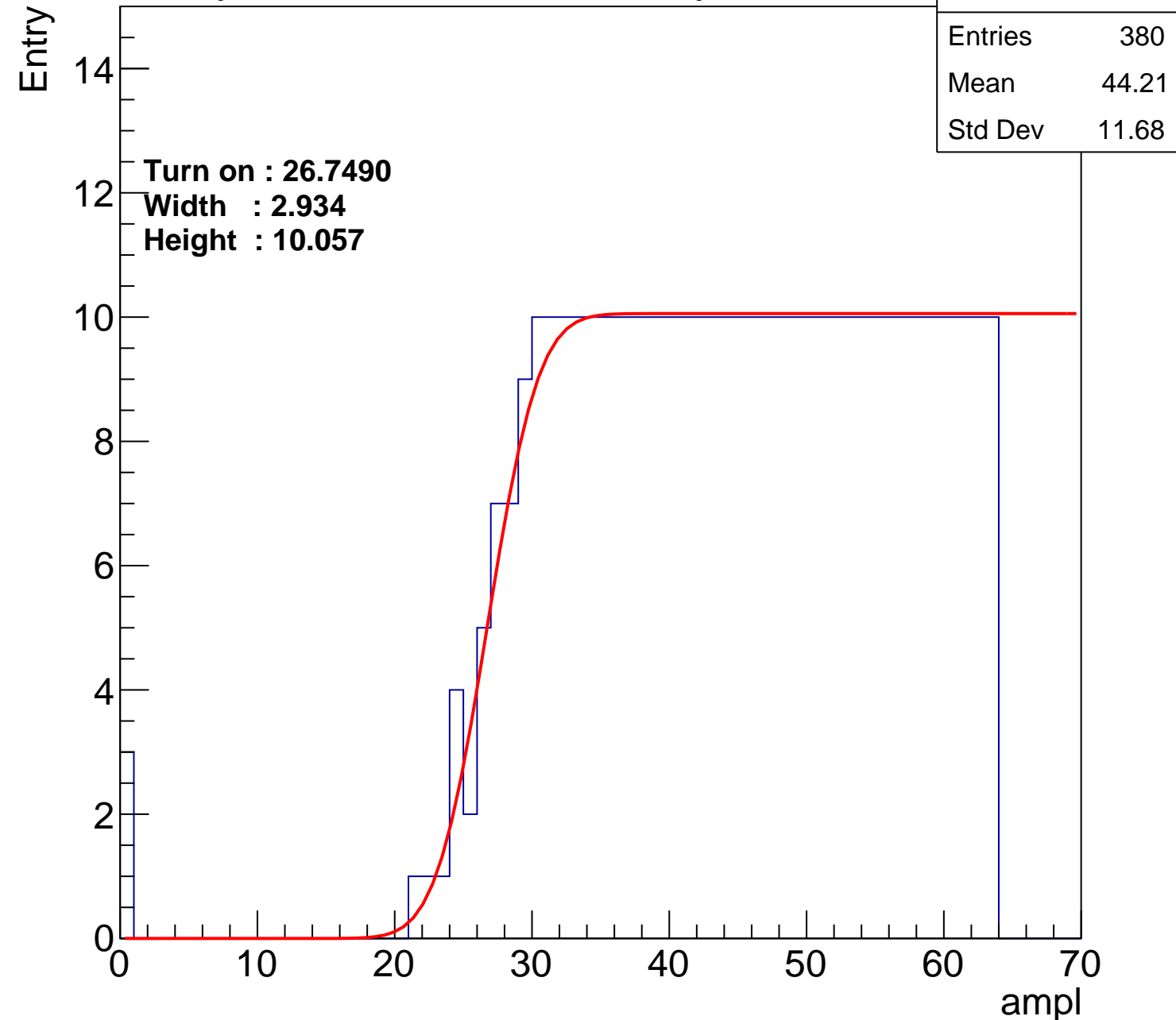
Width : 2.934

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.53
Std Dev	11.17

Turn on : 26.4194

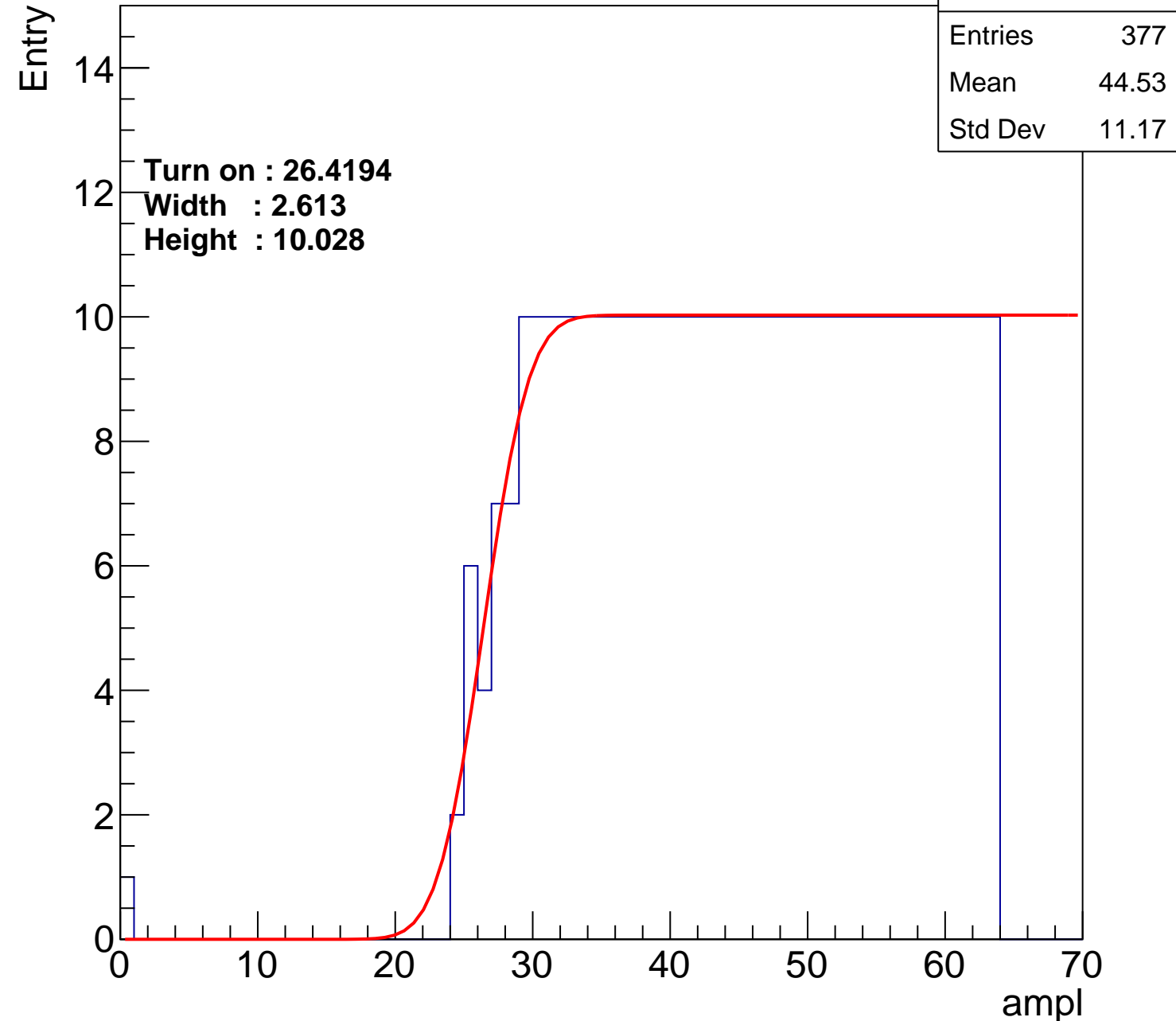
Width : 2.613

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 26.2973

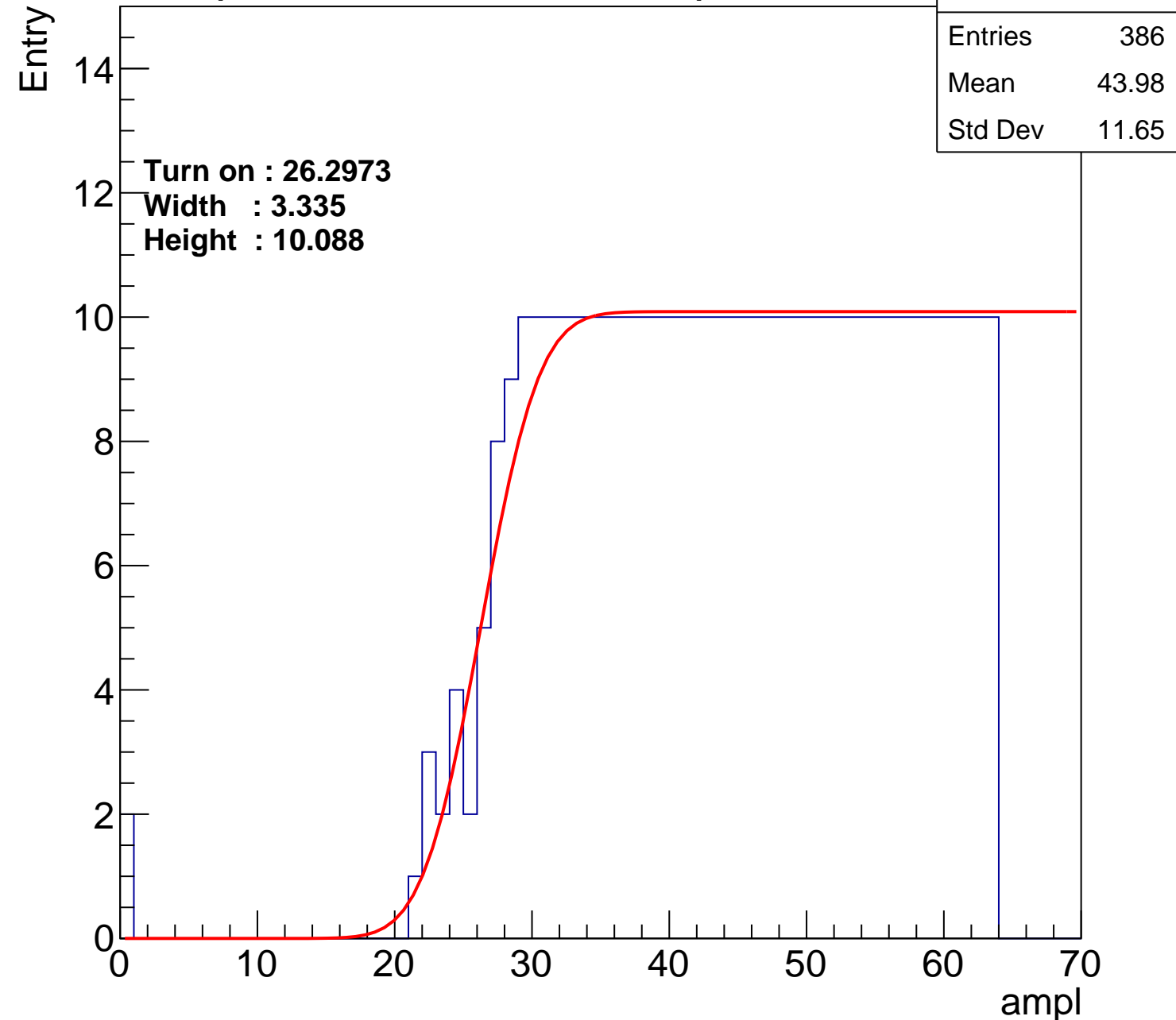
Width : 3.335

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch97

calib_packv5_042523_0143.root, FC#3, port B3

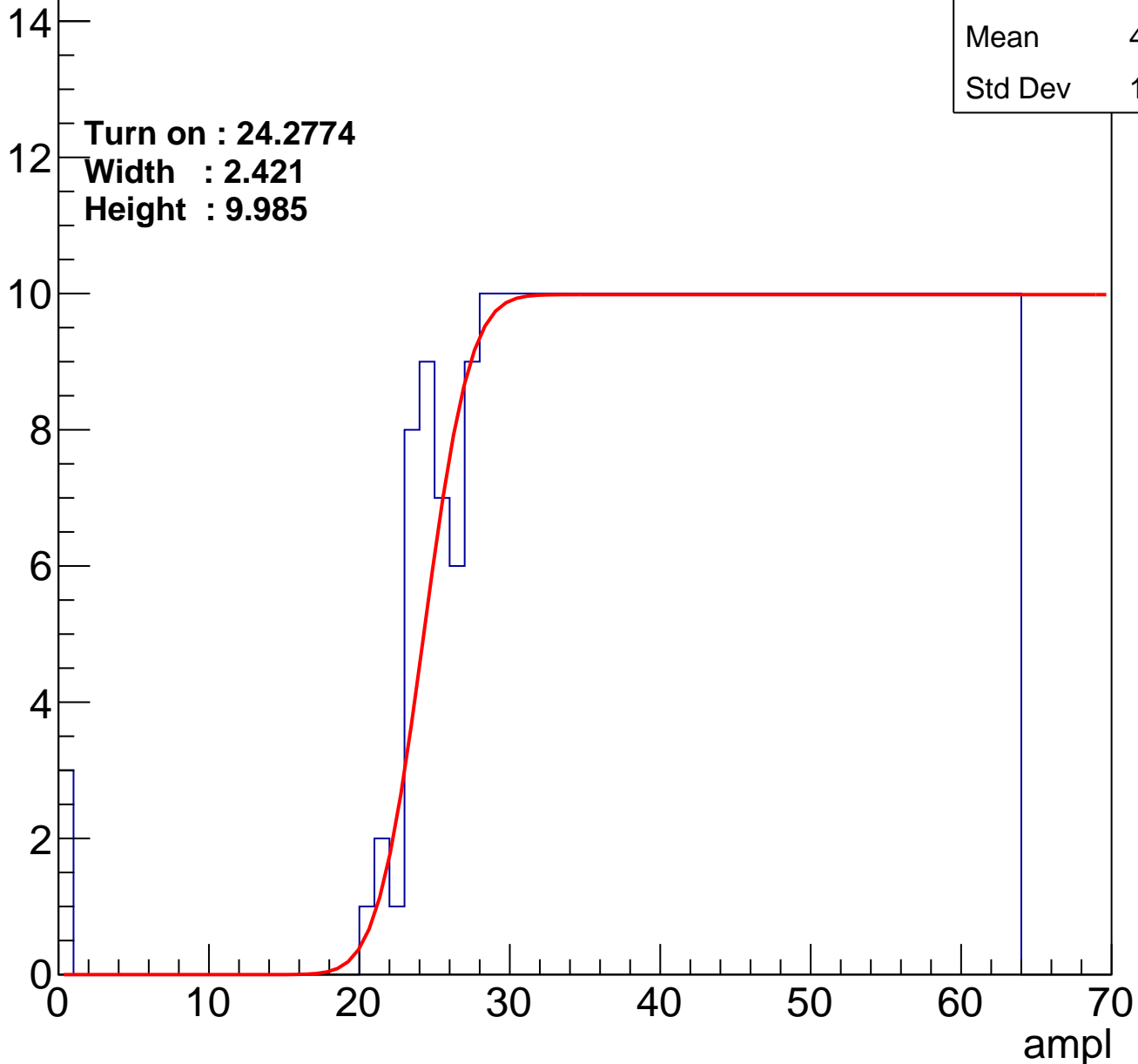
Entries	406
Mean	42.95
Std Dev	12.29

Turn on : 24.2774

Width : 2.421

Height : 9.985

Entry



B1L000S, U21-ch98

calib_packv5_042523_0143.root, FC#3, port B3

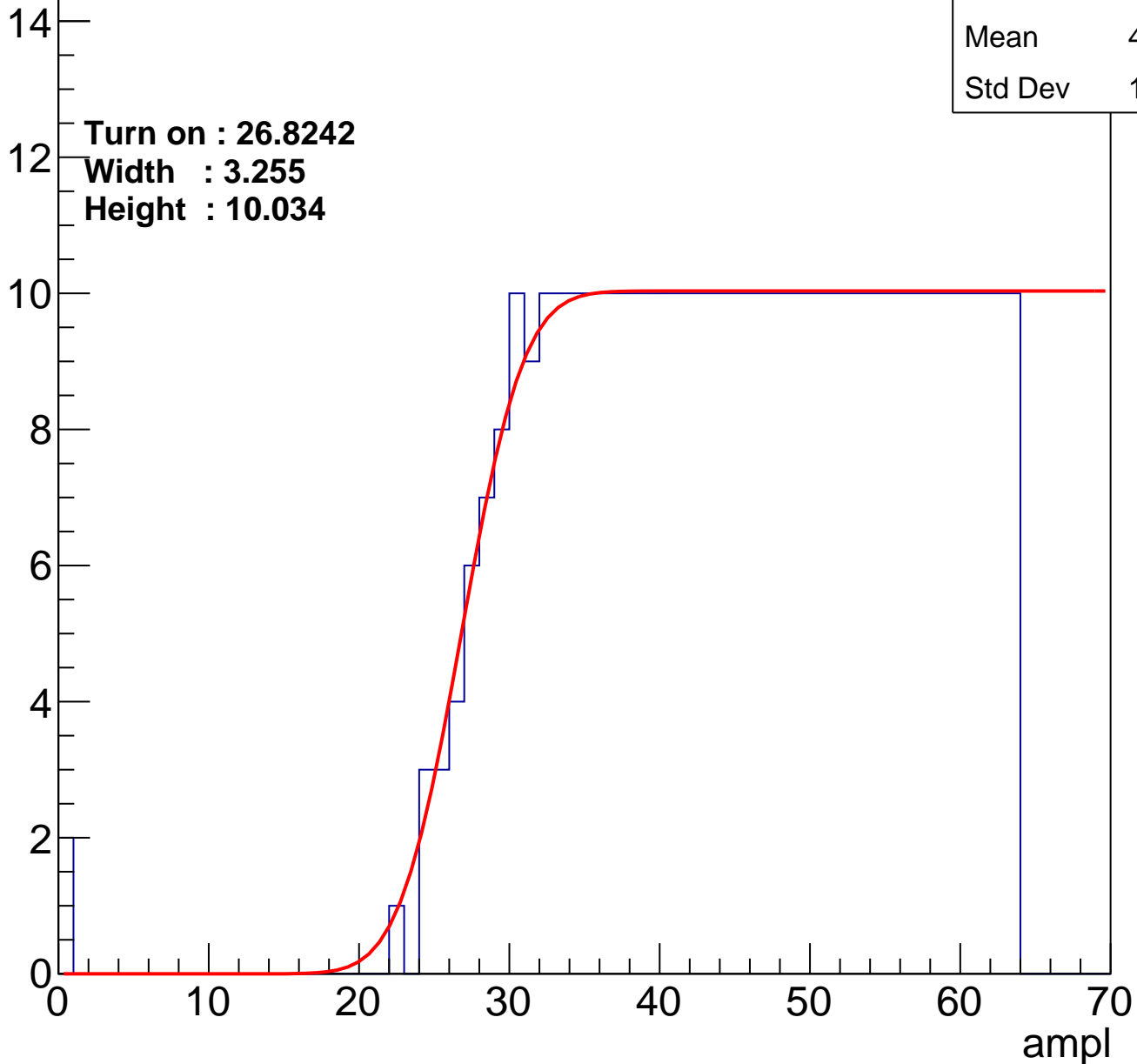
Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 26.8242

Width : 3.255

Height : 10.034

Entry



B1L000S, U21-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.4922

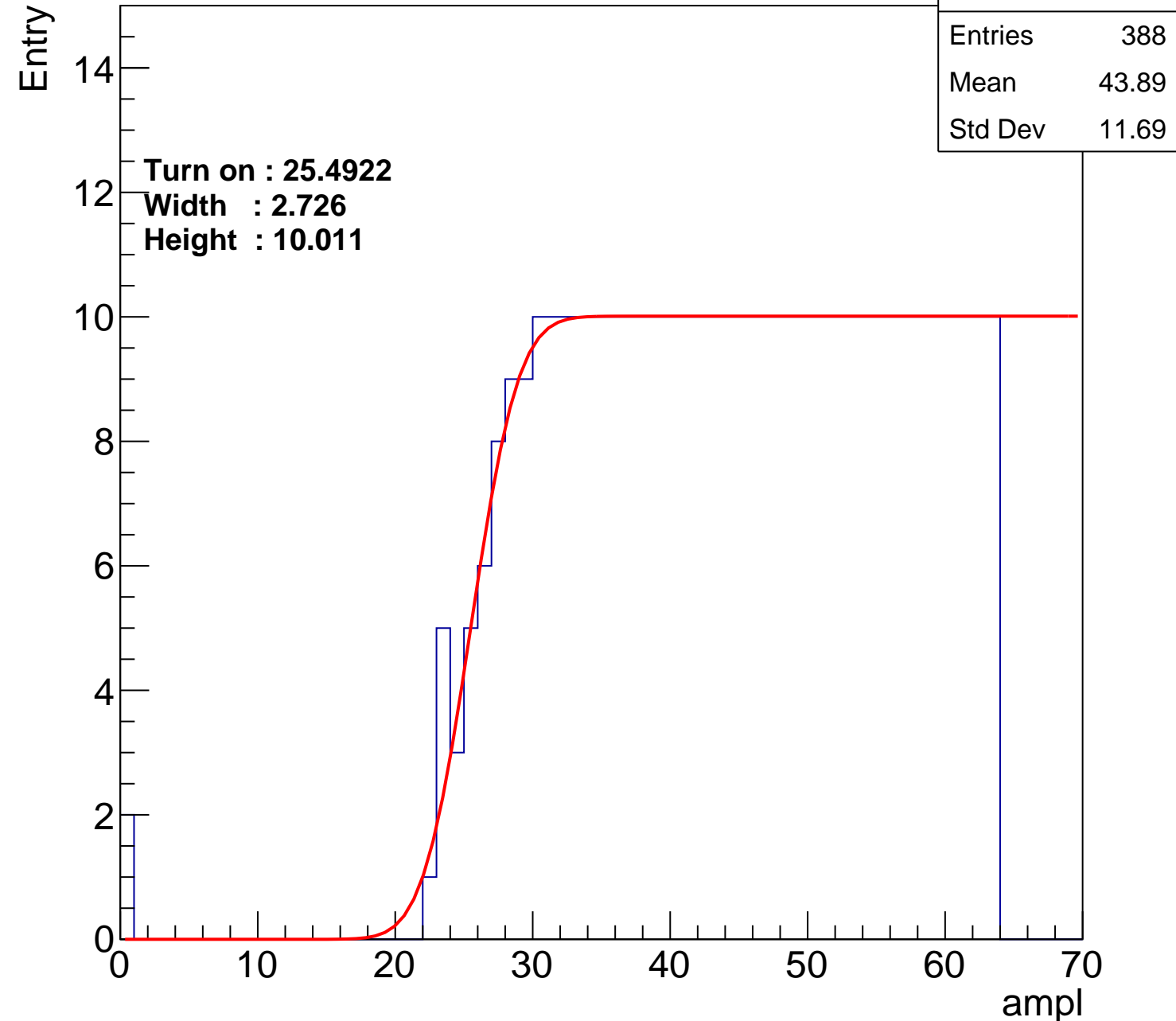
Width : 2.726

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch100

calib_packv5_042523_0143.root, FC#3, port B3

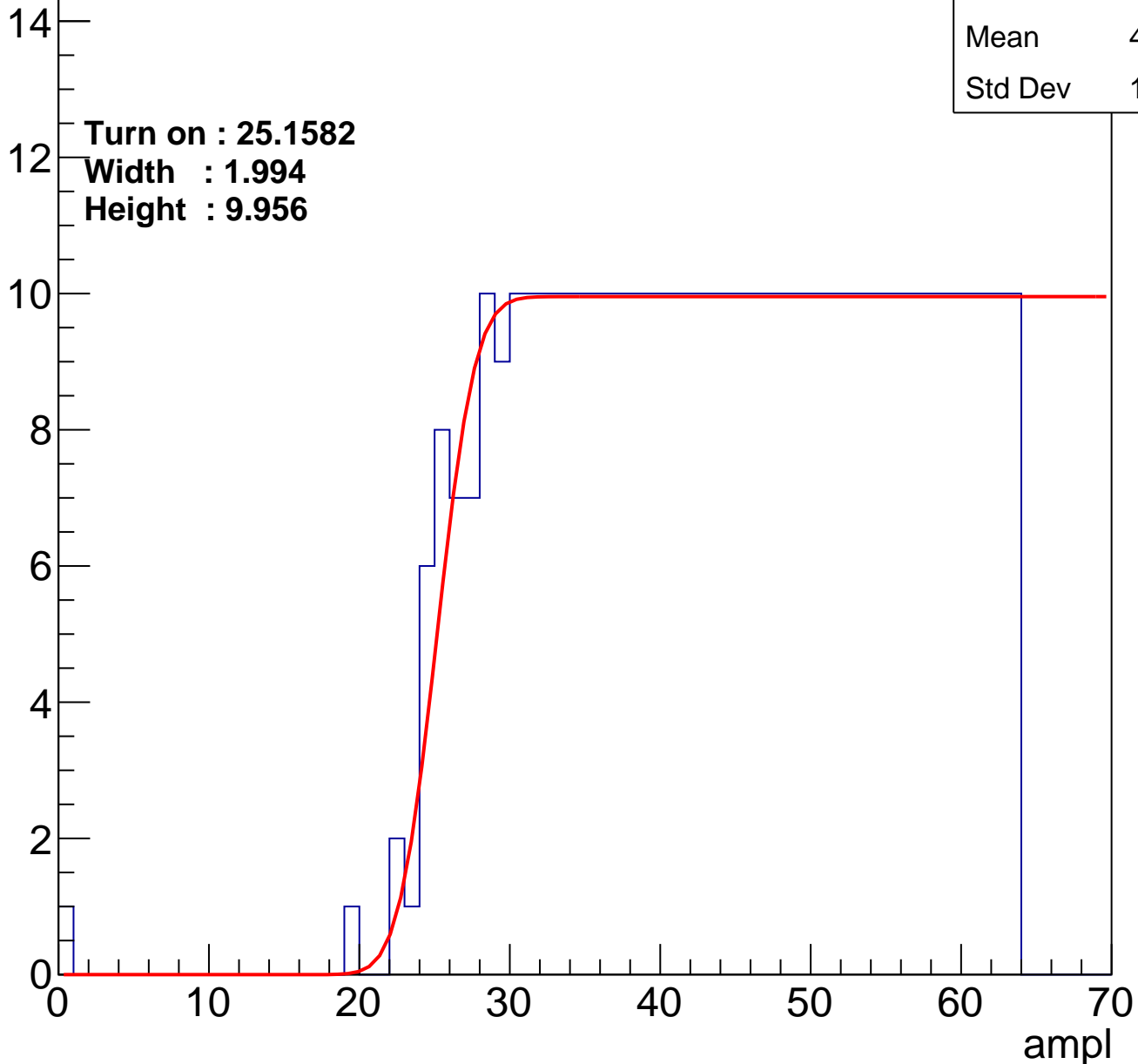
Entries	392
Mean	43.76
Std Dev	11.62

Turn on : 25.1582

Width : 1.994

Height : 9.956

Entry



B1L000S, U21-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.61
Std Dev	11.94

Turn on : 24.3775

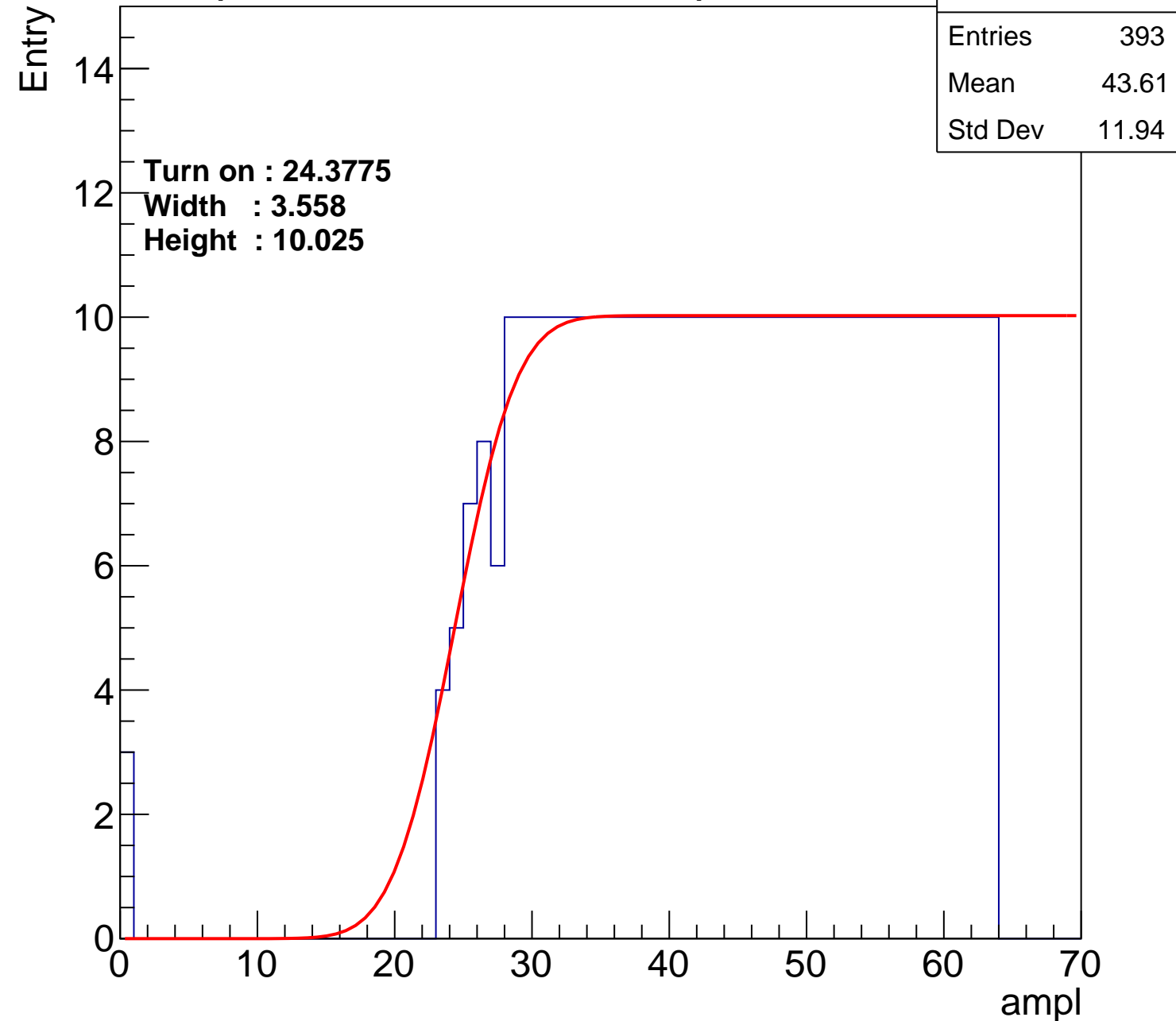
Width : 3.558

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	412
Mean	42.66
Std Dev	12.39

Turn on : 23.1697

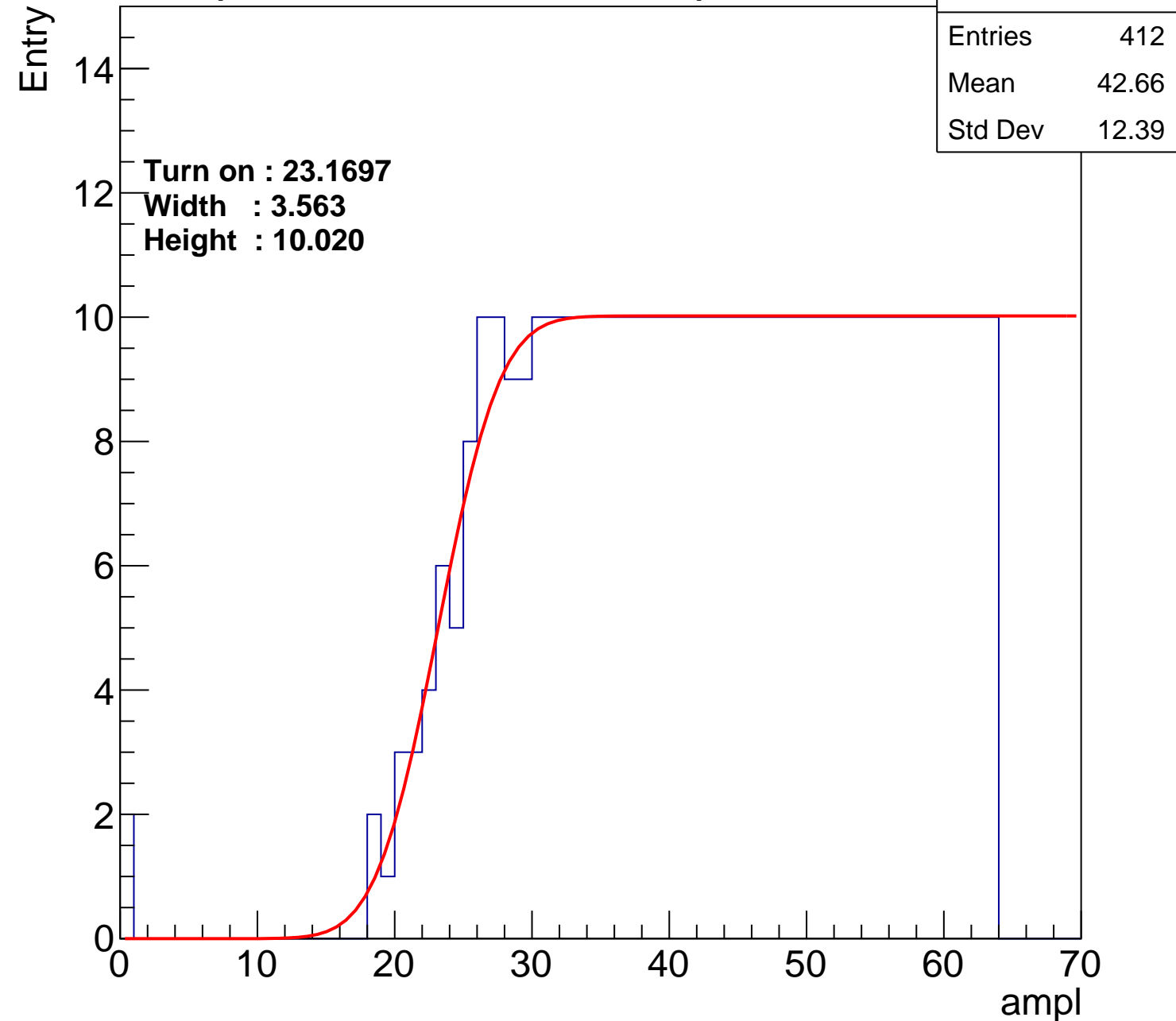
Width : 3.563

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 27.3144

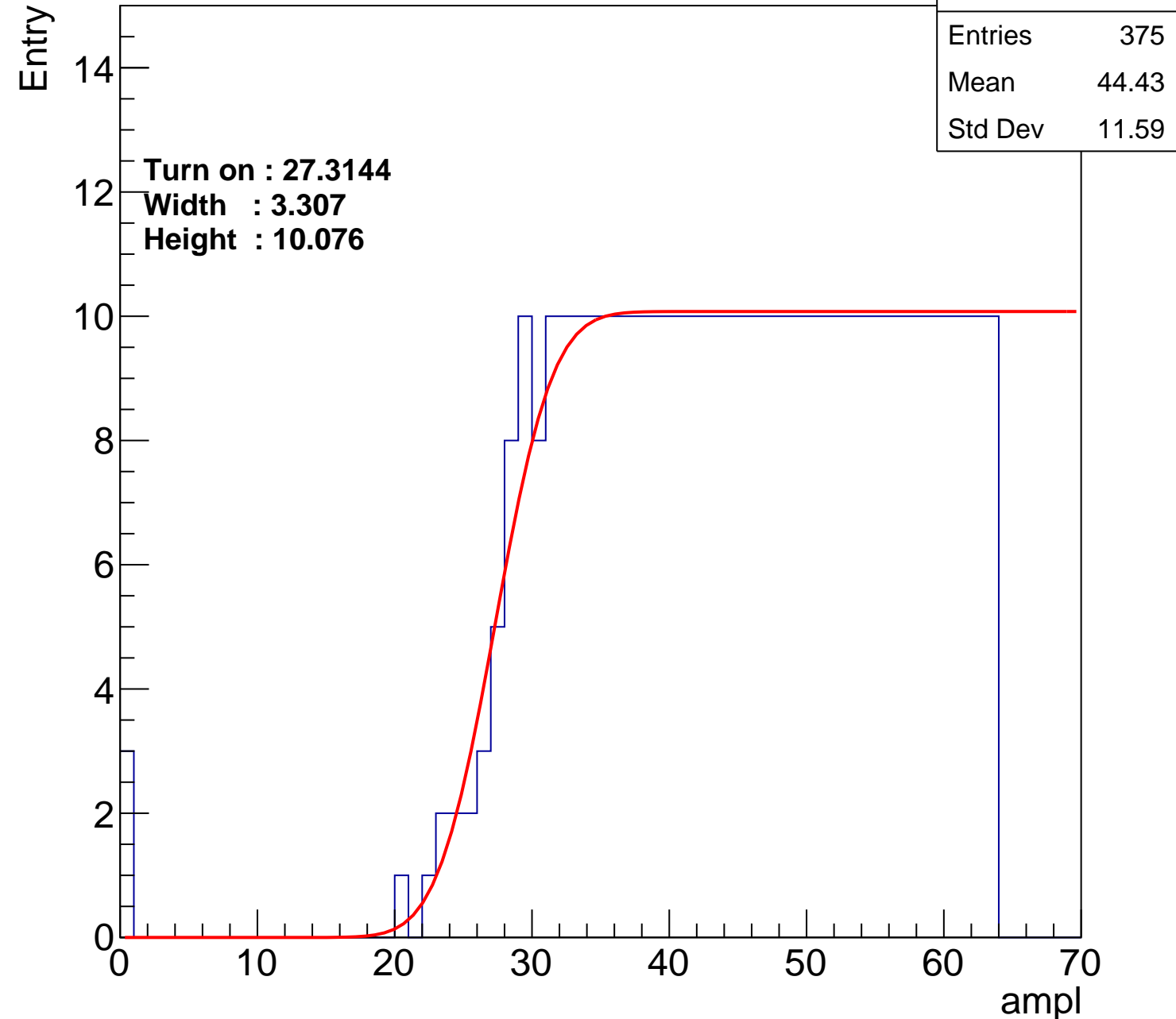
Width : 3.307

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch104

calib_packv5_042523_0143.root, FC#3, port B3

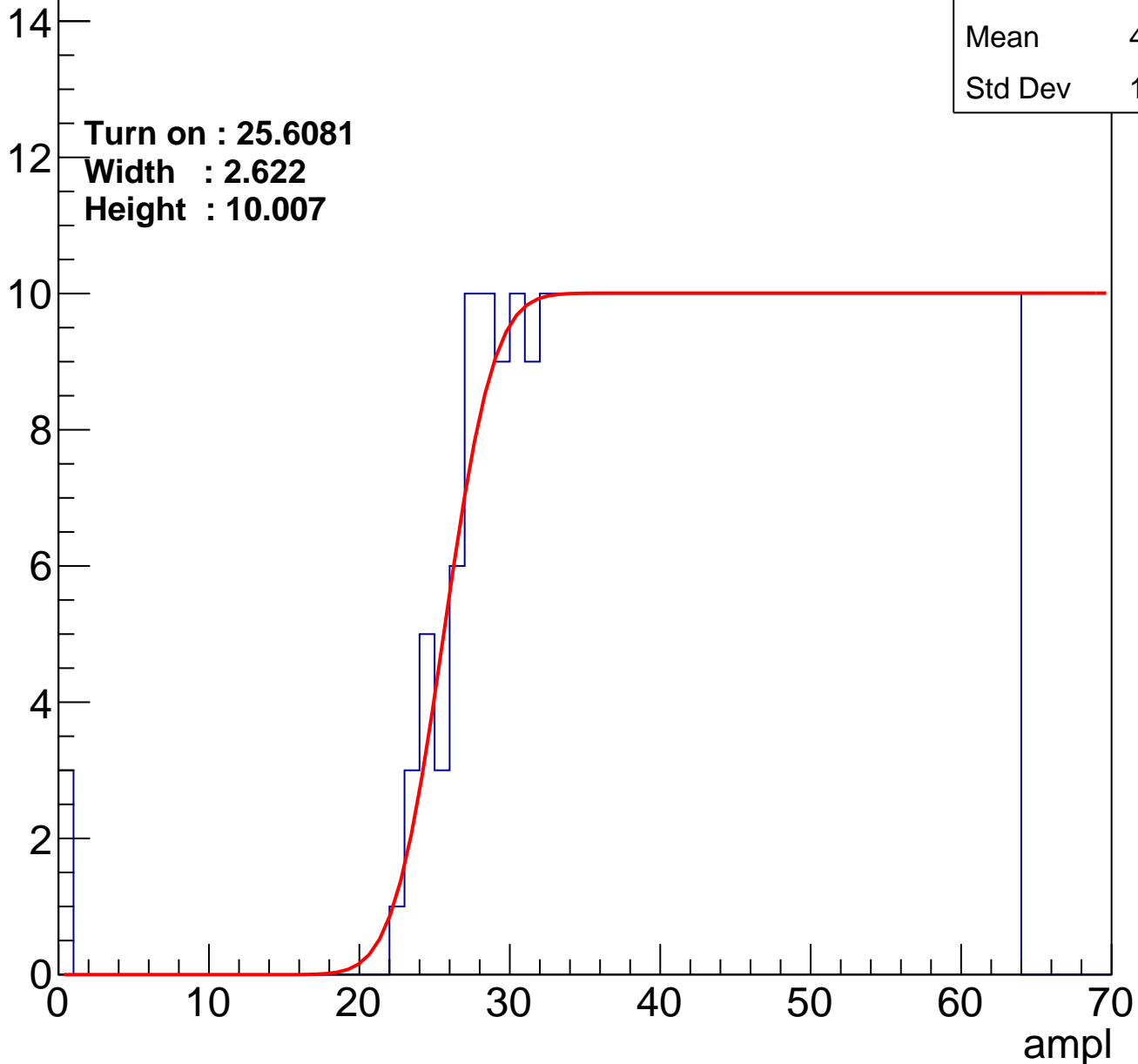
Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 25.6081

Width : 2.622

Height : 10.007

Entry



B1L000S, U21-ch105

calib_packv5_042523_0143.root, FC#3, port B3

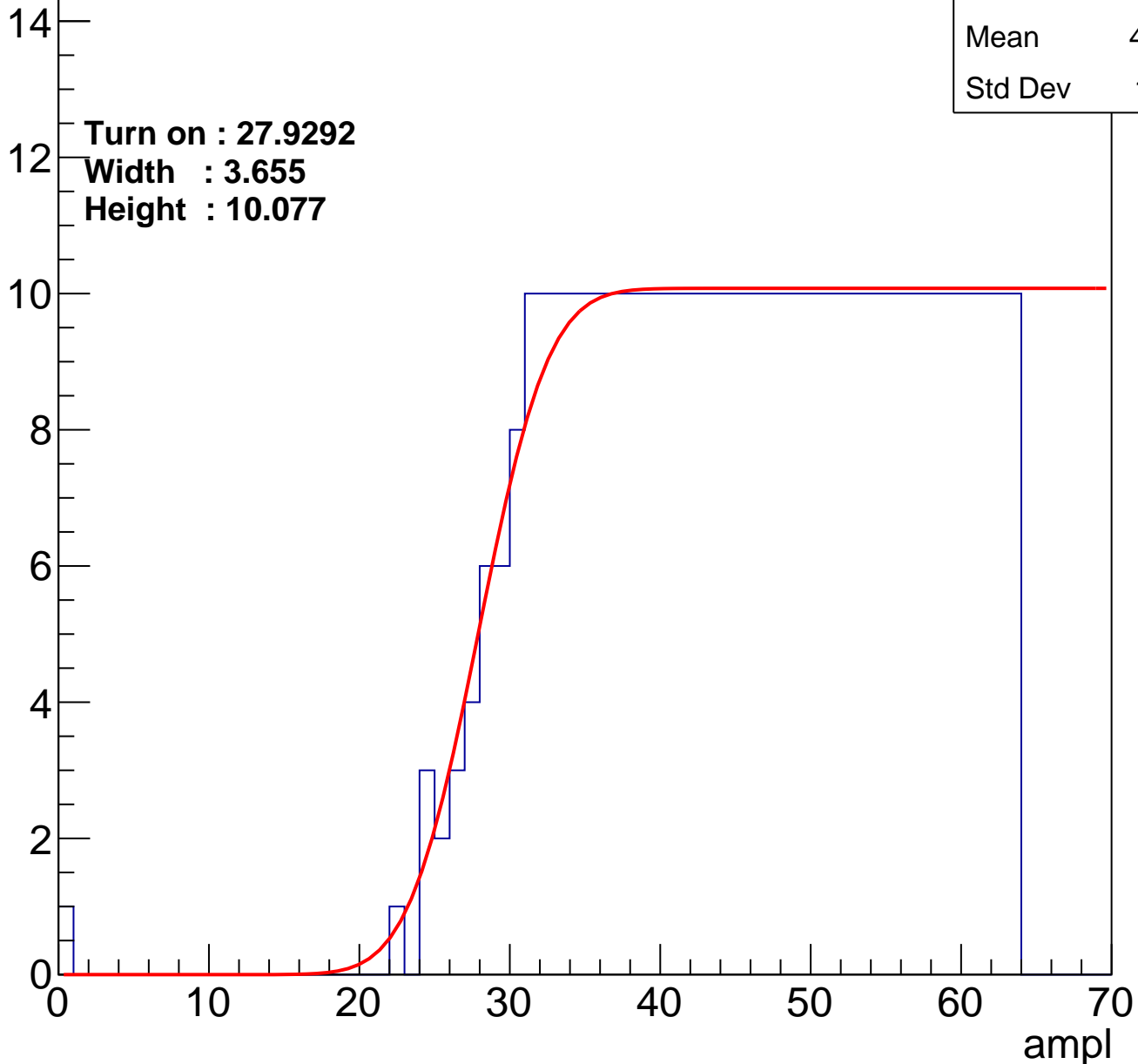
Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 27.9292

Width : 3.655

Height : 10.077

Entry



B1L000S, U21-ch106

calib_packv5_042523_0143.root, FC#3, port B3

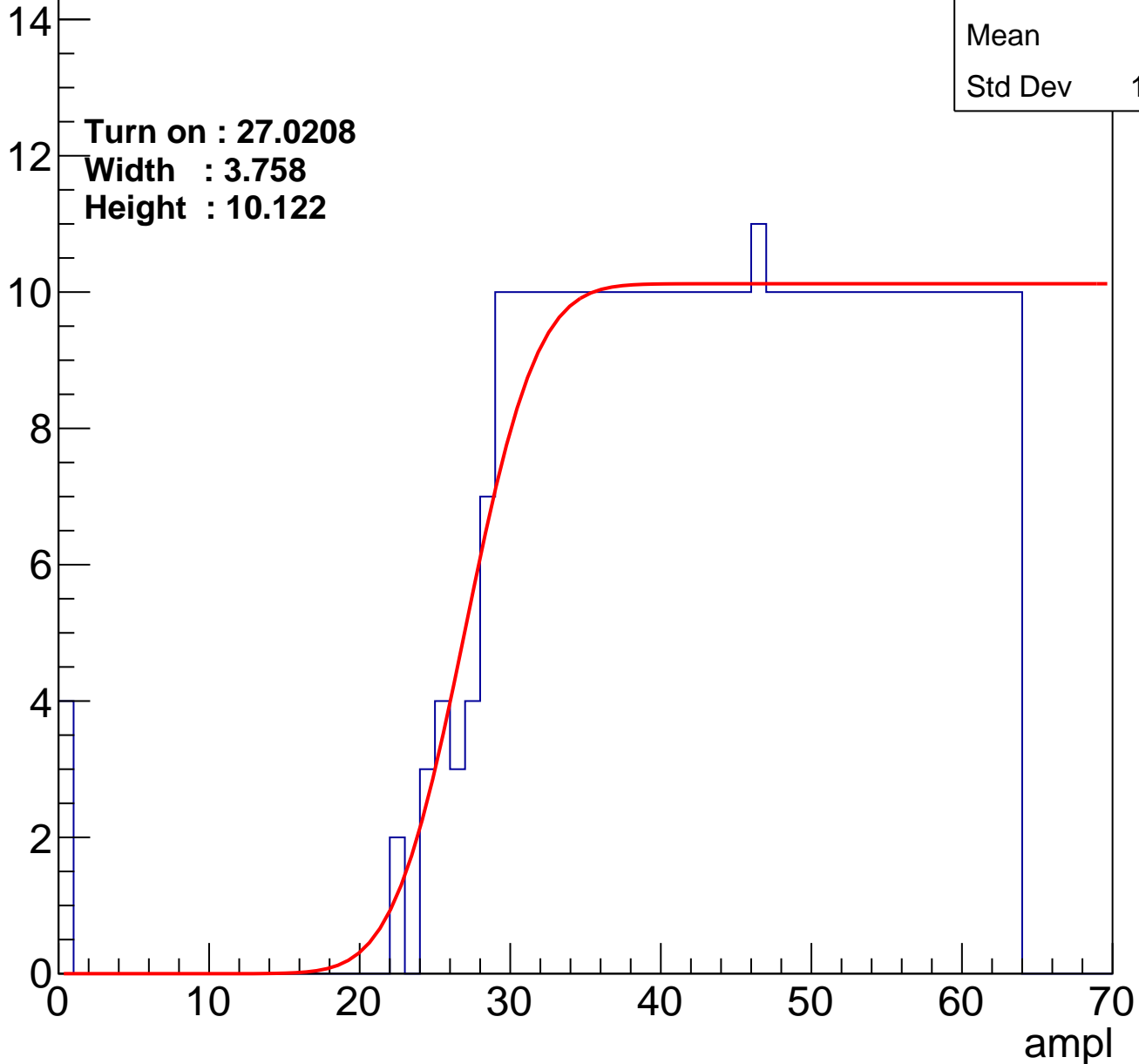
Entries	378
Mean	44.3
Std Dev	11.77

Turn on : 27.0208

Width : 3.758

Height : 10.122

Entry



B1L000S, U21-ch107

calib_packv5_042523_0143.root, FC#3, port B3

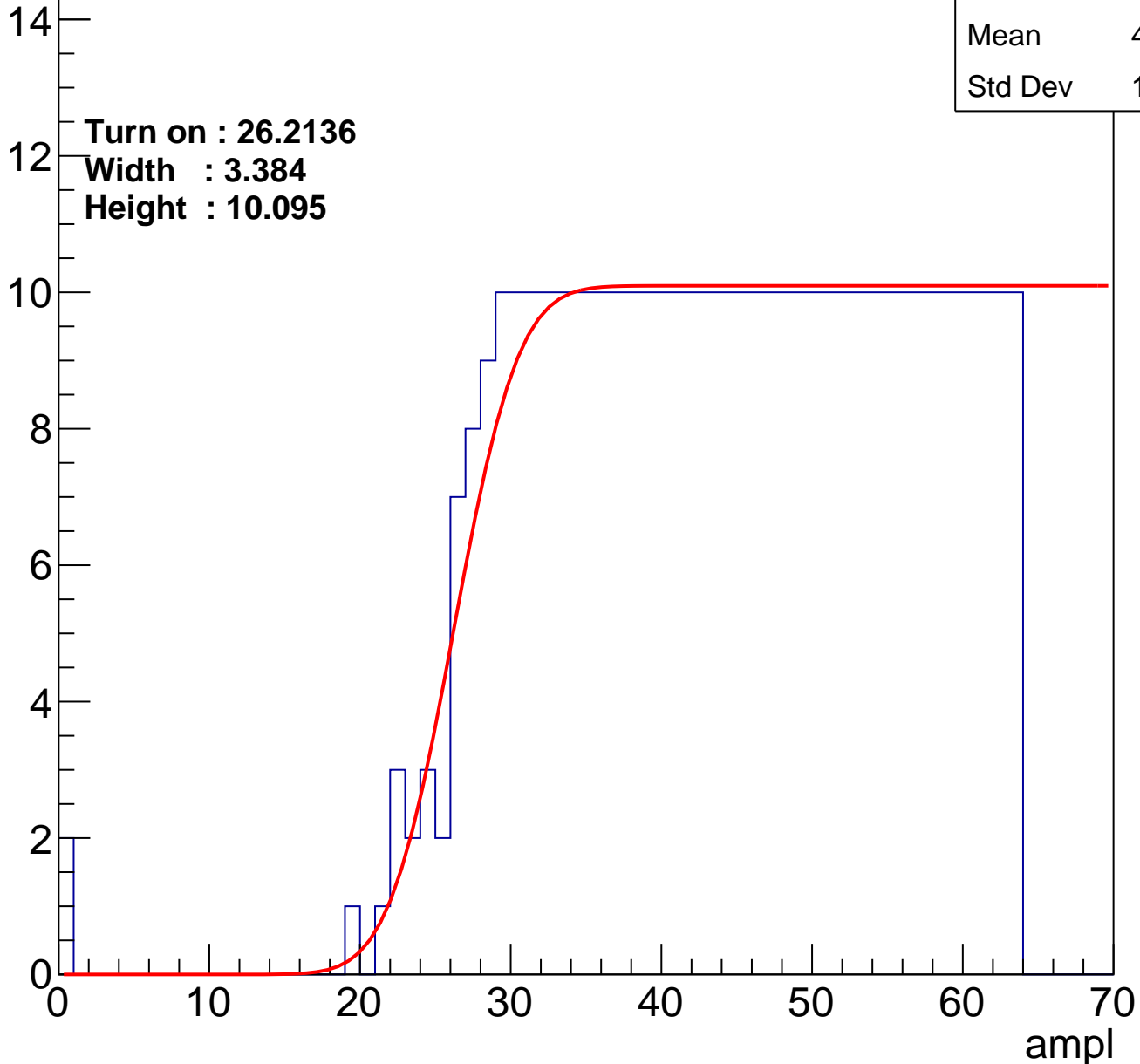
Entries	388
Mean	43.88
Std Dev	11.72

Turn on : 26.2136

Width : 3.384

Height : 10.095

Entry



B1L000S, U21-ch108

calib_packv5_042523_0143.root, FC#3, port B3

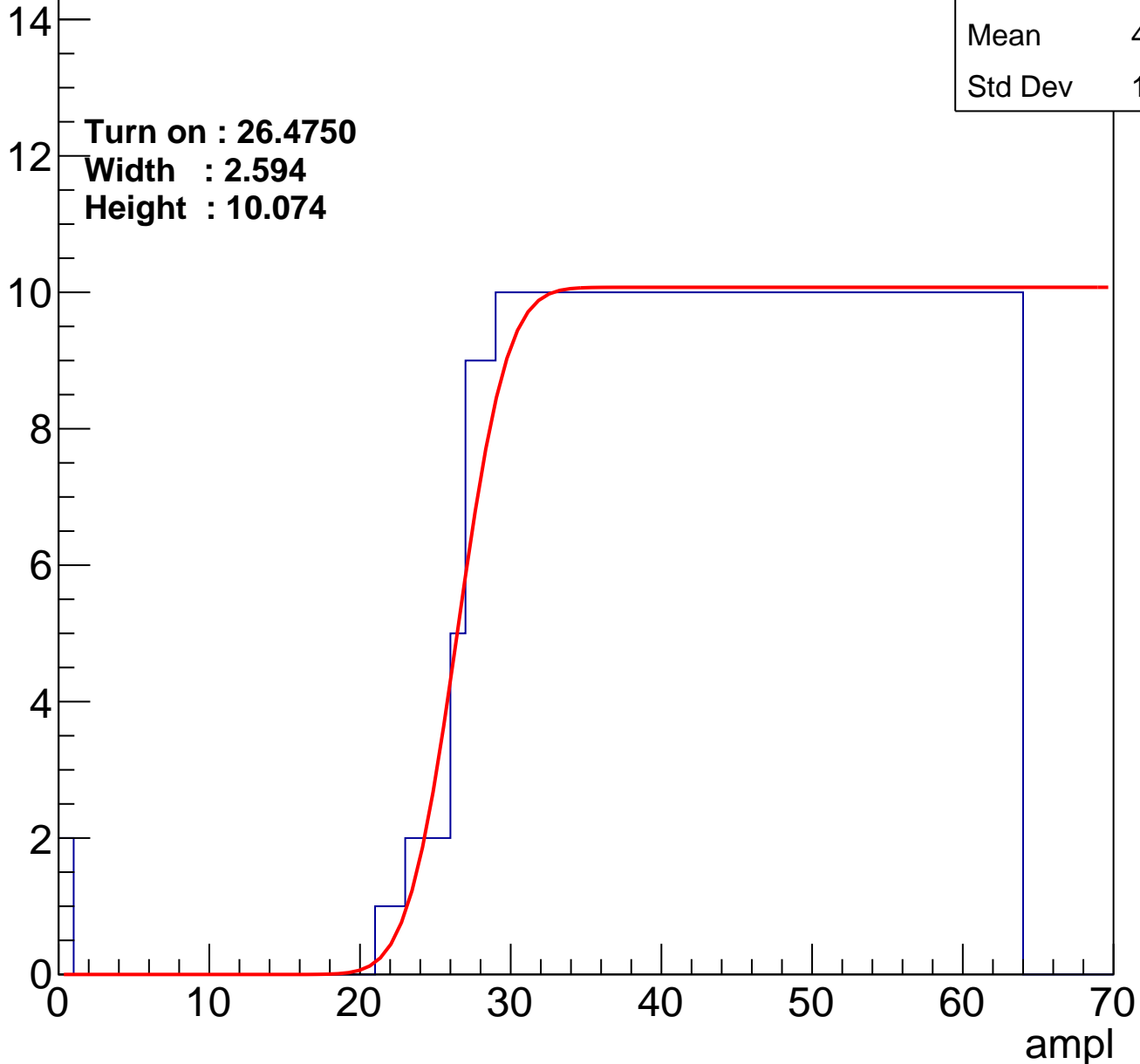
Entries	383
Mean	44.16
Std Dev	11.53

Turn on : 26.4750

Width : 2.594

Height : 10.074

Entry



B1L000S, U21-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.78
Std Dev	11.64

Turn on : 25.4324

Width : 2.504

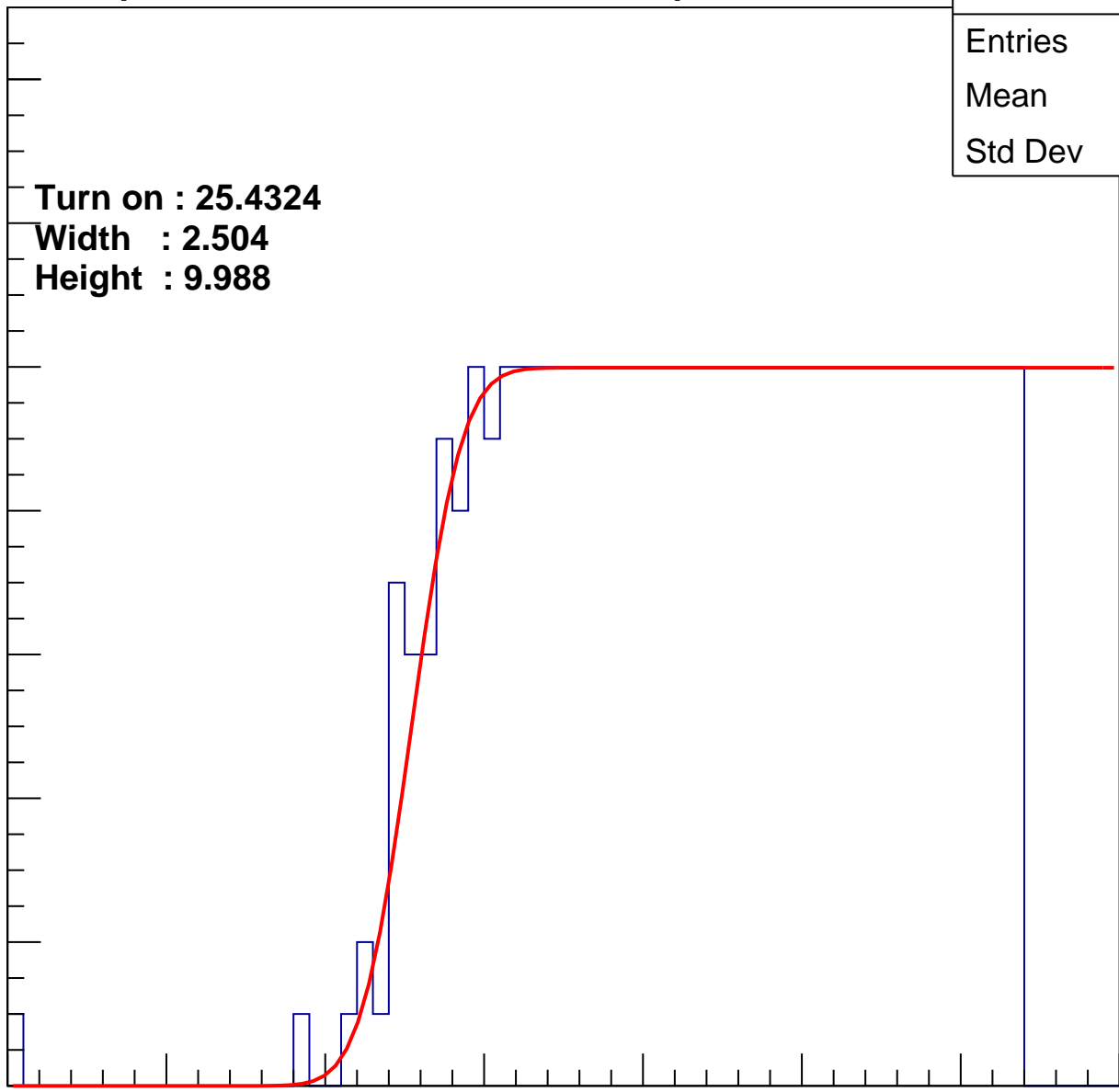
Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.23
Std Dev	11.57

Turn on : 26.8899

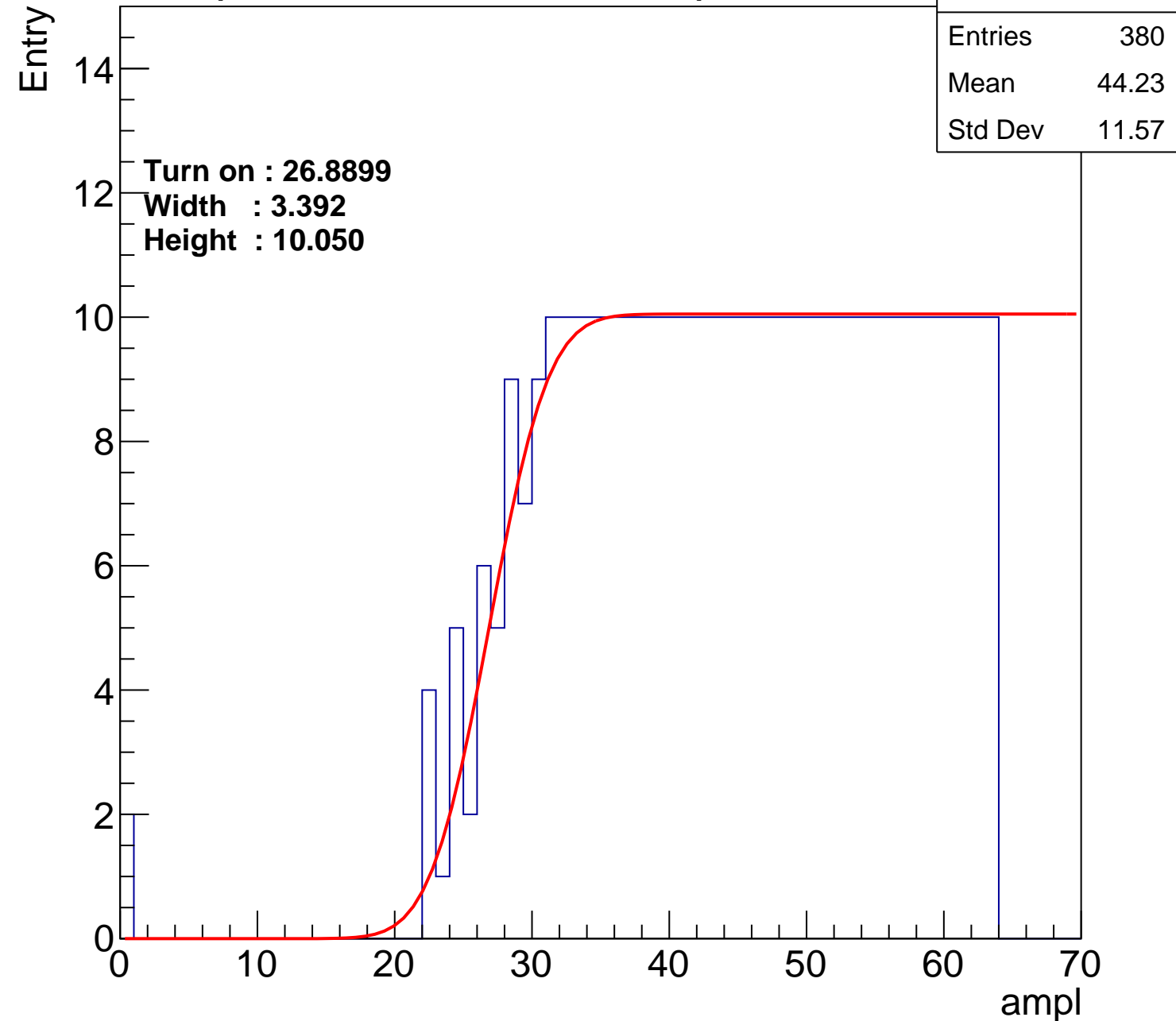
Width : 3.392

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 25.9651

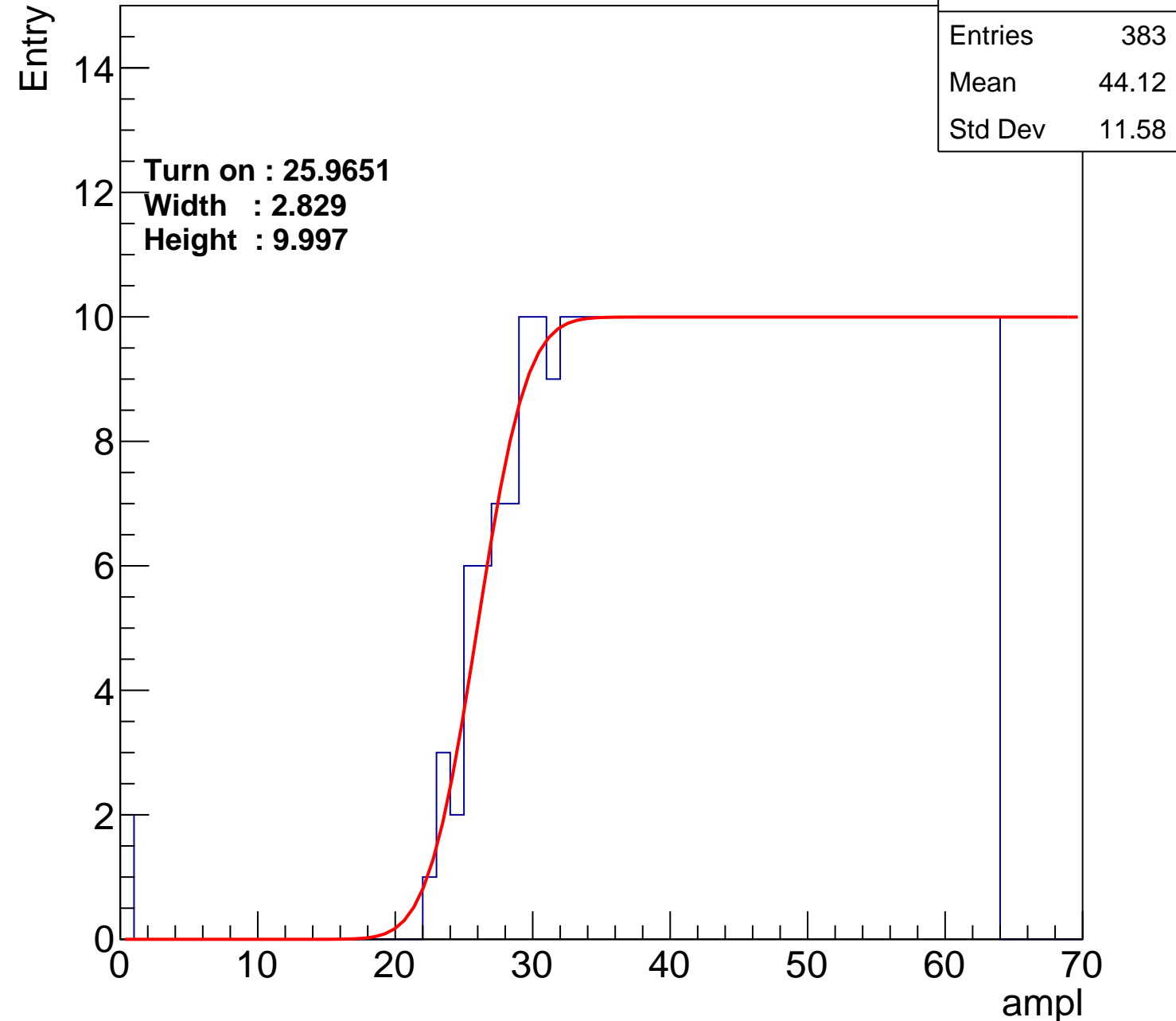
Width : 2.829

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.7926

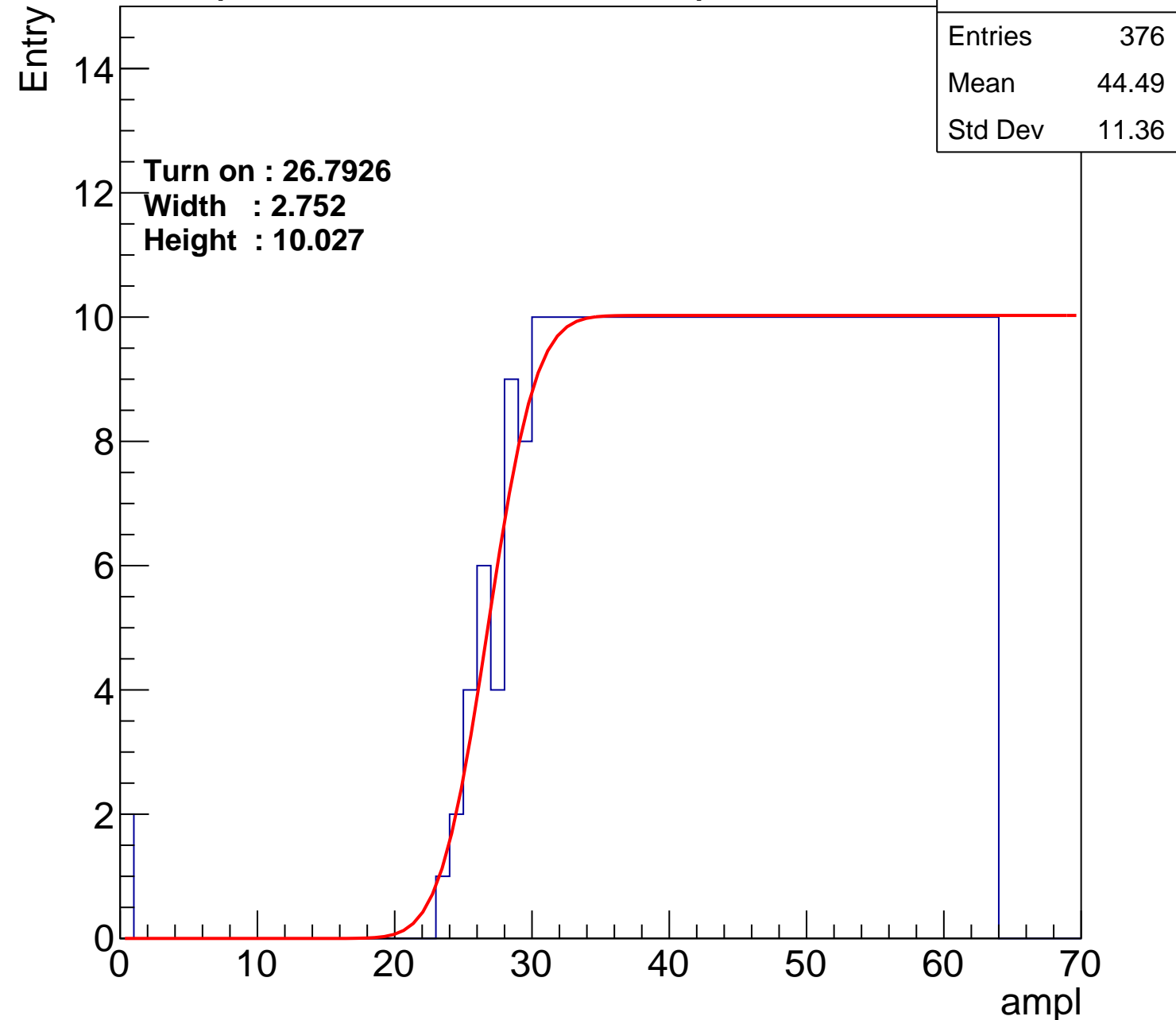
Width : 2.752

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 26.6277

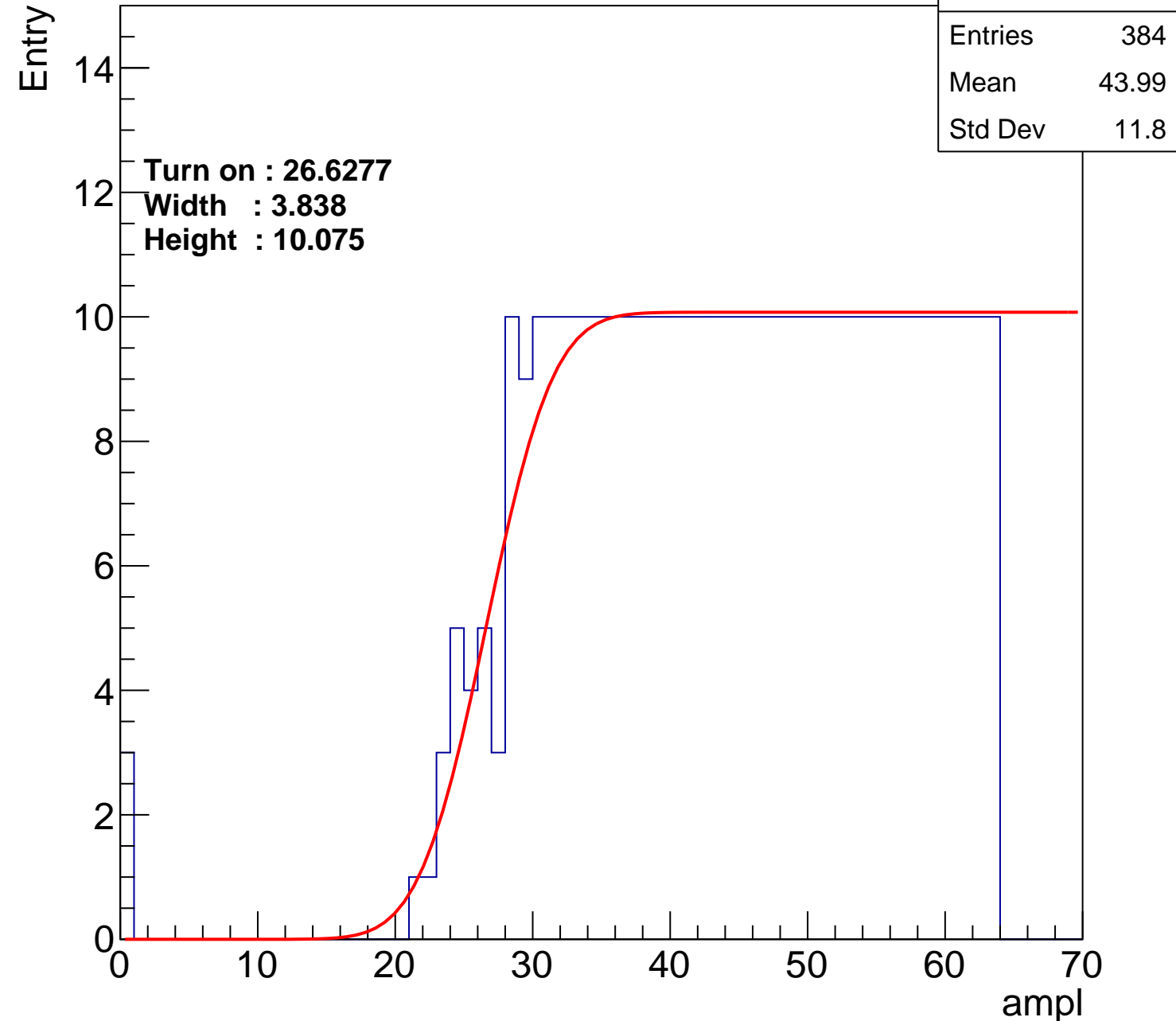
Width : 3.838

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.99
Std Dev	11.93

Turn on : 26.3378

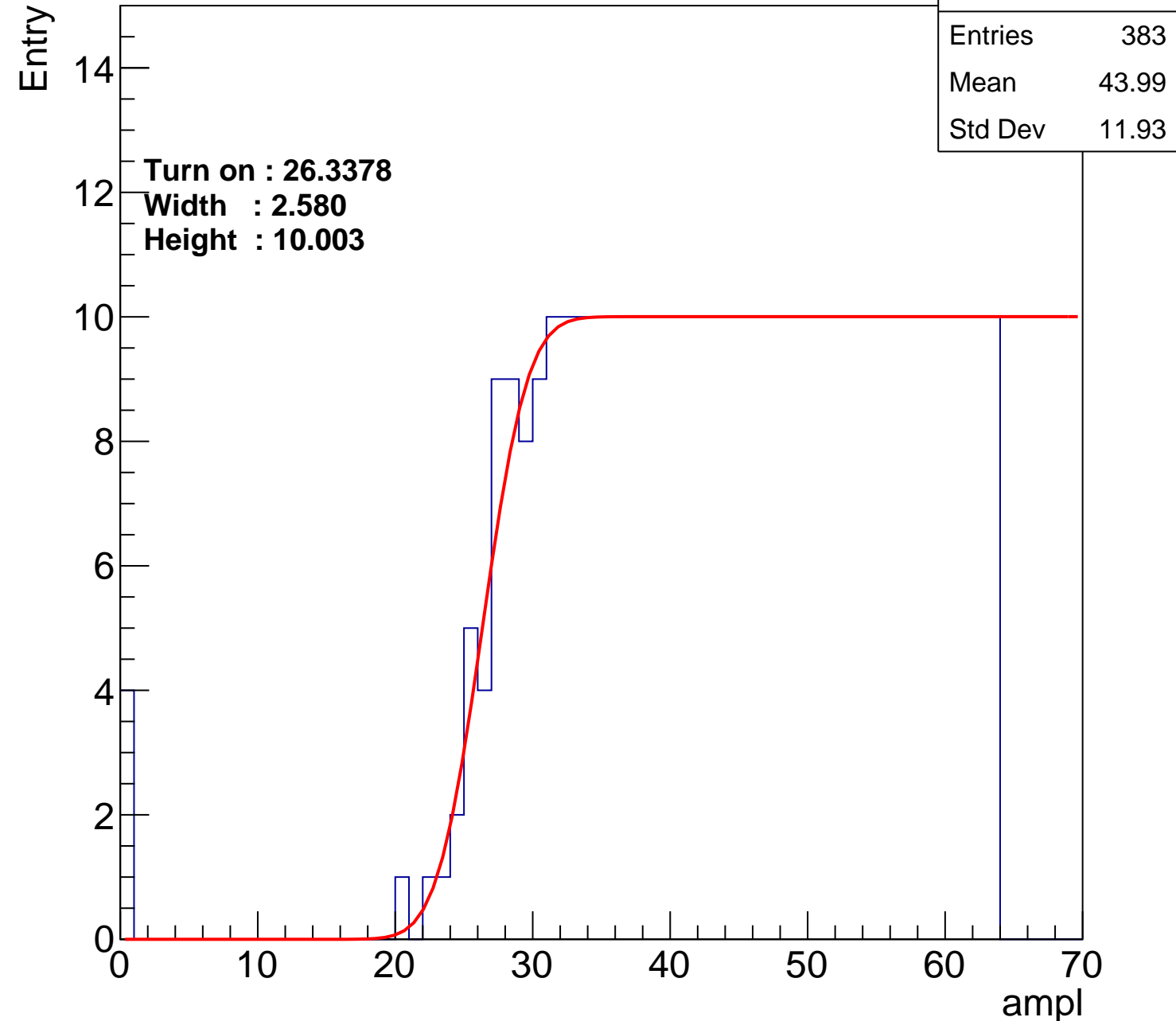
Width : 2.580

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.18
Std Dev	11.67

Turn on : 26.5475

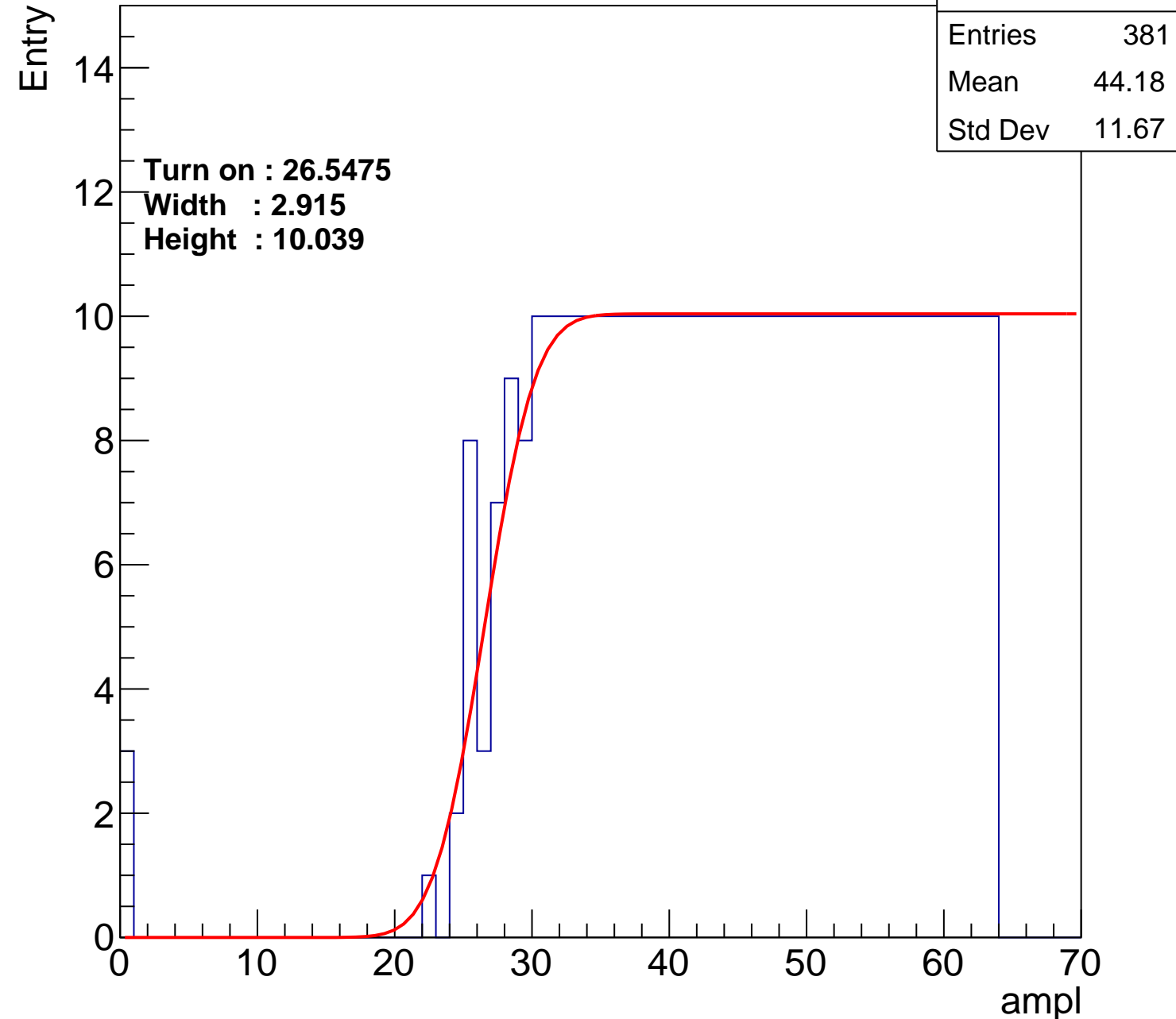
Width : 2.915

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.66
Std Dev	11.84

Turn on : 25.5154

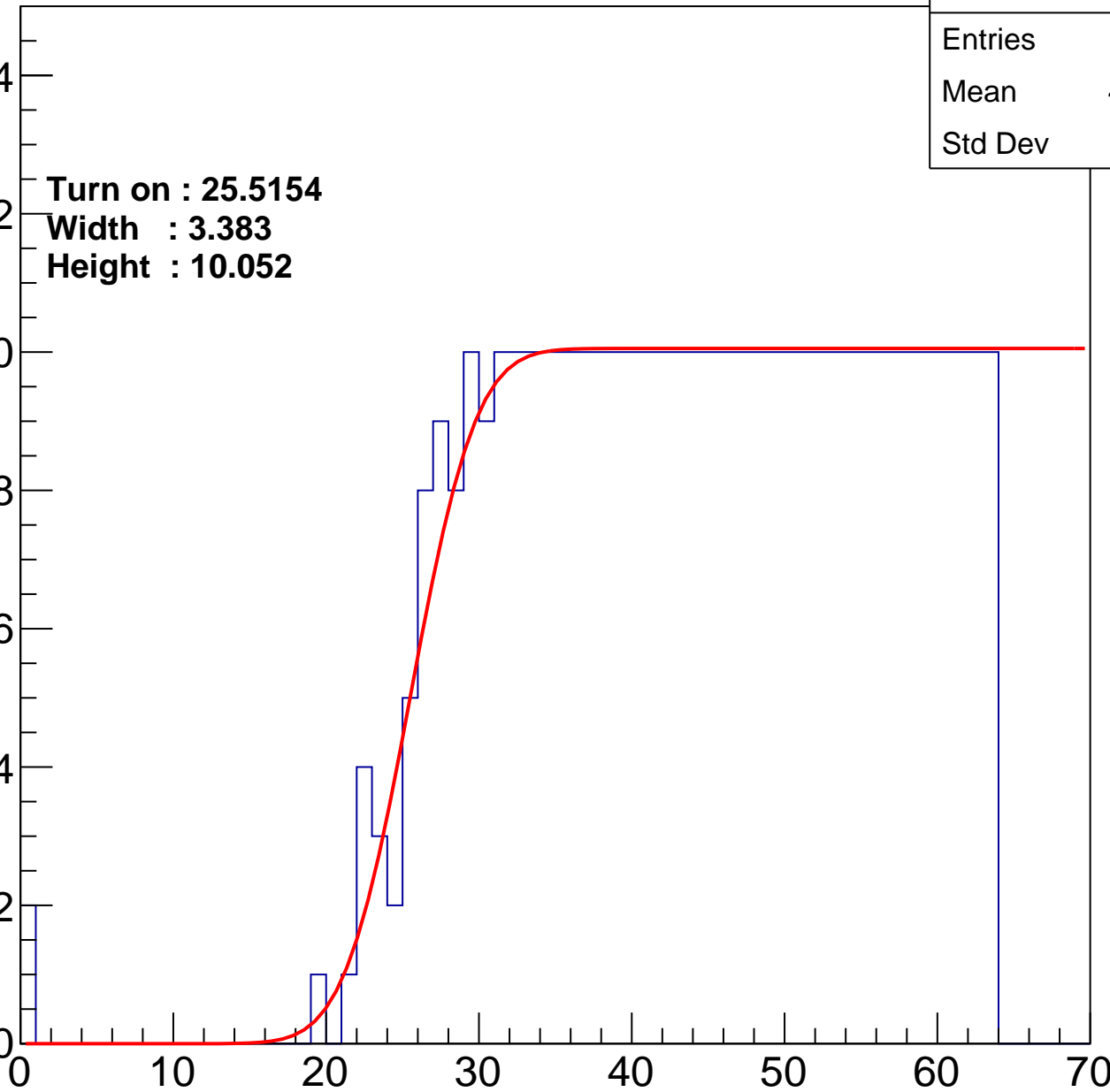
Width : 3.383

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 26.4586

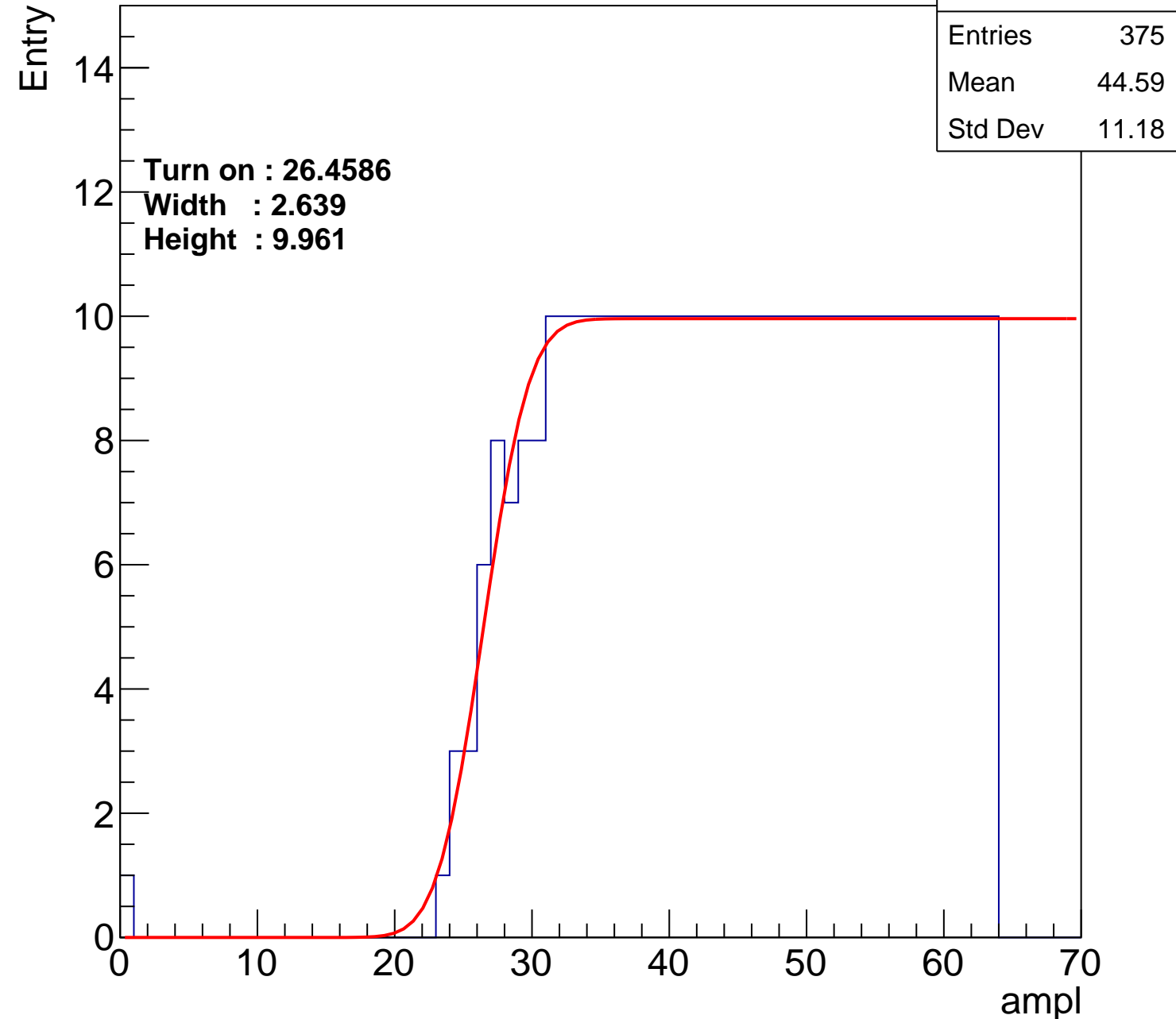
Width : 2.639

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.2313

Width : 2.478

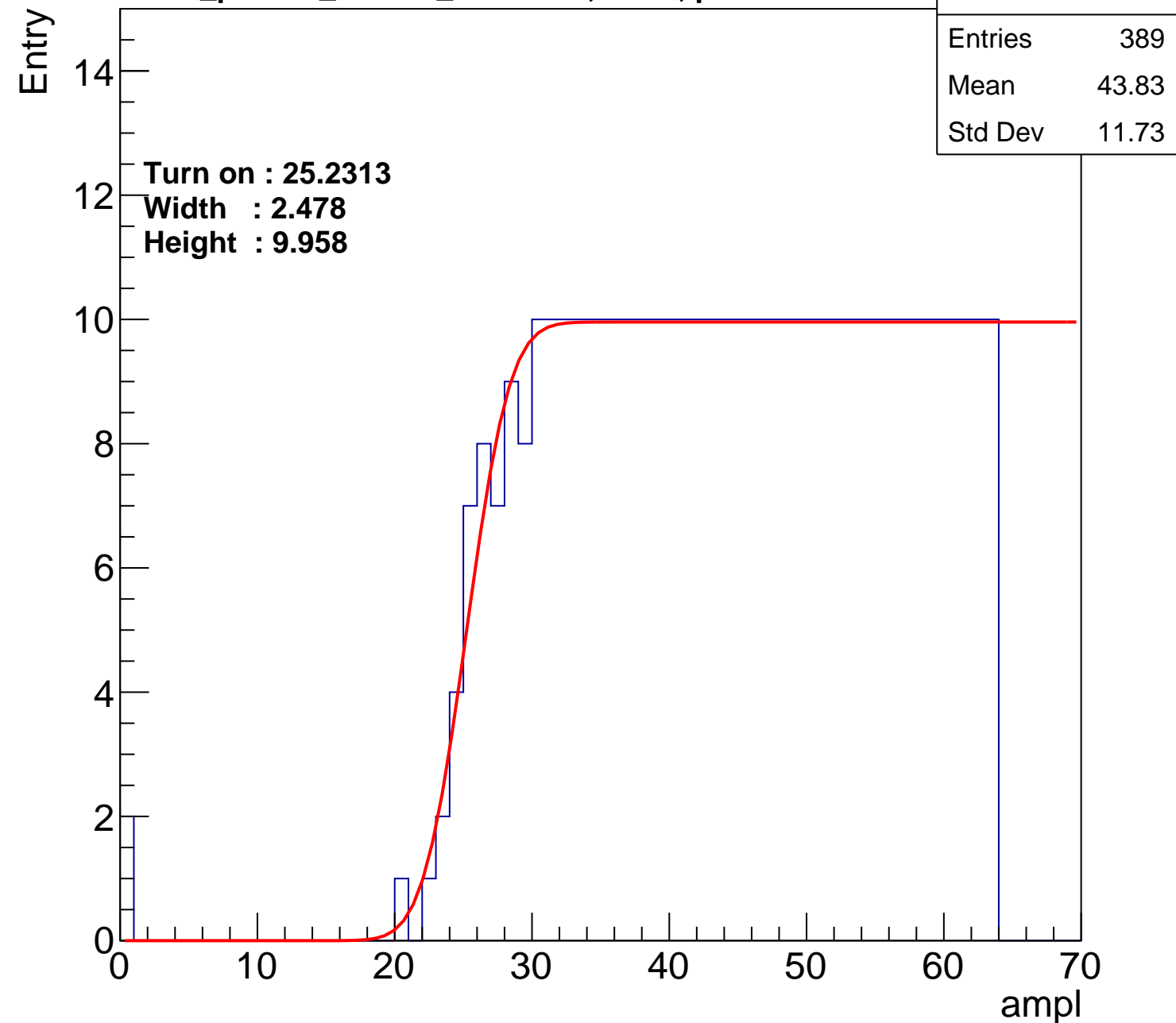
Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.4199

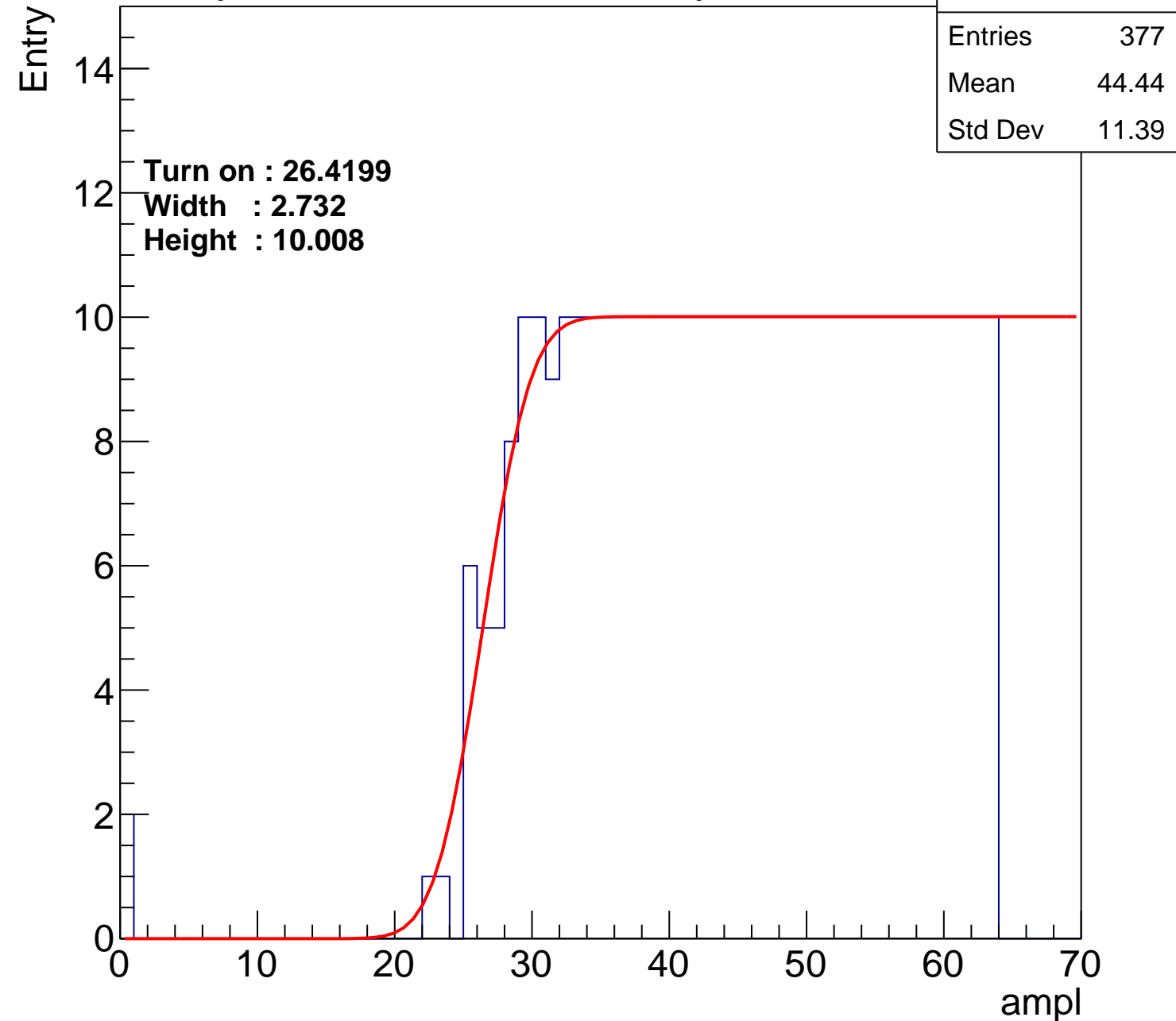
Width : 2.732

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.32
Std Dev	11.29

Turn on : 26.0455

Width : 1.508

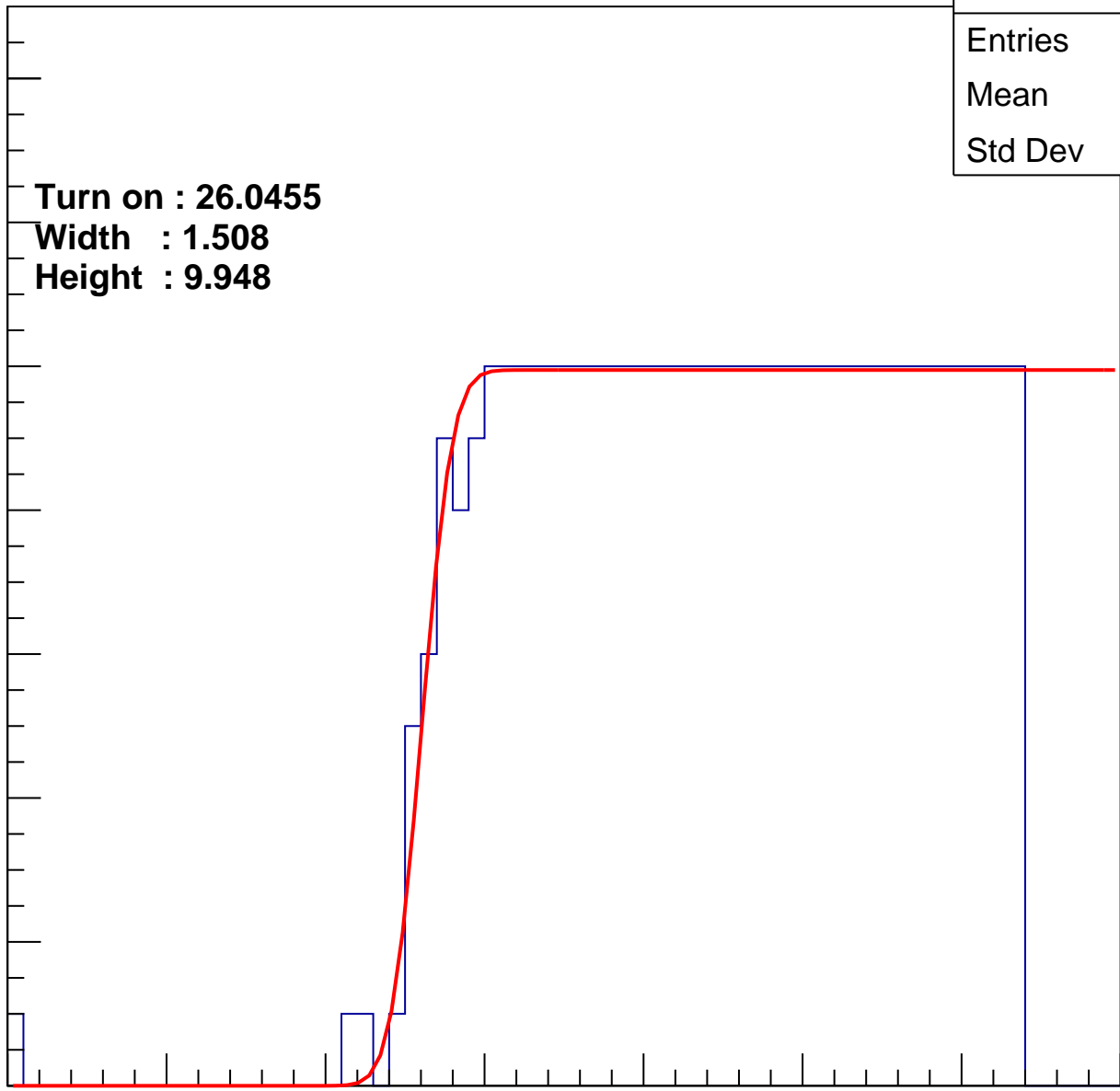
Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U21-ch121

calib_packv5_042523_0143.root, FC#3, port B3

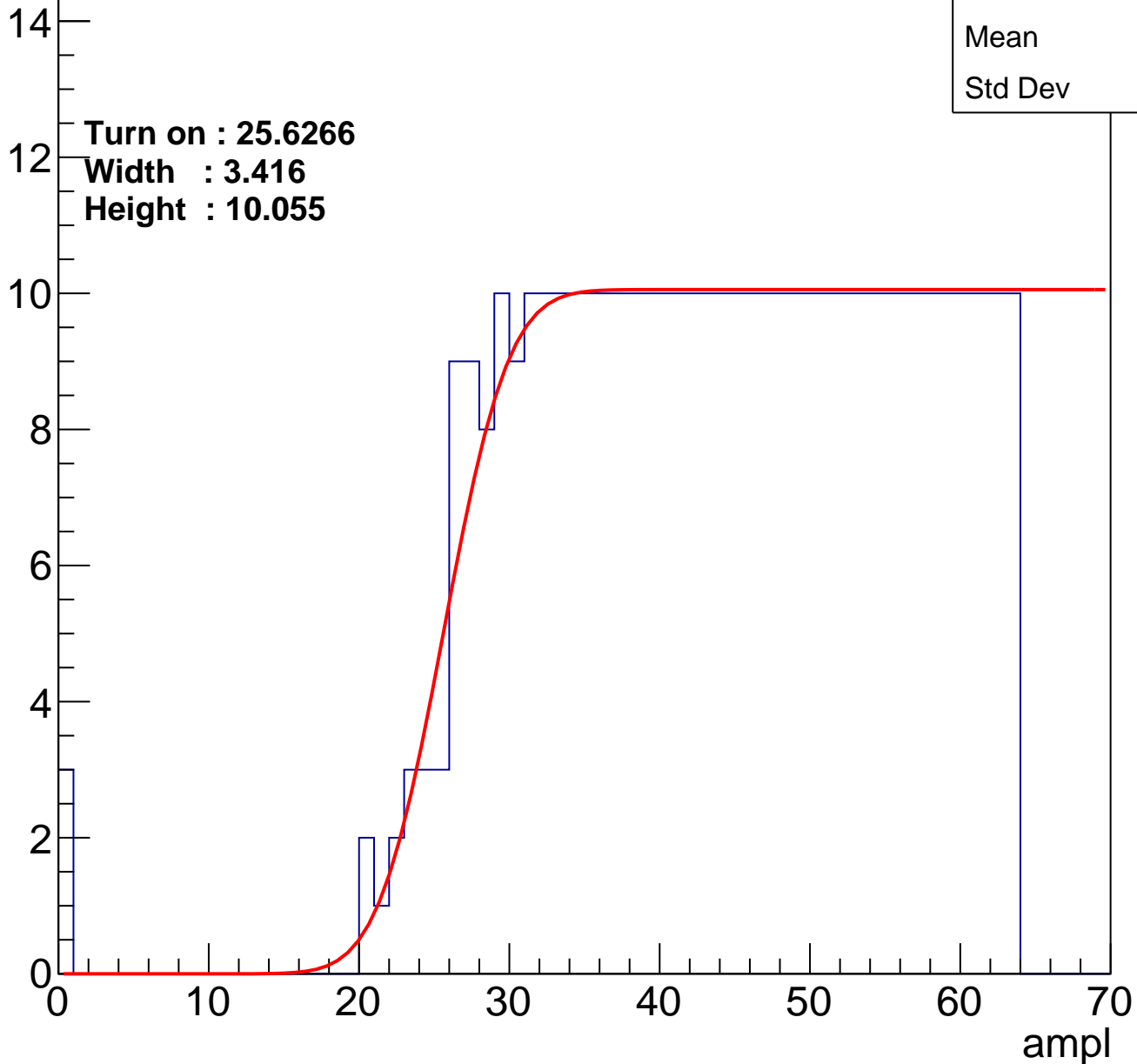
Entries	392
Mean	43.6
Std Dev	12

Turn on : 25.6266

Width : 3.416

Height : 10.055

Entry



B1L000S, U21-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.4776

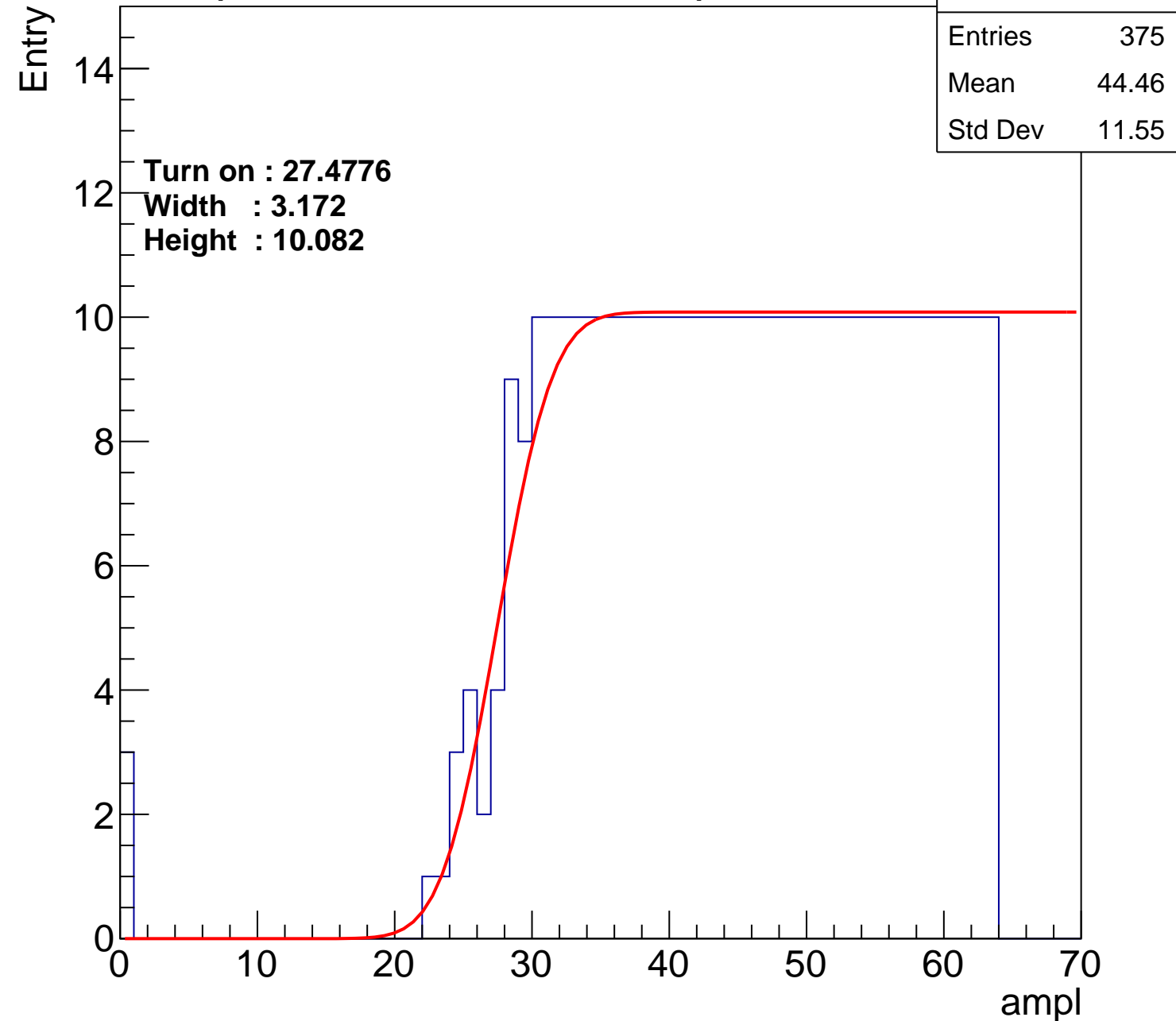
Width : 3.172

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 26.8747

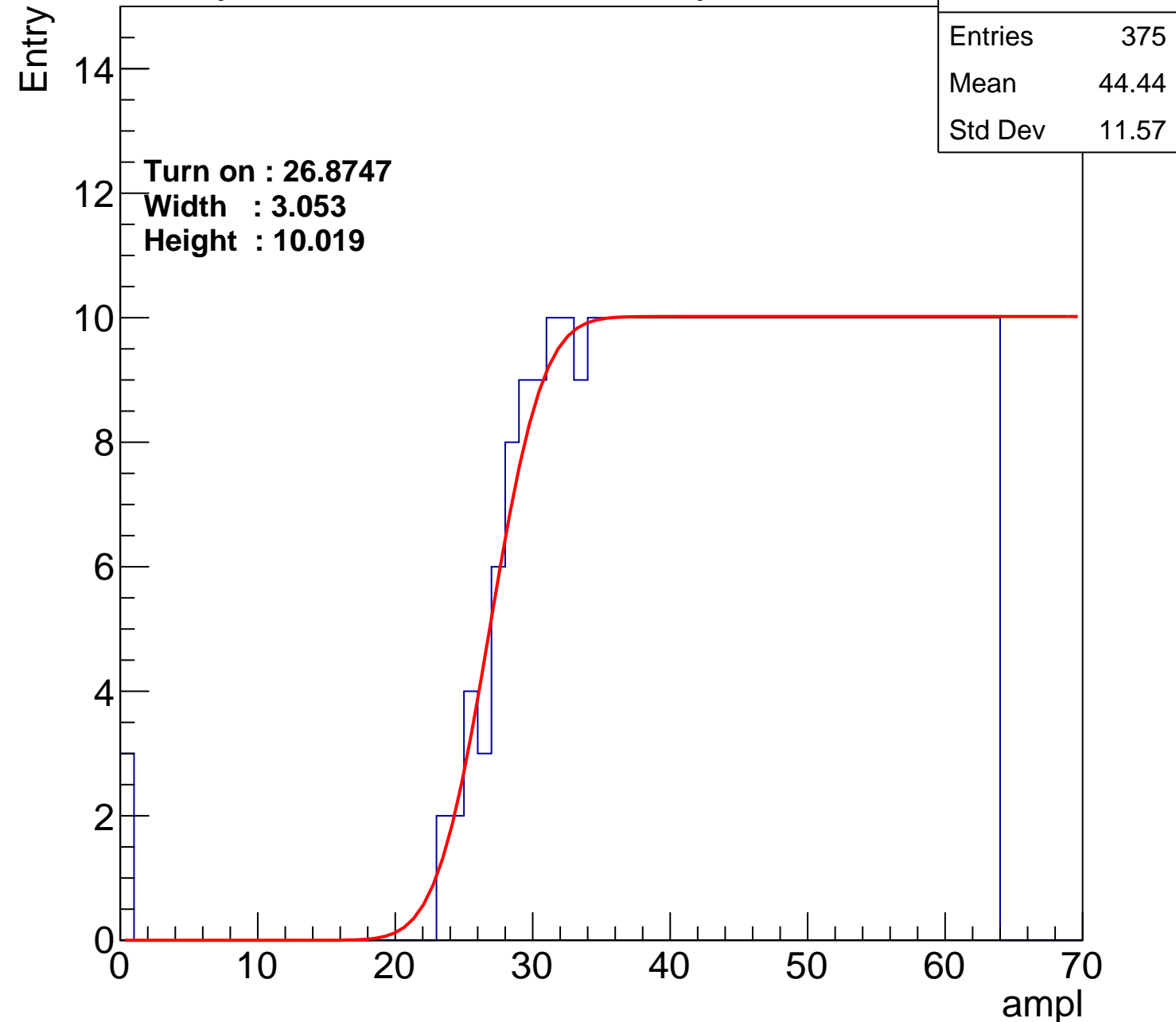
Width : 3.053

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch124

calib_packv5_042523_0143.root, FC#3, port B3

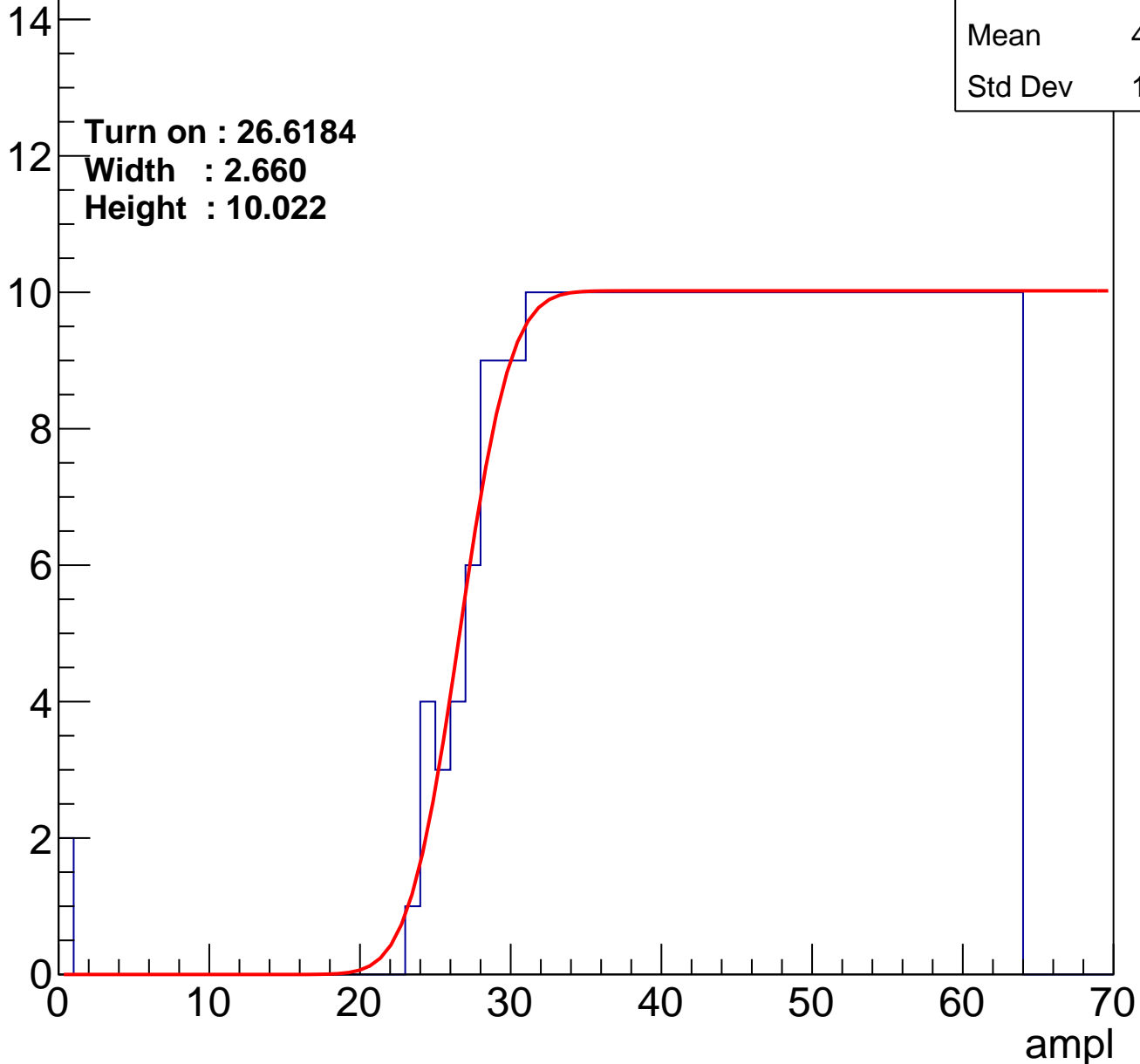
Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.6184

Width : 2.660

Height : 10.022

Entry



B1L000S, U21-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.66
Std Dev	11.19

Turn on : 27.4993

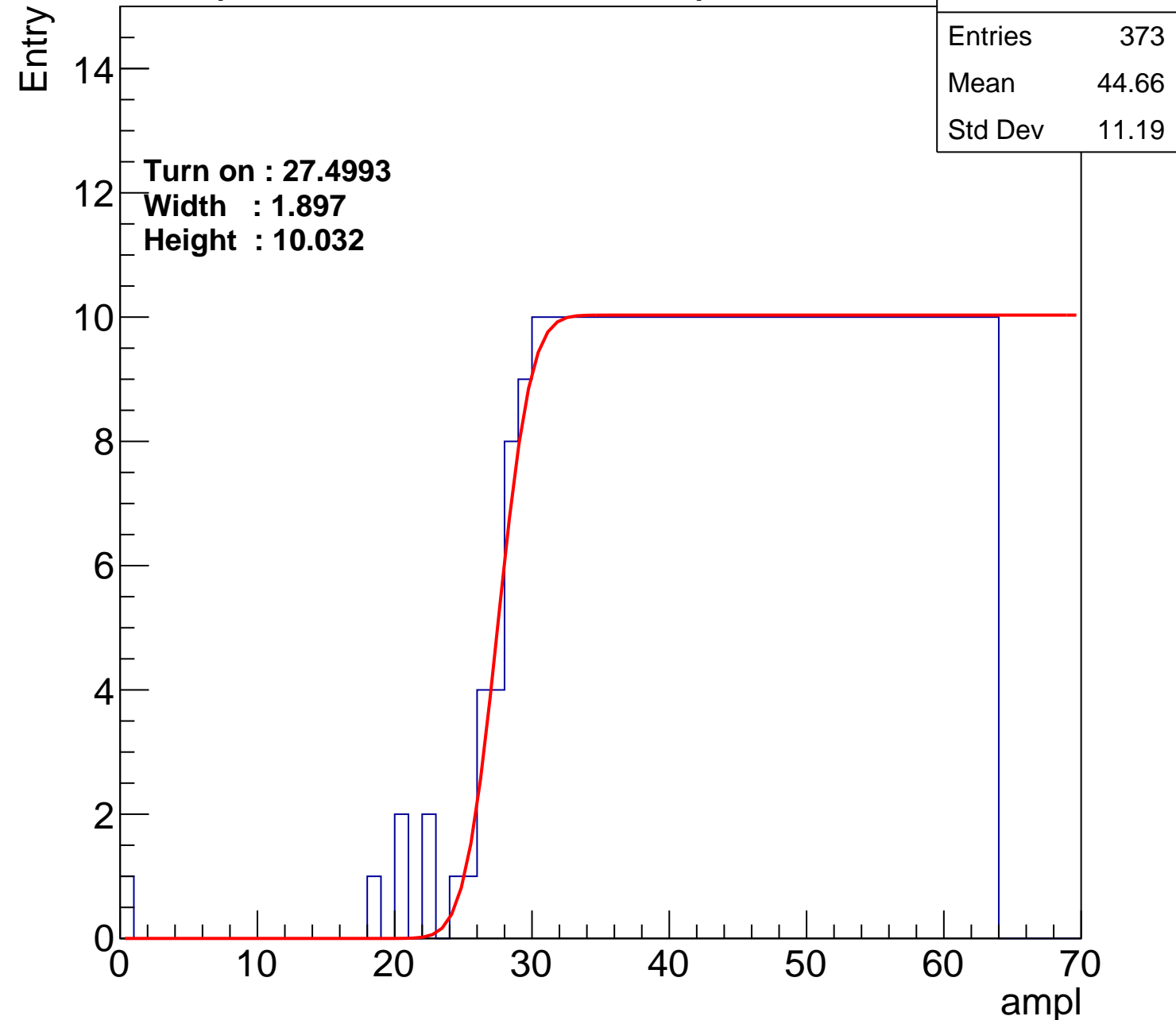
Width : 1.897

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.09
Std Dev	12.34

Turn on : 24.8083

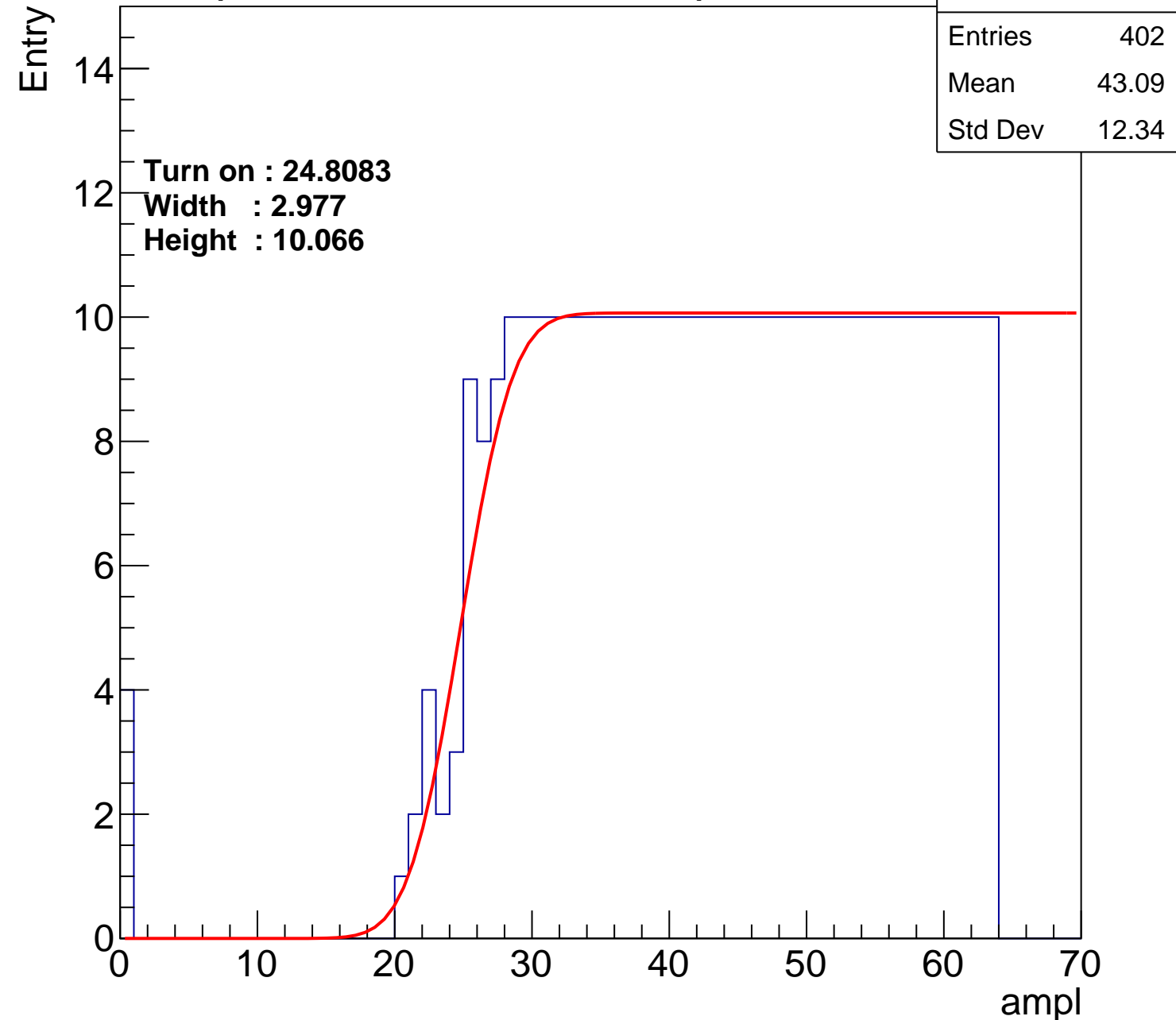
Width : 2.977

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 24.9218

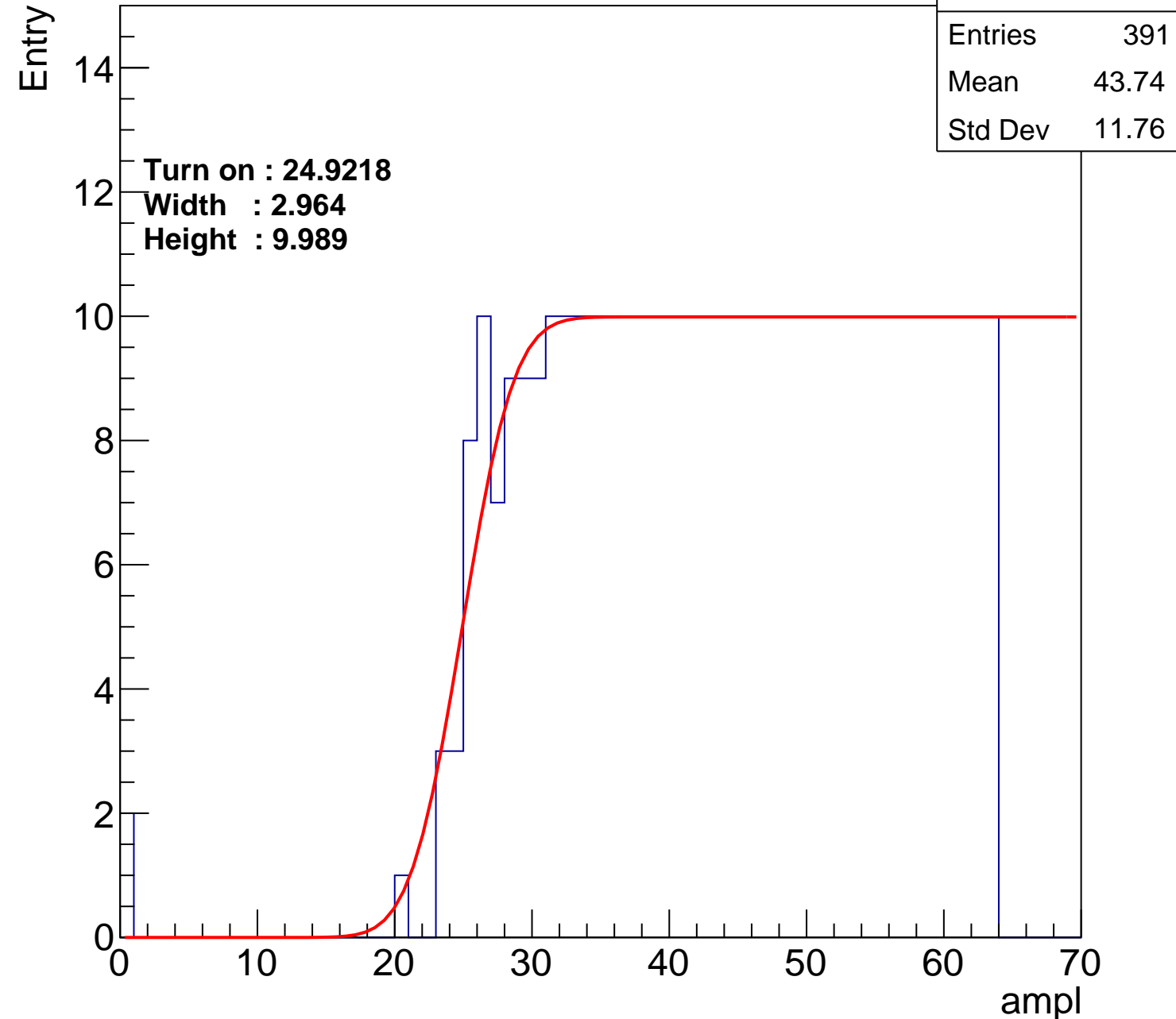
Width : 2.964

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U21-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 24.9218

Width : 2.964

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl

