

B1L100S, U19-ch0, adc0

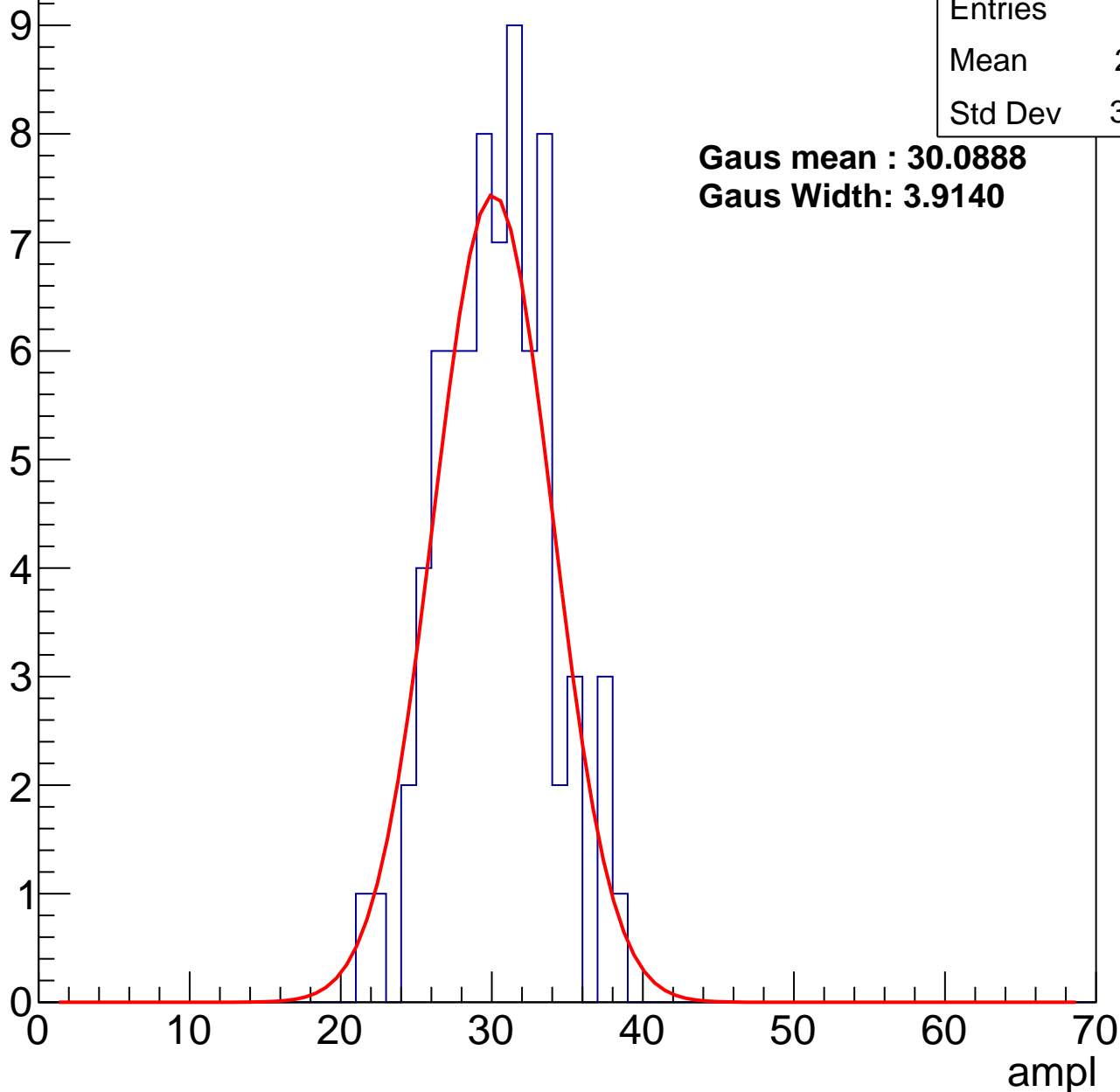
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.81
Std Dev	3.549

Gaus mean : 30.0888

Gaus Width: 3.9140



B1L100S, U19-ch0, adc1

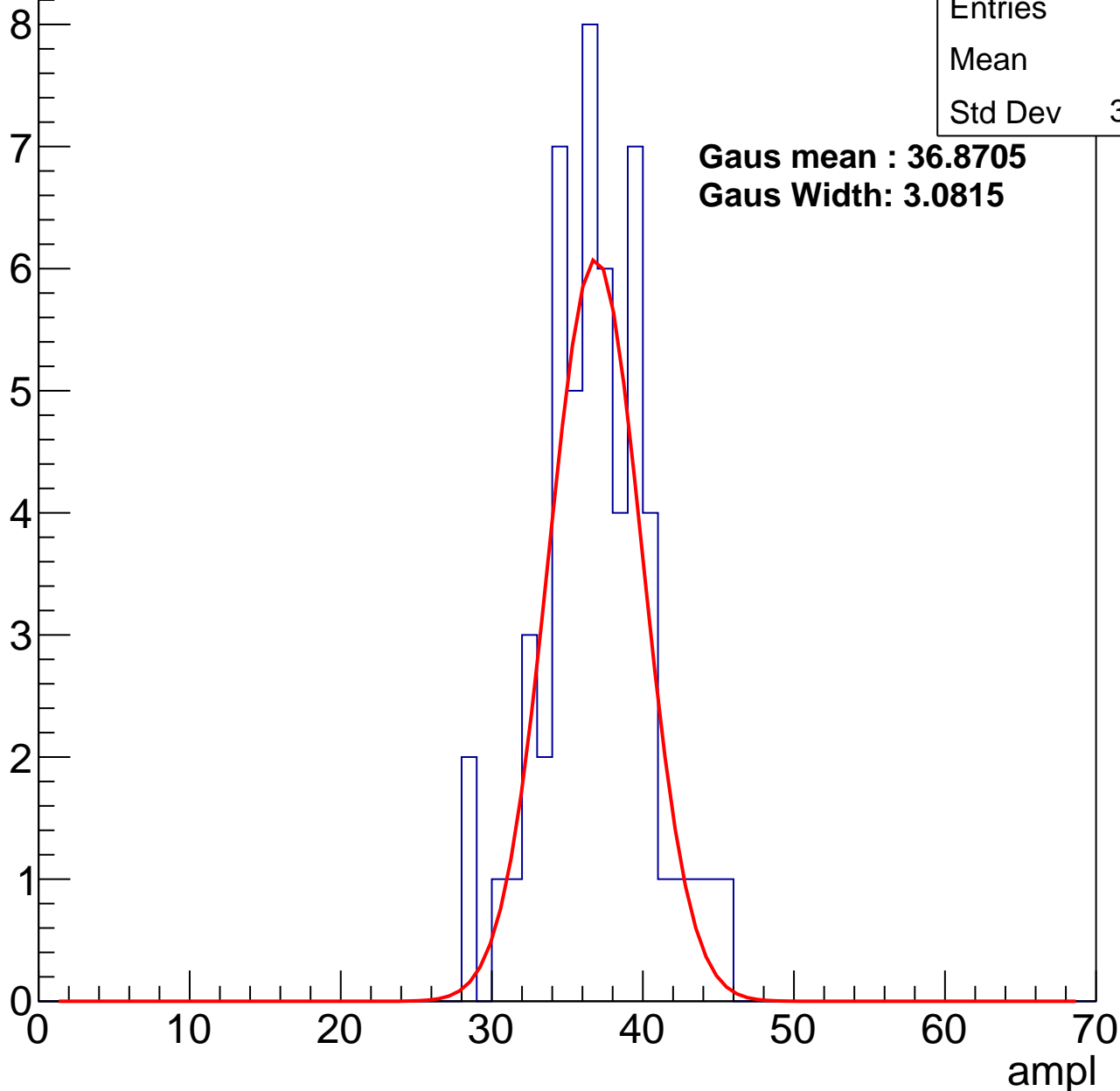
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	36.4
Std Dev	3.524

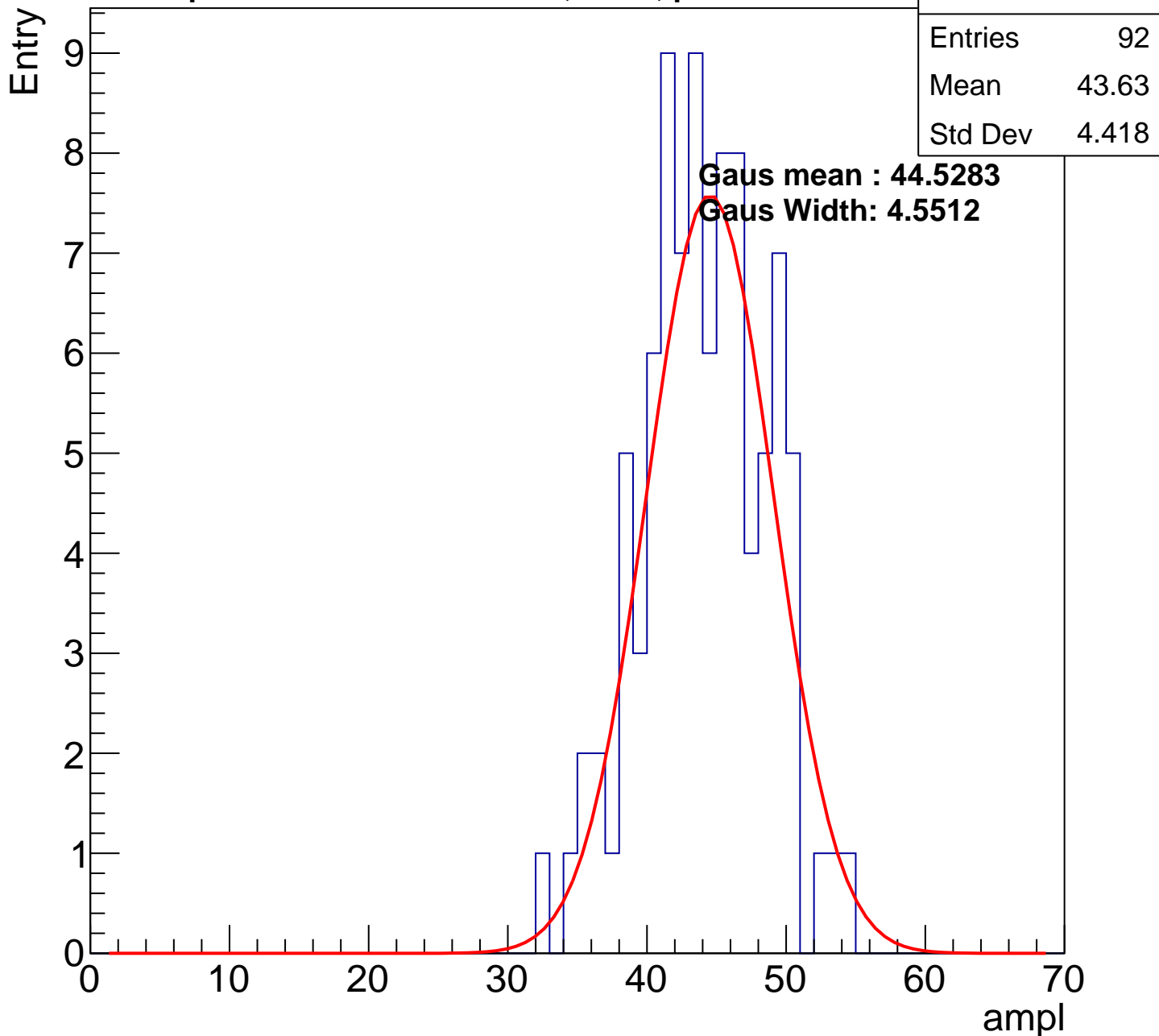
Gaus mean : 36.8705

Gaus Width: 3.0815



B1L100S, U19-ch0, adc2

calib_packv5_042523_0143.root, FC#4, port A2

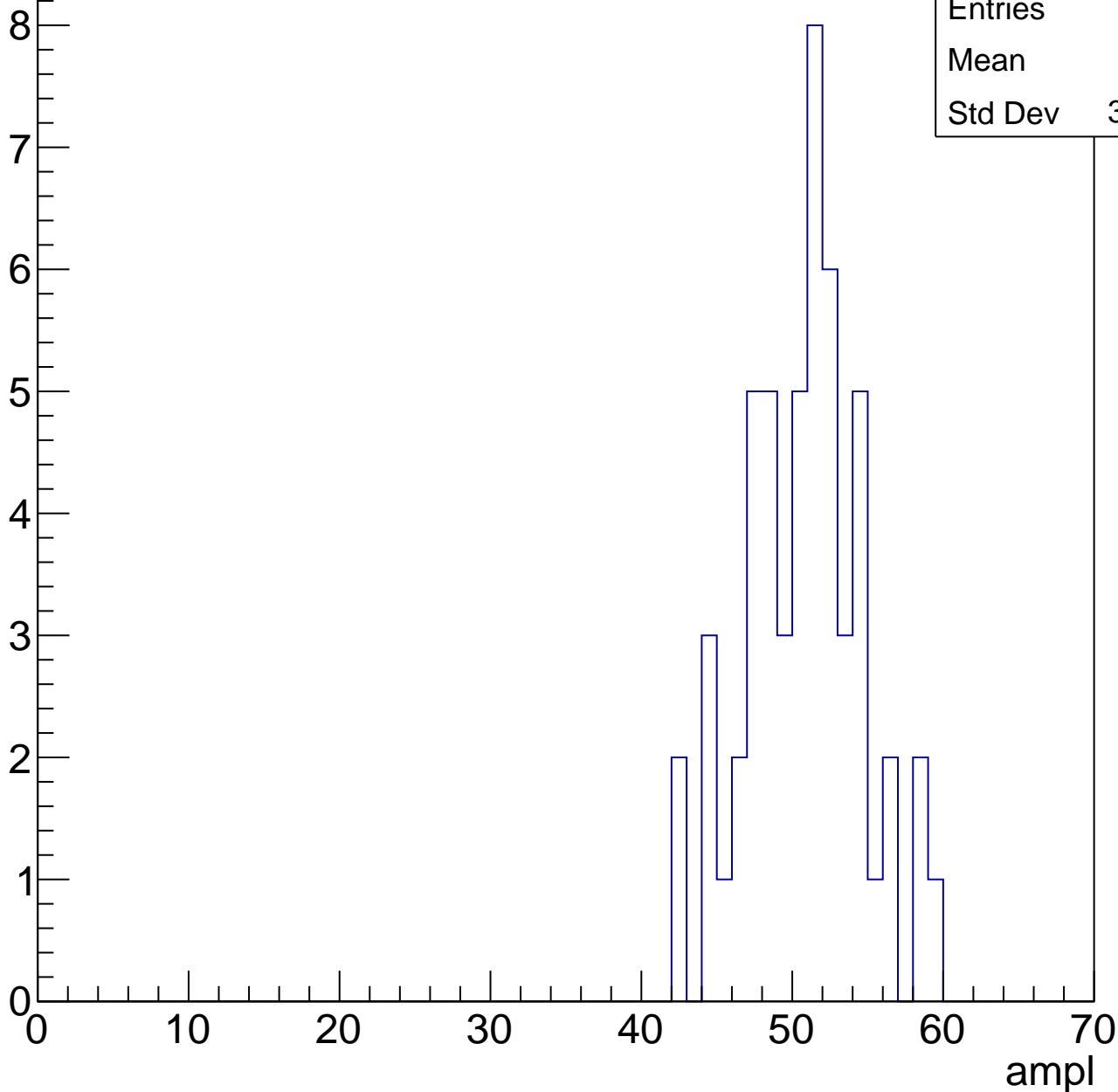


B1L100S, U19-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	50.3
Std Dev	3.852

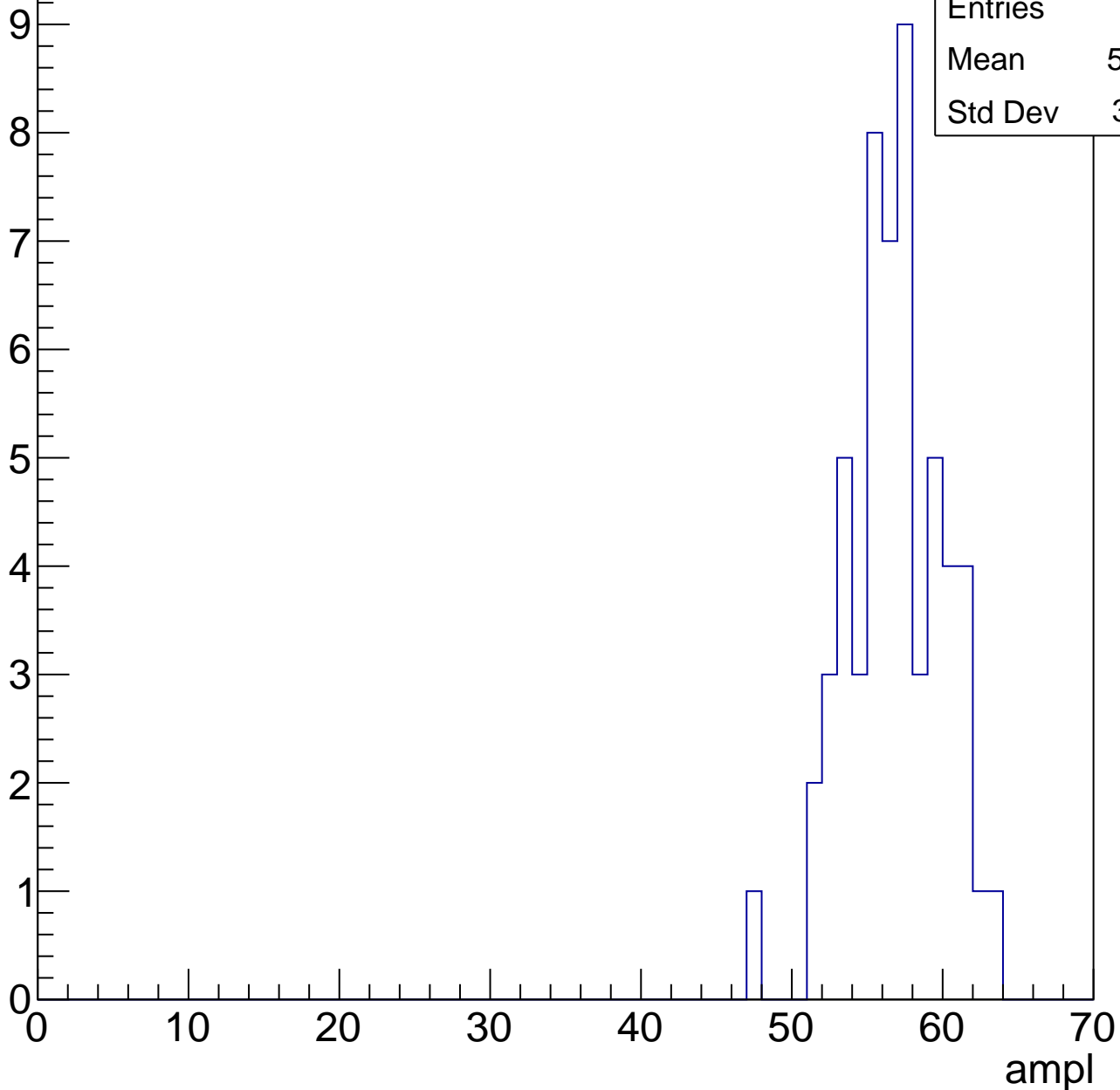


B1L100S, U19-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.34
Std Dev	3.141

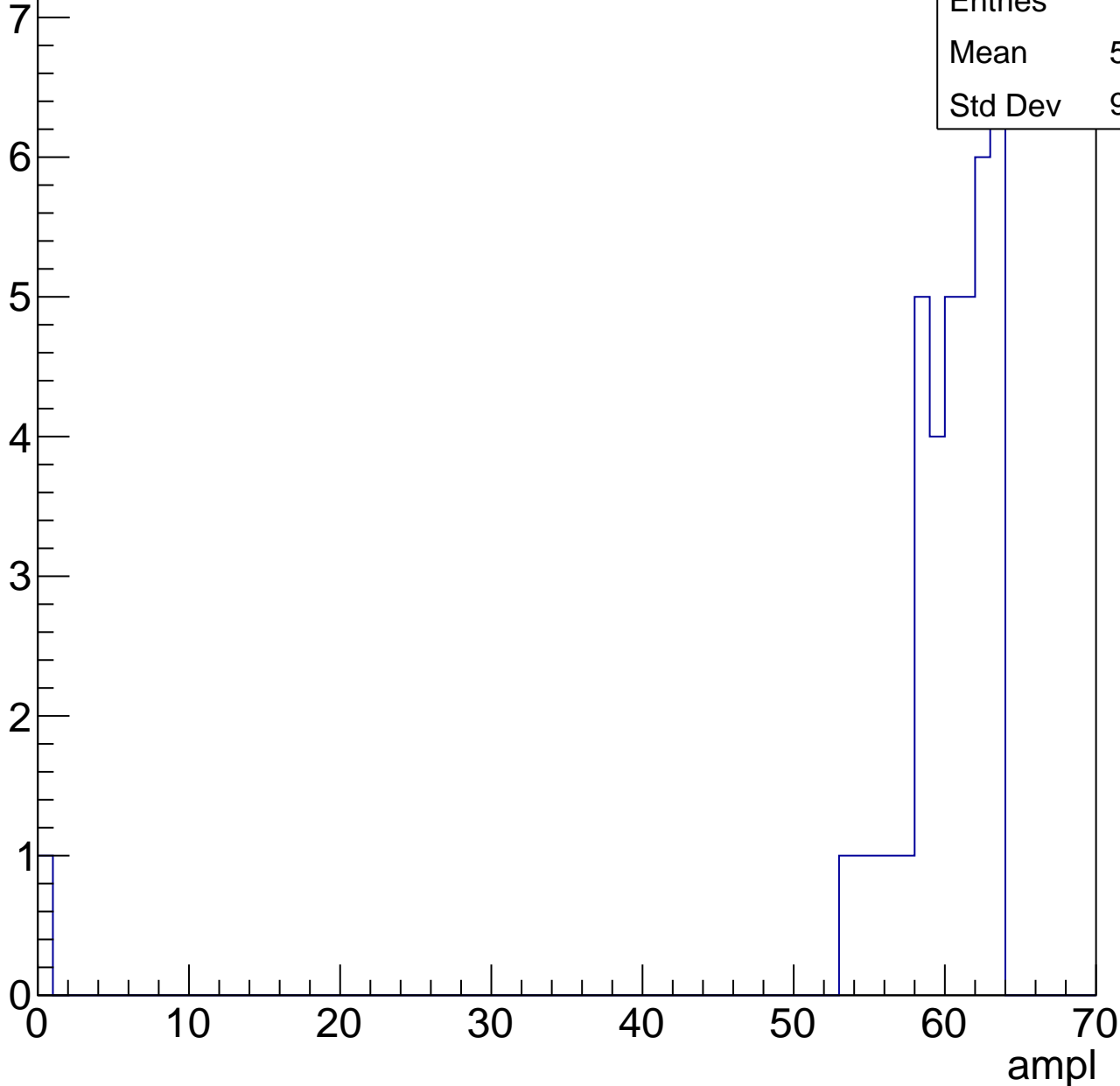


B1L100S, U19-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.39
Std Dev	9.938



B1L100S, U19-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

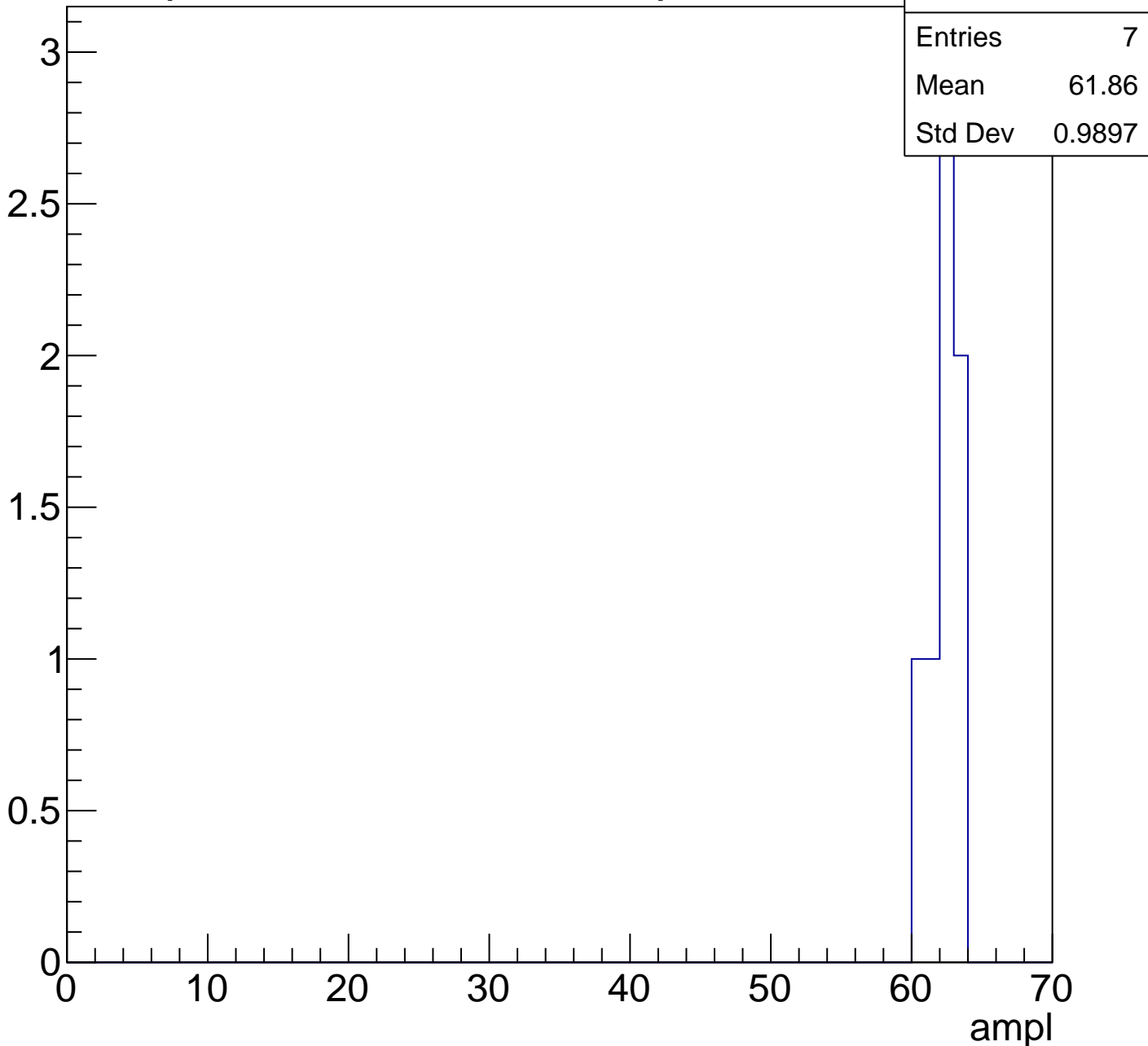
7

Mean

61.86

Std Dev

0.9897



B1L100S, U19-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch1, adc0

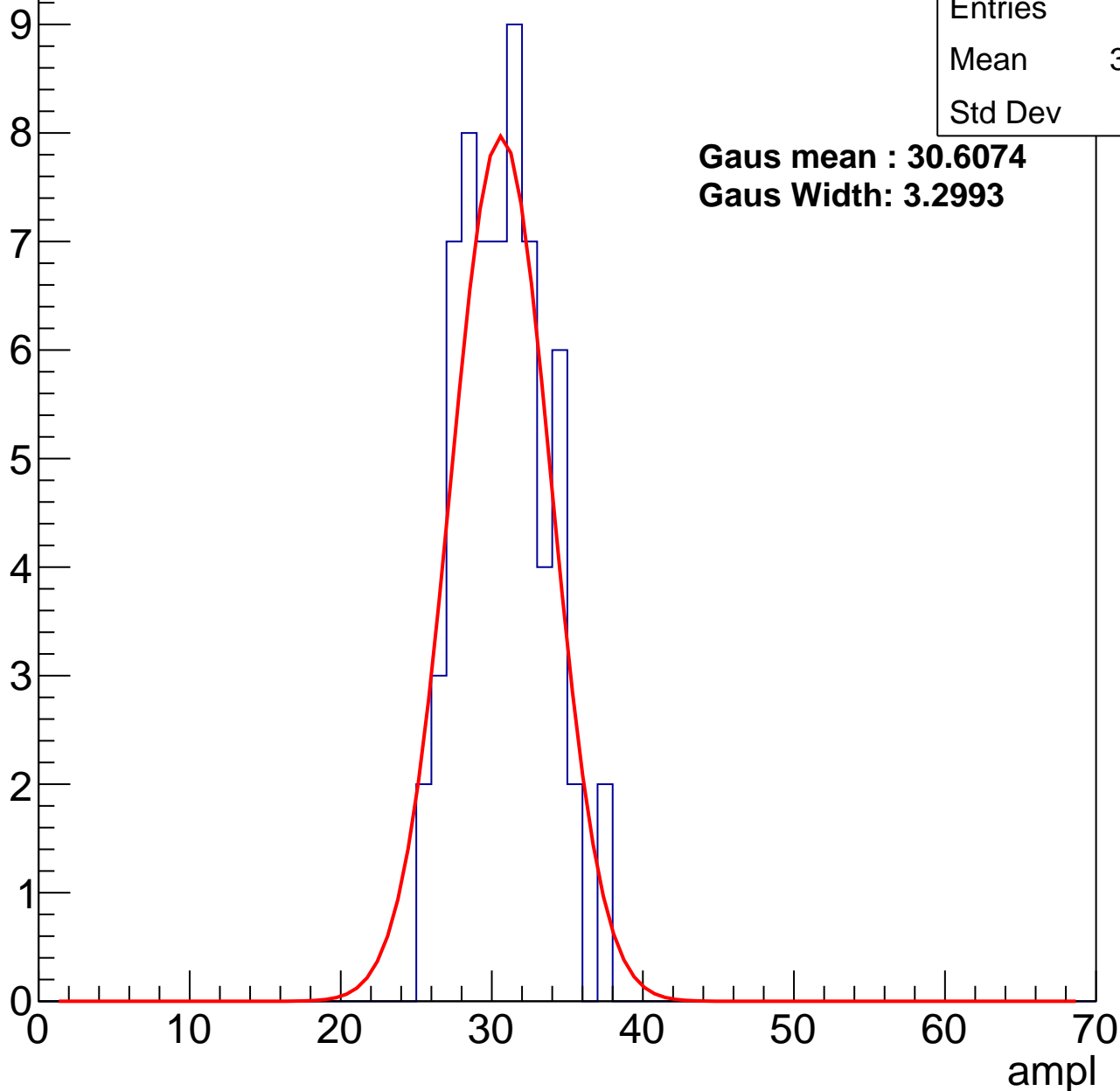
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.27
Std Dev	2.83

Gaus mean : 30.6074

Gaus Width: 3.2993



B1L100S, U19-ch1, adc1

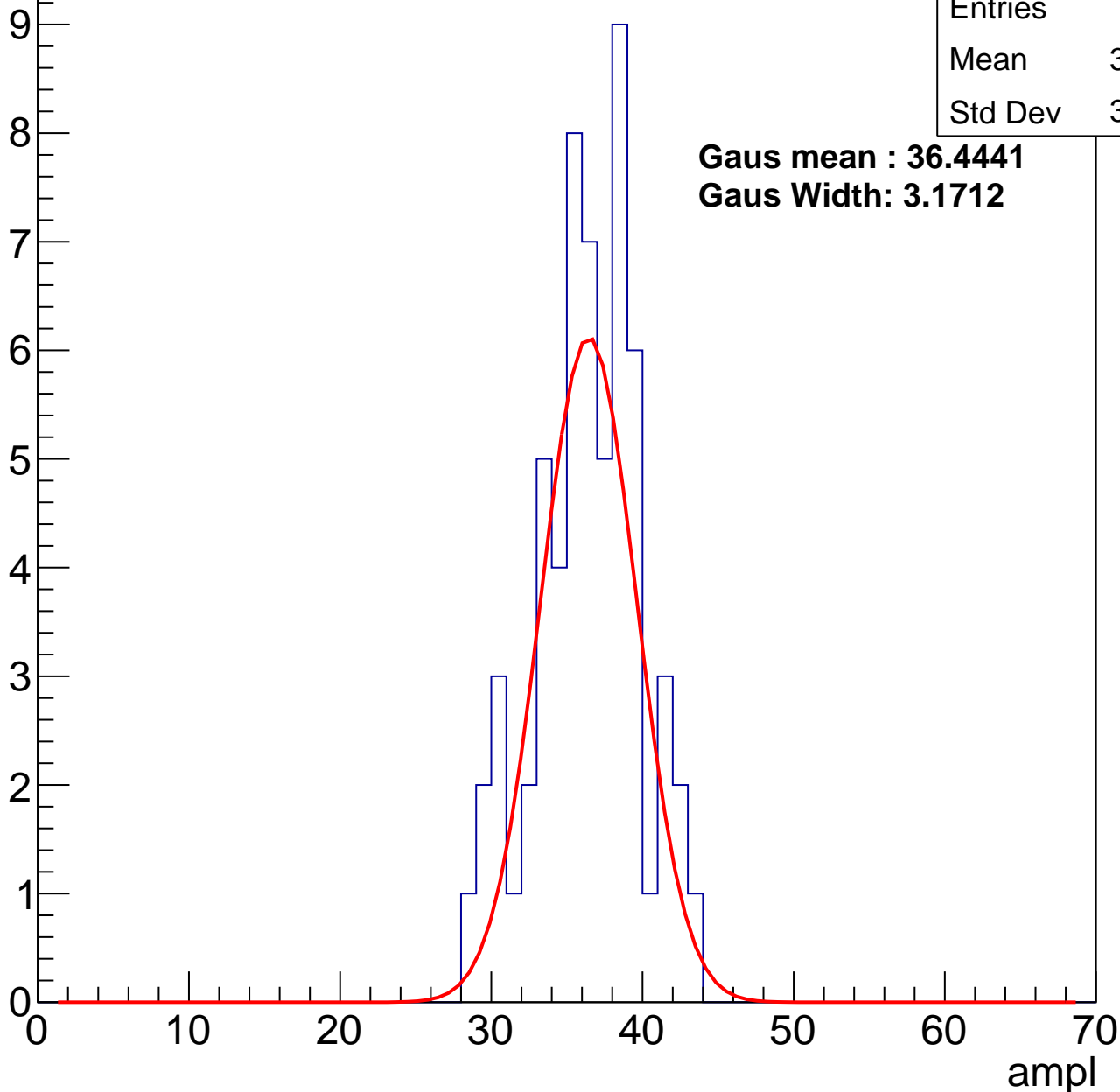
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	35.92
Std Dev	3.422

Gaus mean : 36.4441

Gaus Width: 3.1712



B1L100S, U19-ch1, adc2

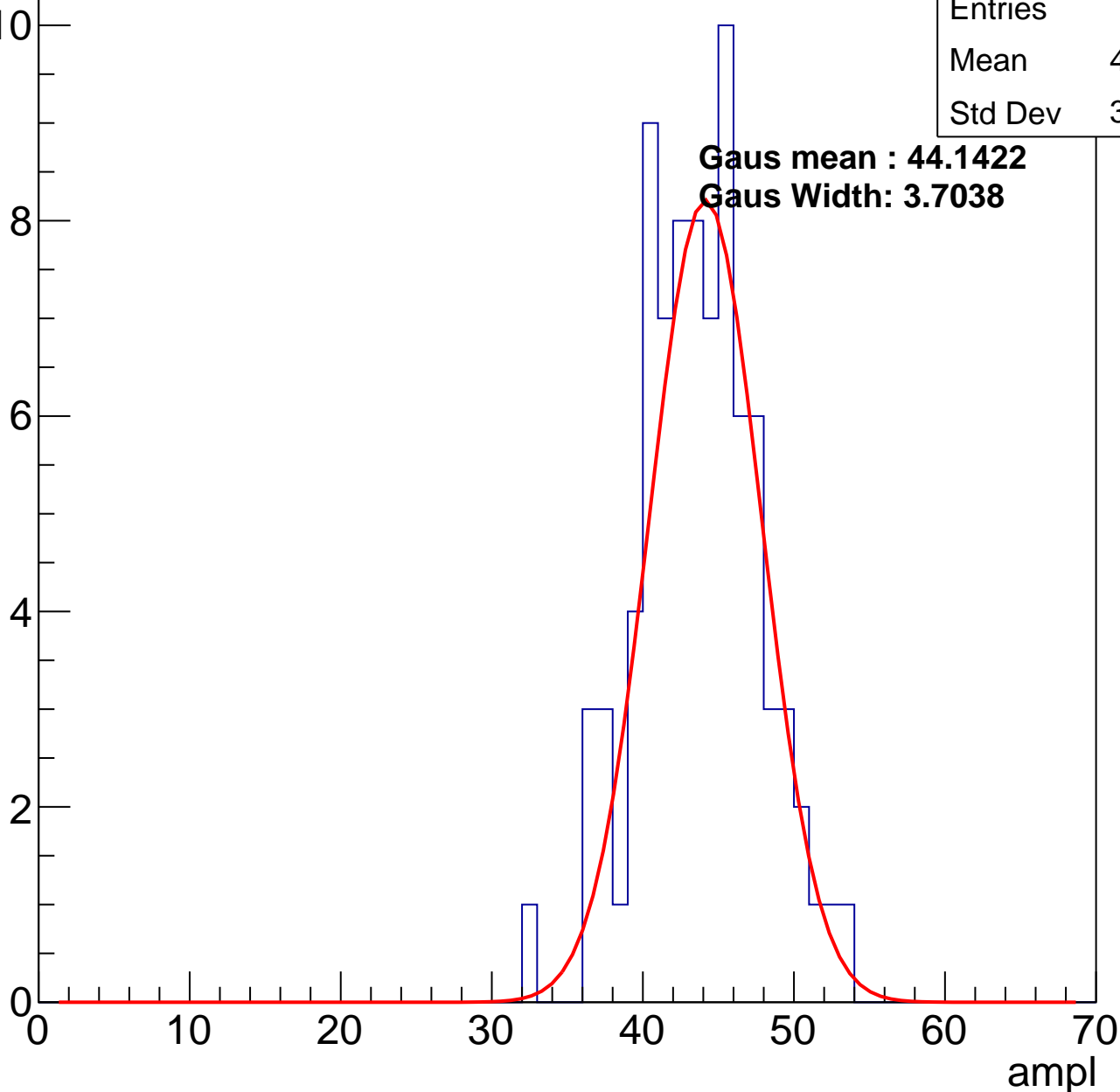
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	43.27
Std Dev	3.932

Gaus mean : 44.1422

Gaus Width: 3.7038

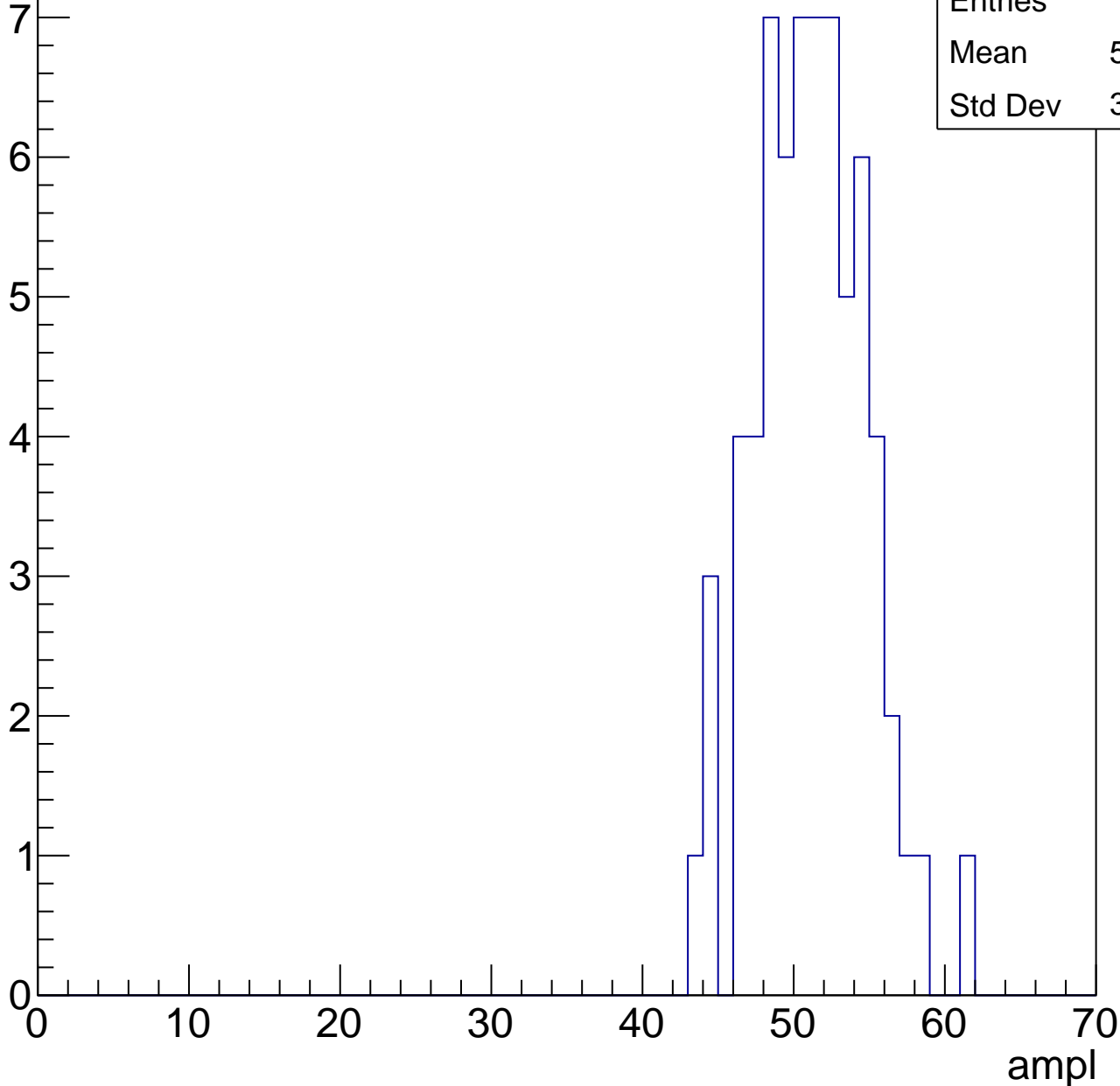


B1L100S, U19-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

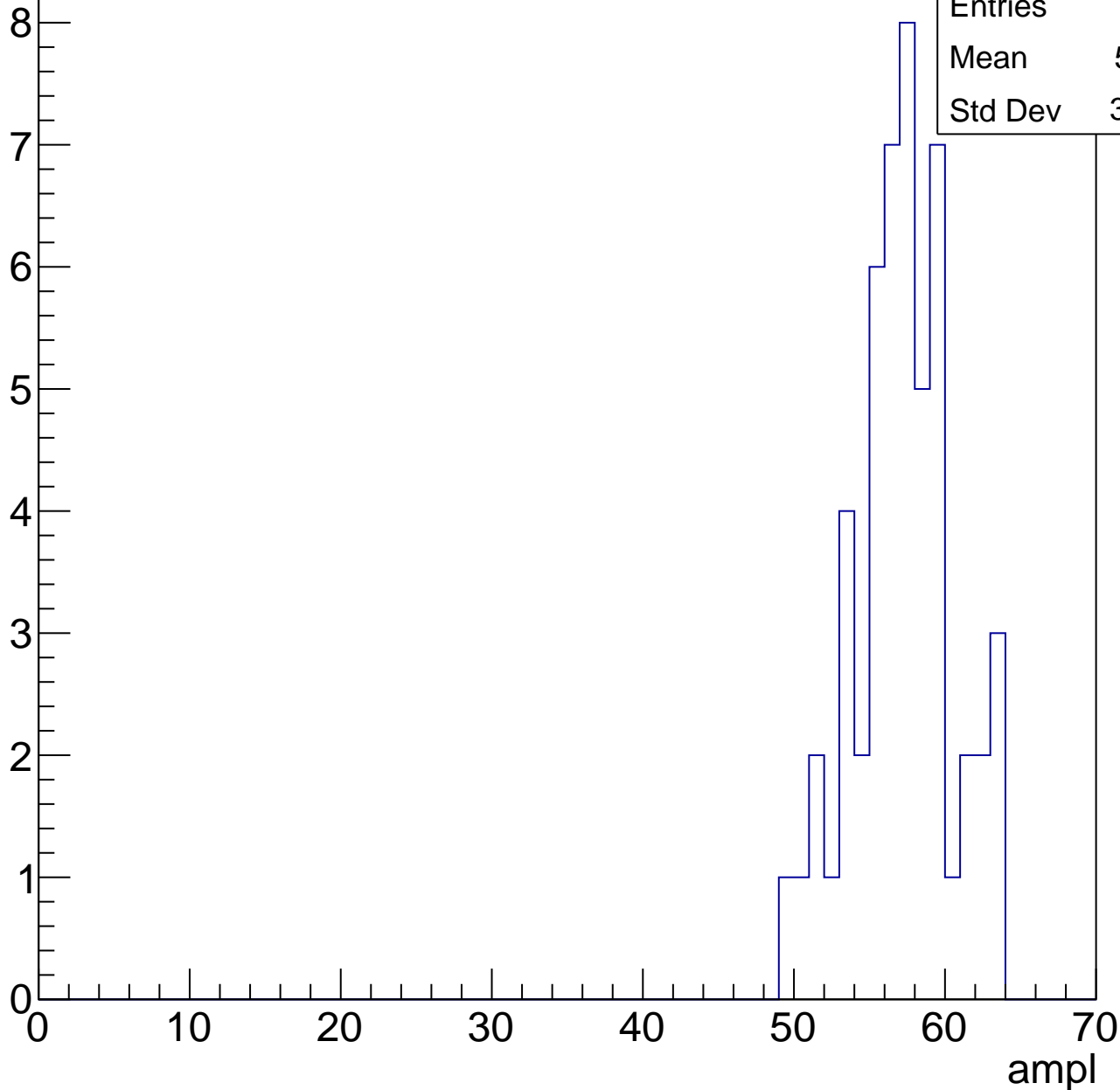
Entries	66
Mean	50.68
Std Dev	3.572



B1L100S, U19-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch1, adc5

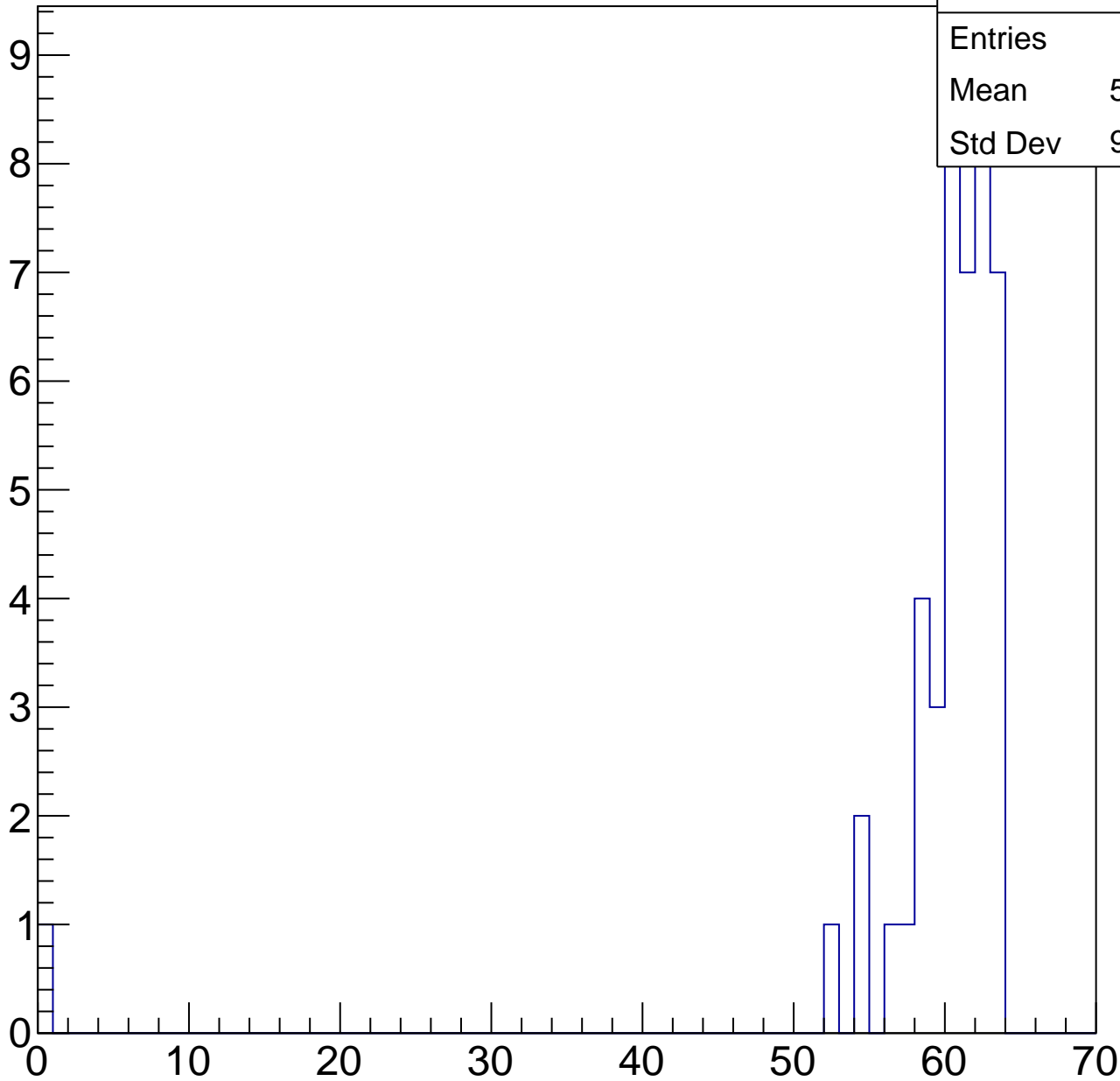
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.77
Std Dev	9.312

ampl



B1L100S, U19-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

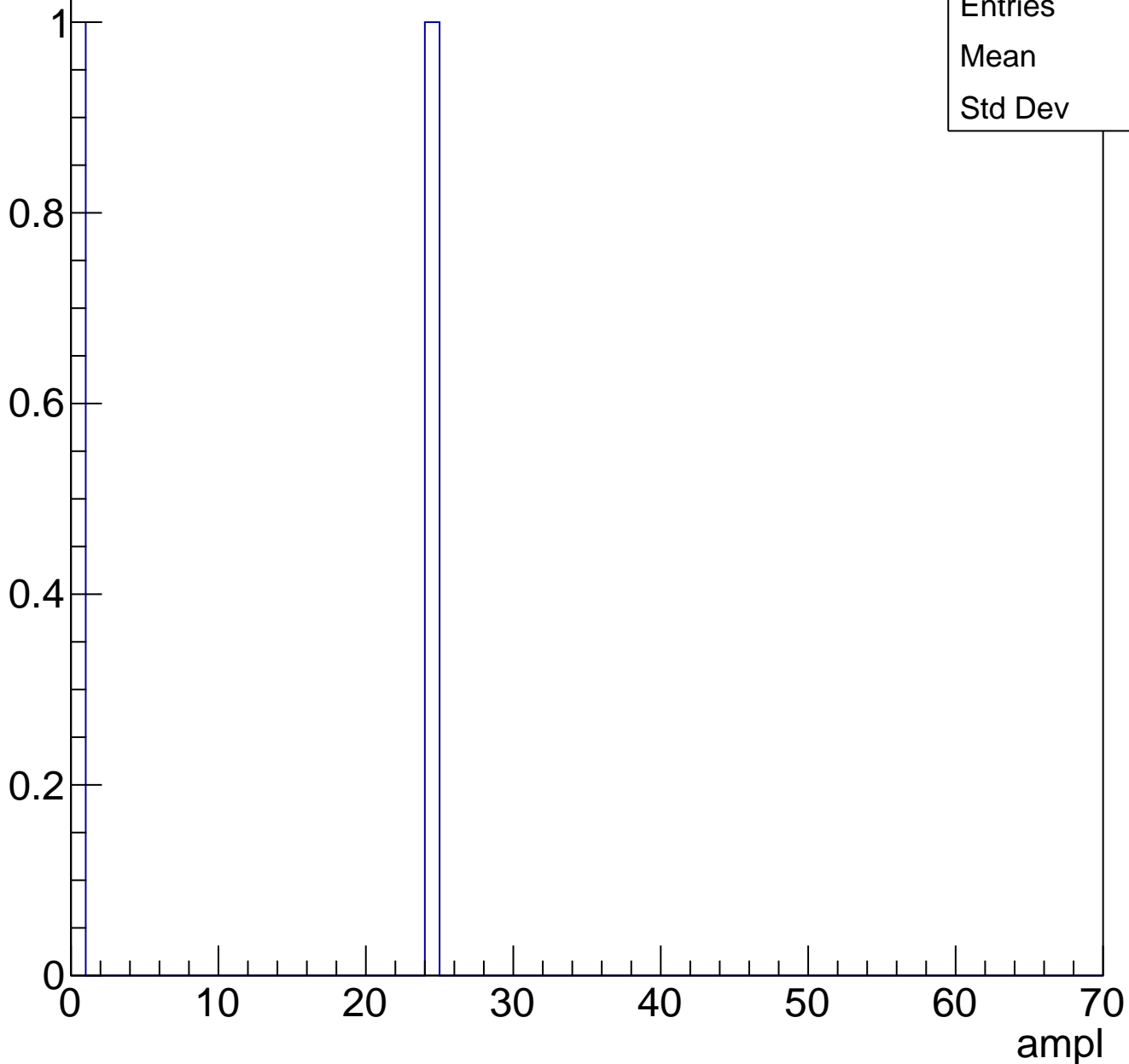


Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch2, adc0

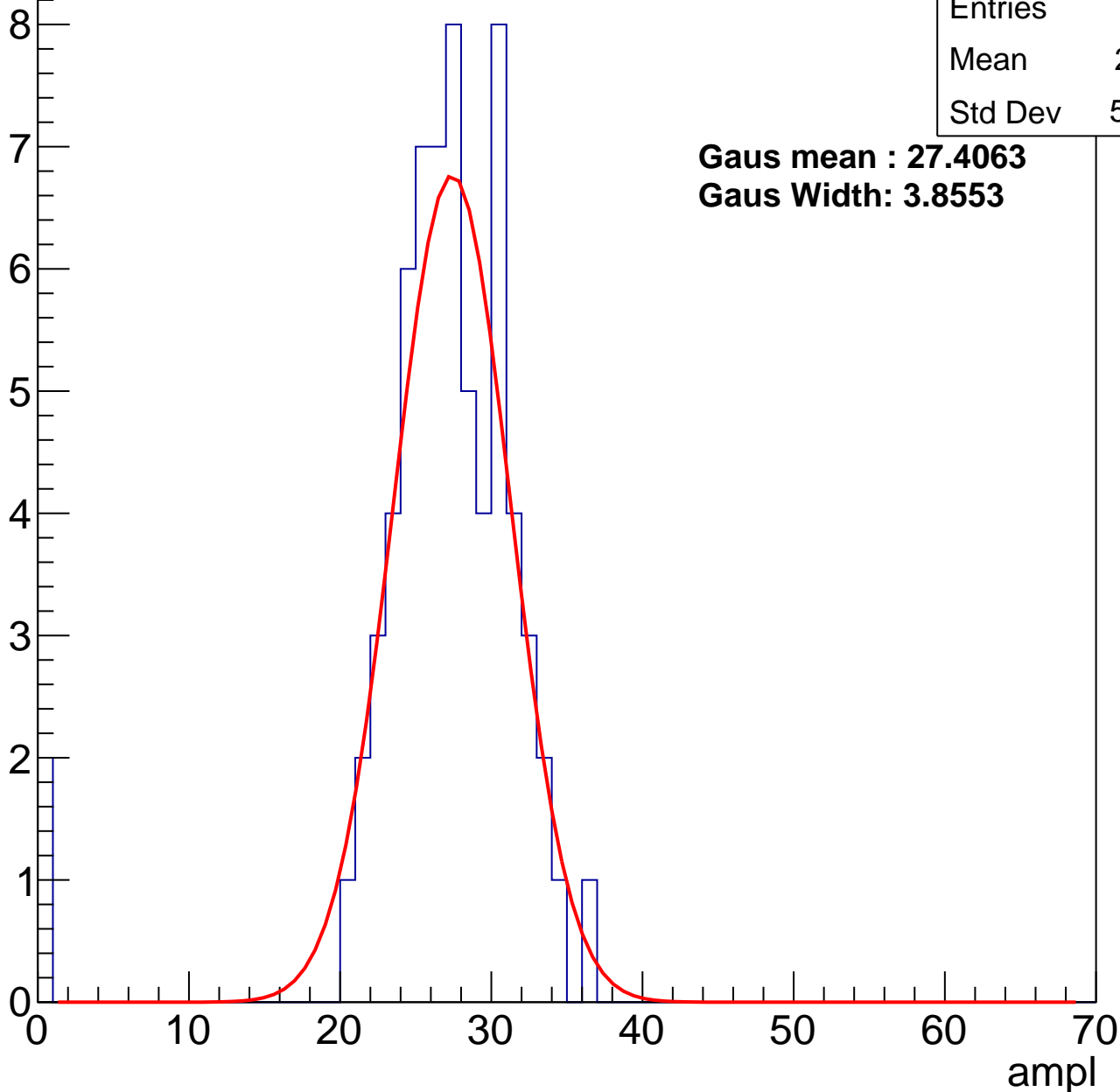
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	26.31
Std Dev	5.699

Gaus mean : 27.4063

Gaus Width: 3.8553



B1L100S, U19-ch2, adc1

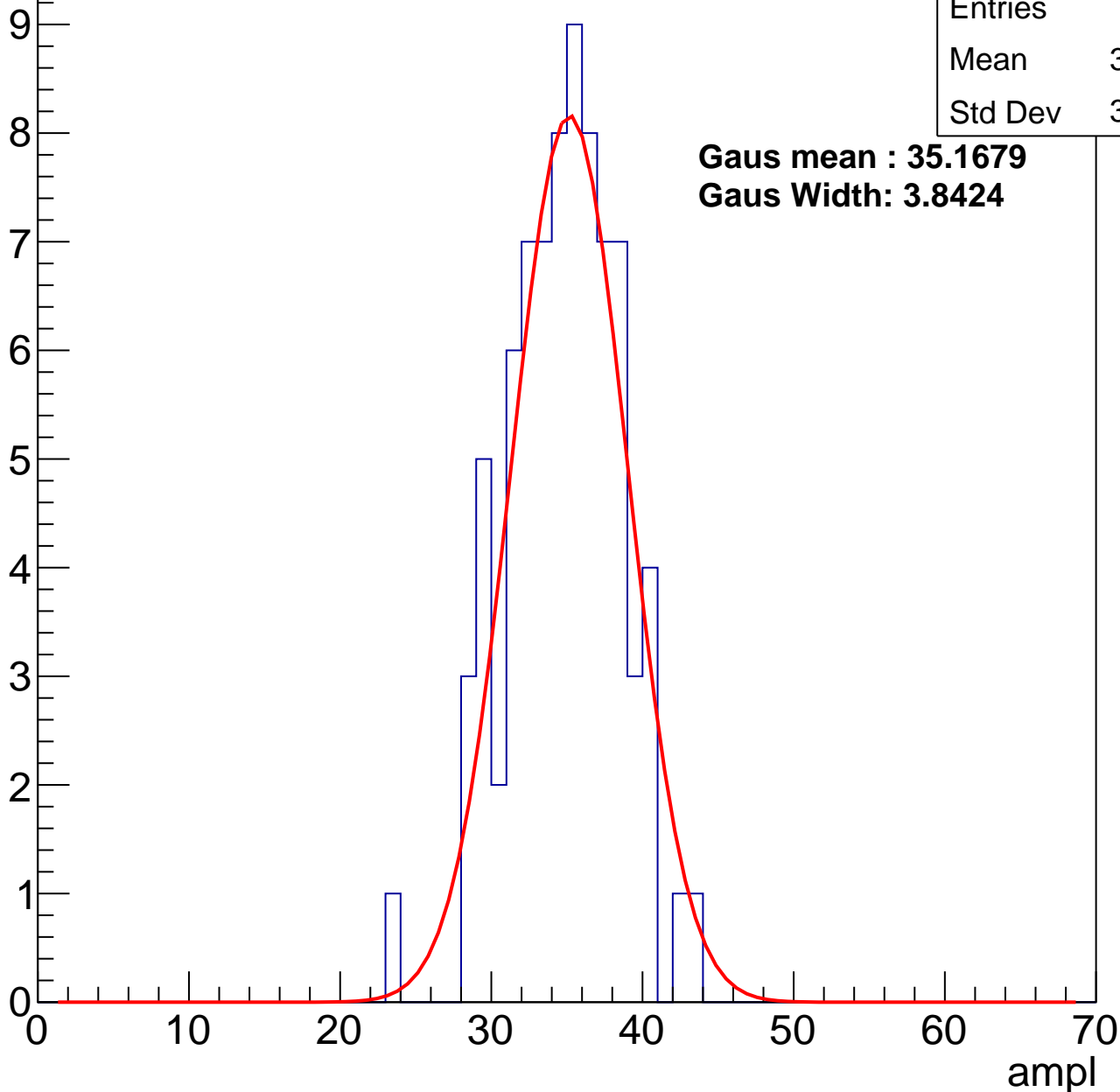
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	34.37
Std Dev	3.636

Gaus mean : 35.1679

Gaus Width: 3.8424



B1L100S, U19-ch2, adc2

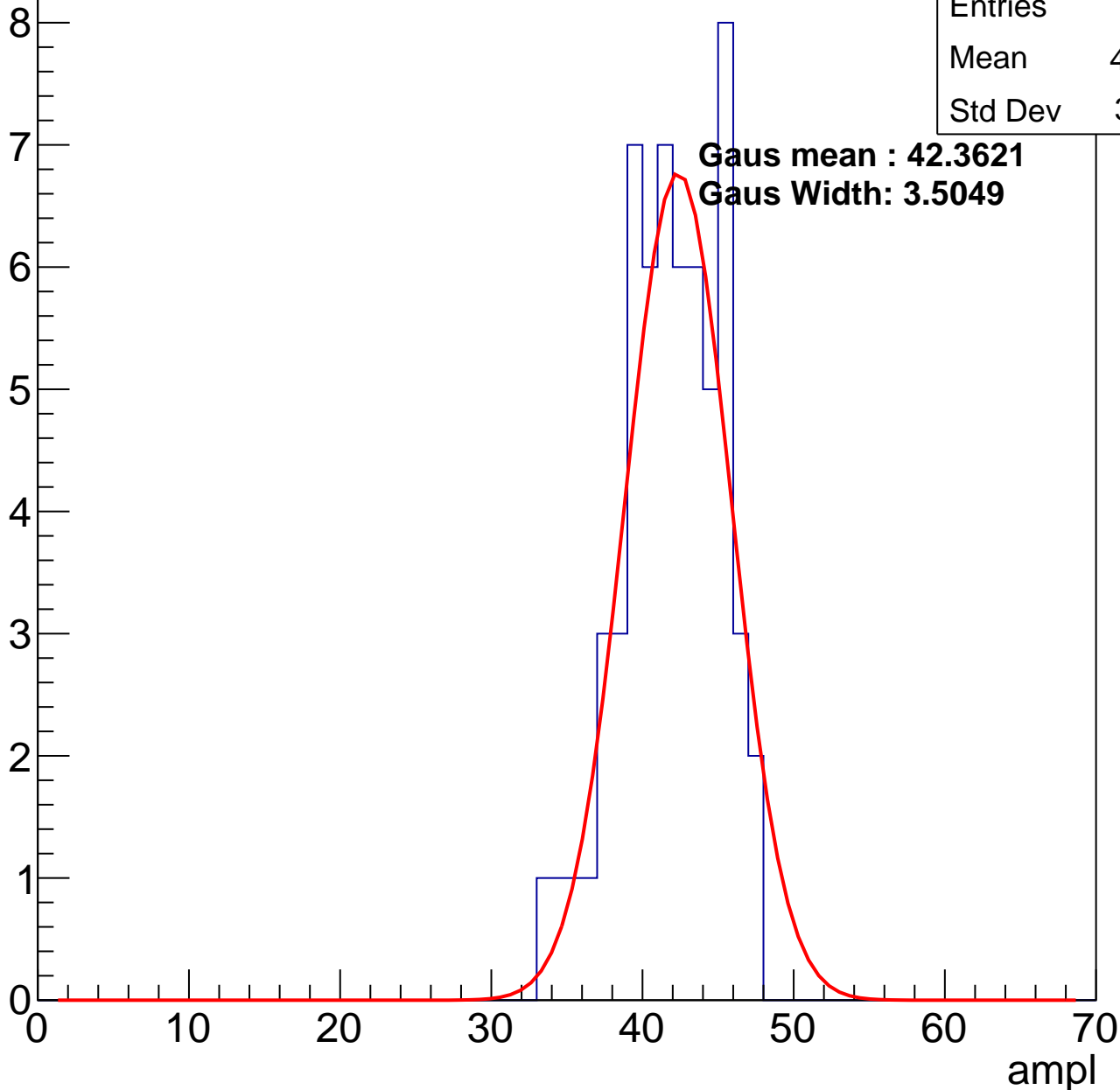
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.42
Std Dev	3.221

Gaus mean : 42.3621

Gaus Width: 3.5049

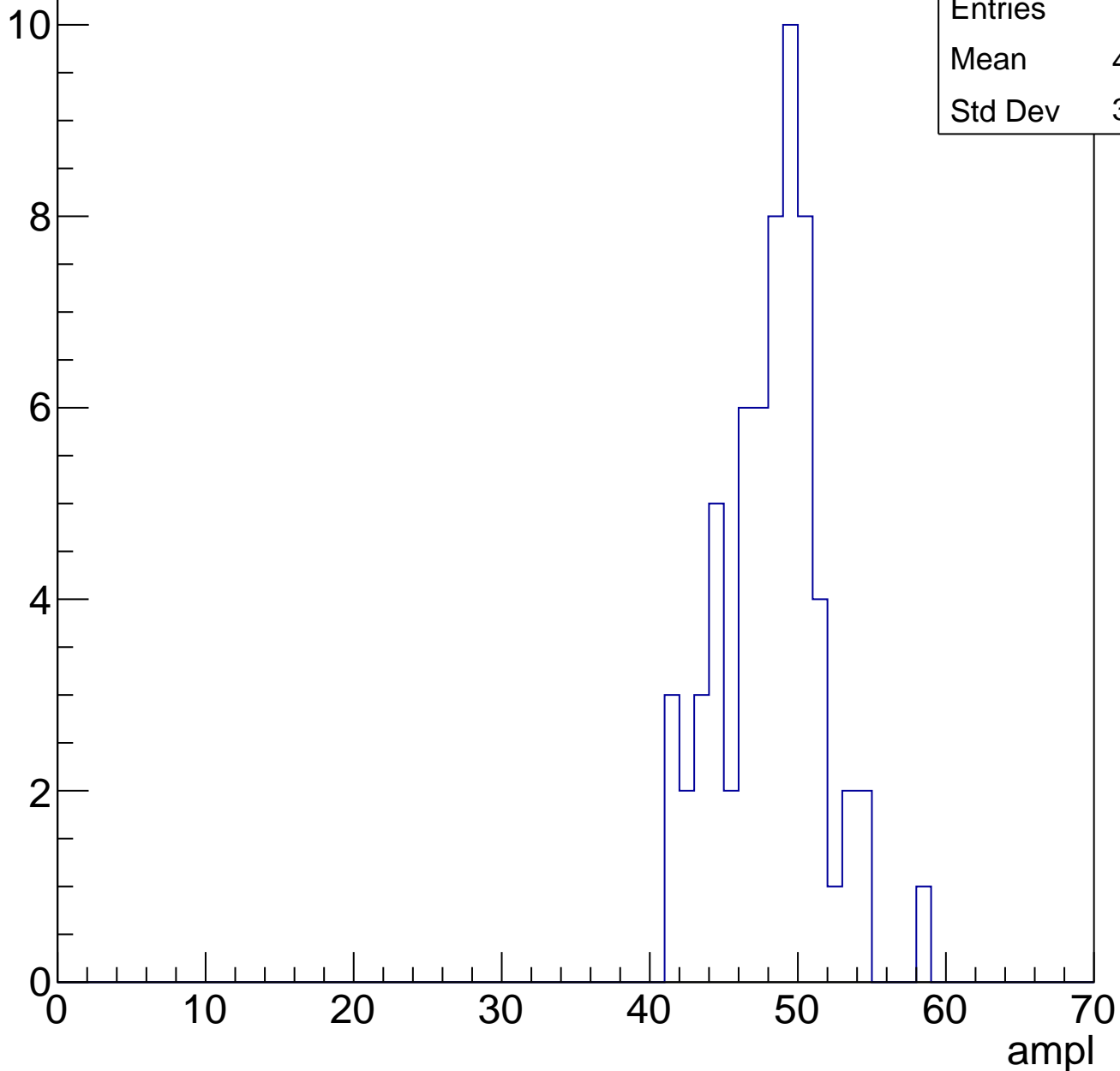


B1L100S, U19-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

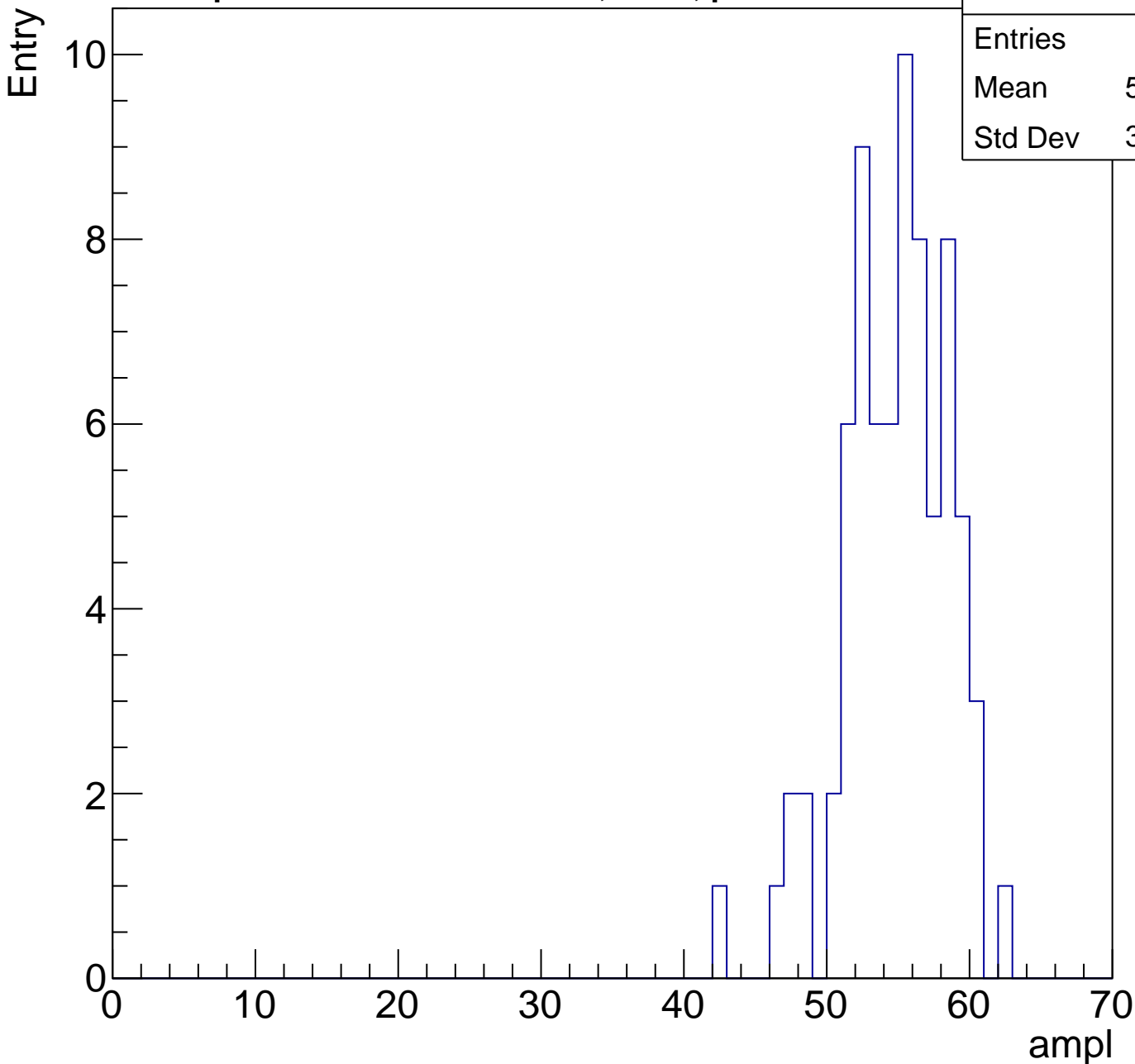
Entries	63
Mean	47.71
Std Dev	3.411



B1L100S, U19-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	54.37
Std Dev	3.665

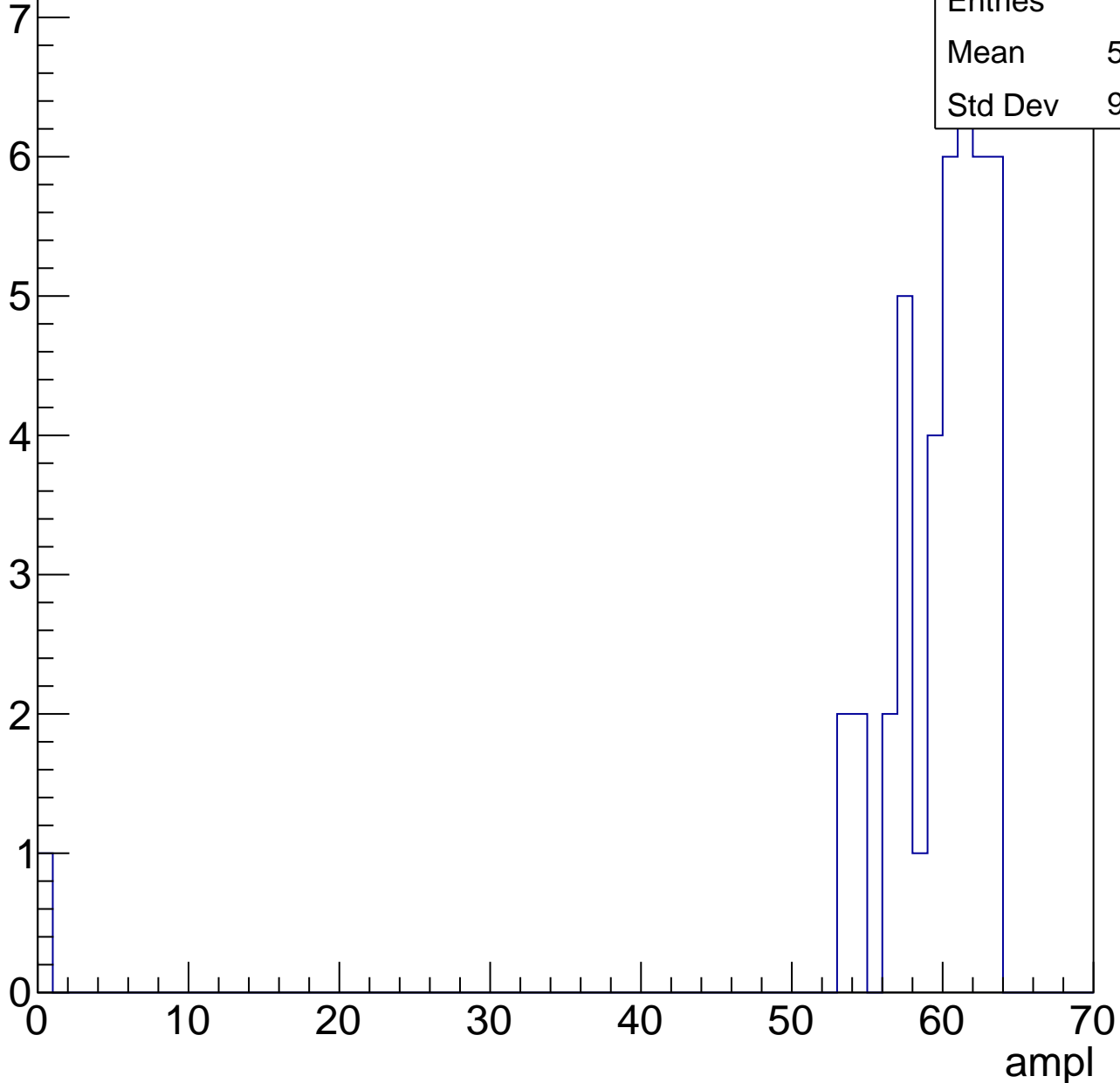


B1L100S, U19-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

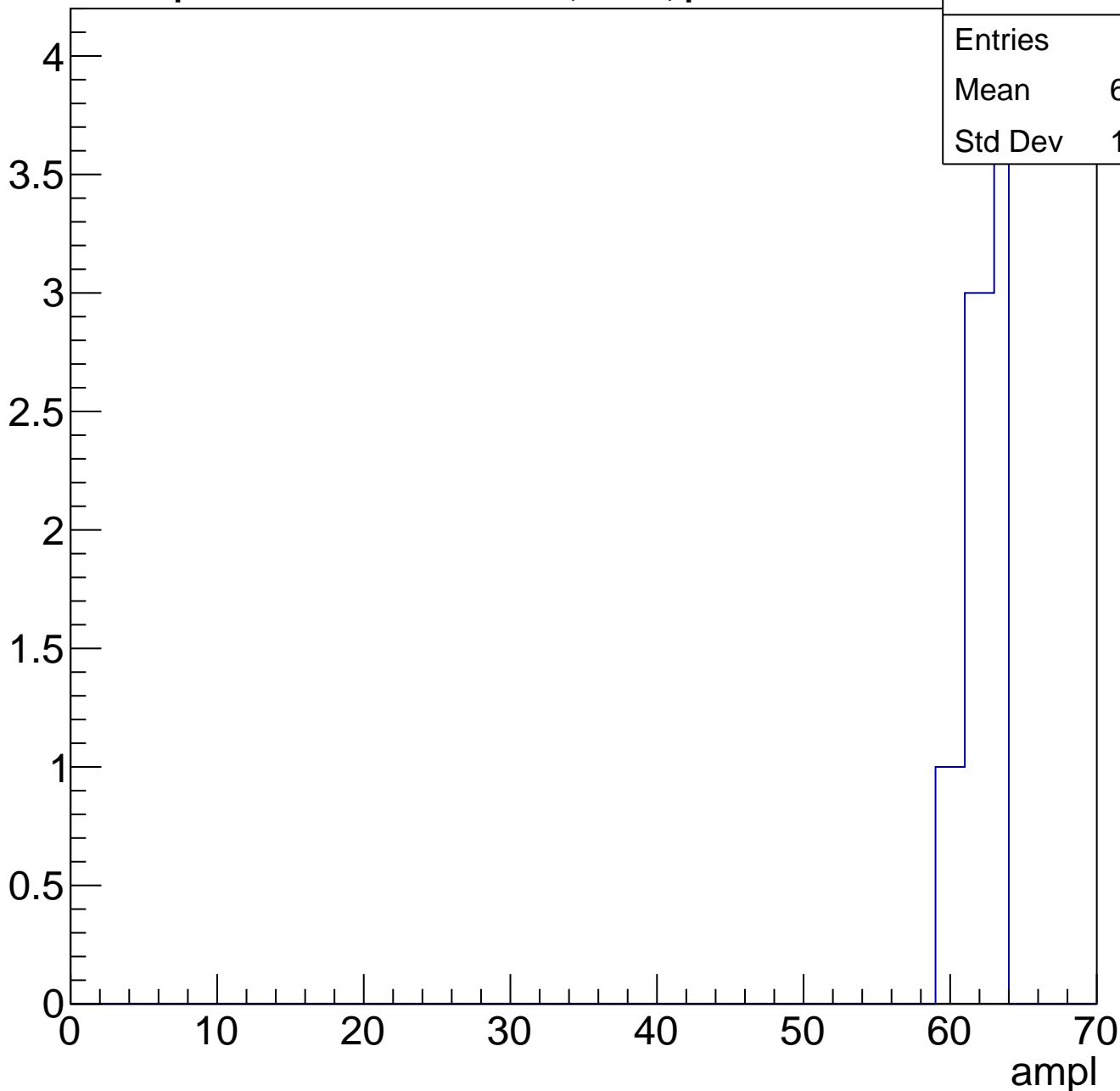
Entries	42
Mean	58.14
Std Dev	9.506



B1L100S, U19-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch3, adc0

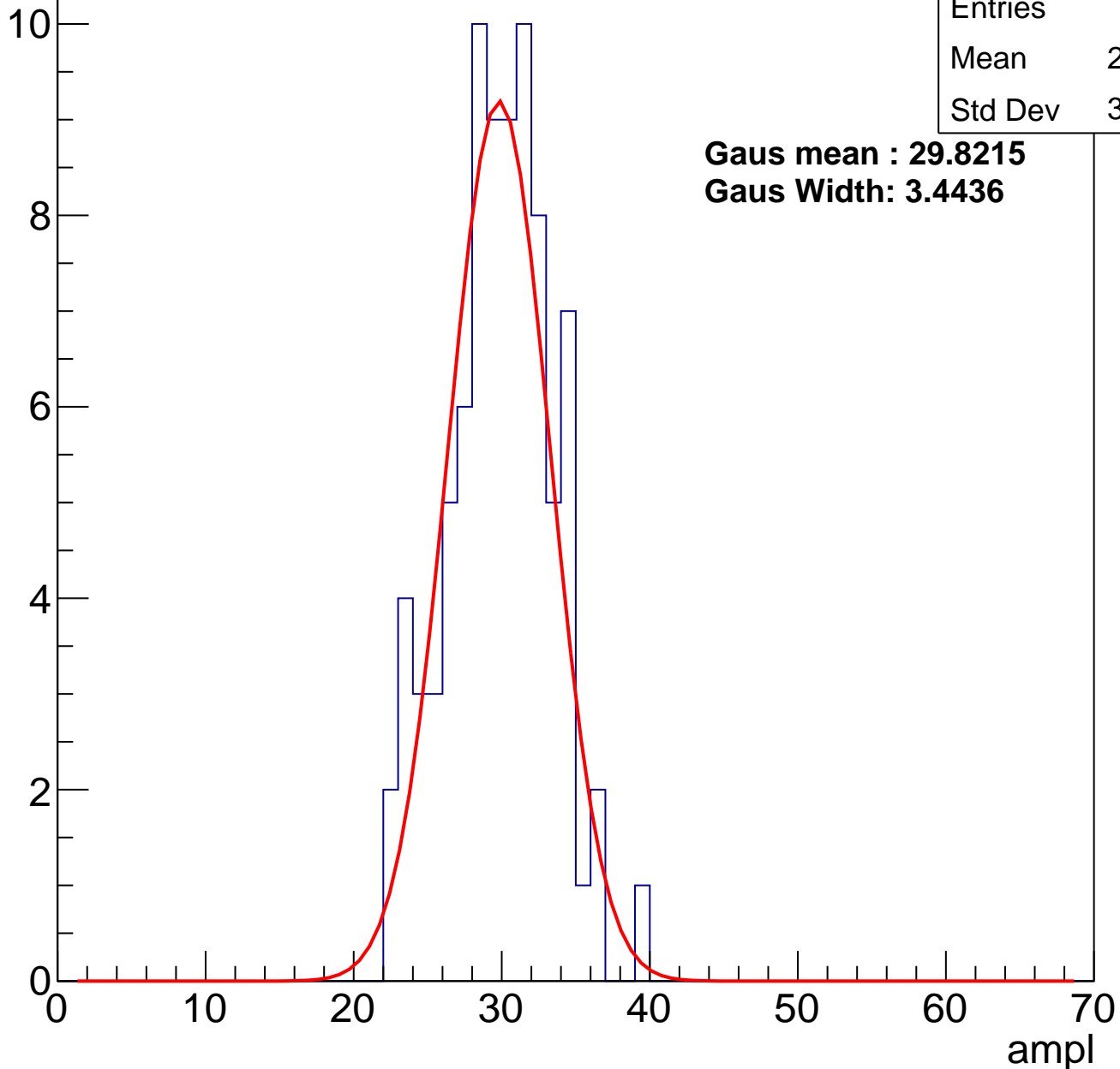
calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	29.42
Std Dev	3.496

Gaus mean : 29.8215

Gaus Width: 3.4436

Entry



B1L100S, U19-ch3, adc1

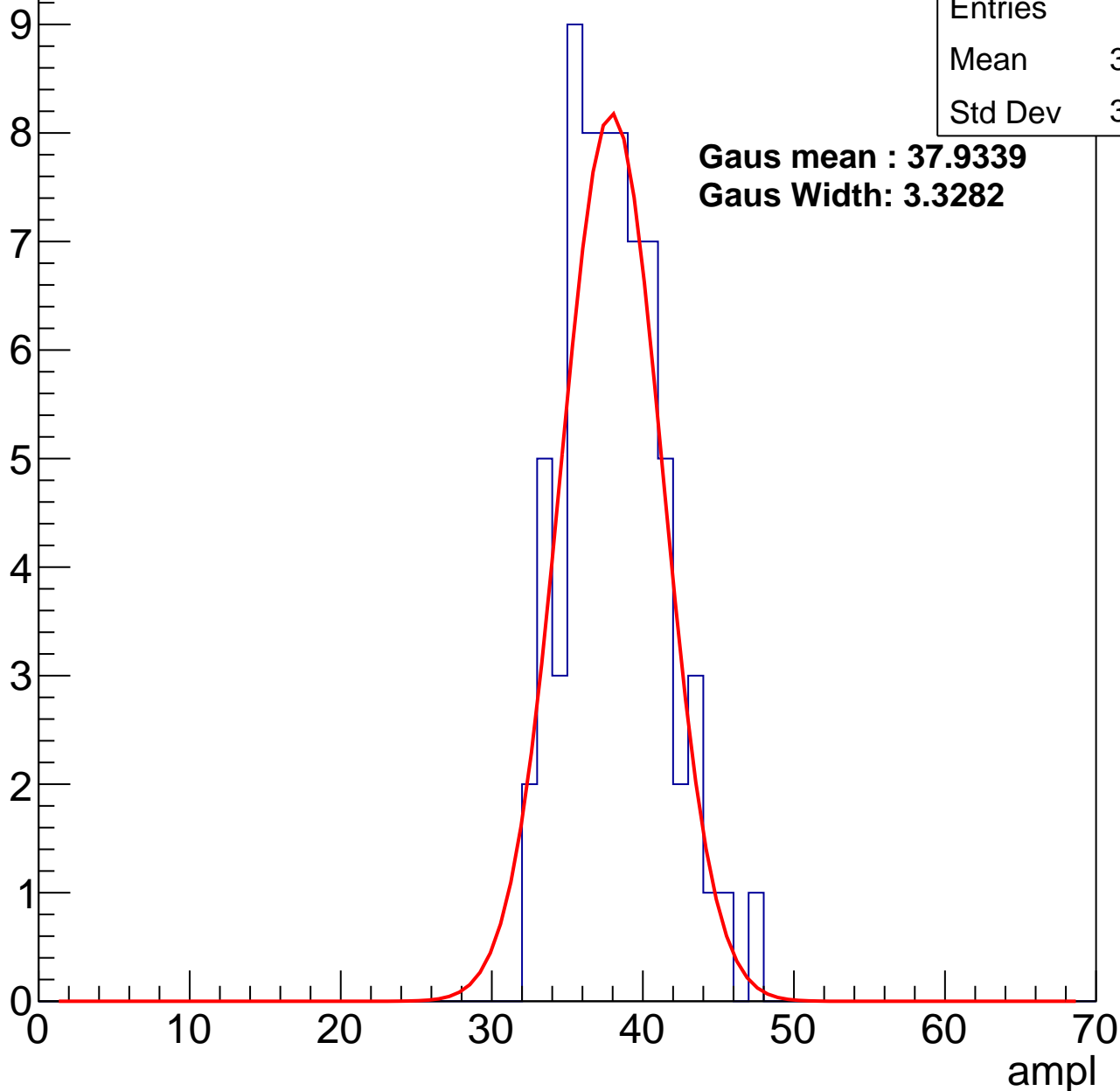
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.73
Std Dev	3.193

Gaus mean : 37.9339

Gaus Width: 3.3282



B1L100S, U19-ch3, adc2

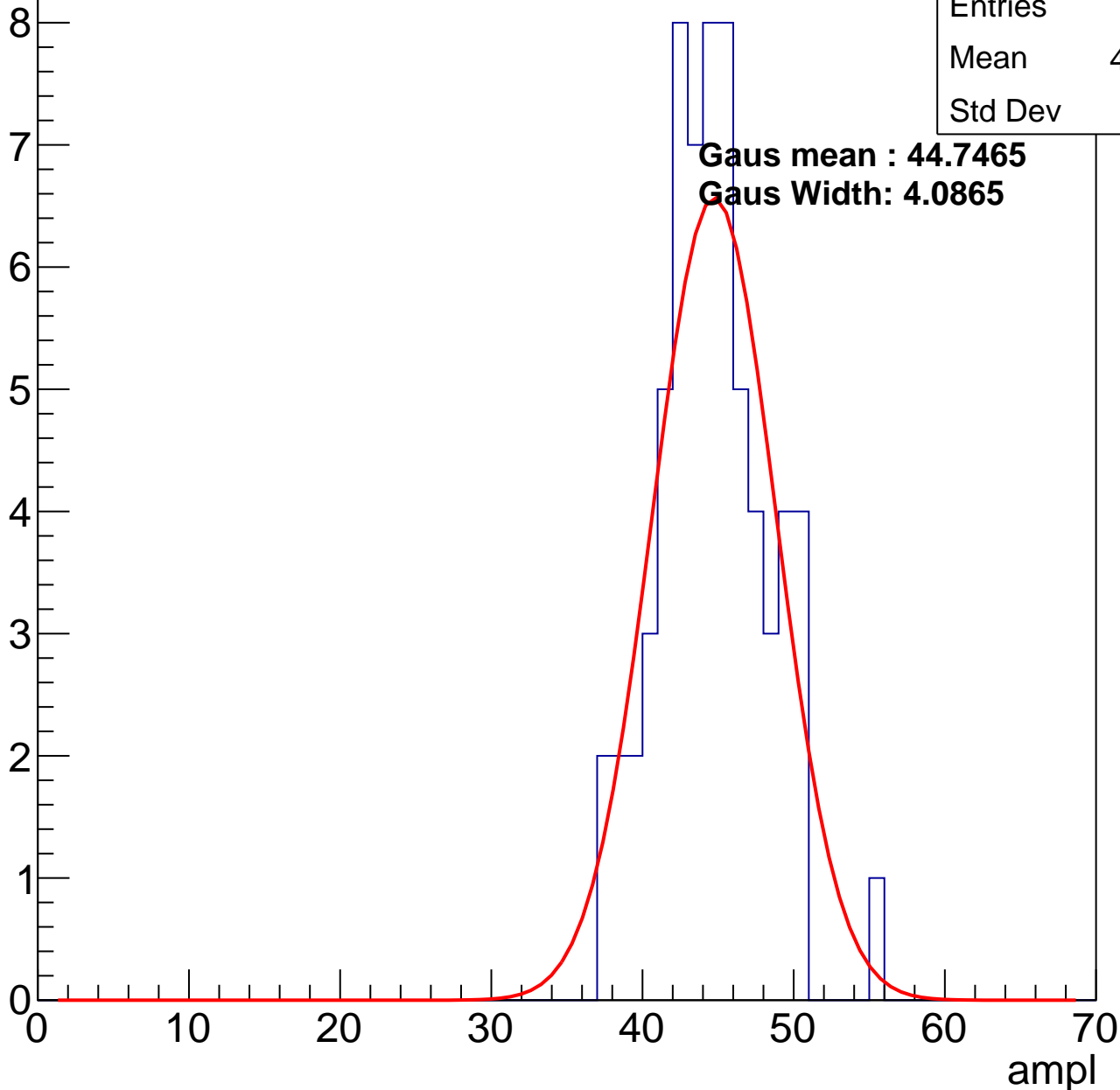
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.17
Std Dev	3.54

Gaus mean : 44.7465

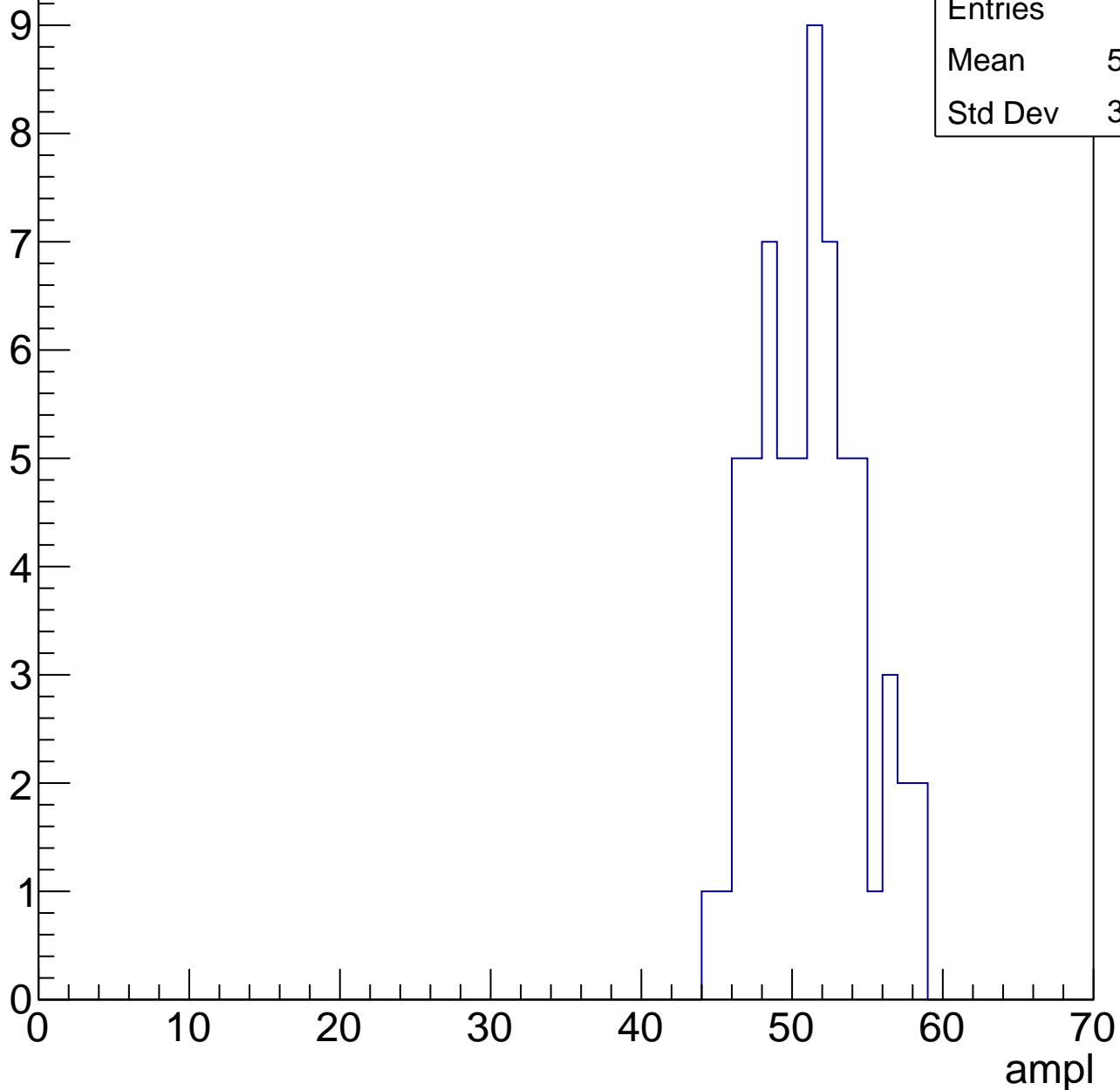
Gaus Width: 4.0865



B1L100S, U19-ch3, adc3

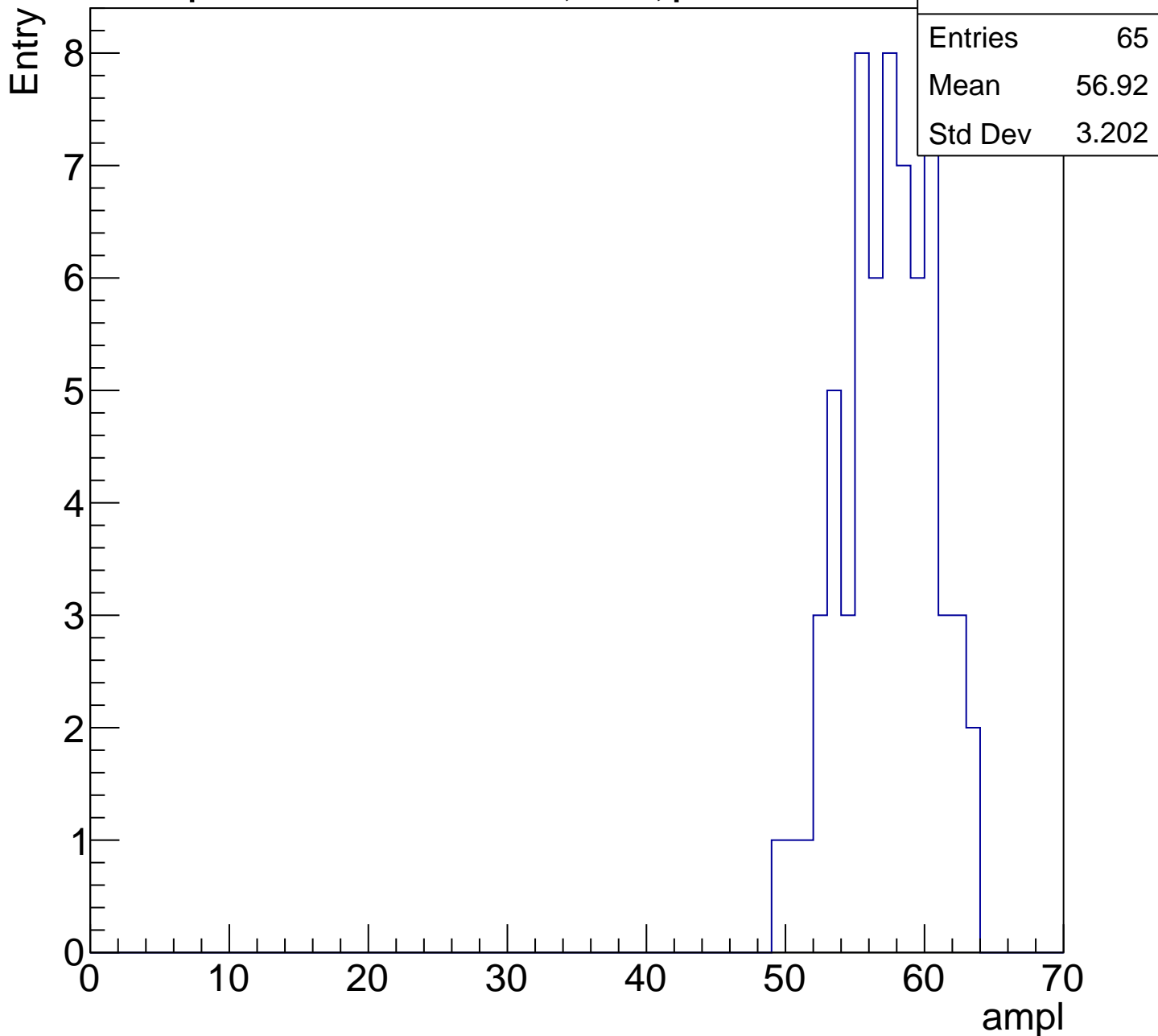
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

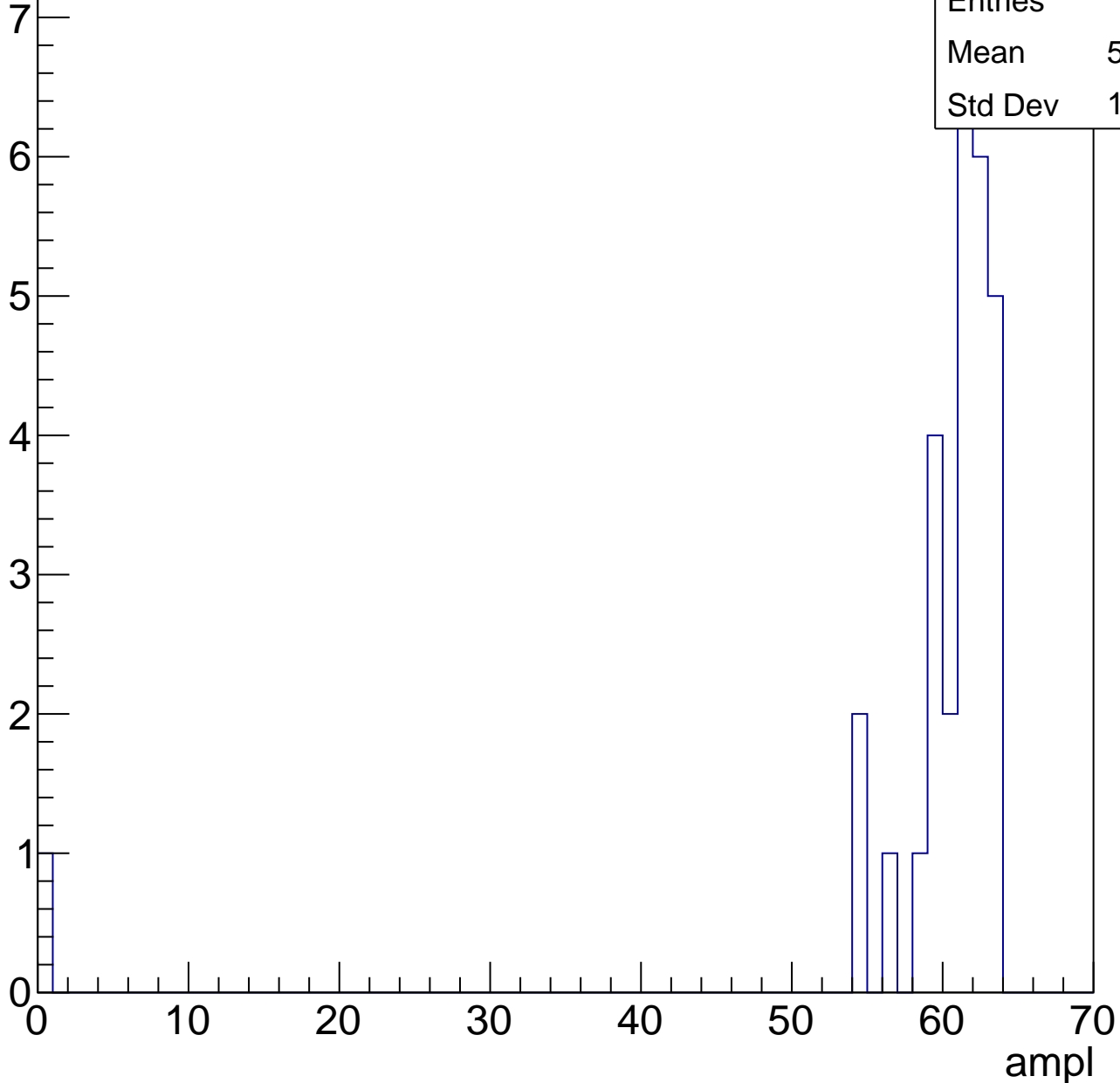


B1L100S, U19-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.34
Std Dev	11.28



B1L100S, U19-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

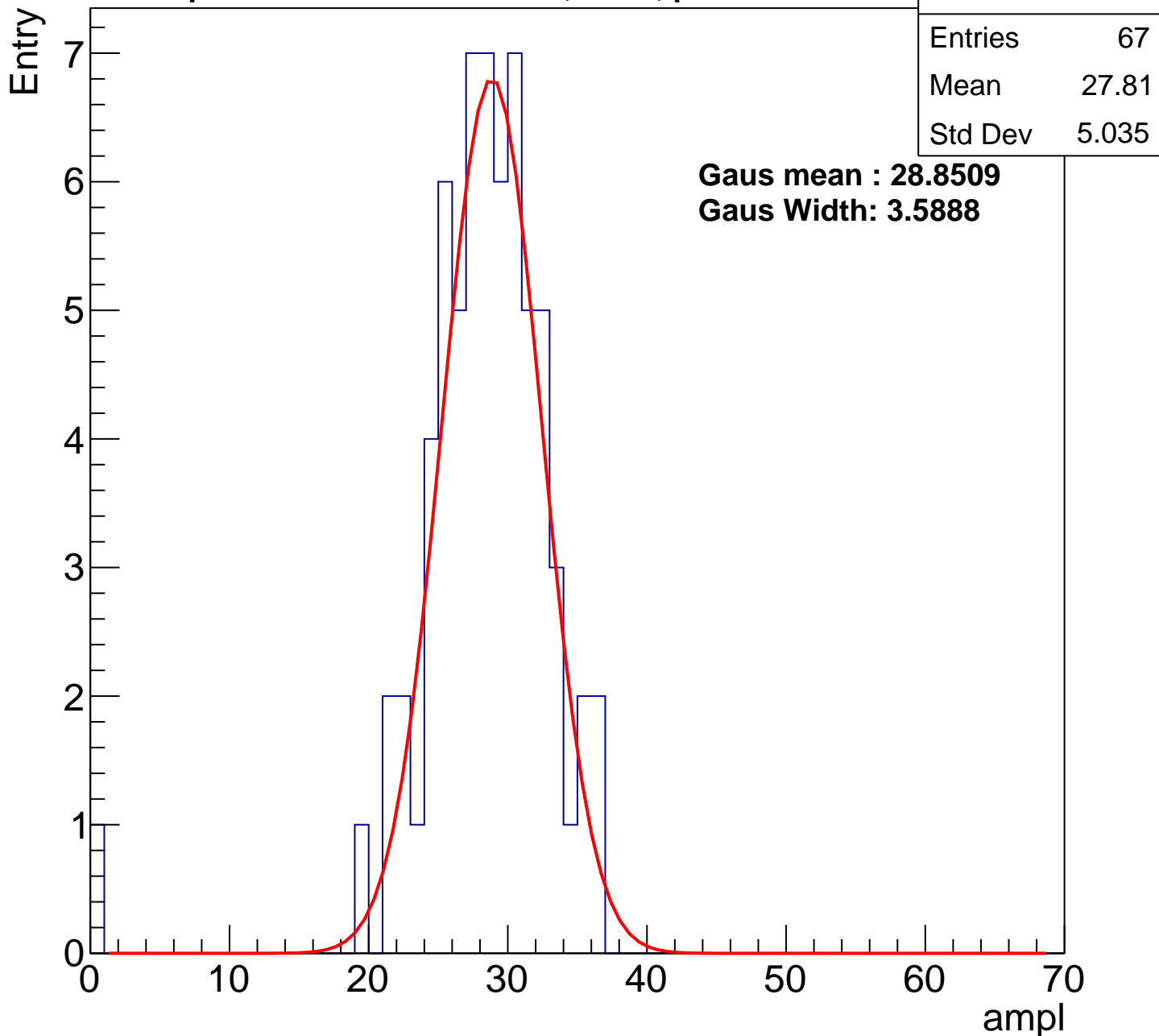
Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch4, adc0

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch4, adc1

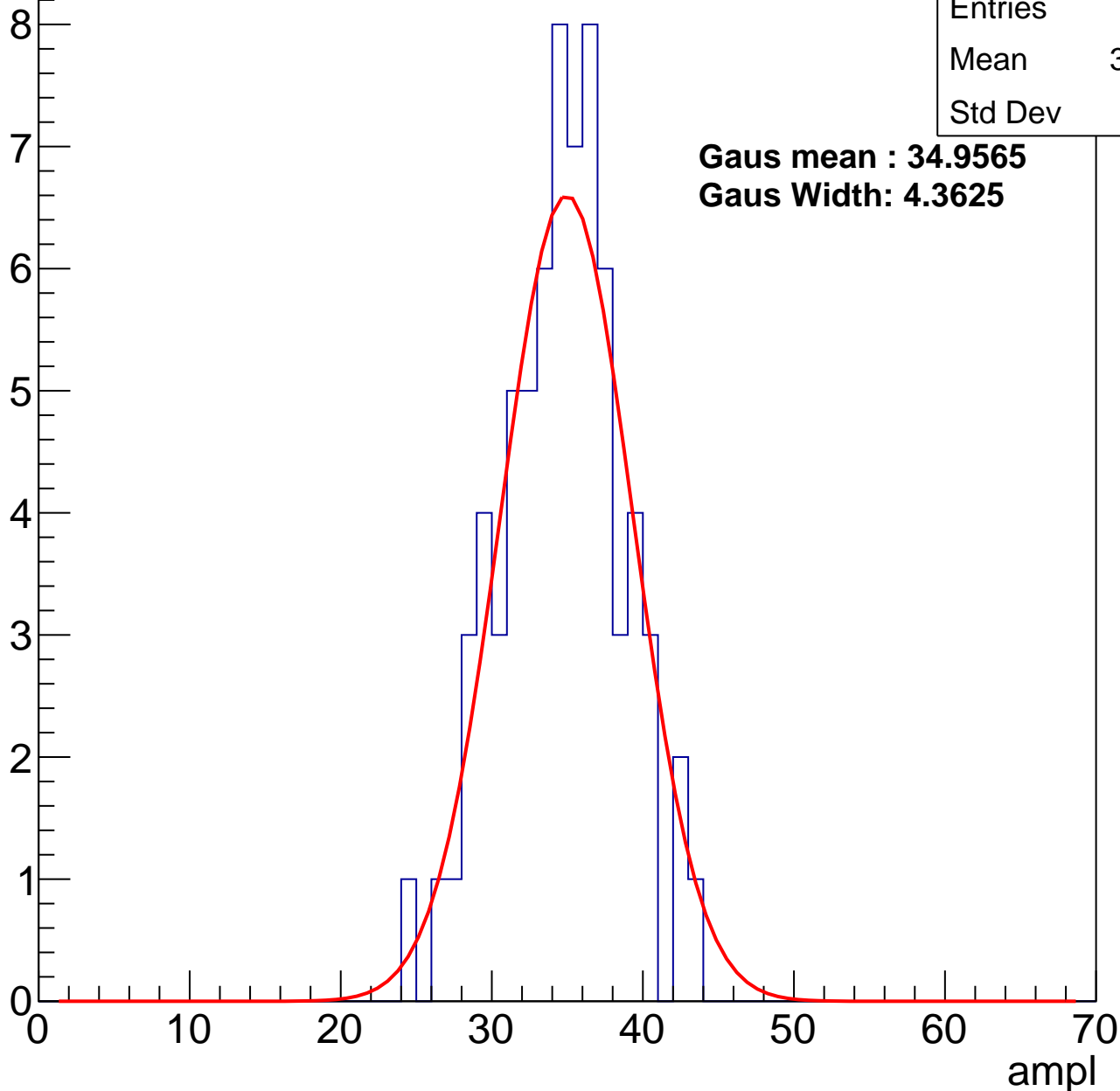
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	34.14
Std Dev	3.93

Gaus mean : 34.9565

Gaus Width: 4.3625



B1L100S, U19-ch4, adc2

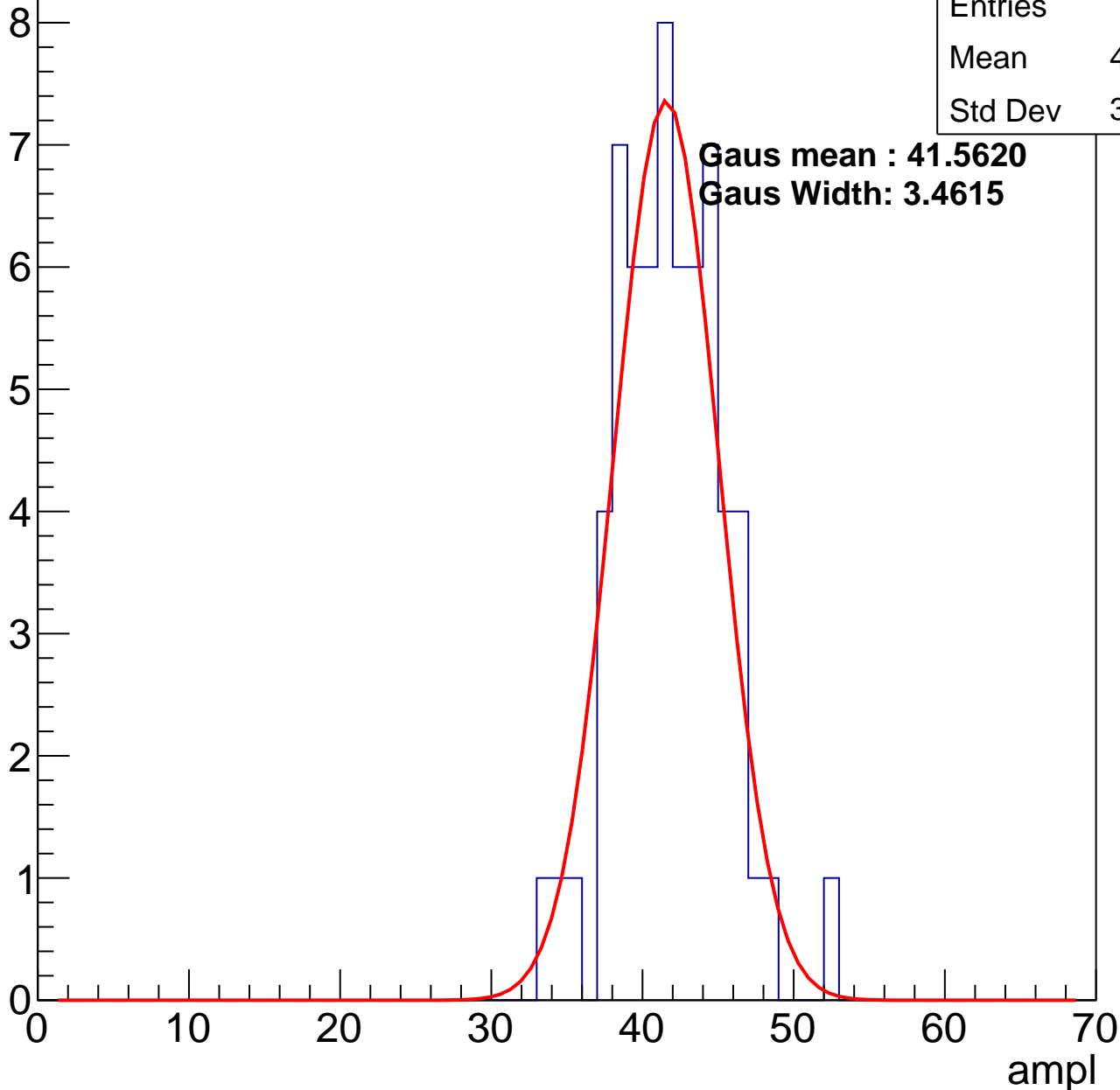
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.36
Std Dev	3.448

Gaus mean : 41.5620

Gaus Width: 3.4615

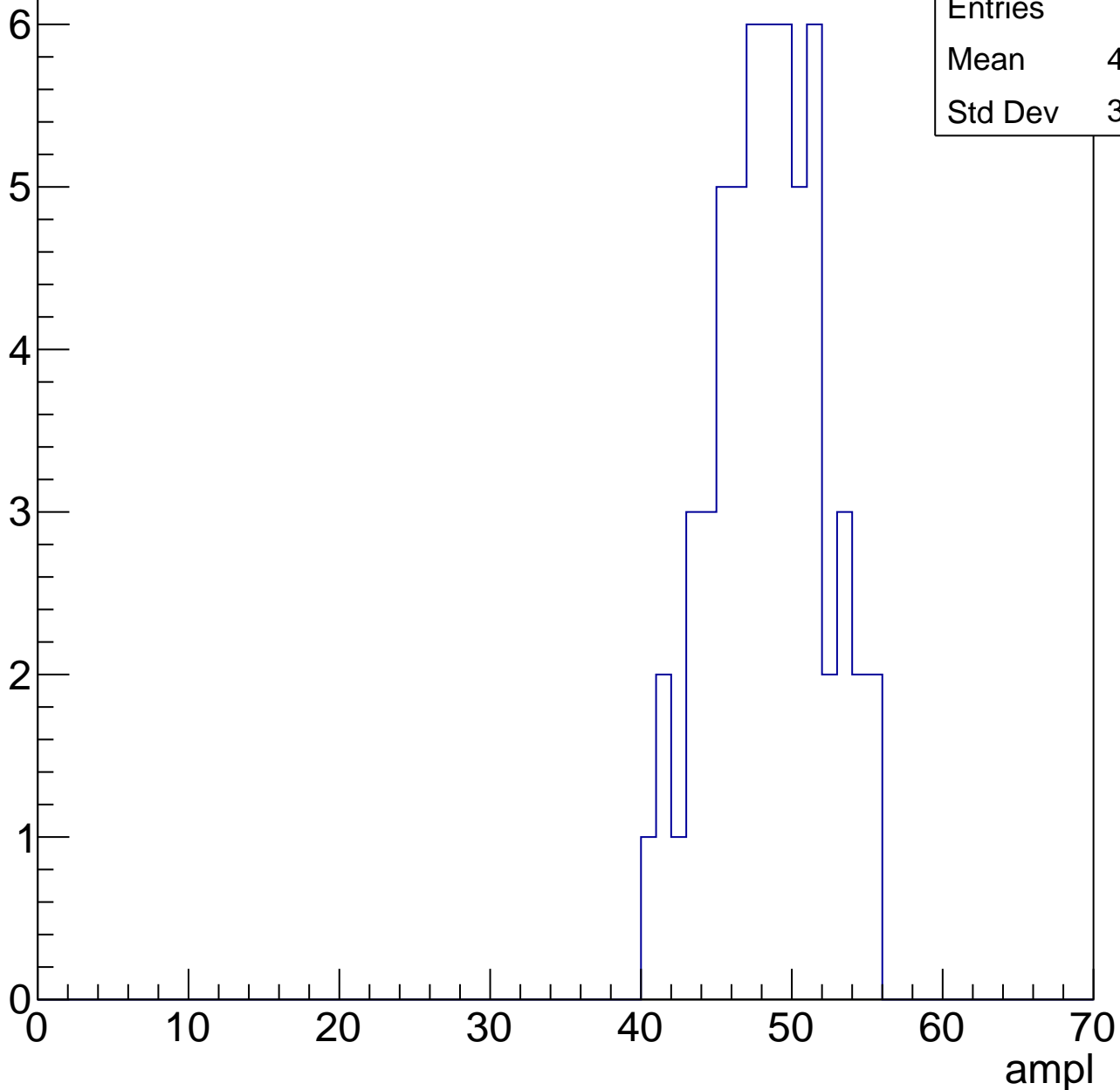


B1L100S, U19-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	47.95
Std Dev	3.593

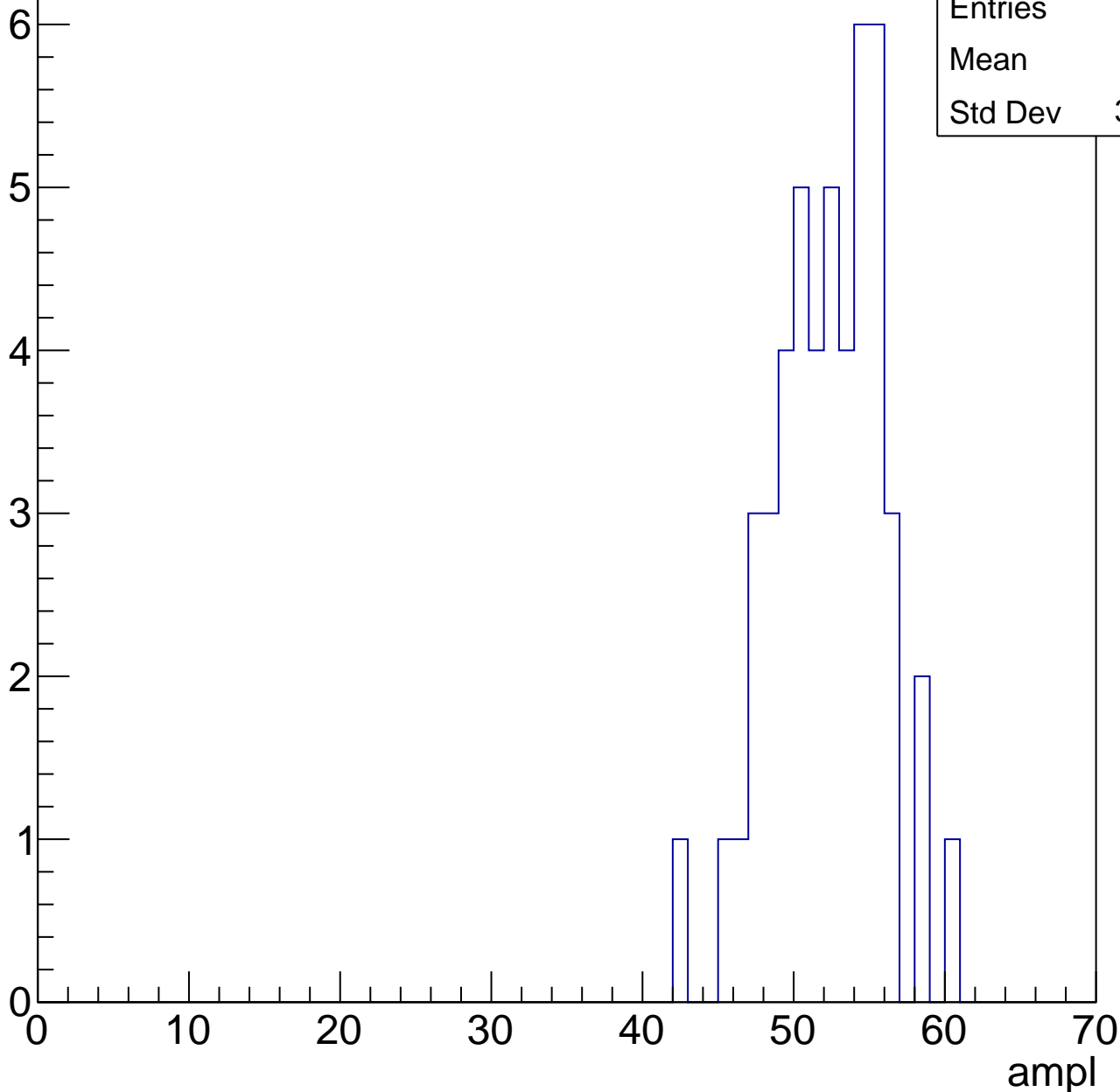


B1L100S, U19-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	51.8
Std Dev	3.591



B1L100S, U19-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 66

Mean 57.38

Std Dev 7.633

8

6

4

2

0

ampl

0

10

20

30

40

50

60

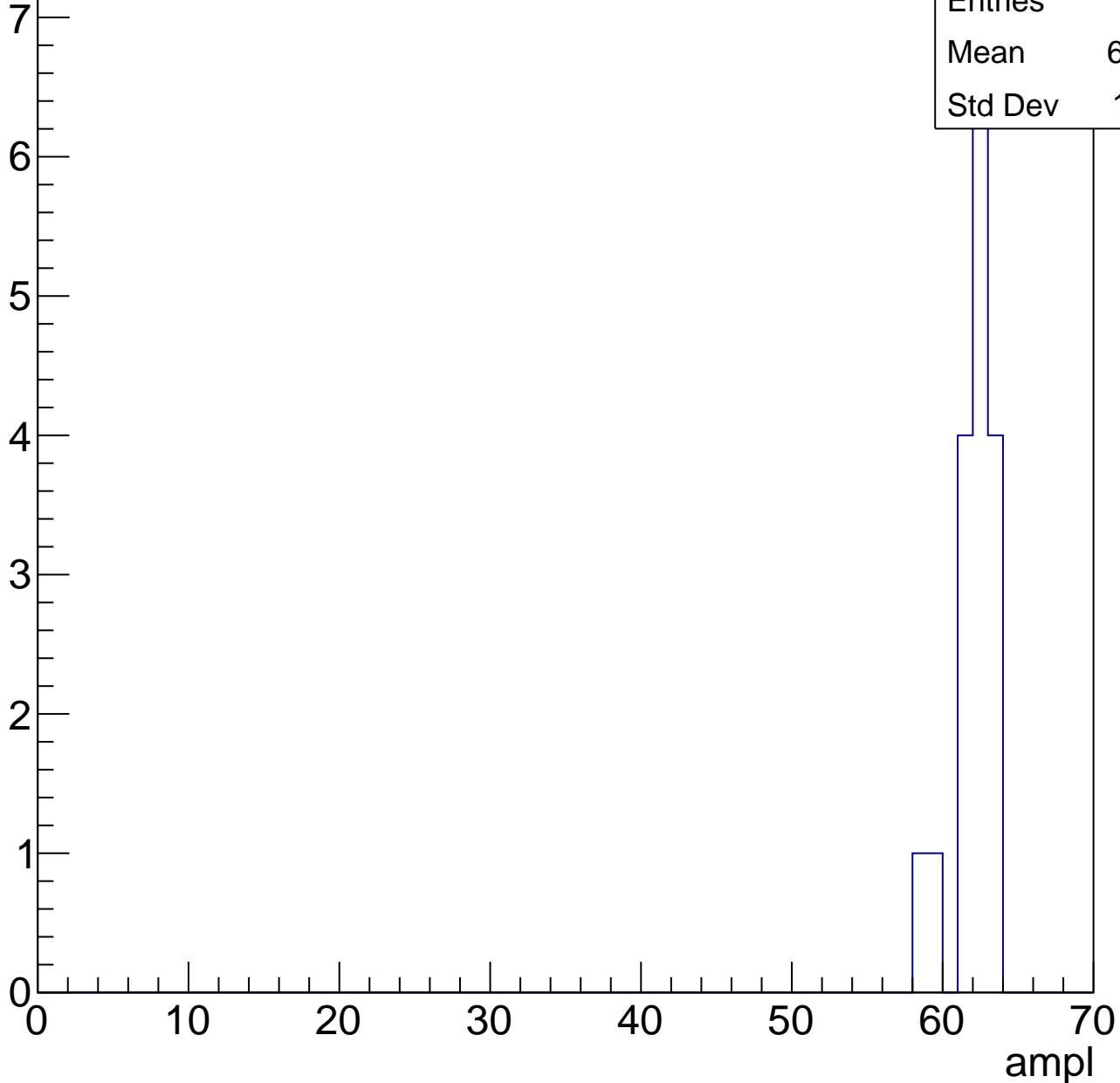
70

B1L100S, U19-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	61.59
Std Dev	1.331



B1L100S, U19-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U19-ch5, adc0

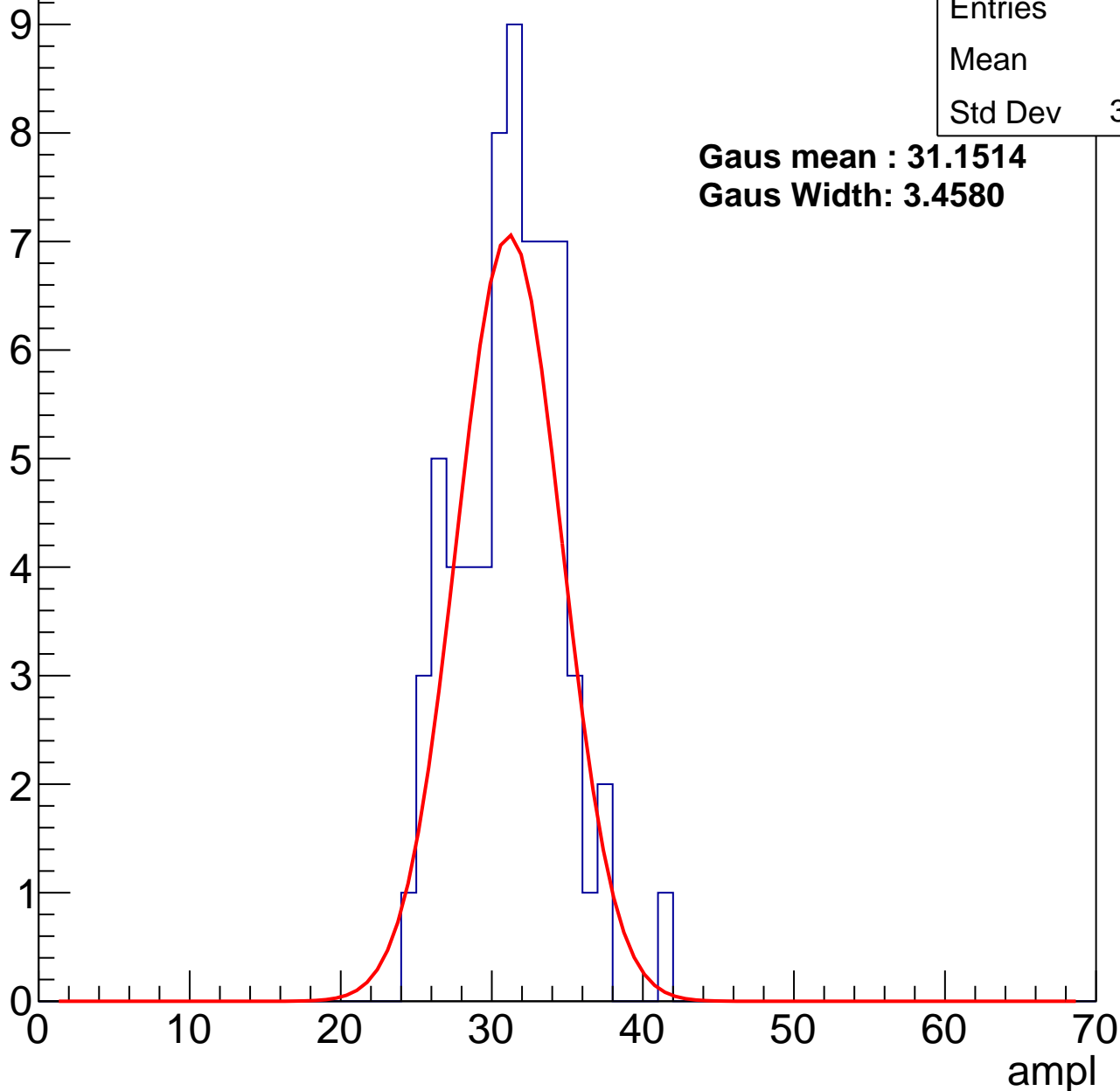
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.8
Std Dev	3.363

Gaus mean : 31.1514

Gaus Width: 3.4580



B1L100S, U19-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	38.3
Std Dev	3.337

Gaus mean : 37.9394

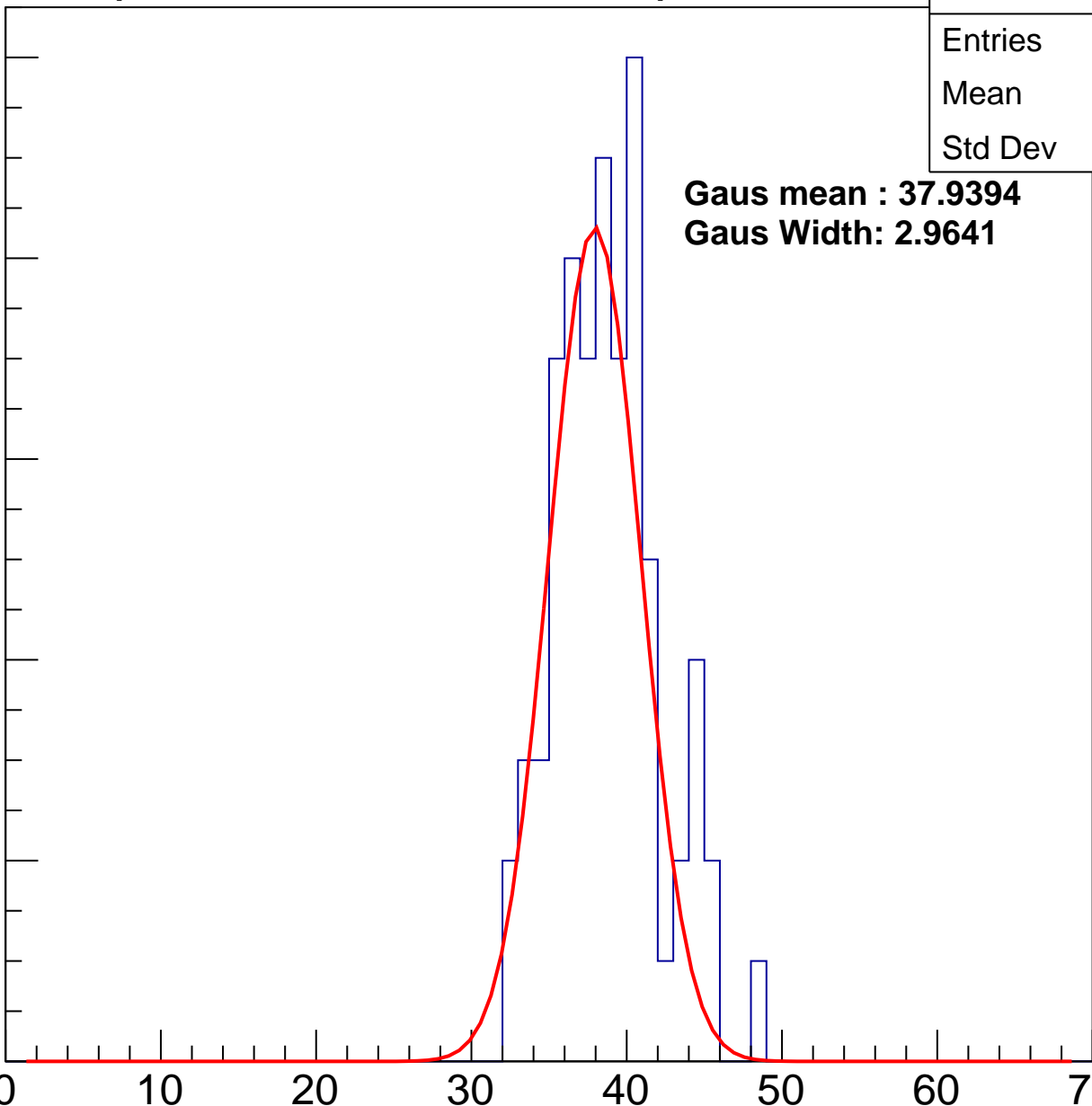
Gaus Width: 2.9641

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch5, adc2

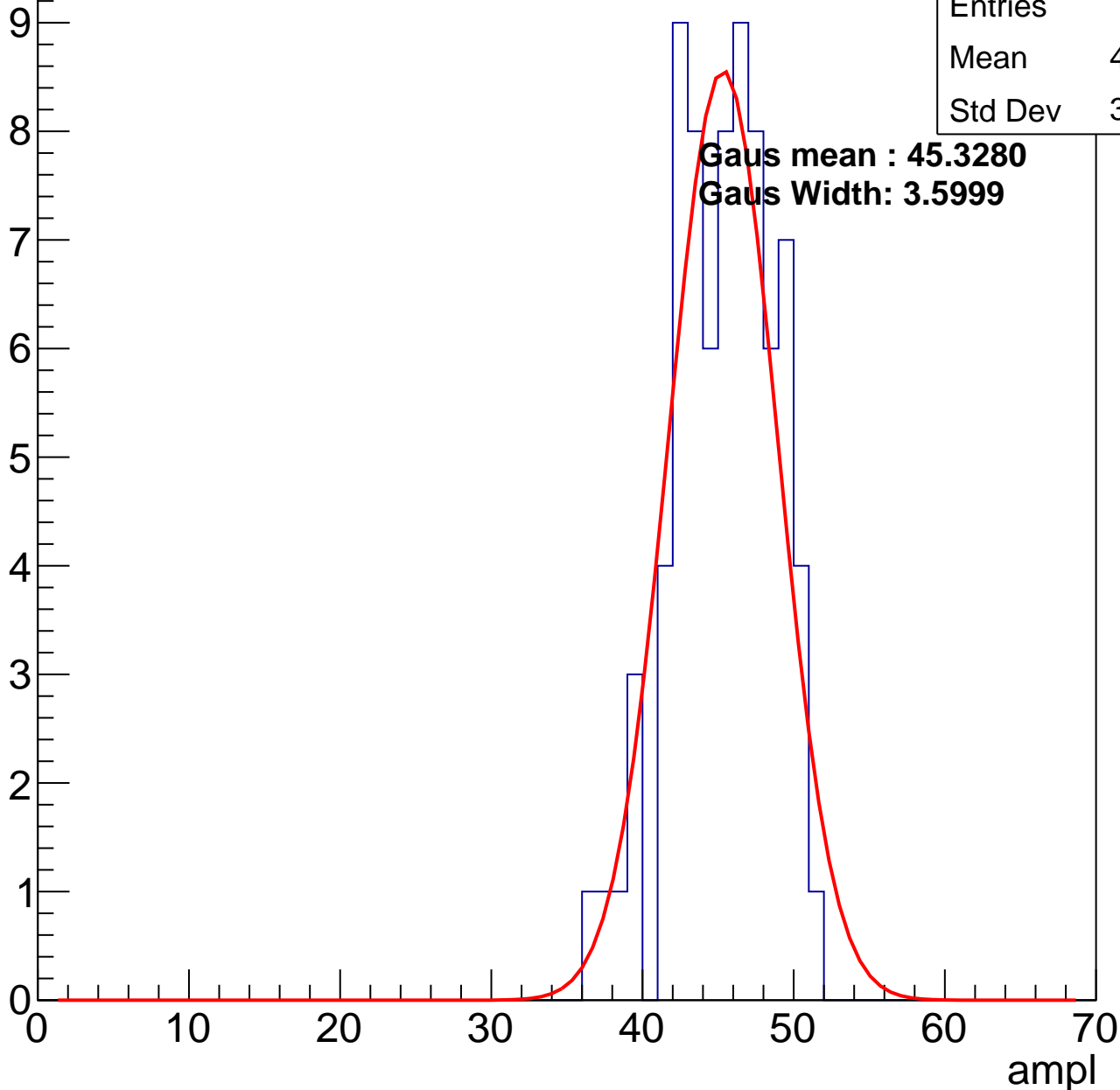
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	44.87
Std Dev	3.298

Gaus mean : 45.3280

Gaus Width: 3.5999

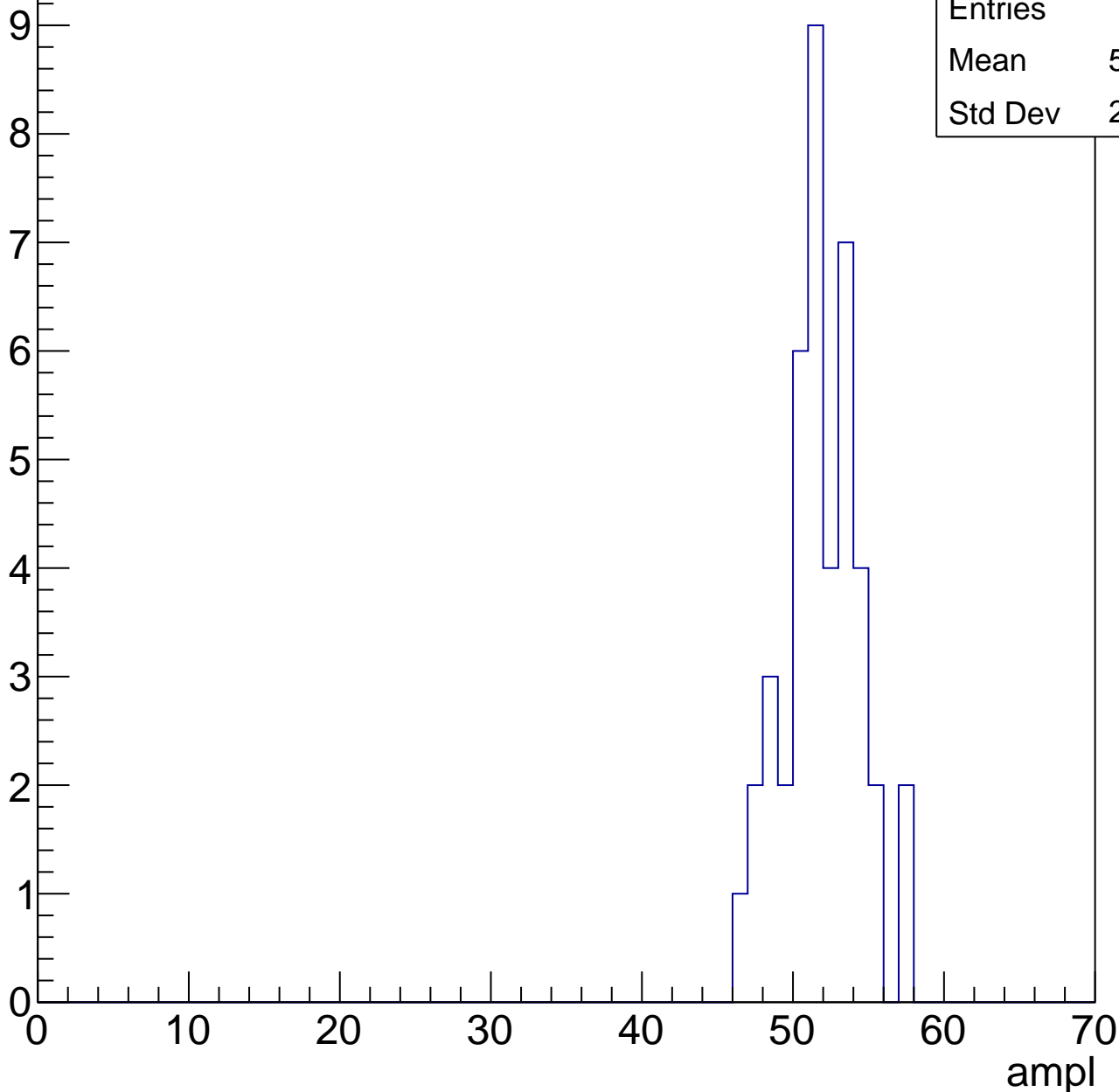


B1L100S, U19-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	51.43
Std Dev	2.499

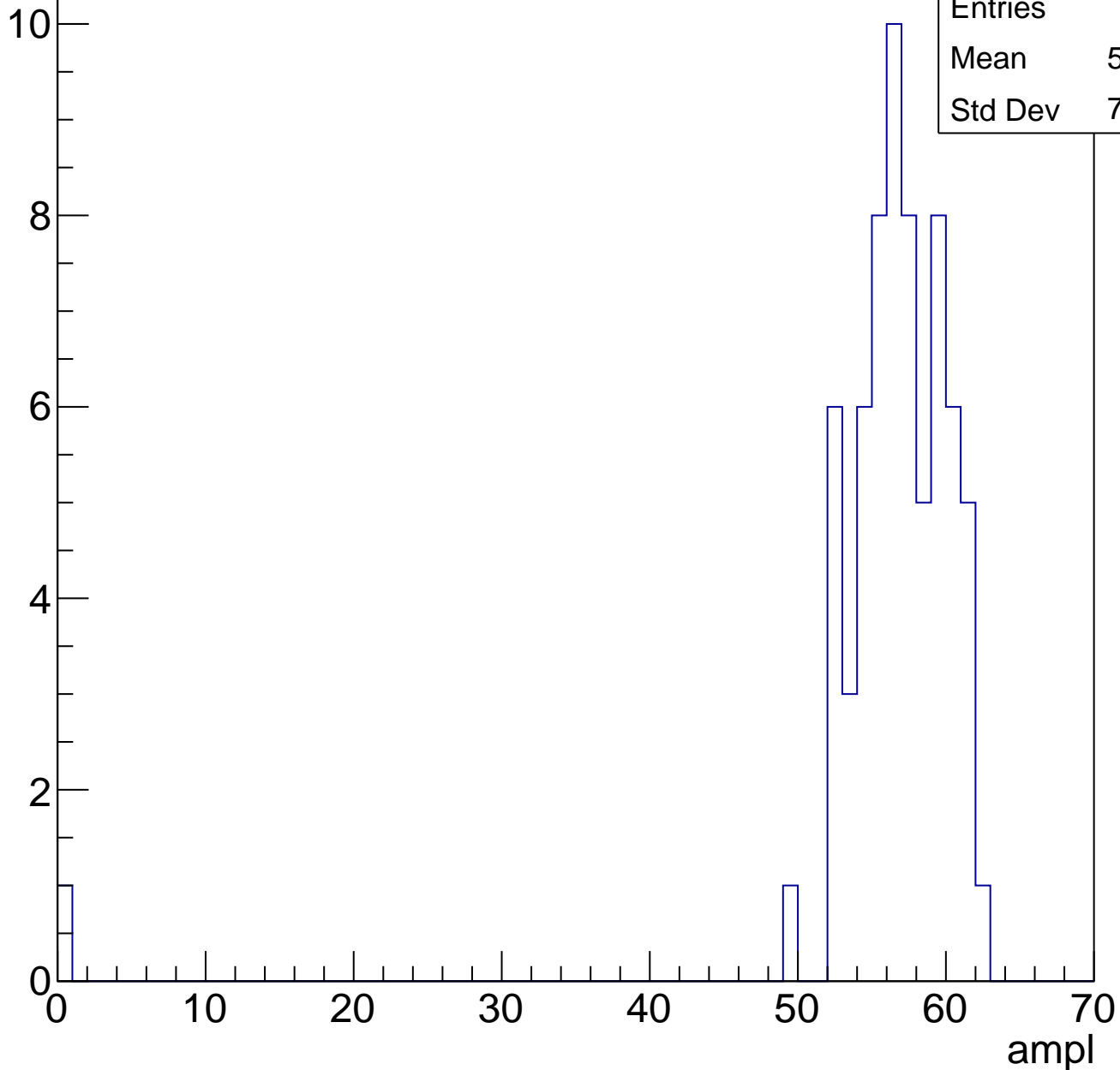


B1L100S, U19-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	55.72
Std Dev	7.368

Entry



B1L100S, U19-ch5, adc5

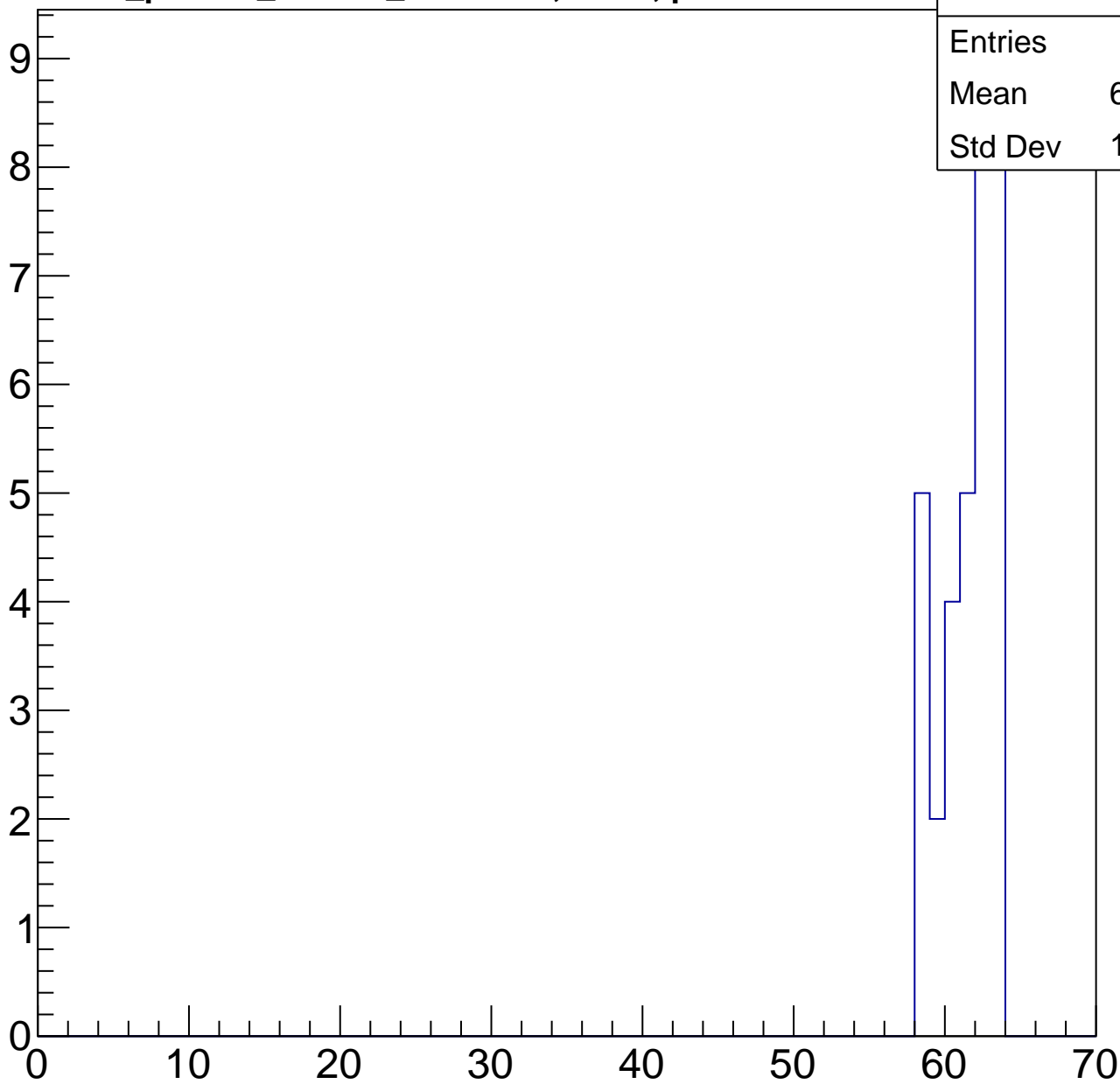
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	34
Mean	61.12
Std Dev	1.728

ampl



B1L100S, U19-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch6, adc0

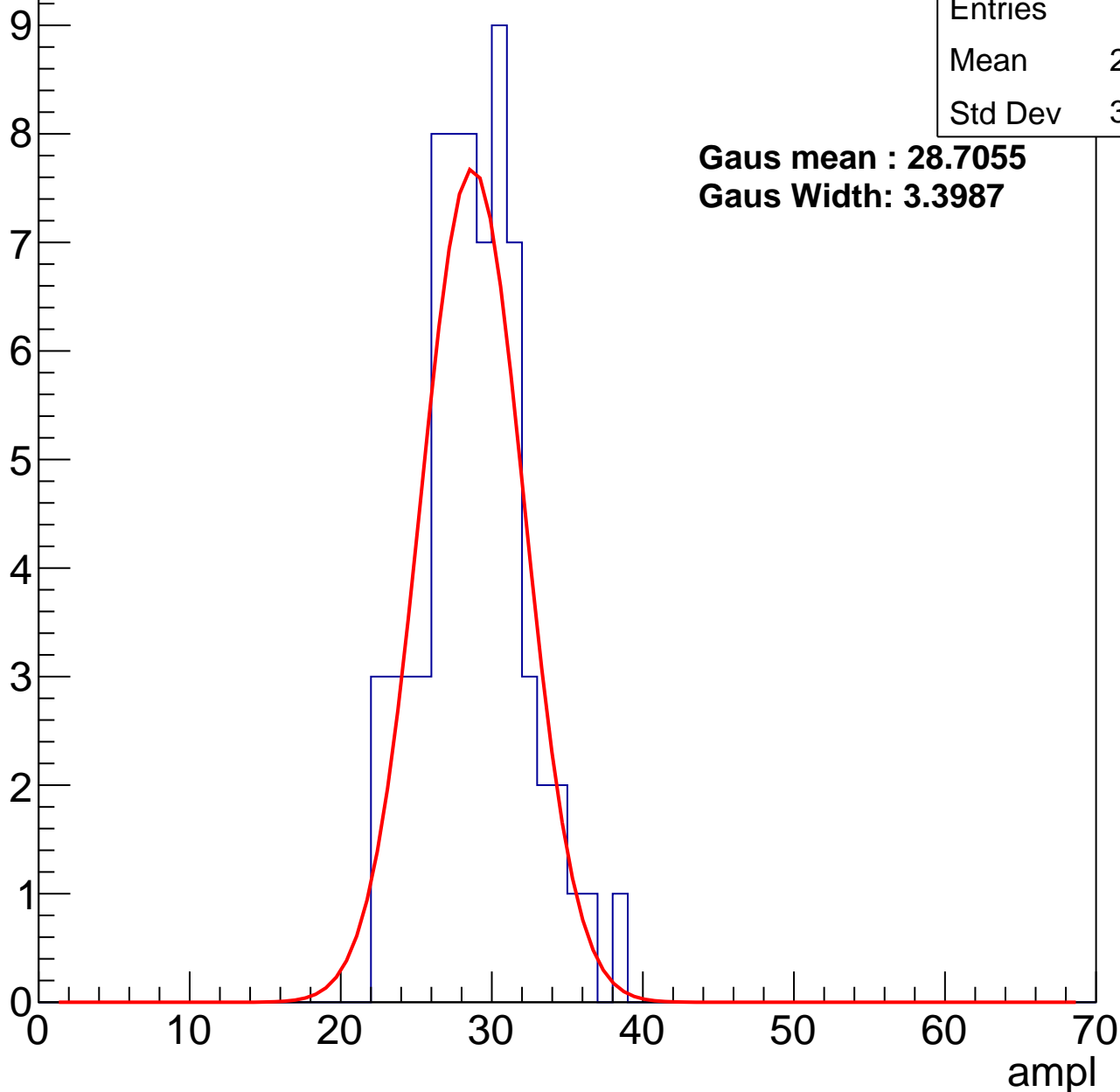
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.39
Std Dev	3.355

Gaus mean : 28.7055

Gaus Width: 3.3987



B1L100S, U19-ch6, adc1

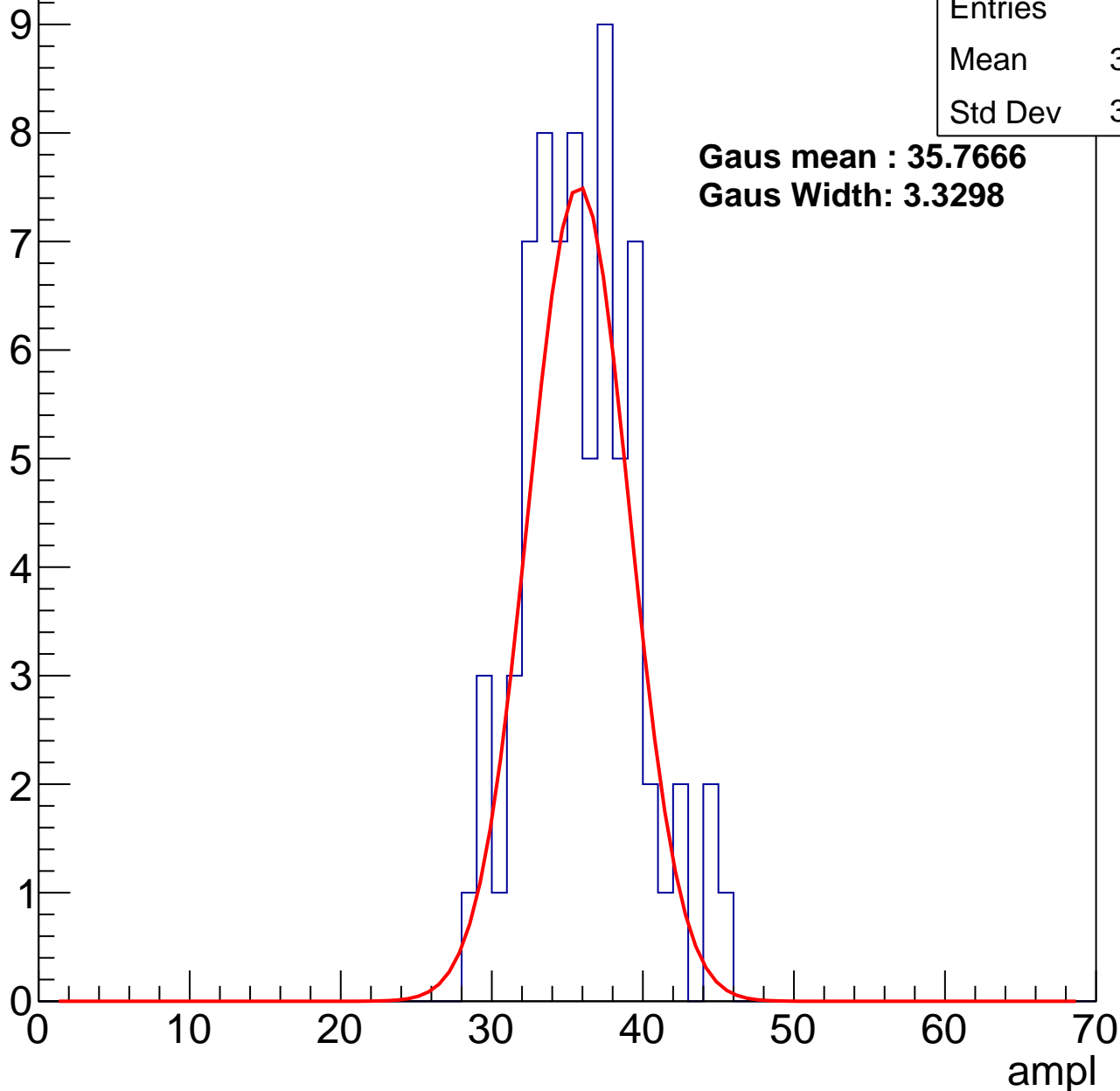
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	35.53
Std Dev	3.644

Gaus mean : 35.7666

Gaus Width: 3.3298



B1L100S, U19-ch6, adc2

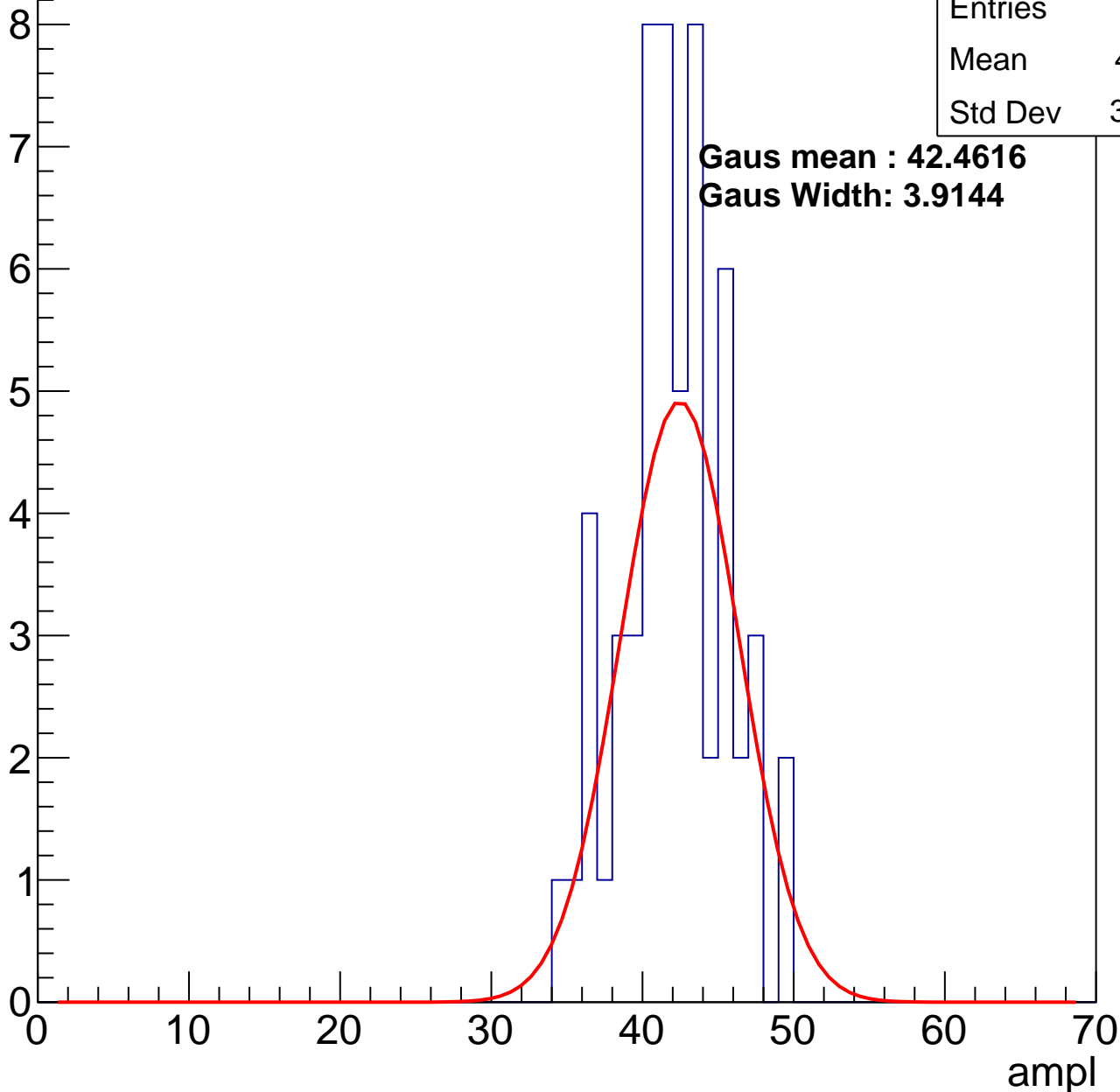
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	41.61
Std Dev	3.412

Gaus mean : 42.4616

Gaus Width: 3.9144



B1L100S, U19-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

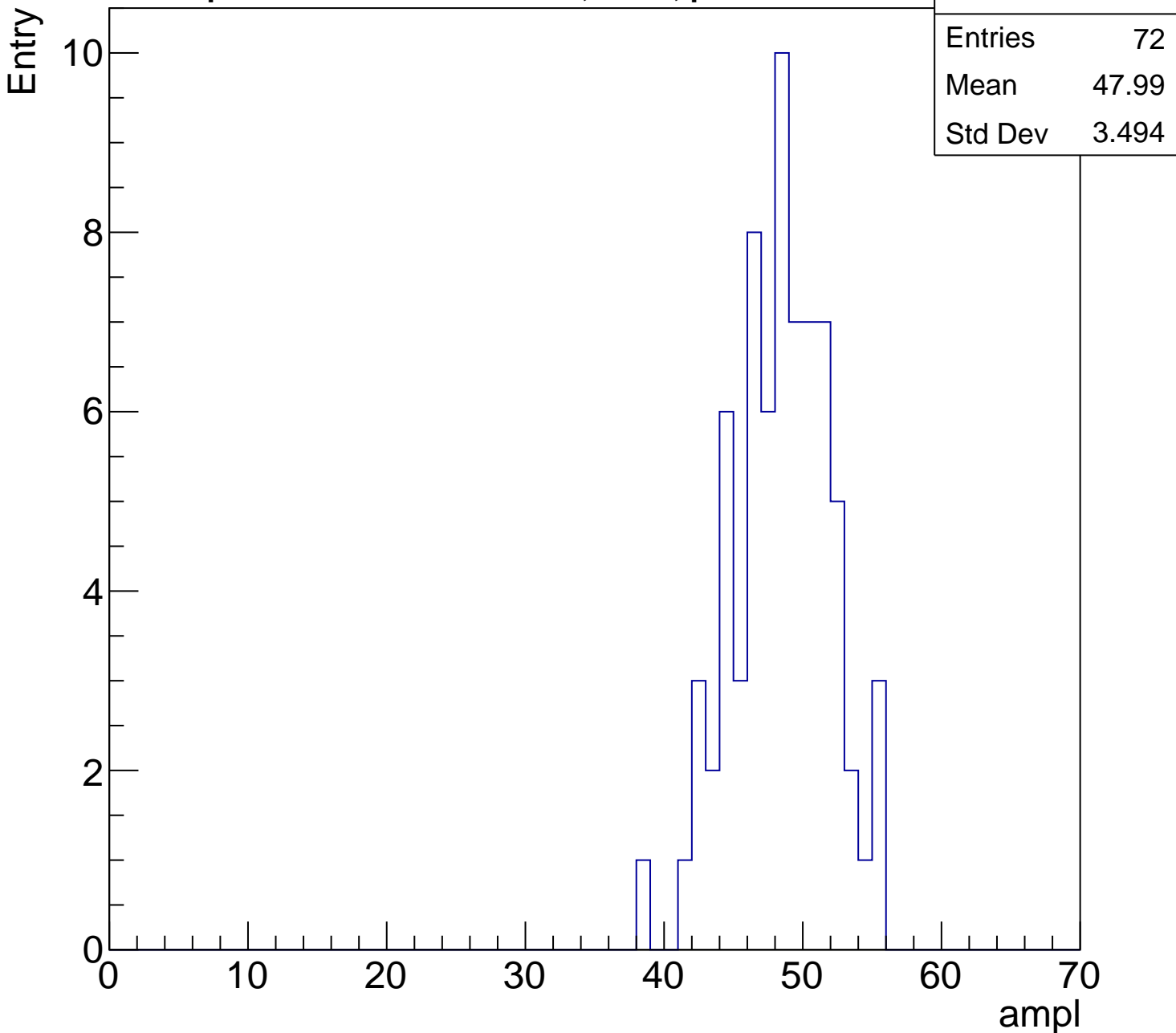
Entries	72
Mean	47.99
Std Dev	3.494

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

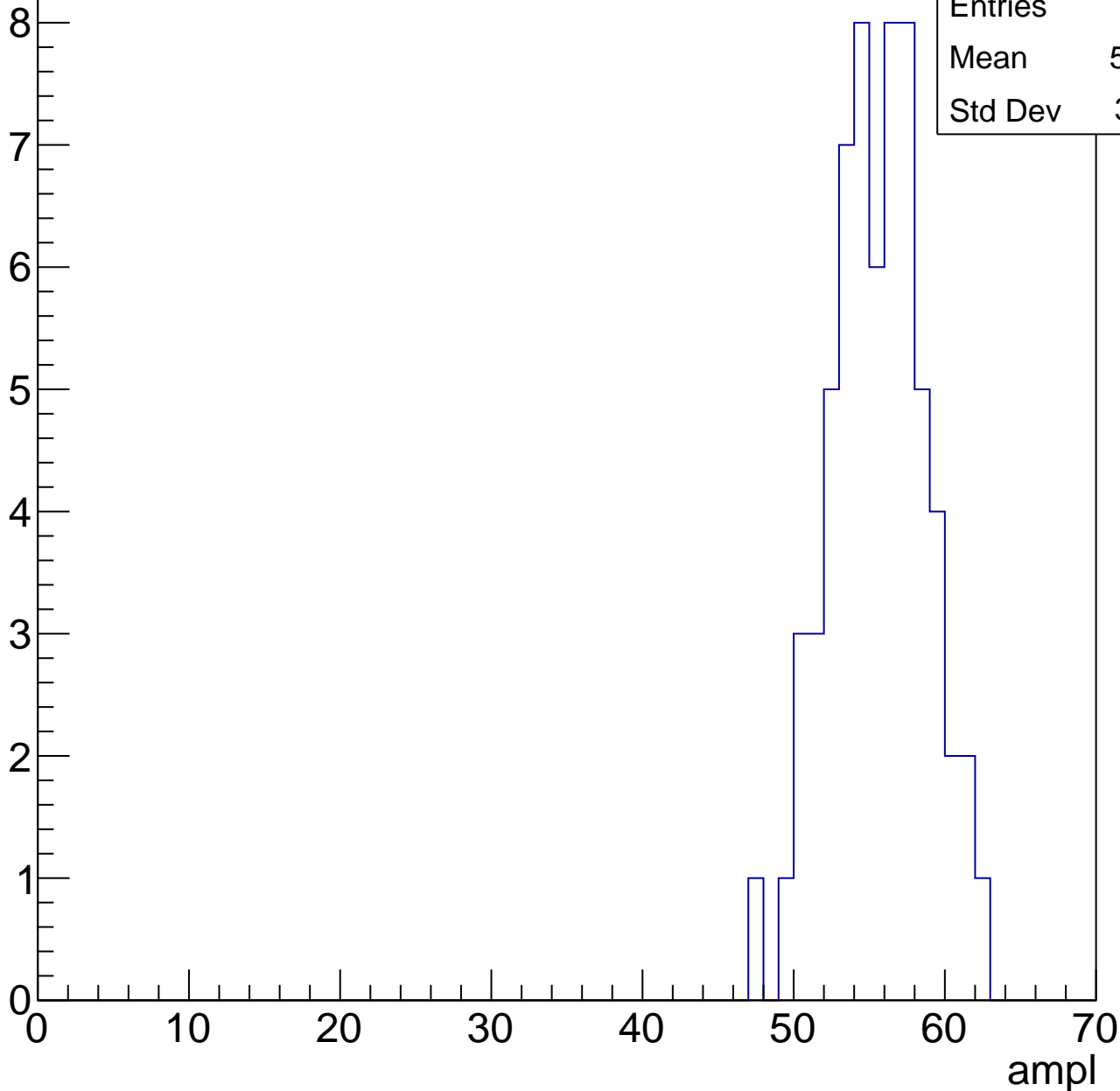


B1L100S, U19-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.09
Std Dev	3.121



B1L100S, U19-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

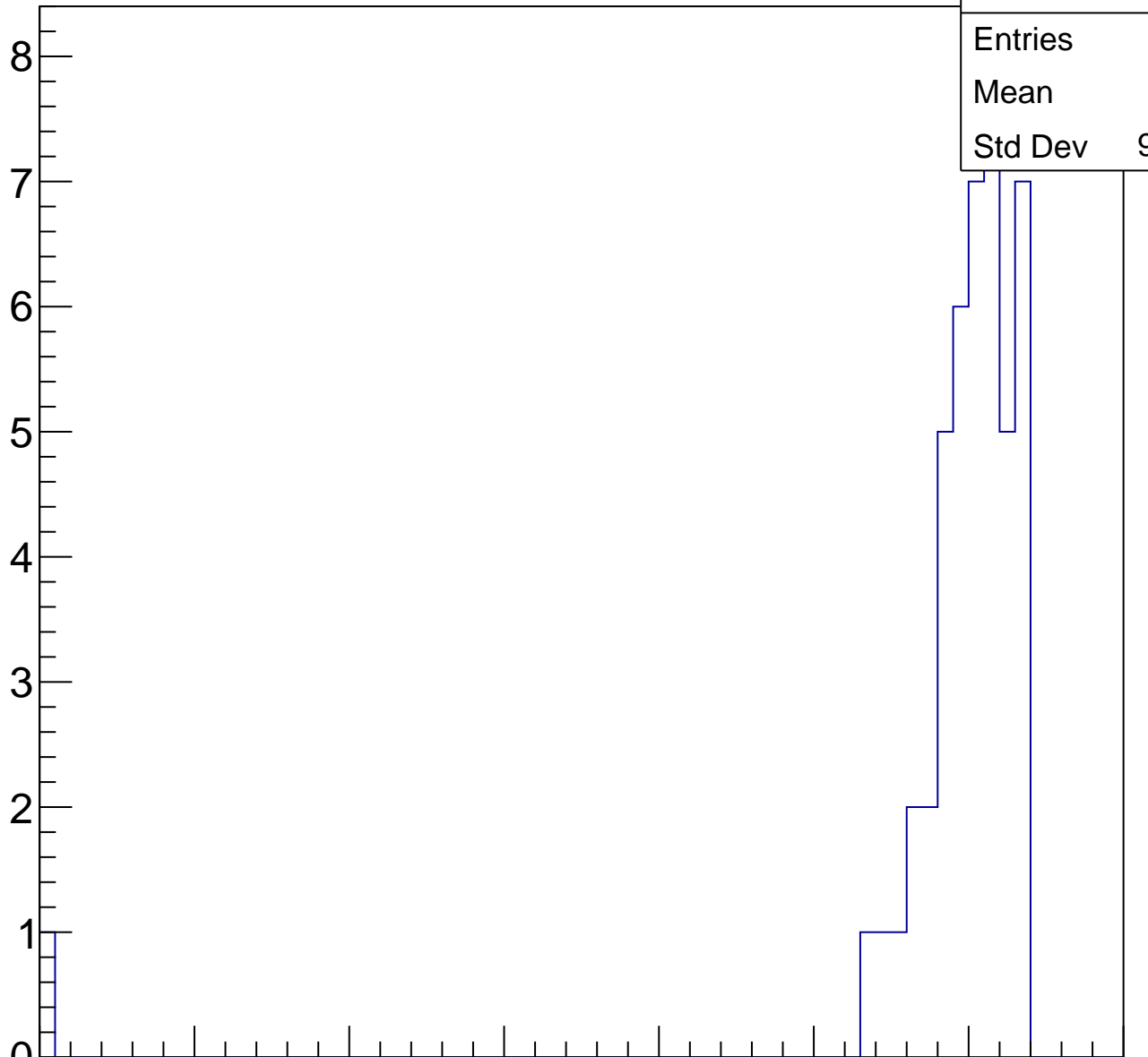
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.5
Std Dev	9.057

ampl

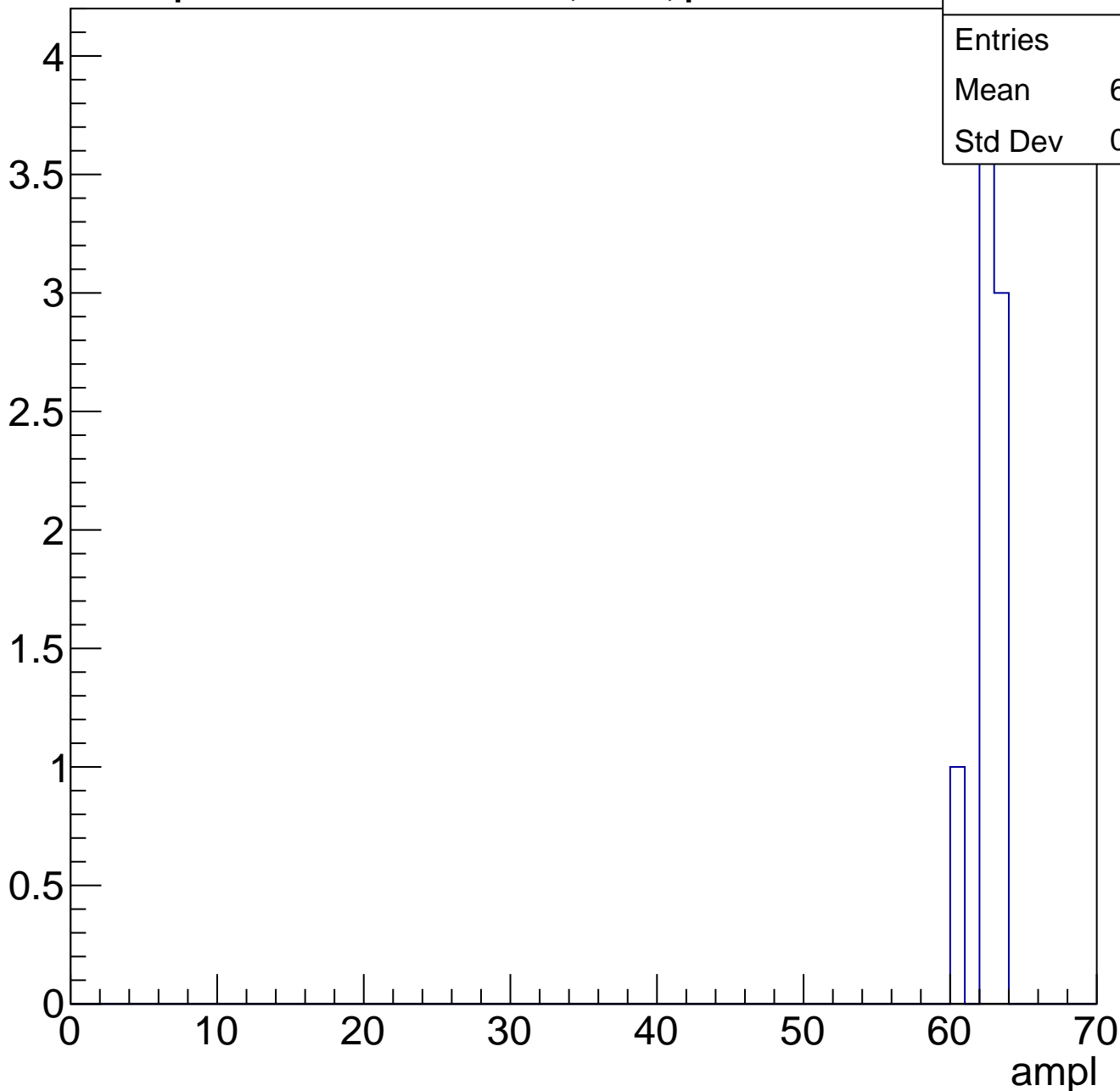
0 10 20 30 40 50 60 70



B1L100S, U19-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch7, adc0

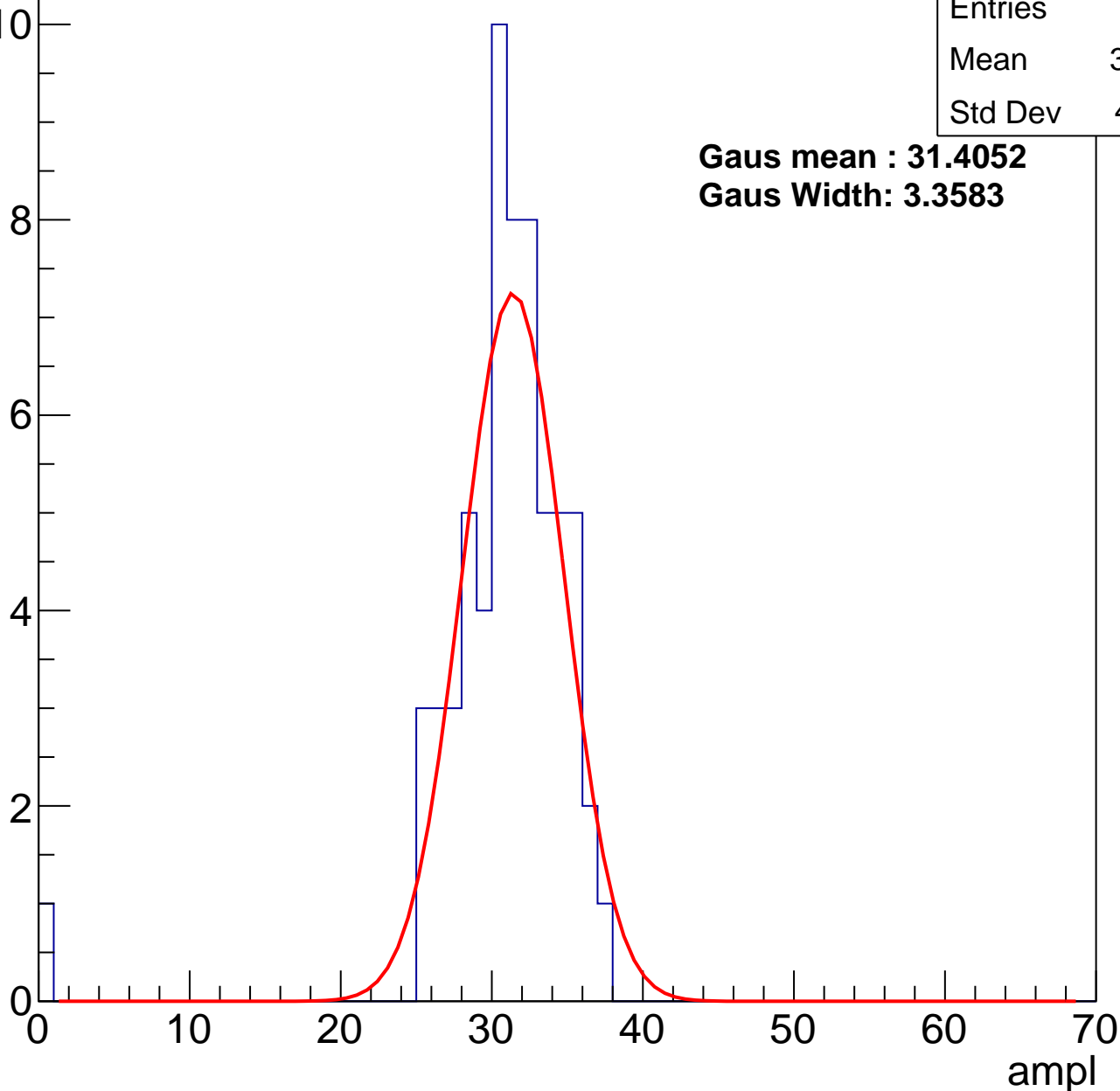
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	30.37
Std Dev	4.841

Gaus mean : 31.4052

Gaus Width: 3.3583



B1L100S, U19-ch7, adc1

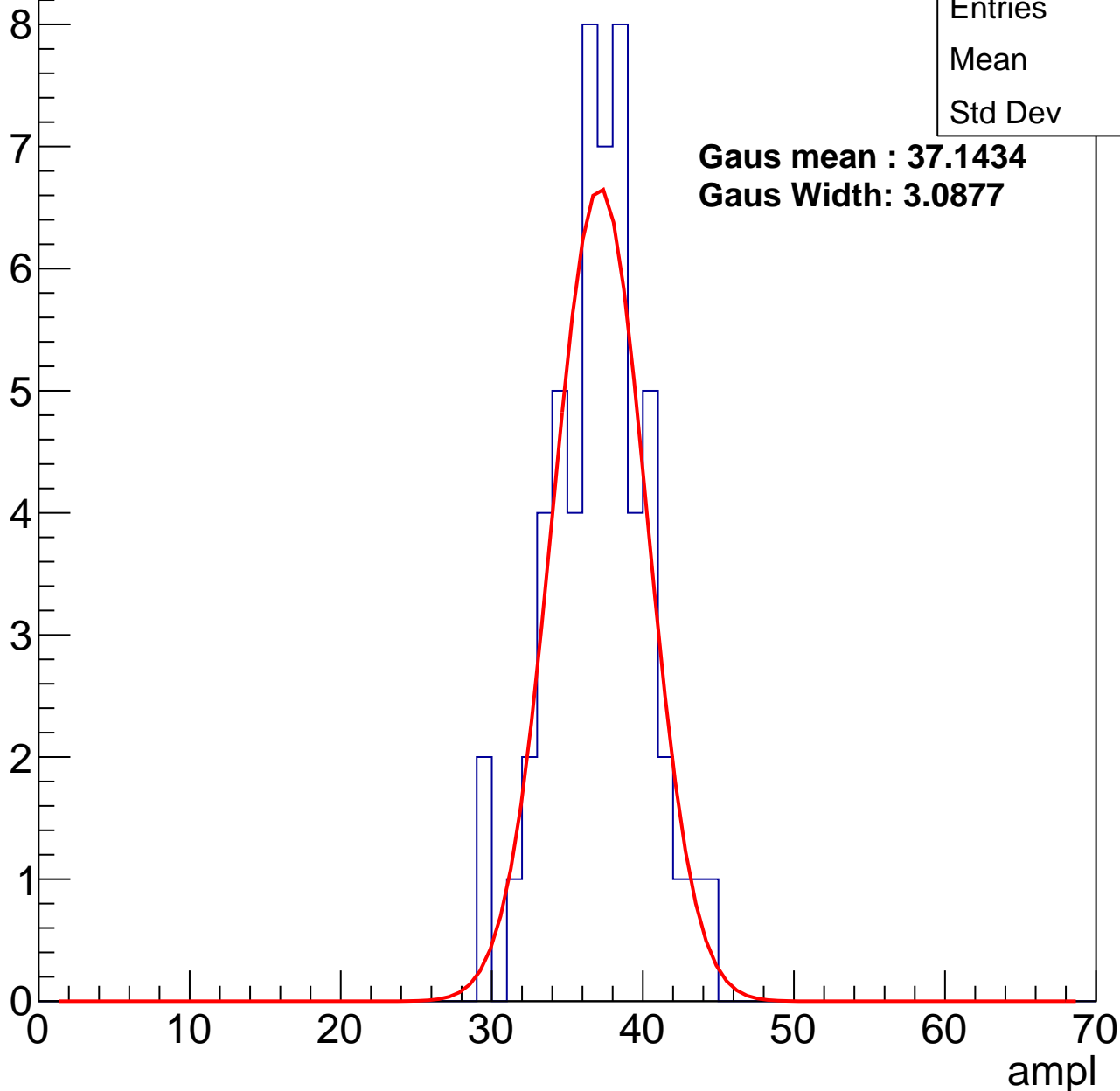
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	36.6
Std Dev	3.16

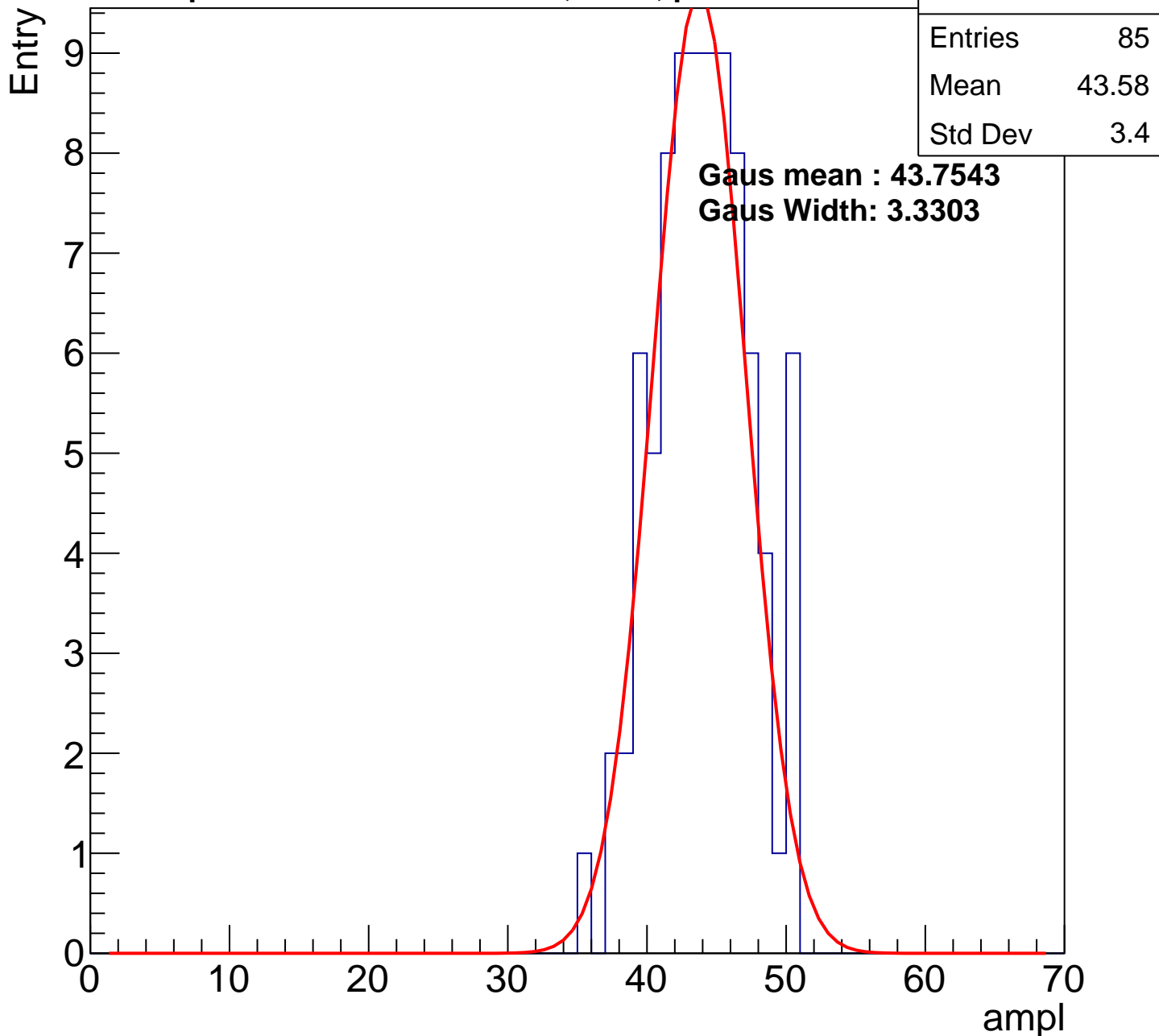
Gaus mean : 37.1434

Gaus Width: 3.0877



B1L100S, U19-ch7, adc2

calib_packv5_042523_0143.root, FC#4, port A2

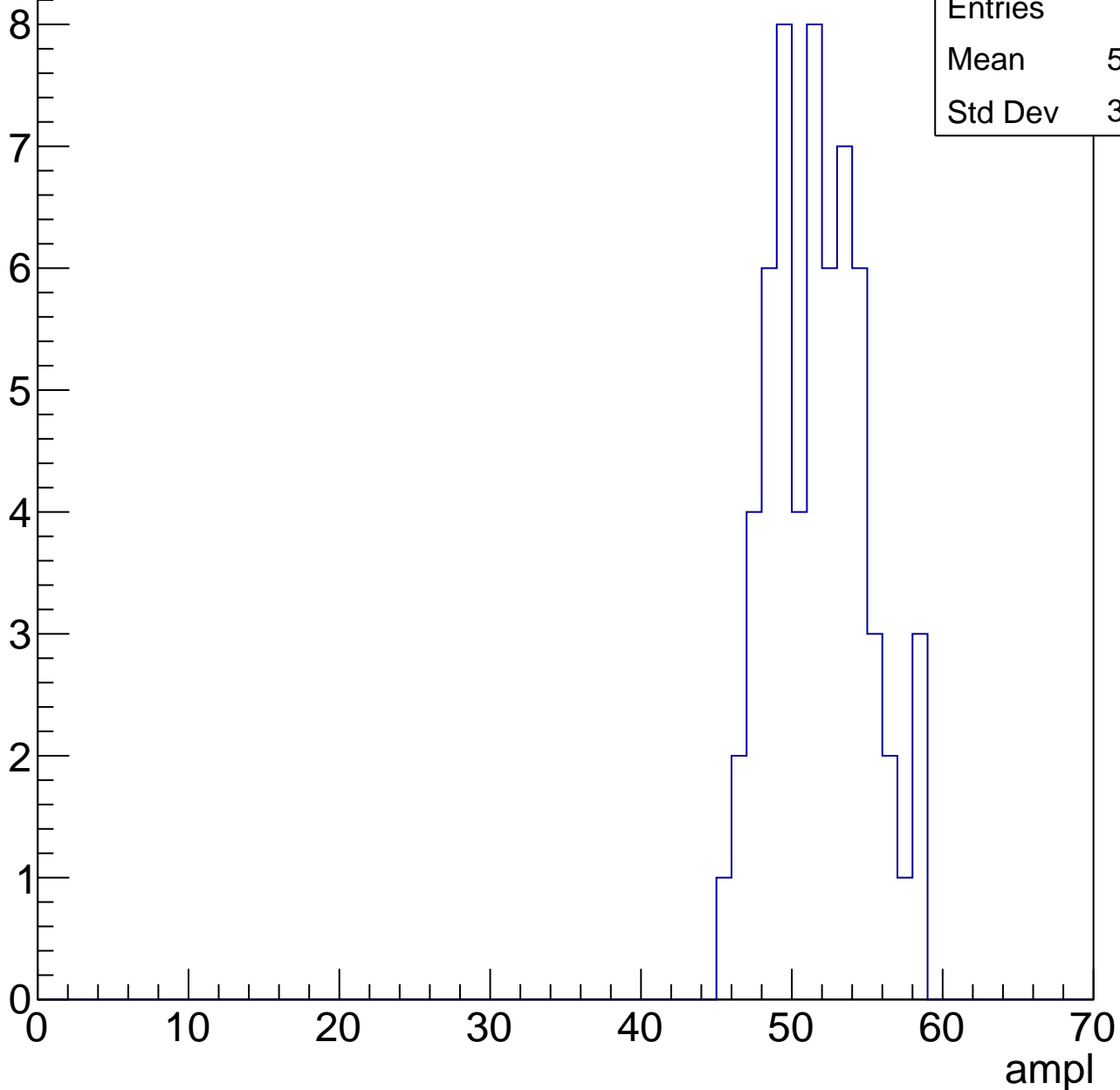


B1L100S, U19-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	51.28
Std Dev	3.147



B1L100S, U19-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

ampl

Entries	59
Mean	56.76
Std Dev	3.126

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

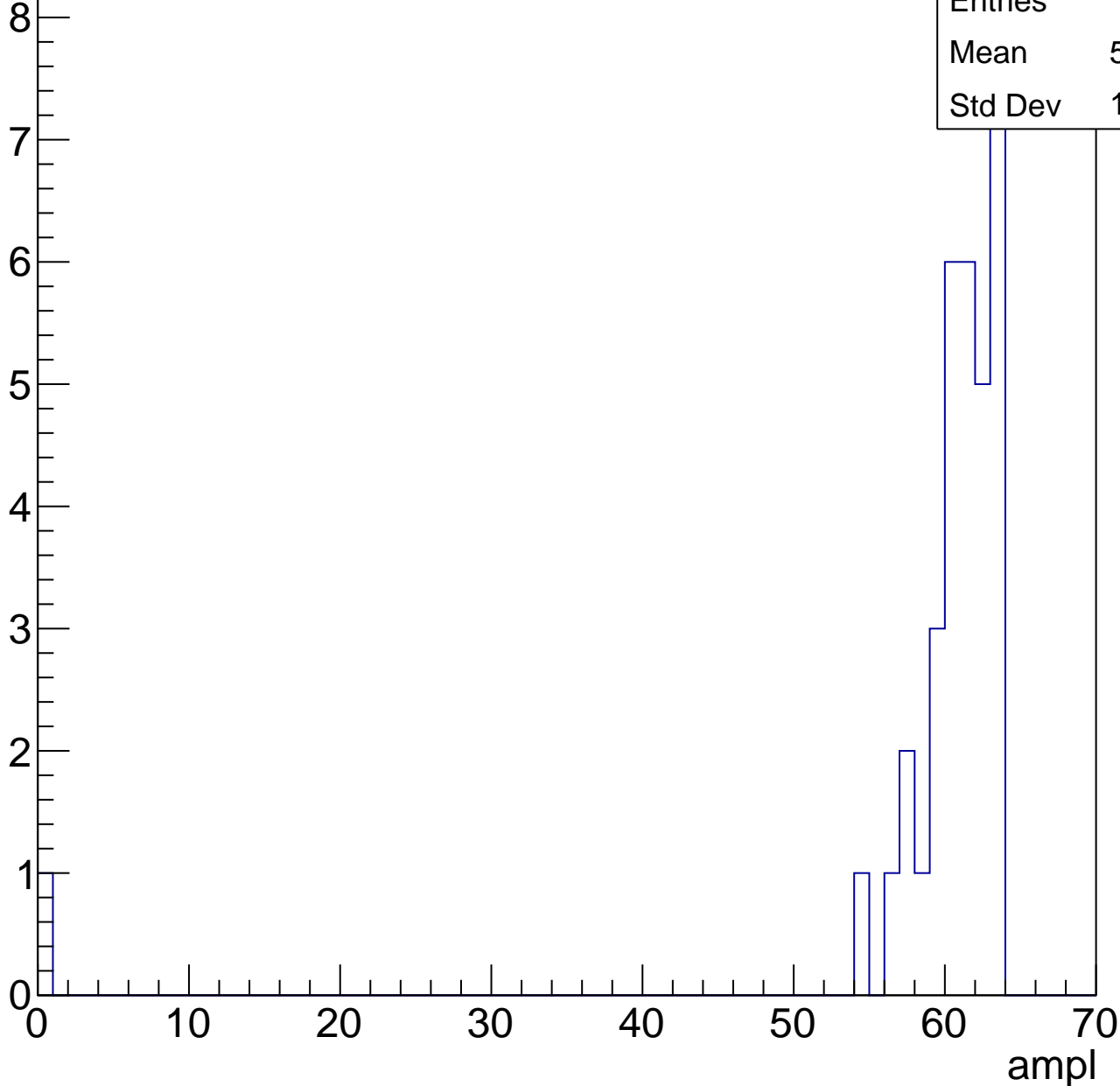
ampl

B1L100S, U19-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.79
Std Dev	10.47



B1L100S, U19-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch8, adc0

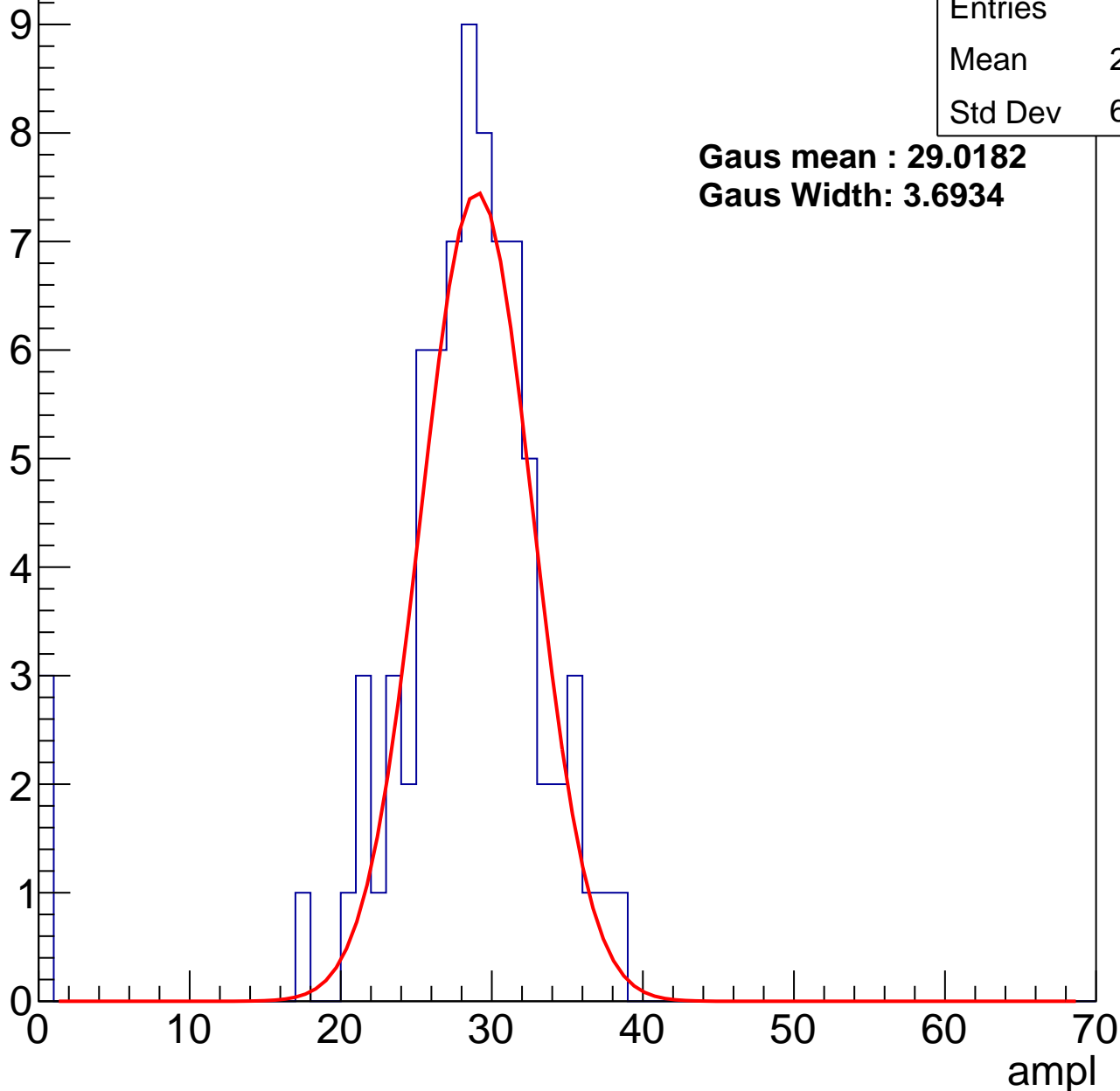
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	27.28
Std Dev	6.717

Gaus mean : 29.0182

Gaus Width: 3.6934



B1L100S, U19-ch8, adc1

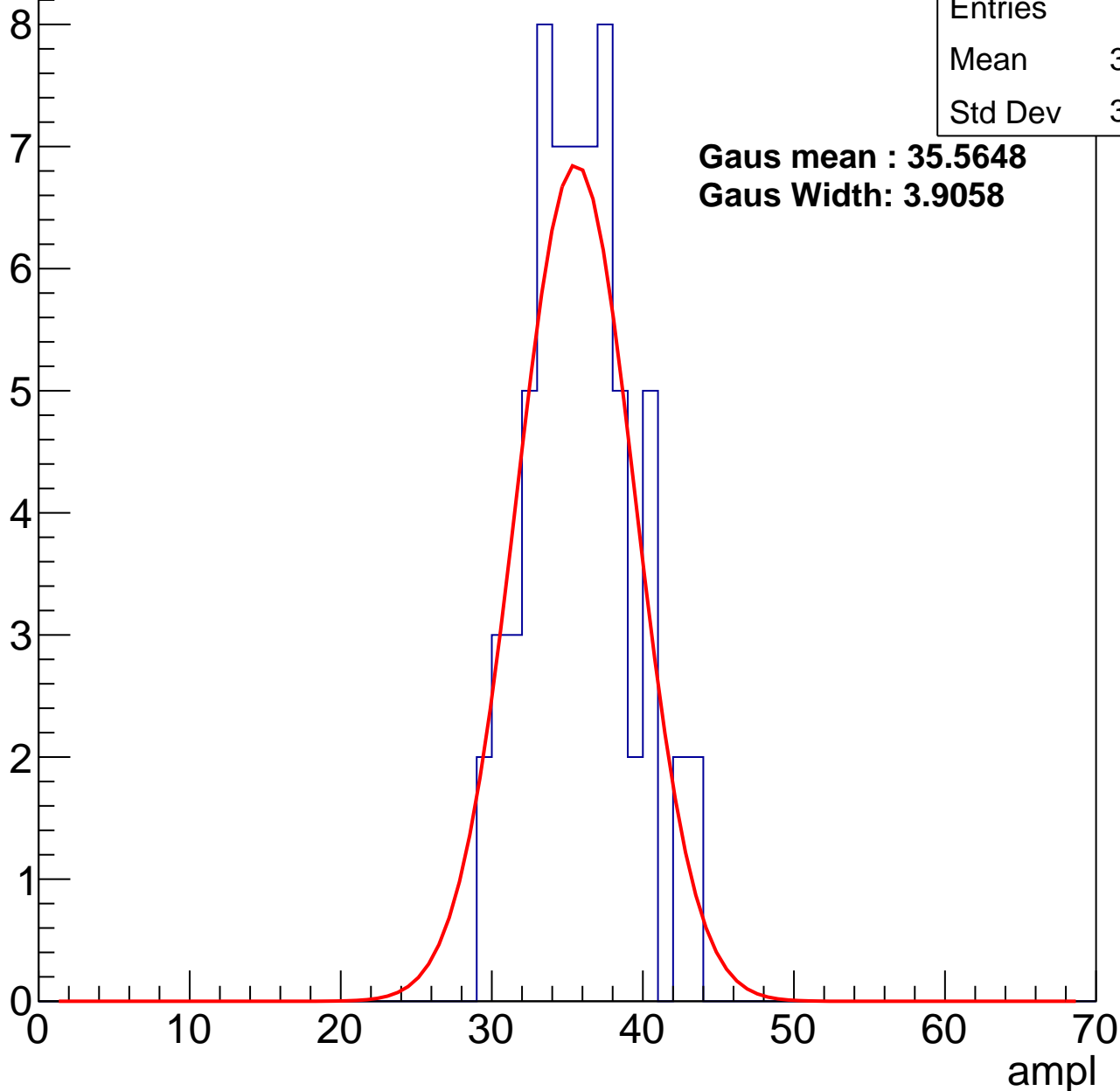
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	35.36
Std Dev	3.342

Gaus mean : 35.5648

Gaus Width: 3.9058



B1L100S, U19-ch8, adc2

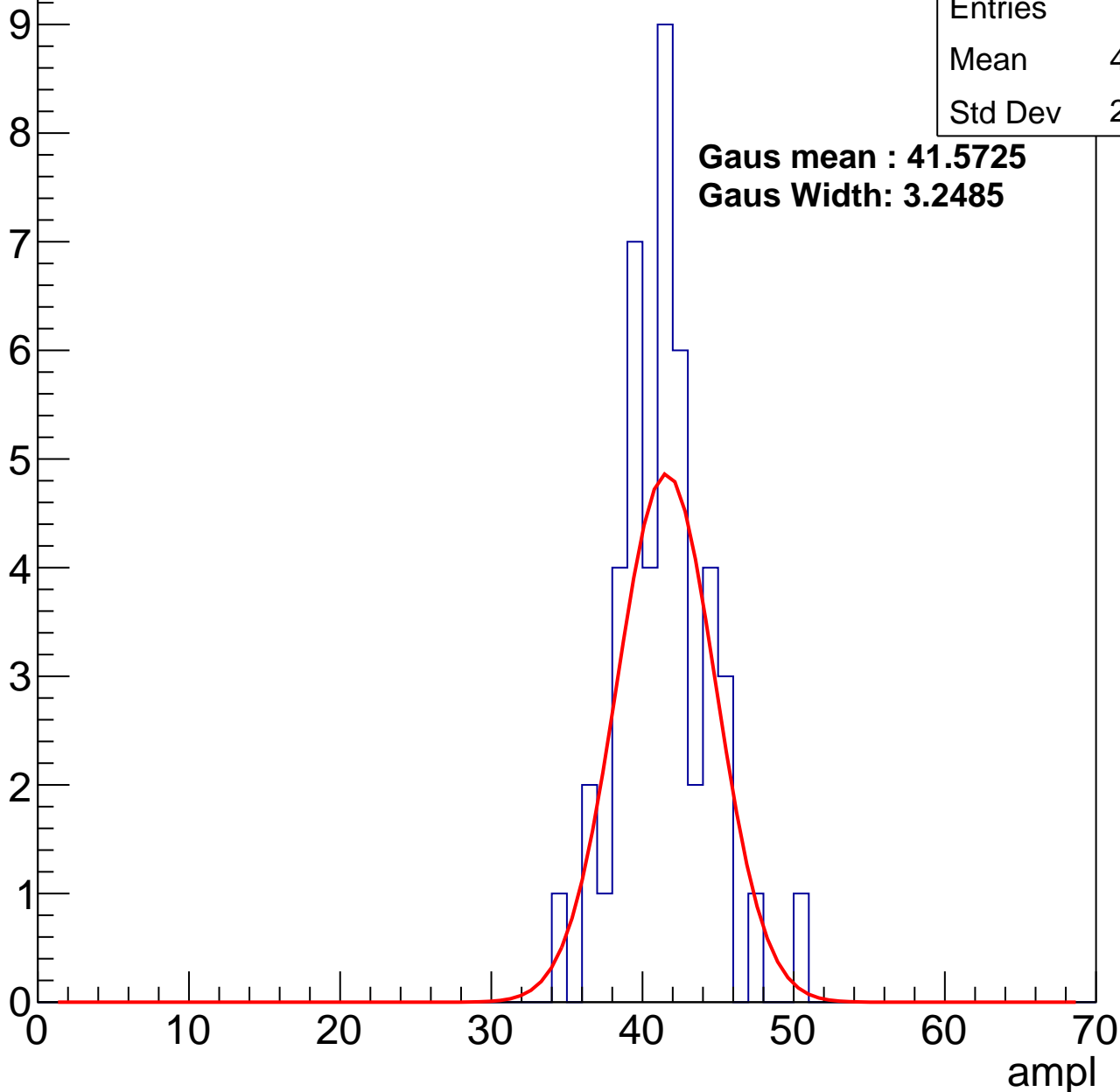
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	40.96
Std Dev	2.974

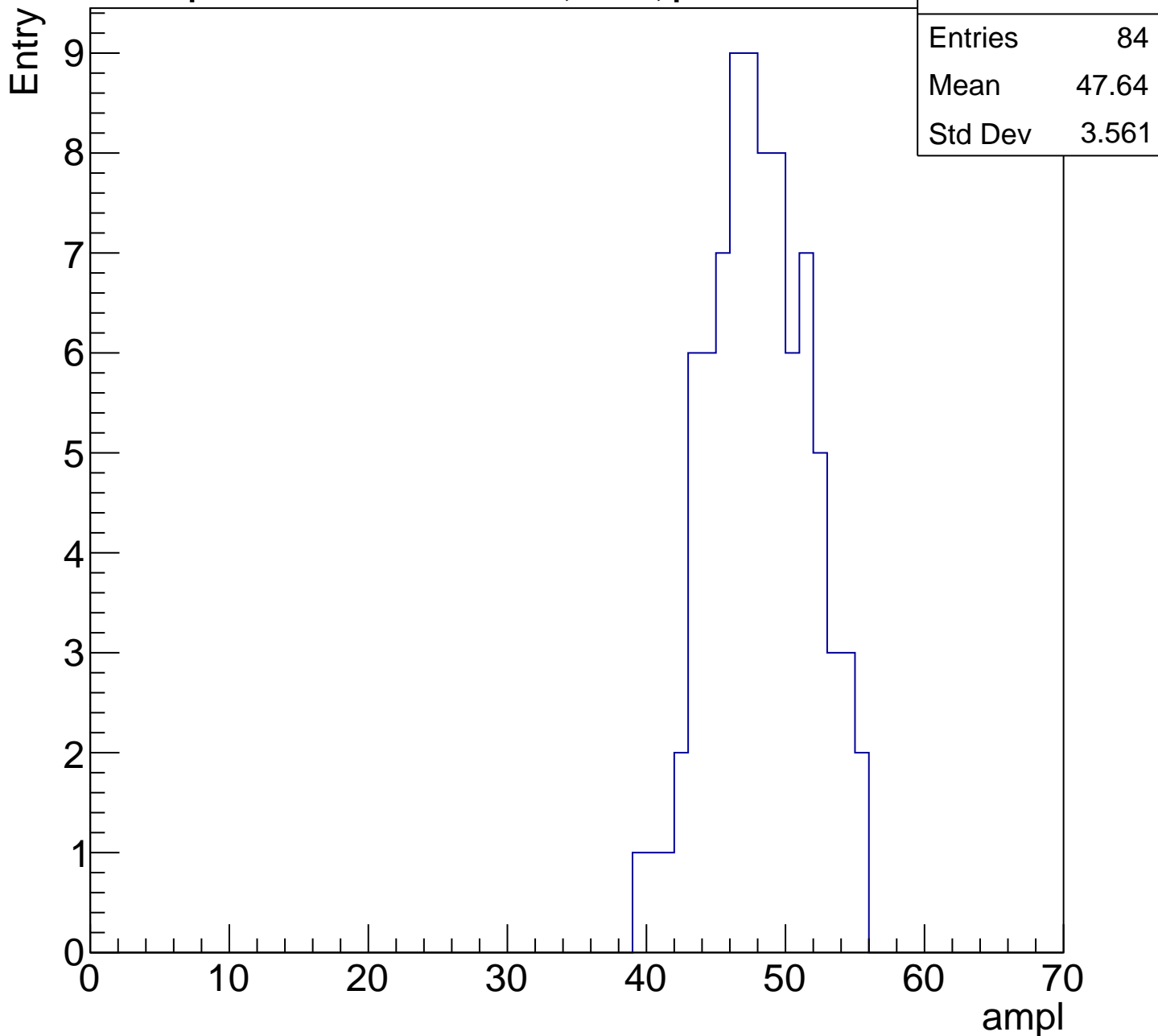
Gaus mean : 41.5725

Gaus Width: 3.2485



B1L100S, U19-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

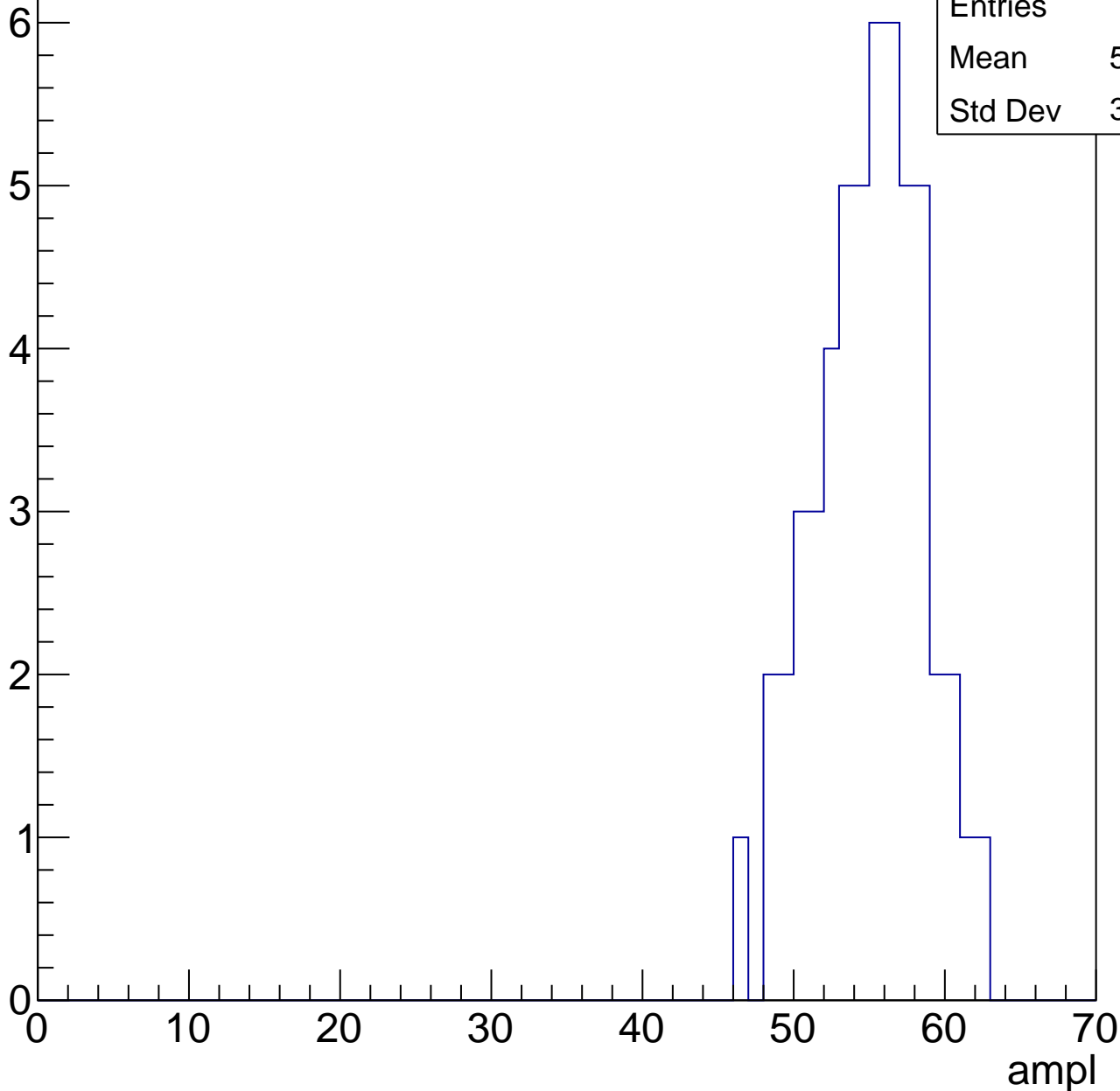


B1L100S, U19-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	54.49
Std Dev	3.527

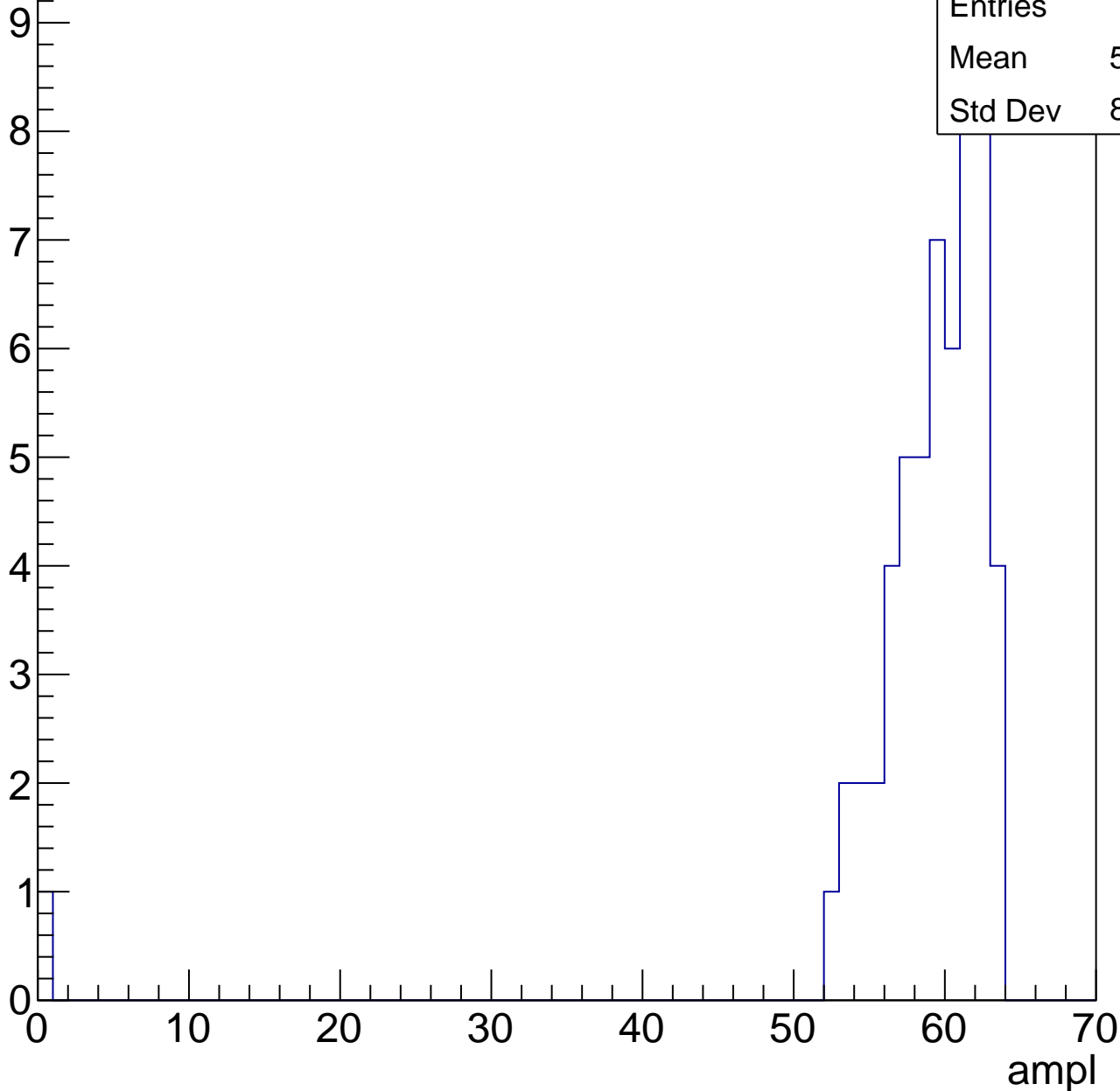


B1L100S, U19-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.95
Std Dev	8.303

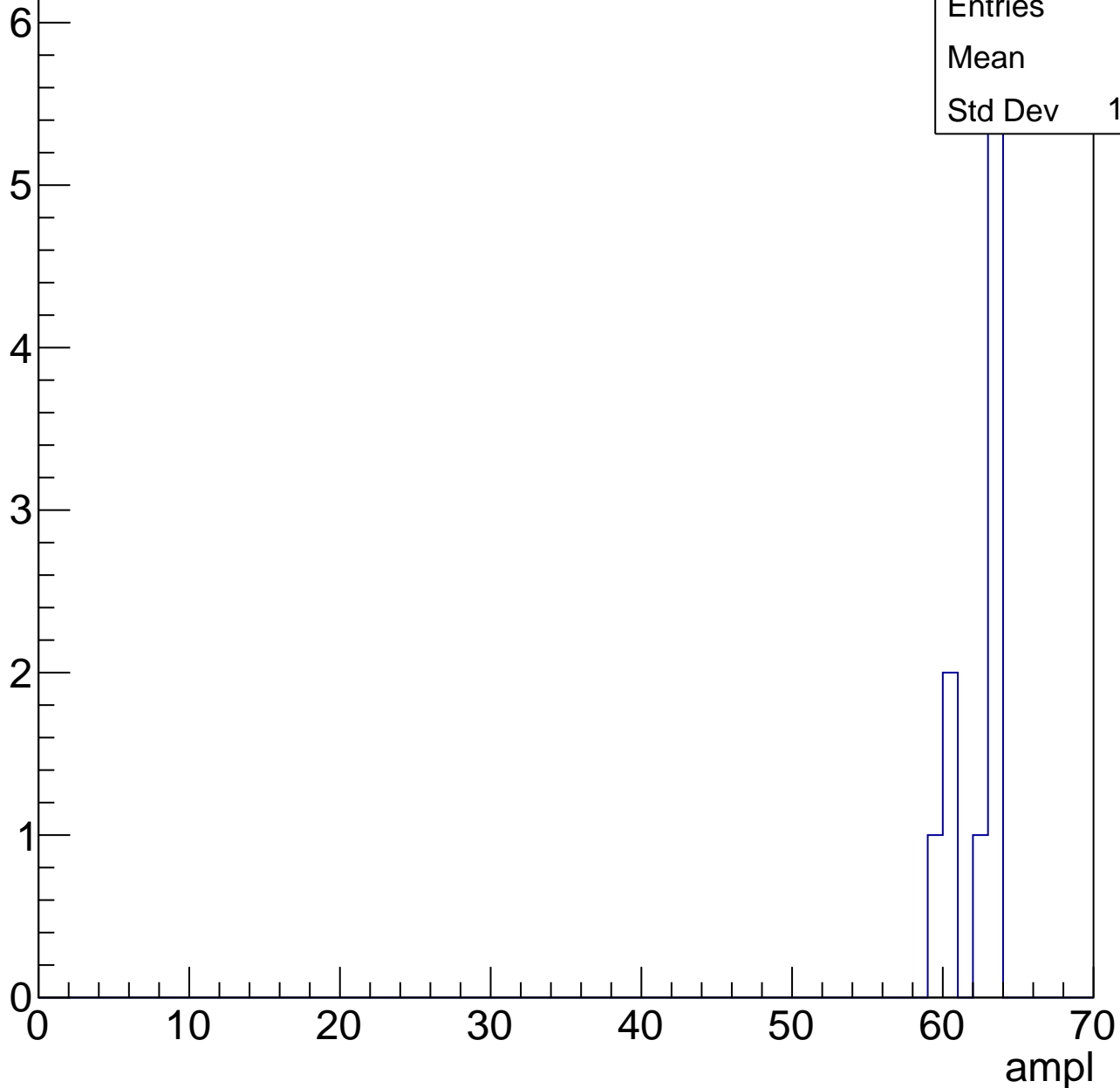


B1L100S, U19-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	61.9
Std Dev	1.513



B1L100S, U19-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch9, adc0

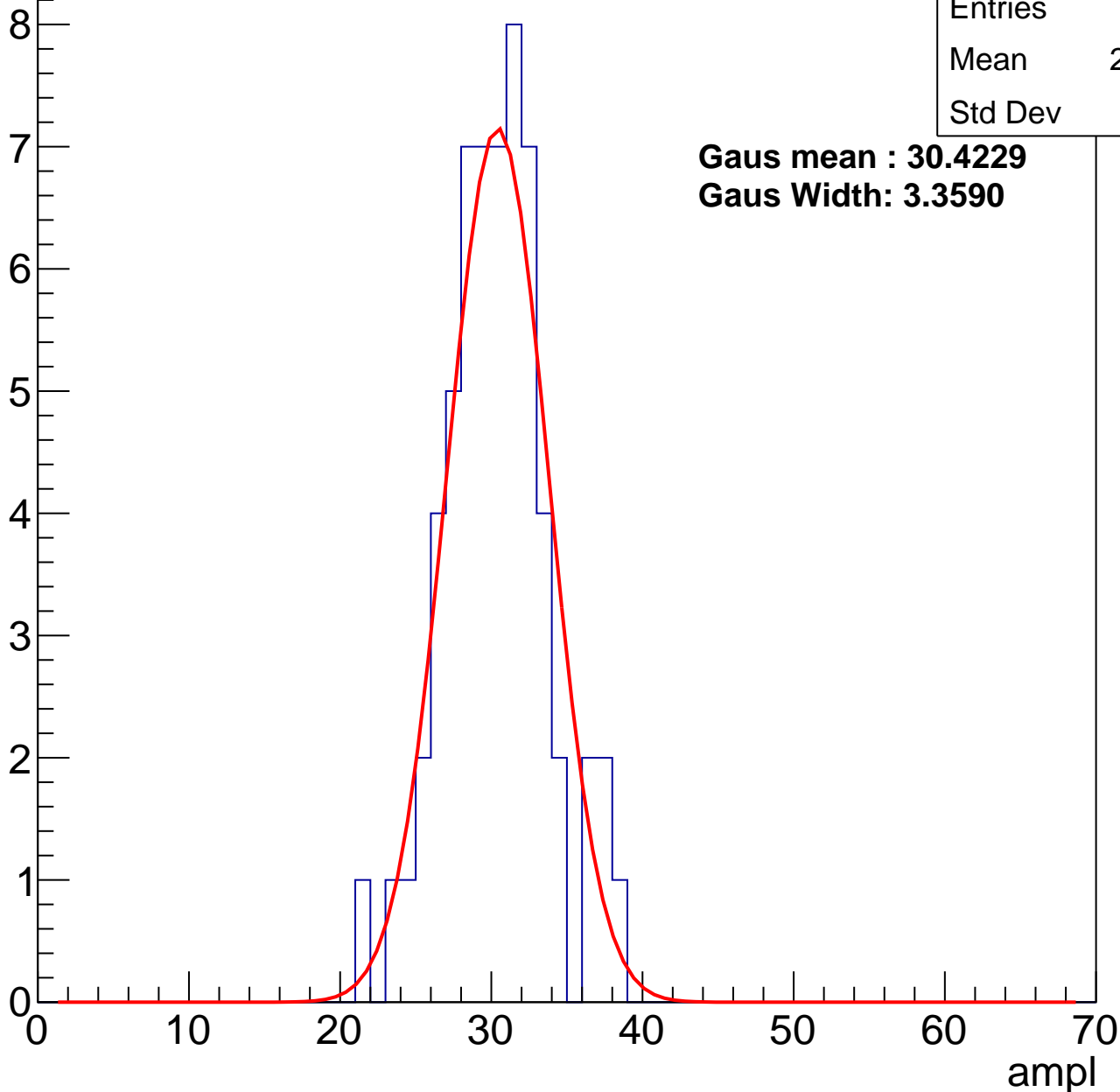
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.87
Std Dev	3.38

Gaus mean : 30.4229

Gaus Width: 3.3590



B1L100S, U19-ch9, adc1

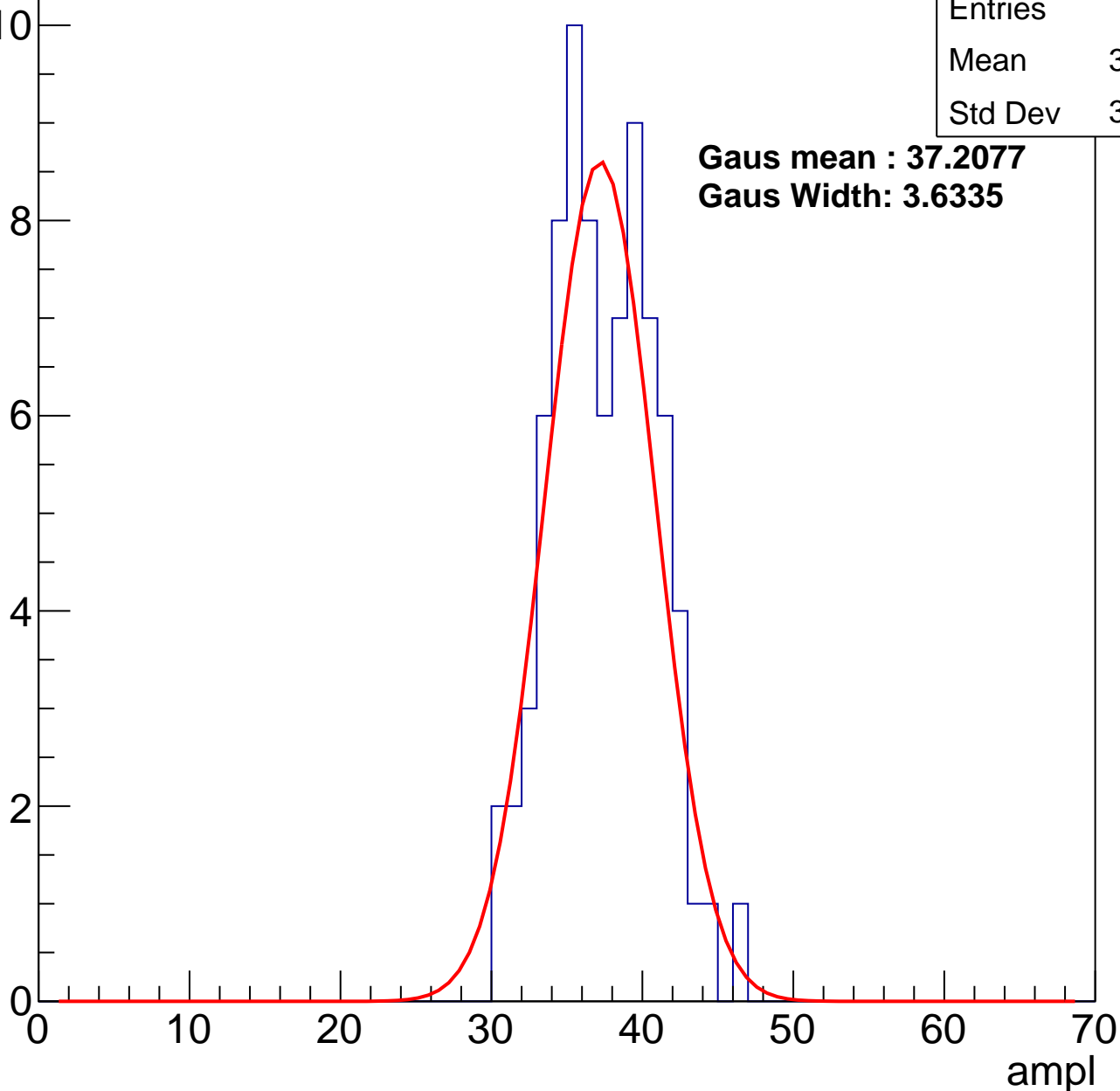
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.94
Std Dev	3.386

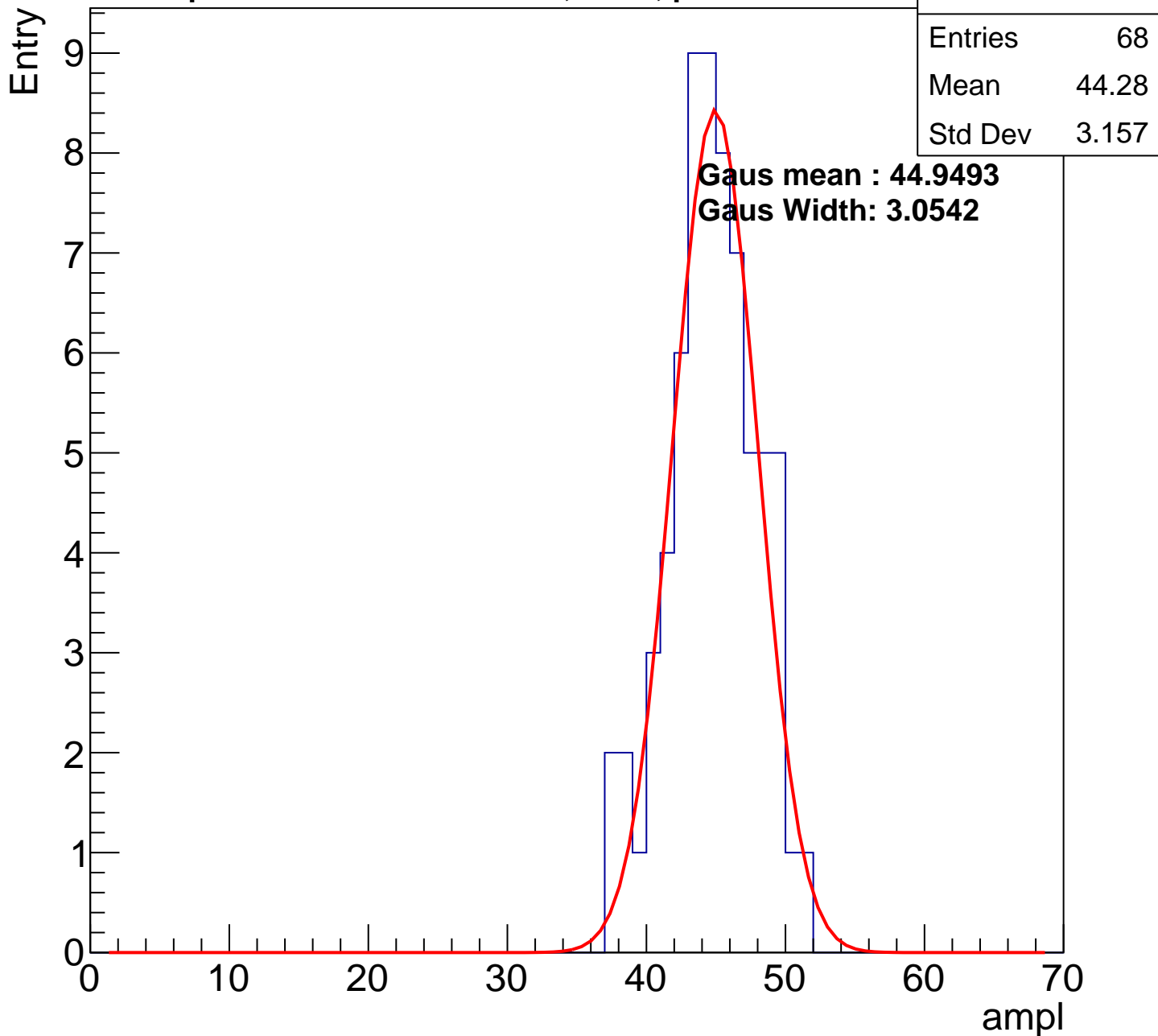
Gaus mean : 37.2077

Gaus Width: 3.6335



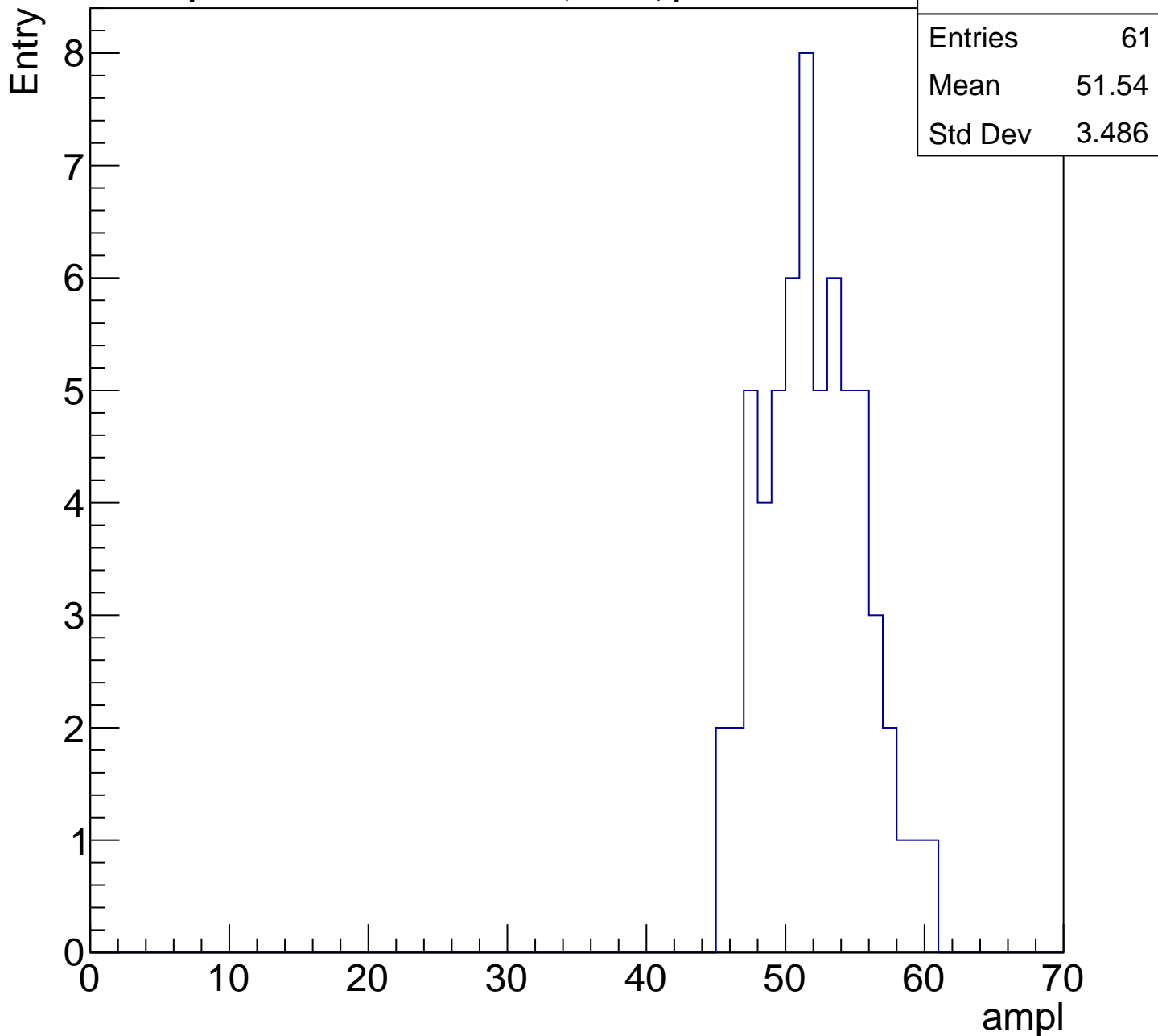
B1L100S, U19-ch9, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

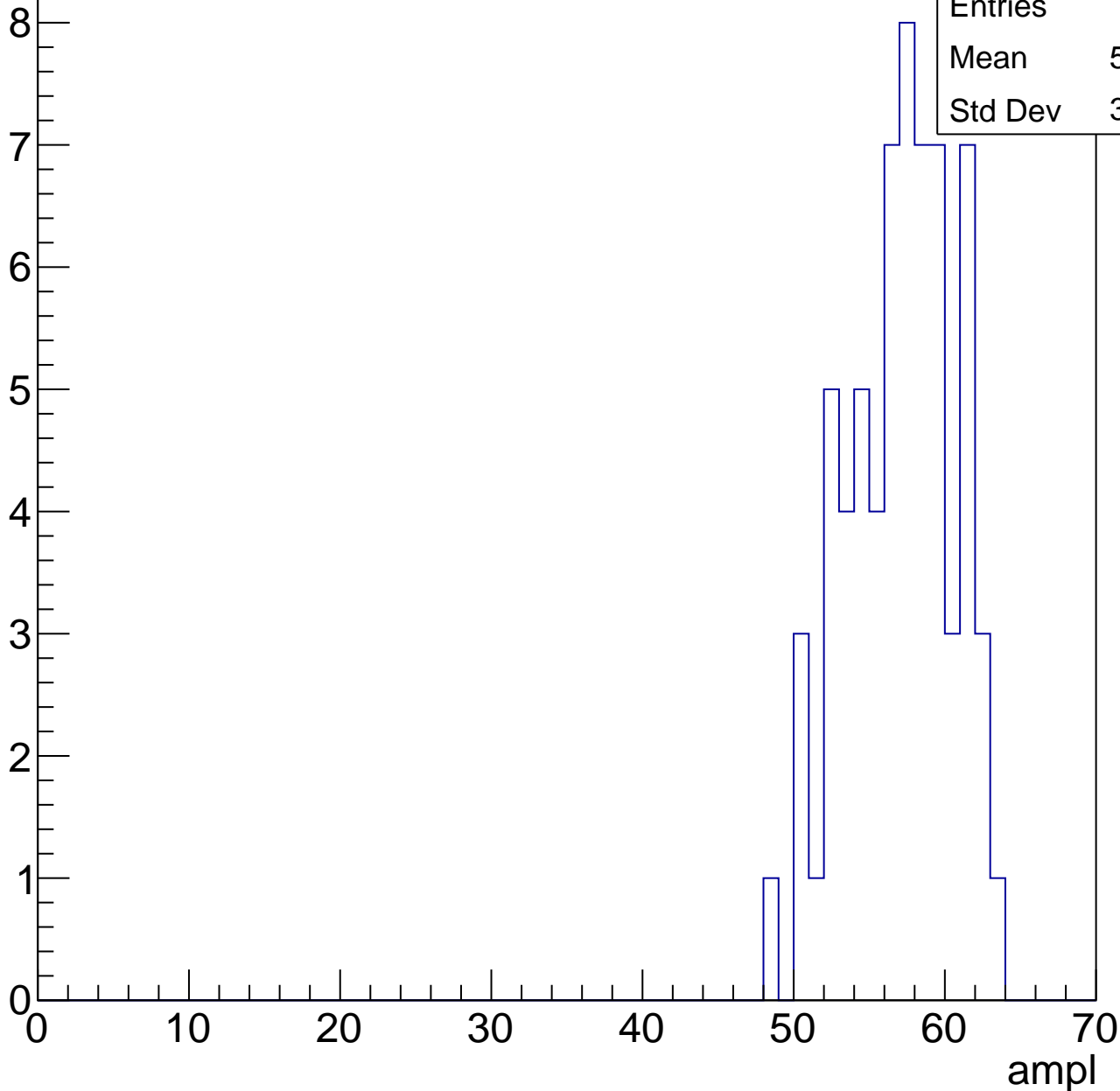


B1L100S, U19-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	56.58
Std Dev	3.469

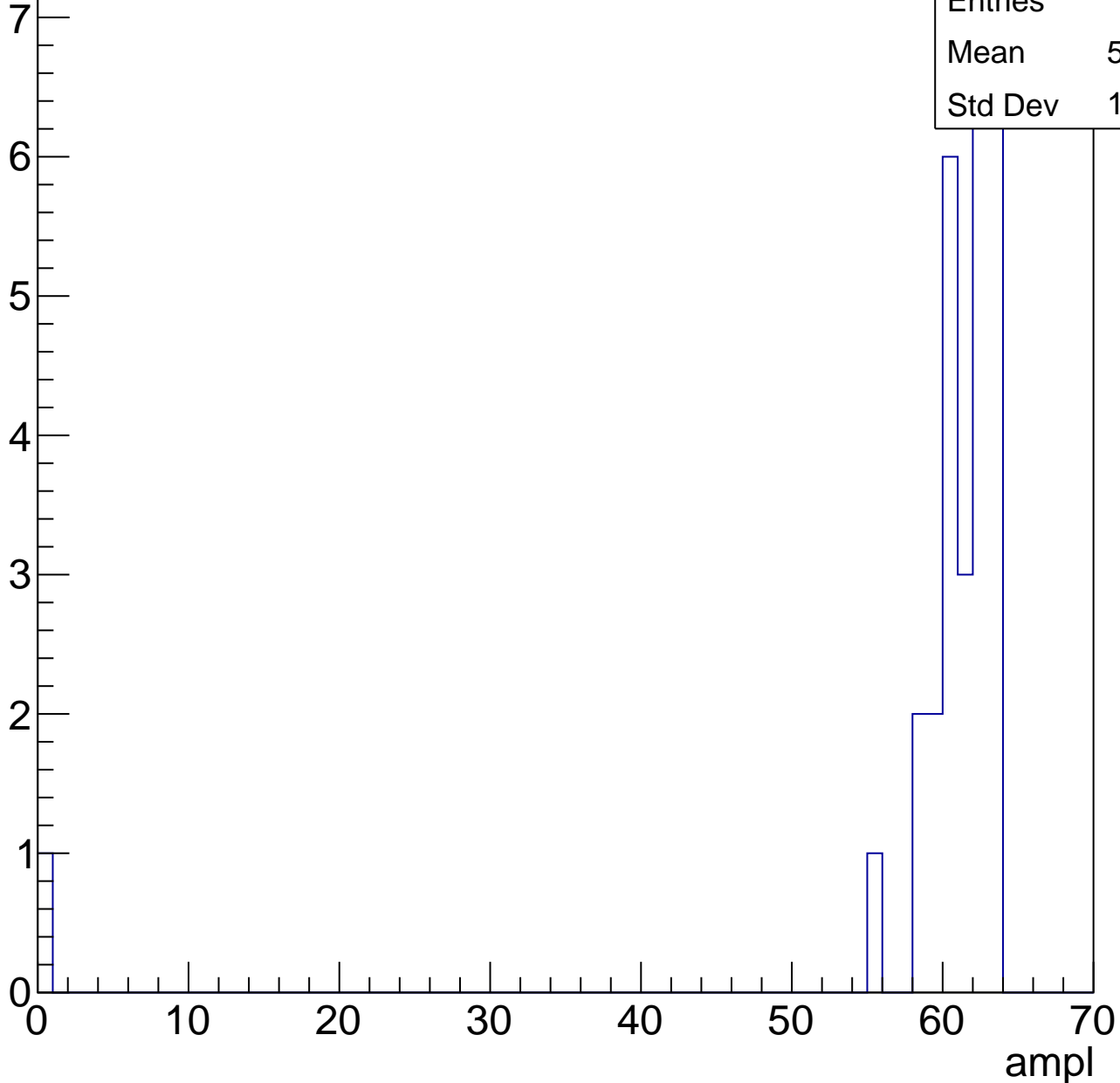


B1L100S, U19-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.86
Std Dev	11.28



B1L100S, U19-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U19-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch10, adc0

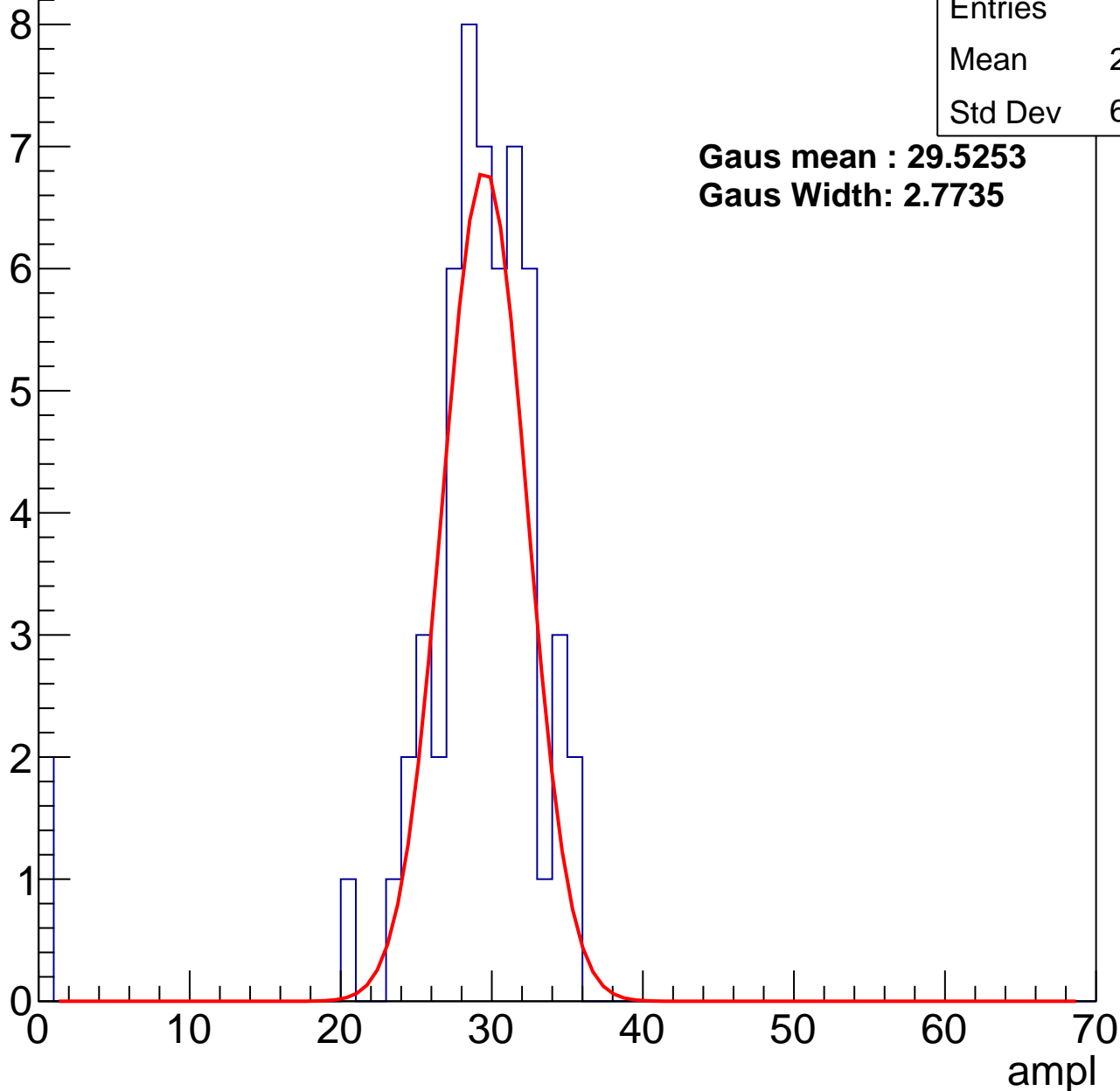
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	28.09
Std Dev	6.142

Gaus mean : 29.5253

Gaus Width: 2.7735



B1L100S, U19-ch10, adc1

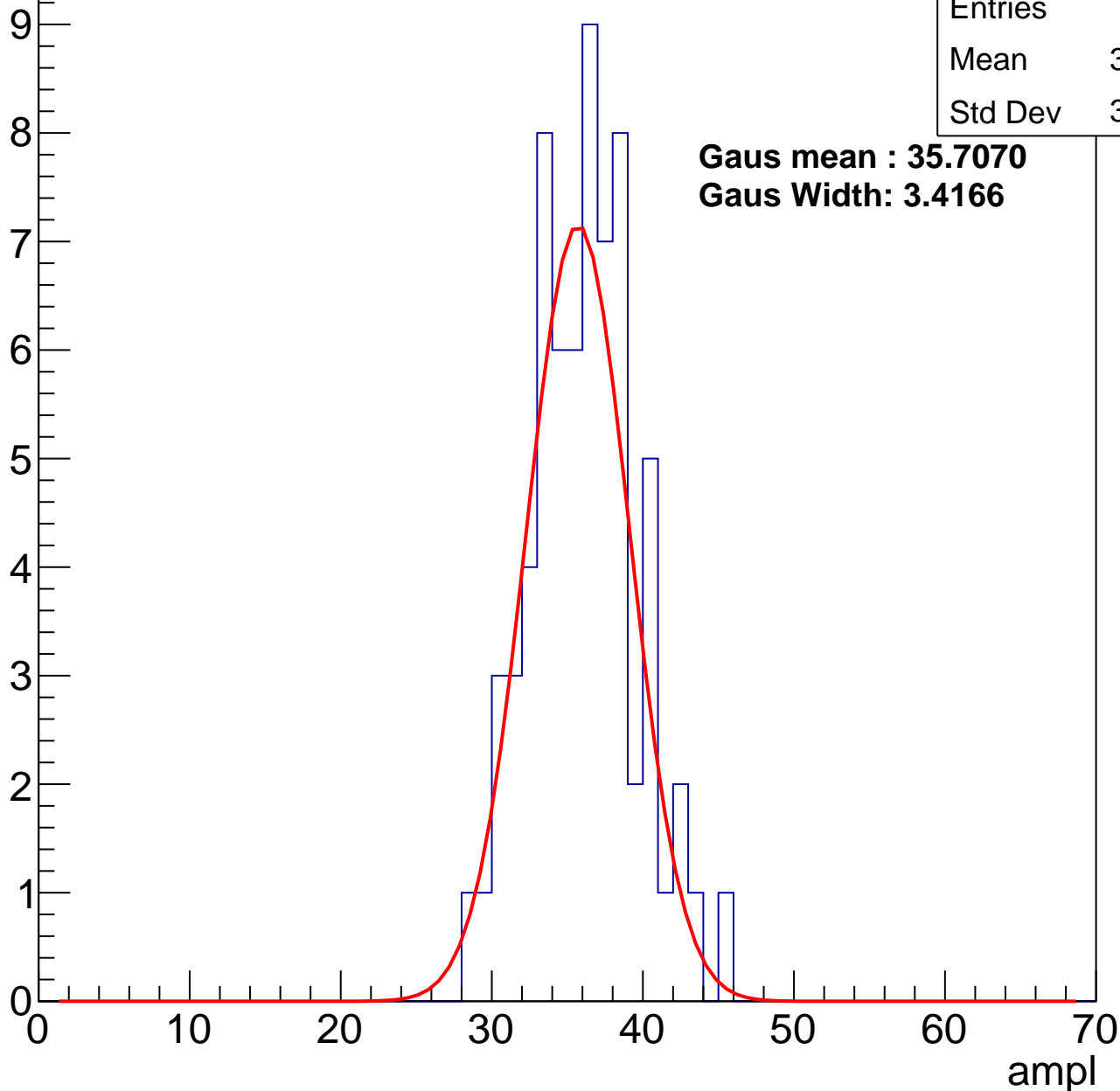
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.65
Std Dev	3.467

Gaus mean : 35.7070

Gaus Width: 3.4166



B1L100S, U19-ch10, adc2

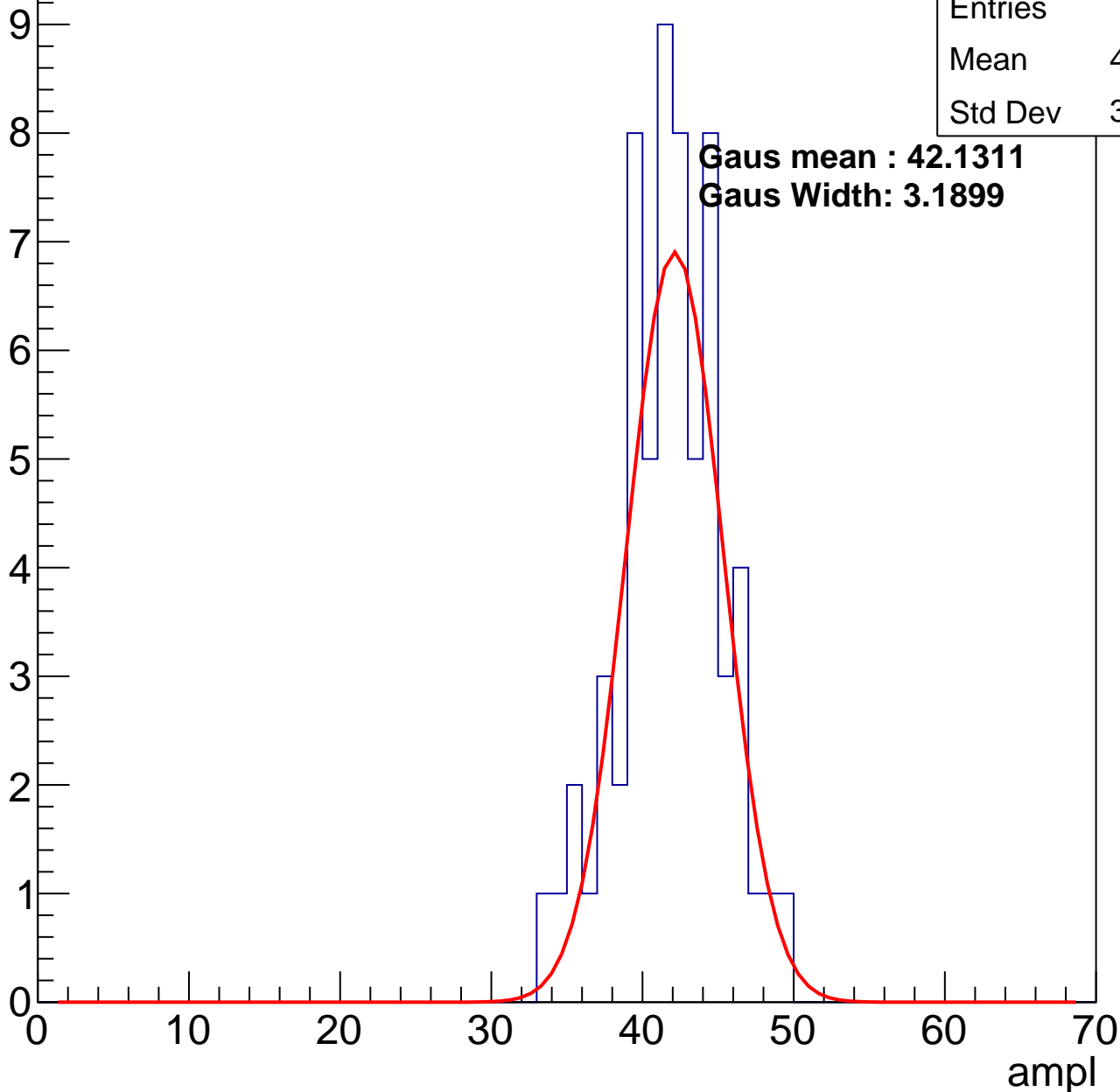
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	41.38
Std Dev	3.335

Gaus mean : 42.1311

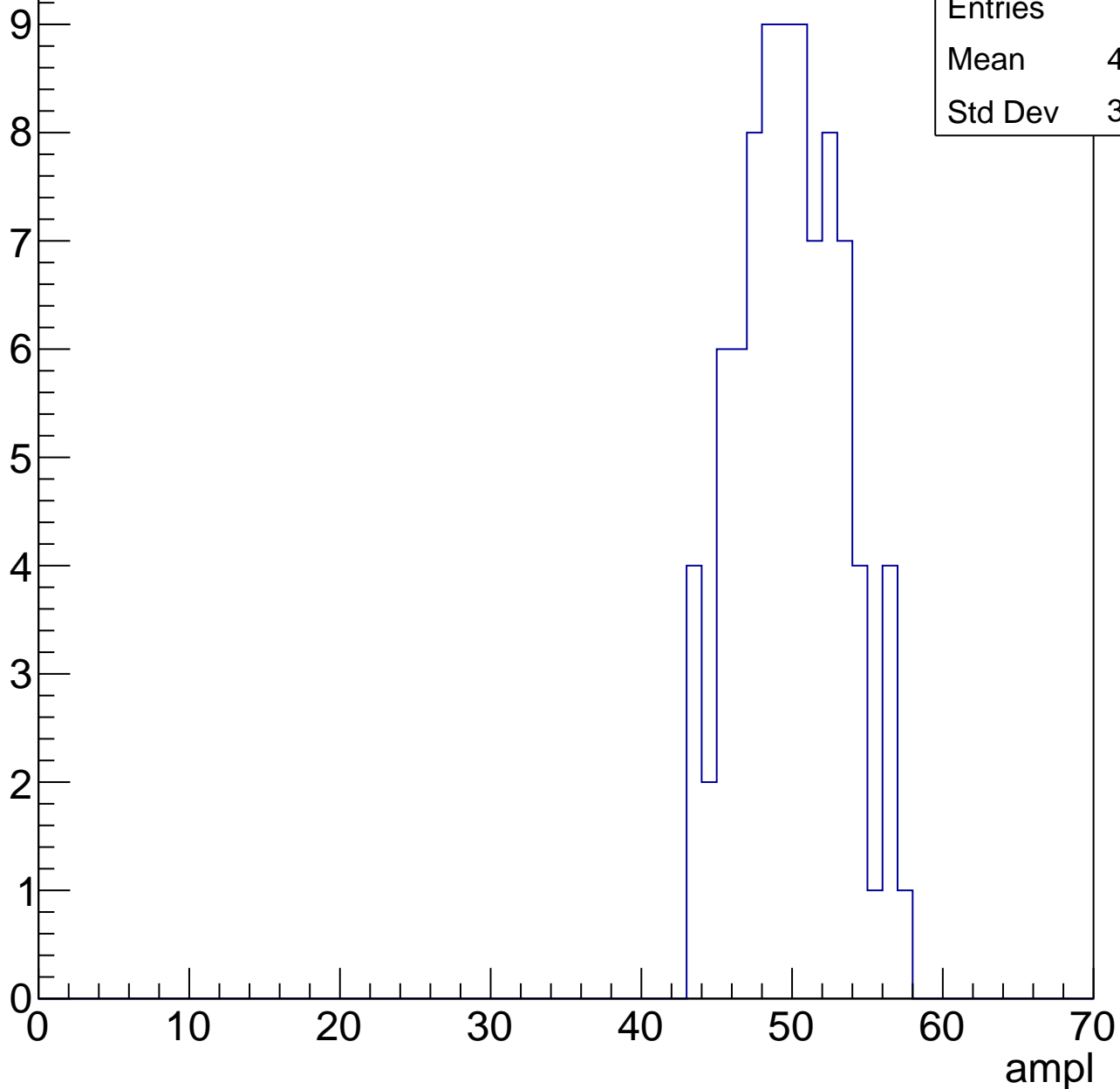
Gaus Width: 3.1899



B1L100S, U19-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



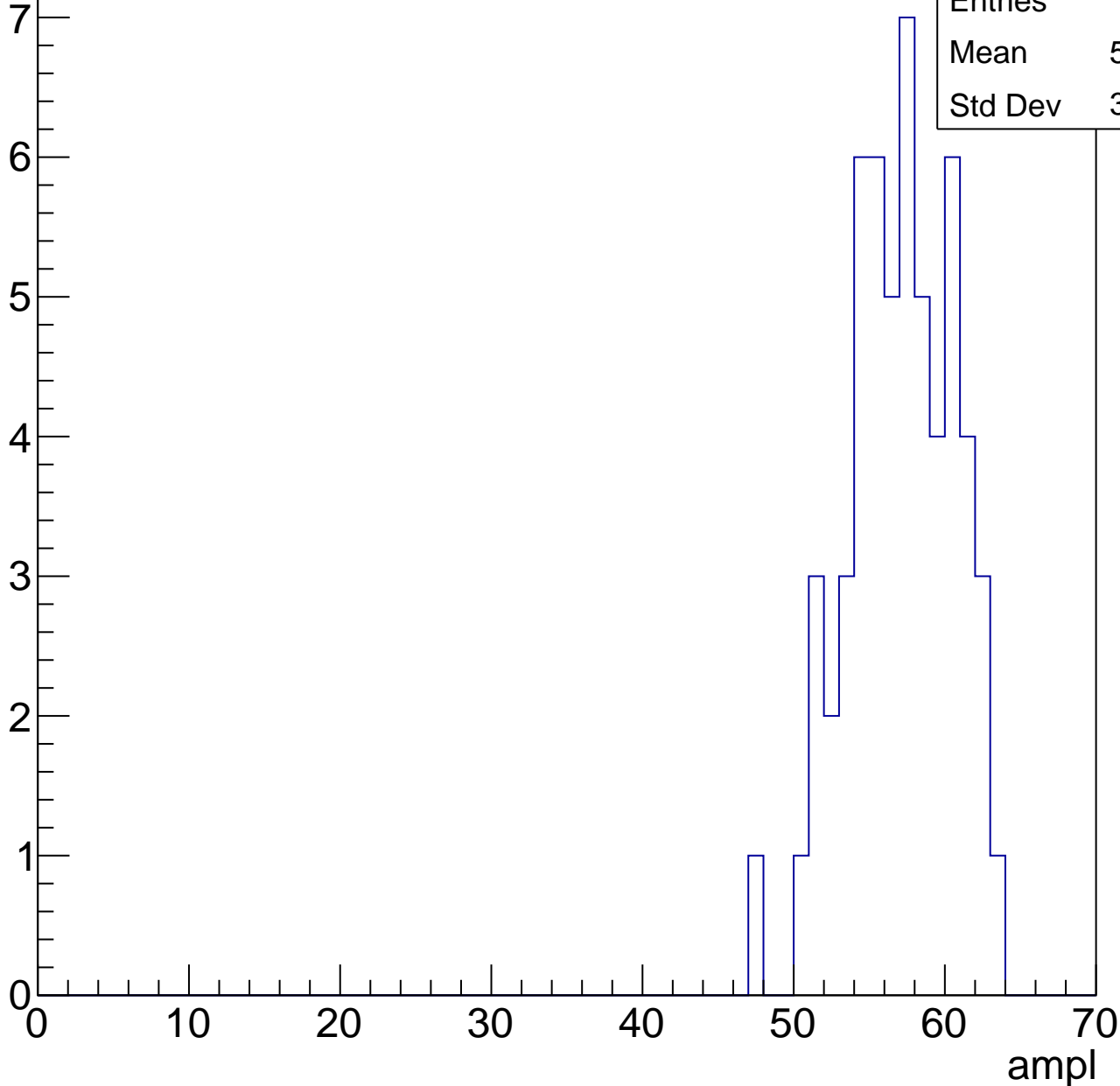
Entries	85
Mean	49.42
Std Dev	3.407

B1L100S, U19-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

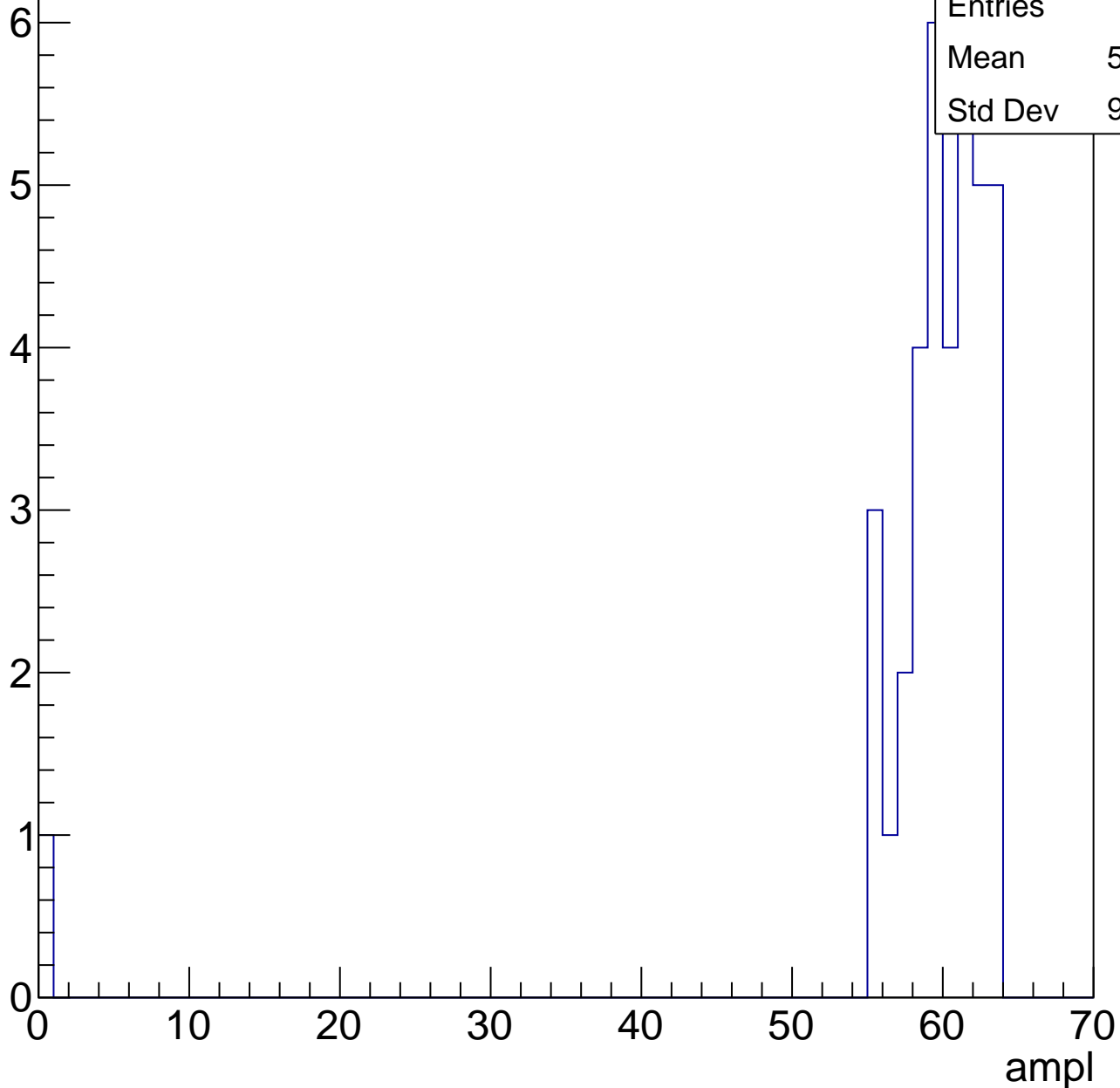
Entries	57
Mean	56.58
Std Dev	3.449



B1L100S, U19-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

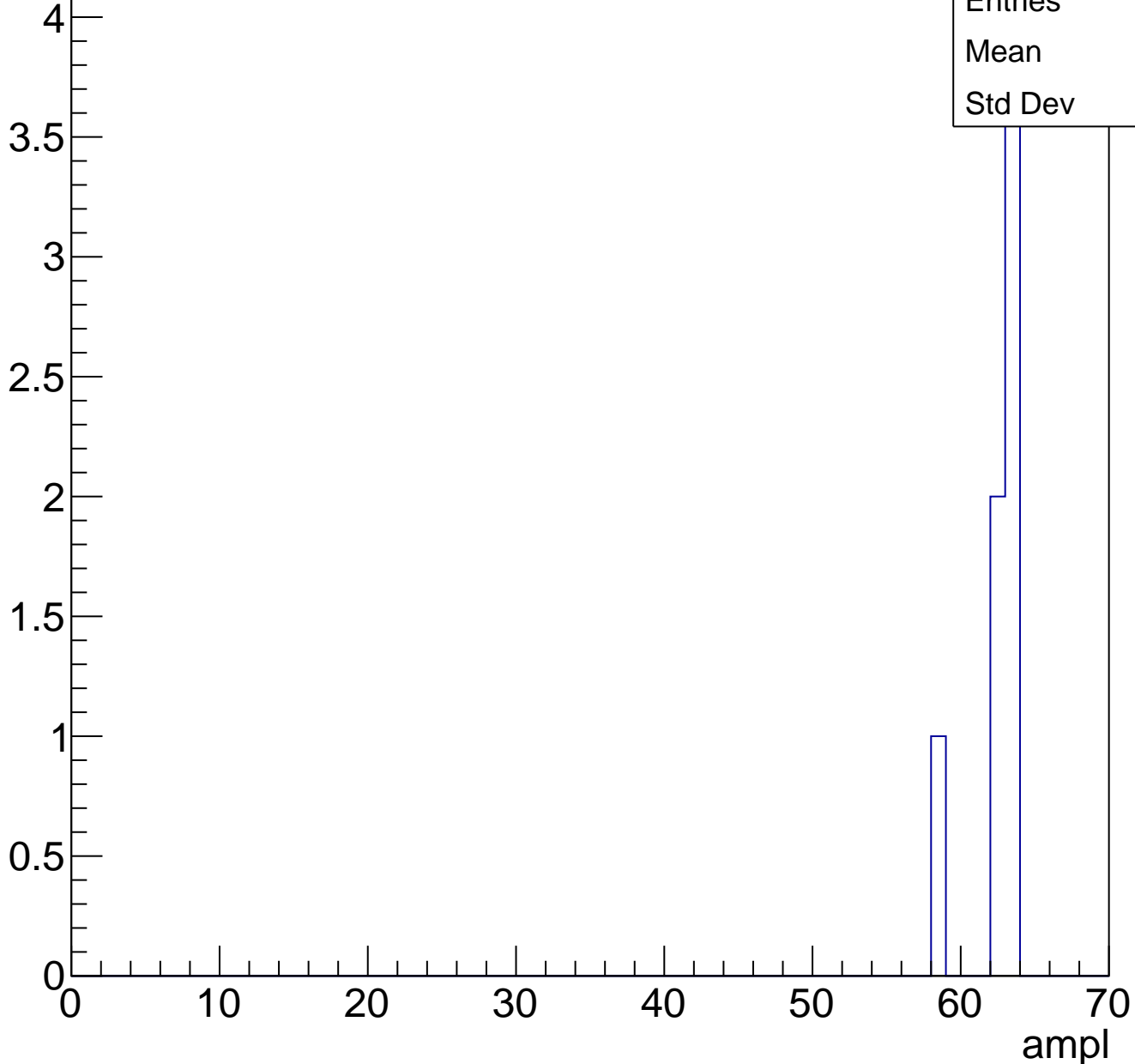
Entry



B1L100S, U19-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	7
Mean	62
Std Dev	1.69

B1L100S, U19-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch11, adc0

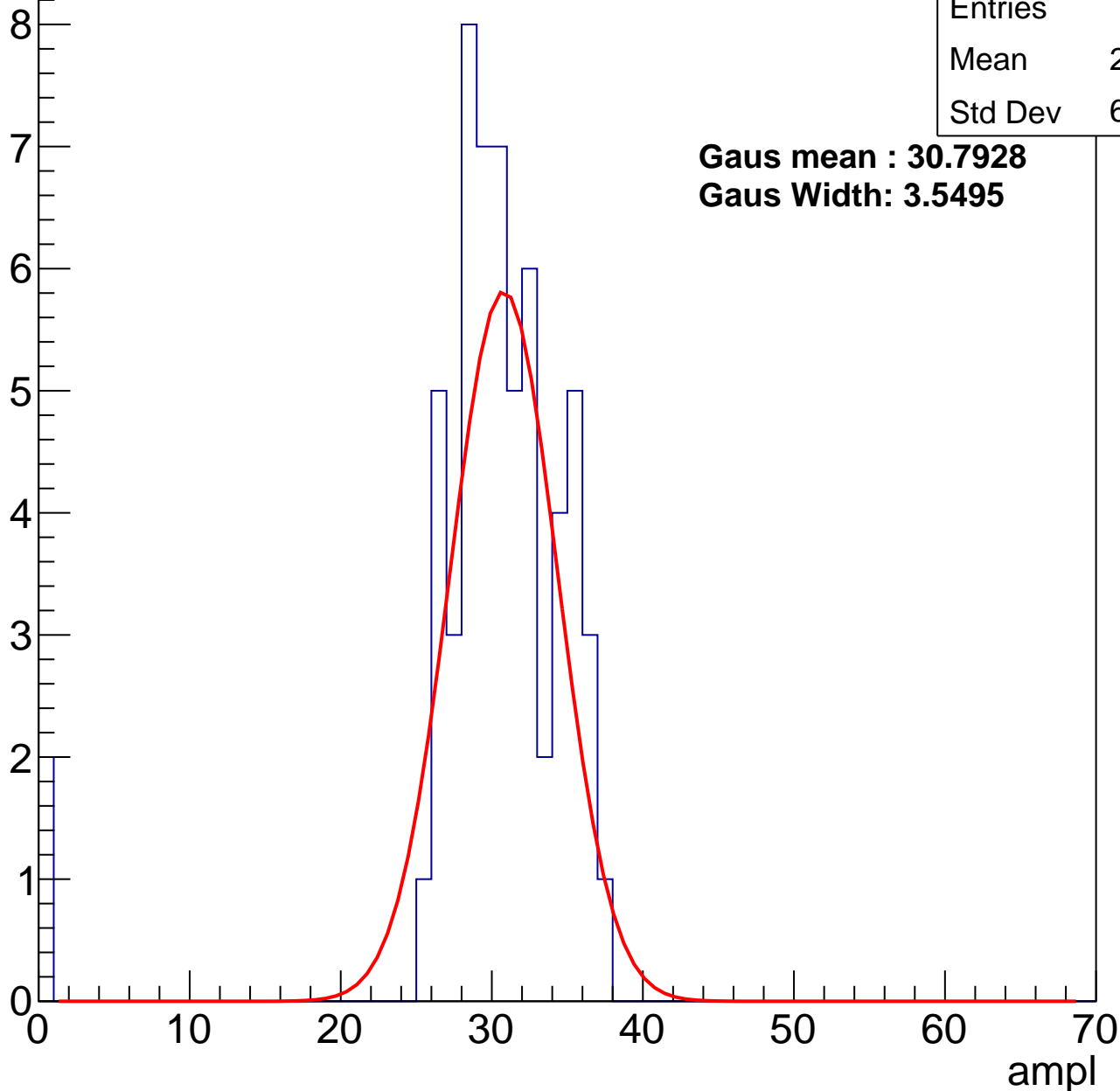
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.53
Std Dev	6.312

Gaus mean : 30.7928

Gaus Width: 3.5495



B1L100S, U19-ch11, adc1

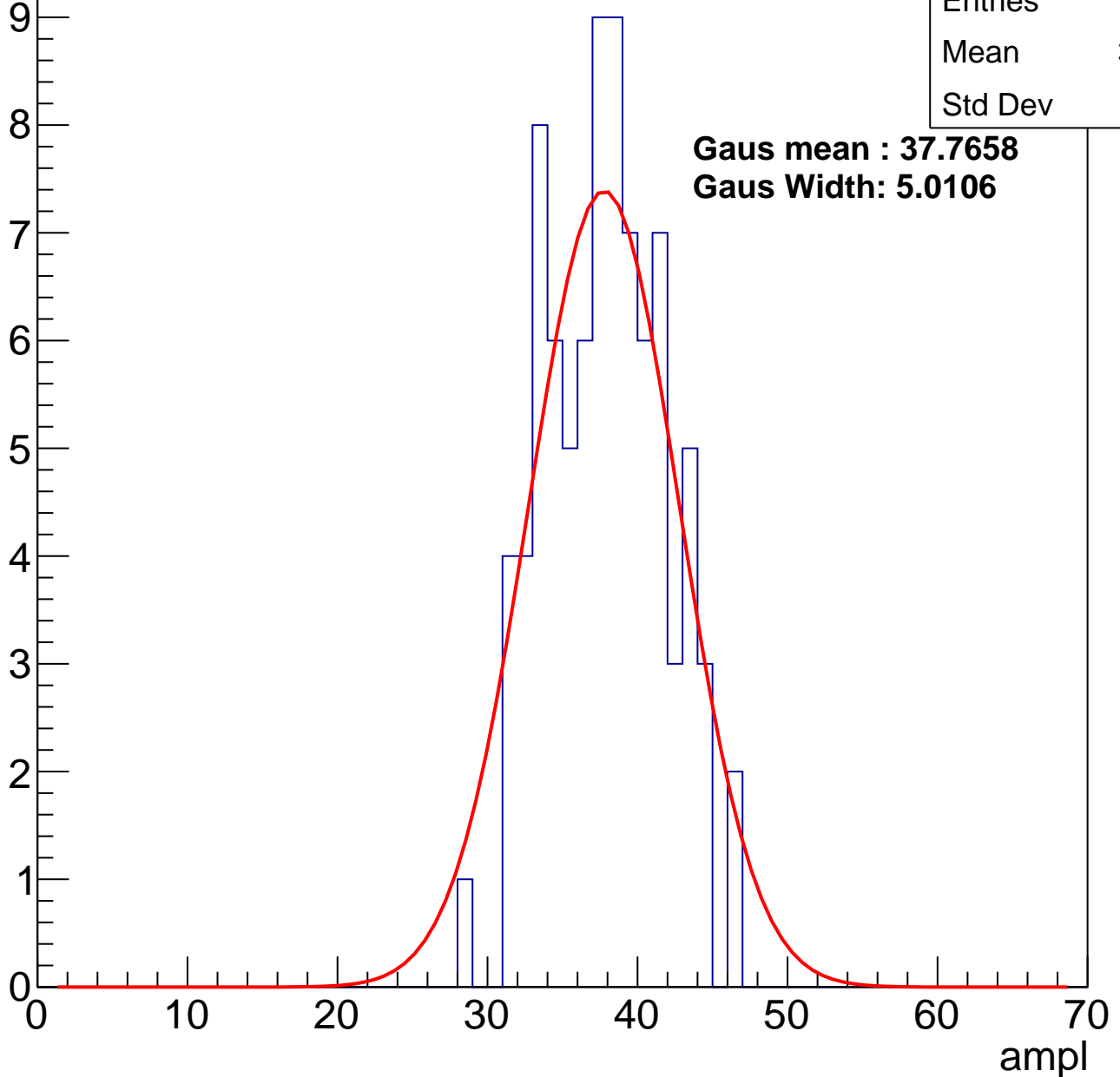
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	37.4
Std Dev	3.89

Gaus mean : 37.7658

Gaus Width: 5.0106



B1L100S, U19-ch11, adc2

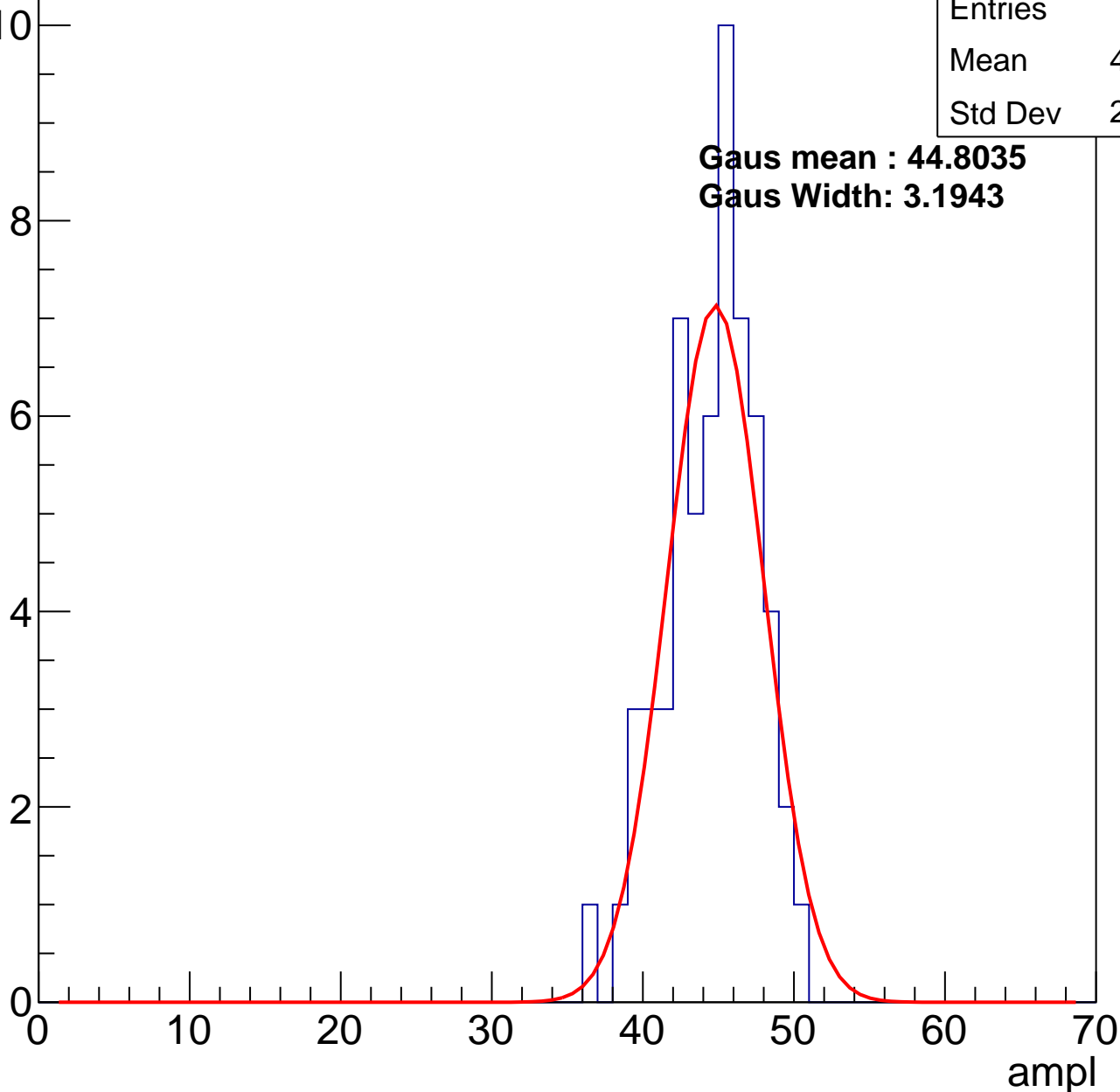
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.08
Std Dev	2.982

Gaus mean : 44.8035

Gaus Width: 3.1943



B1L100S, U19-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	51.64
Std Dev	3.31

Entry

10

8

6

4

2

0

0

10

20

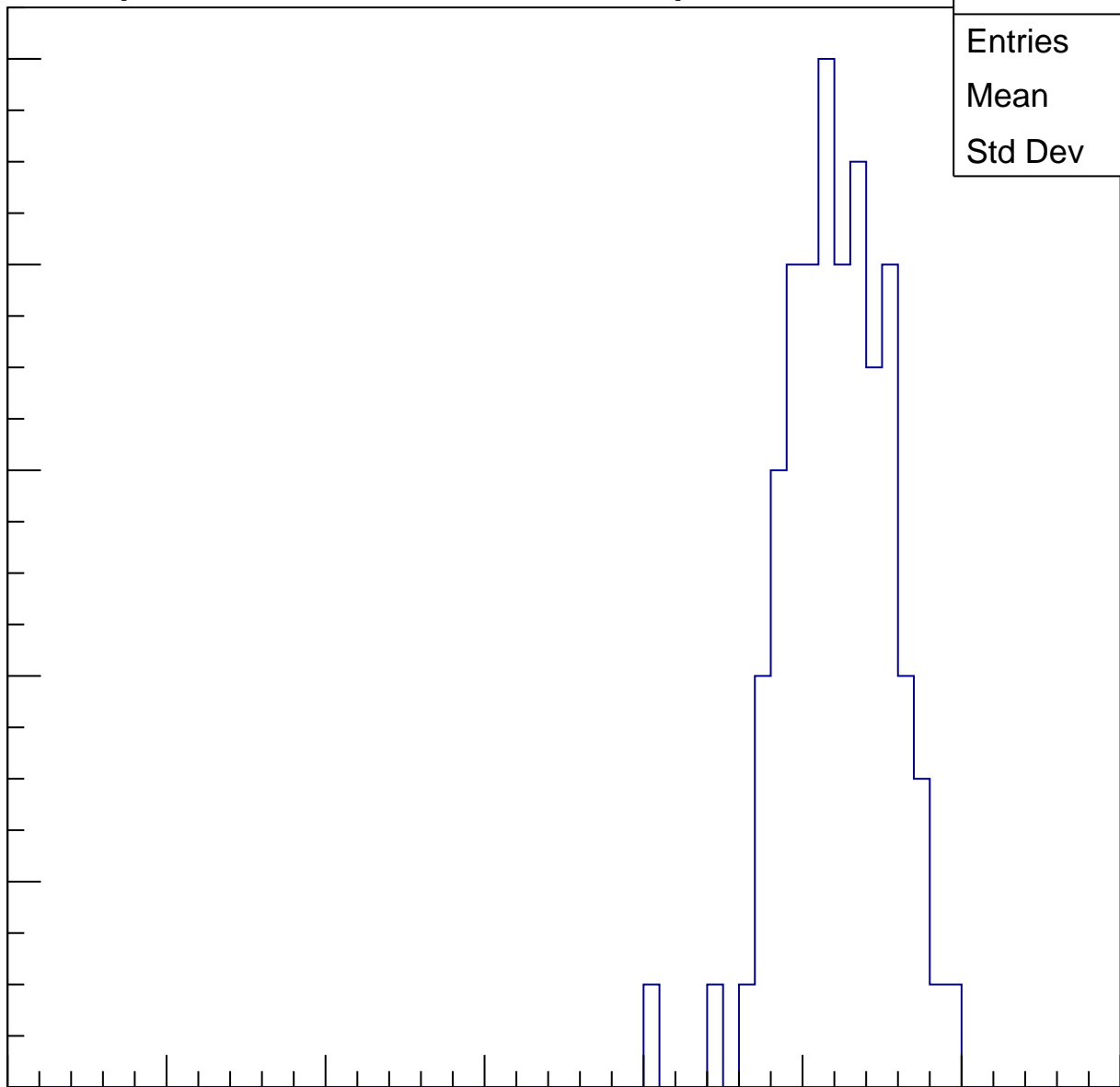
30

40

50

60

ampl

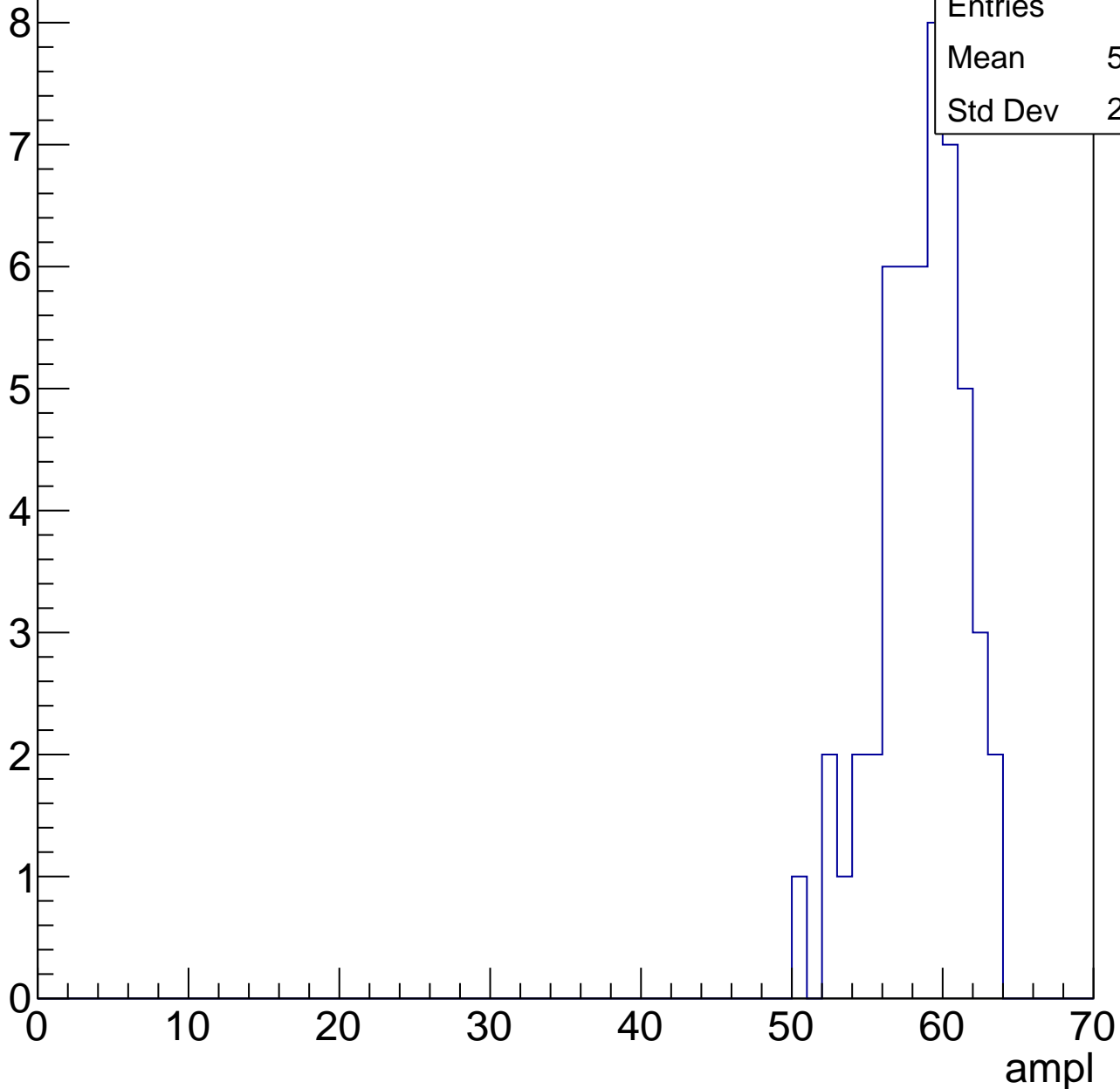


B1L100S, U19-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

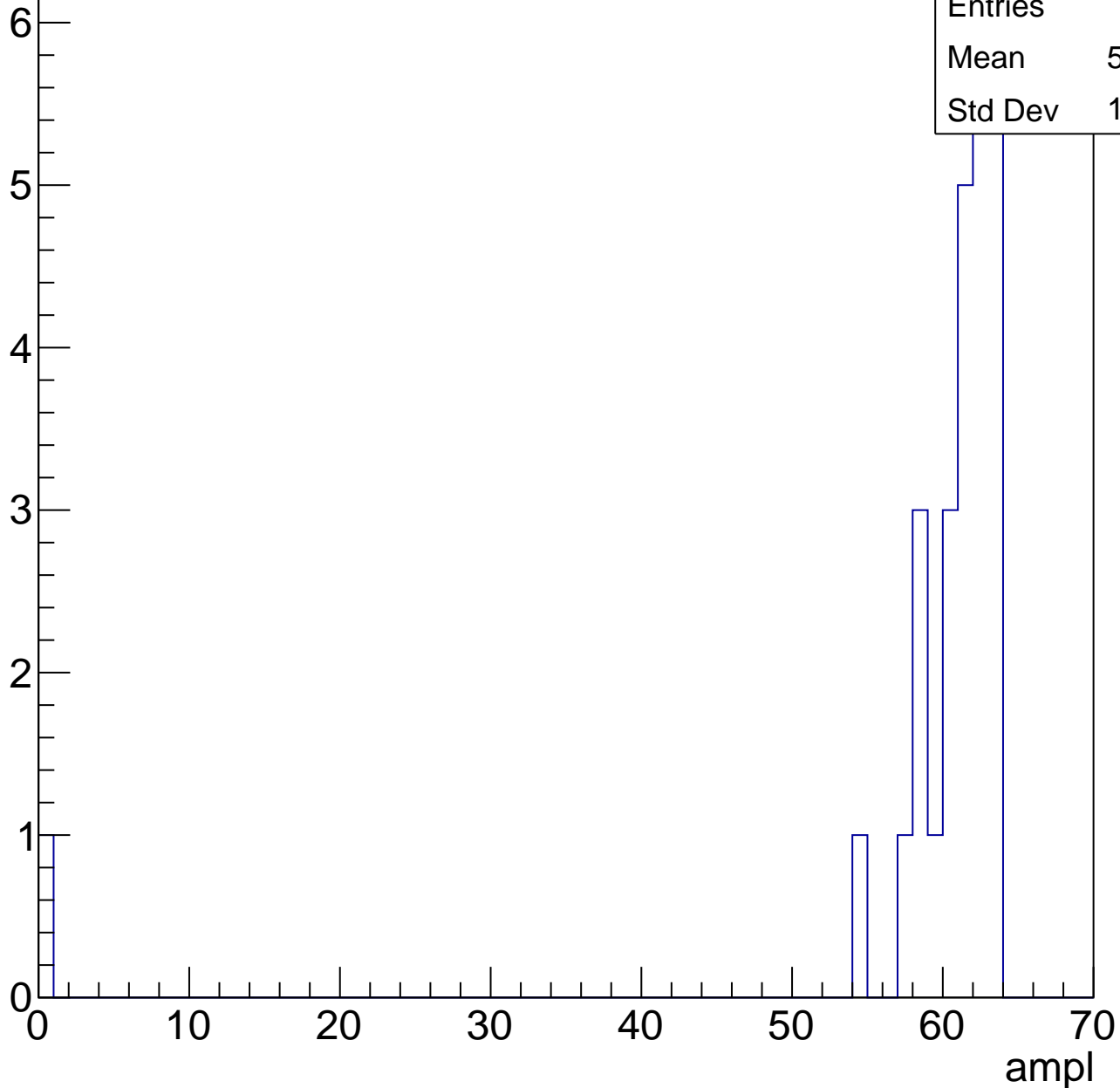
Entries	51
Mean	58.04
Std Dev	2.869



B1L100S, U19-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

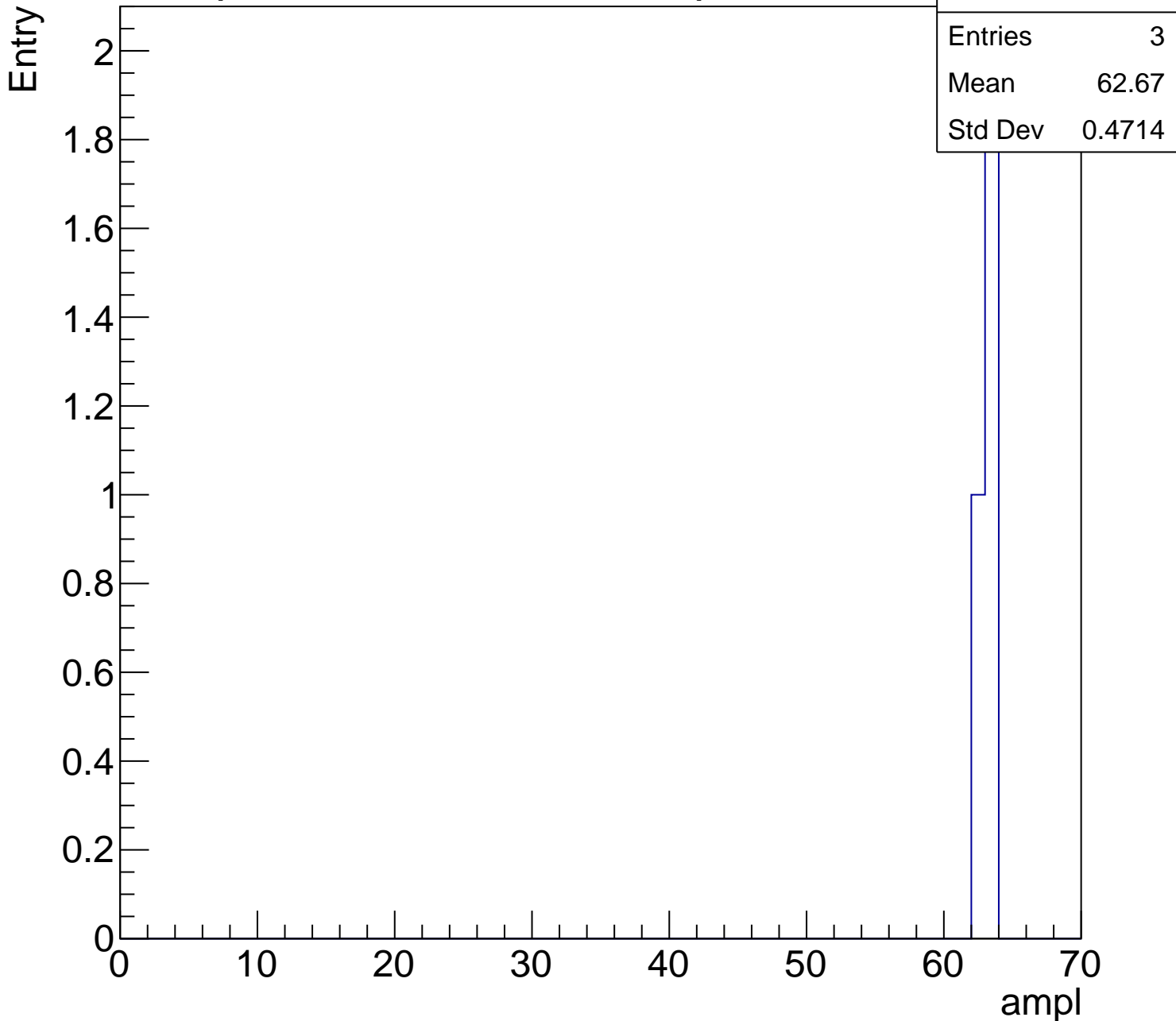
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch12, adc0

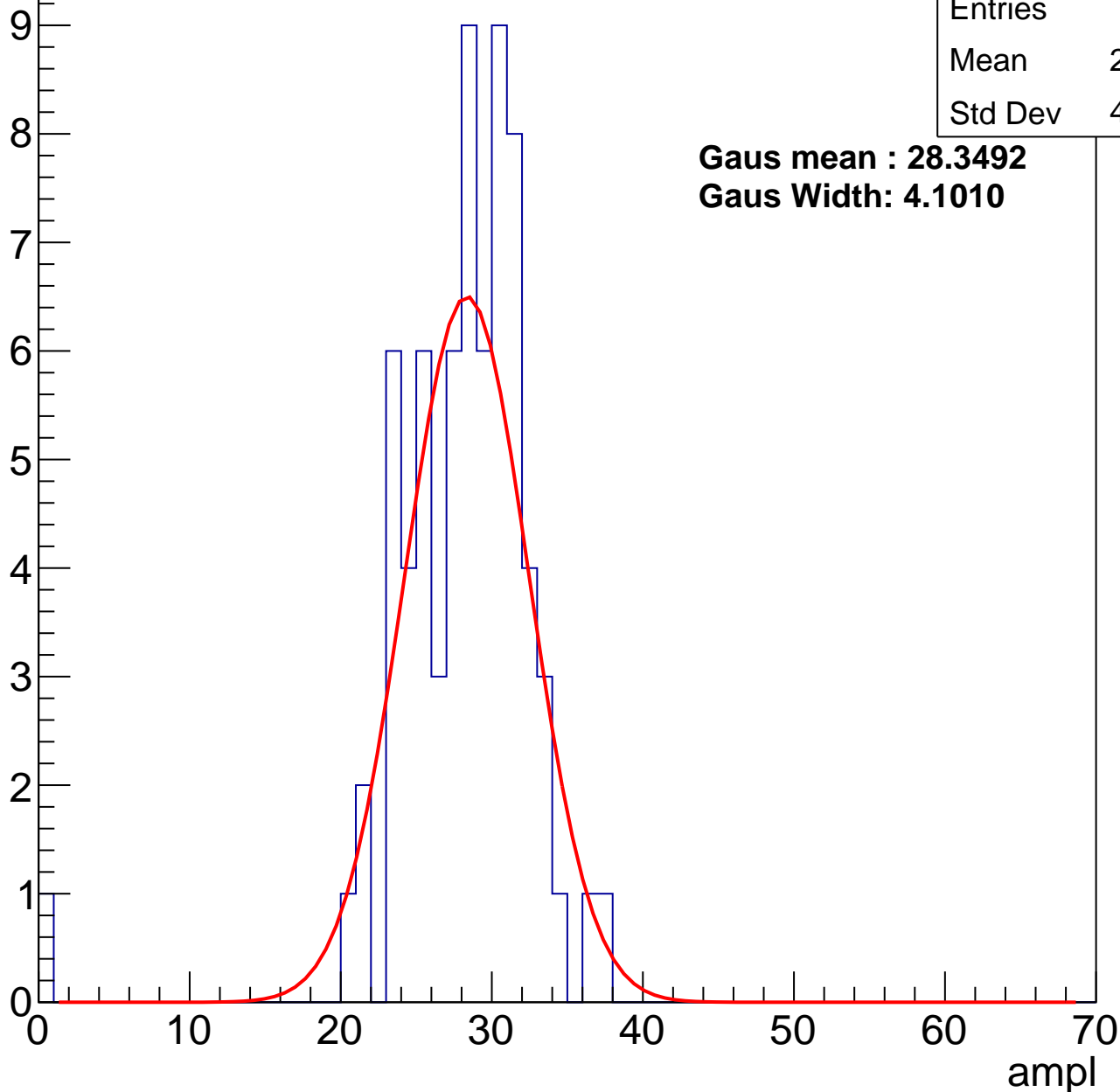
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.66
Std Dev	4.832

Gaus mean : 28.3492

Gaus Width: 4.1010



B1L100S, U19-ch12, adc1

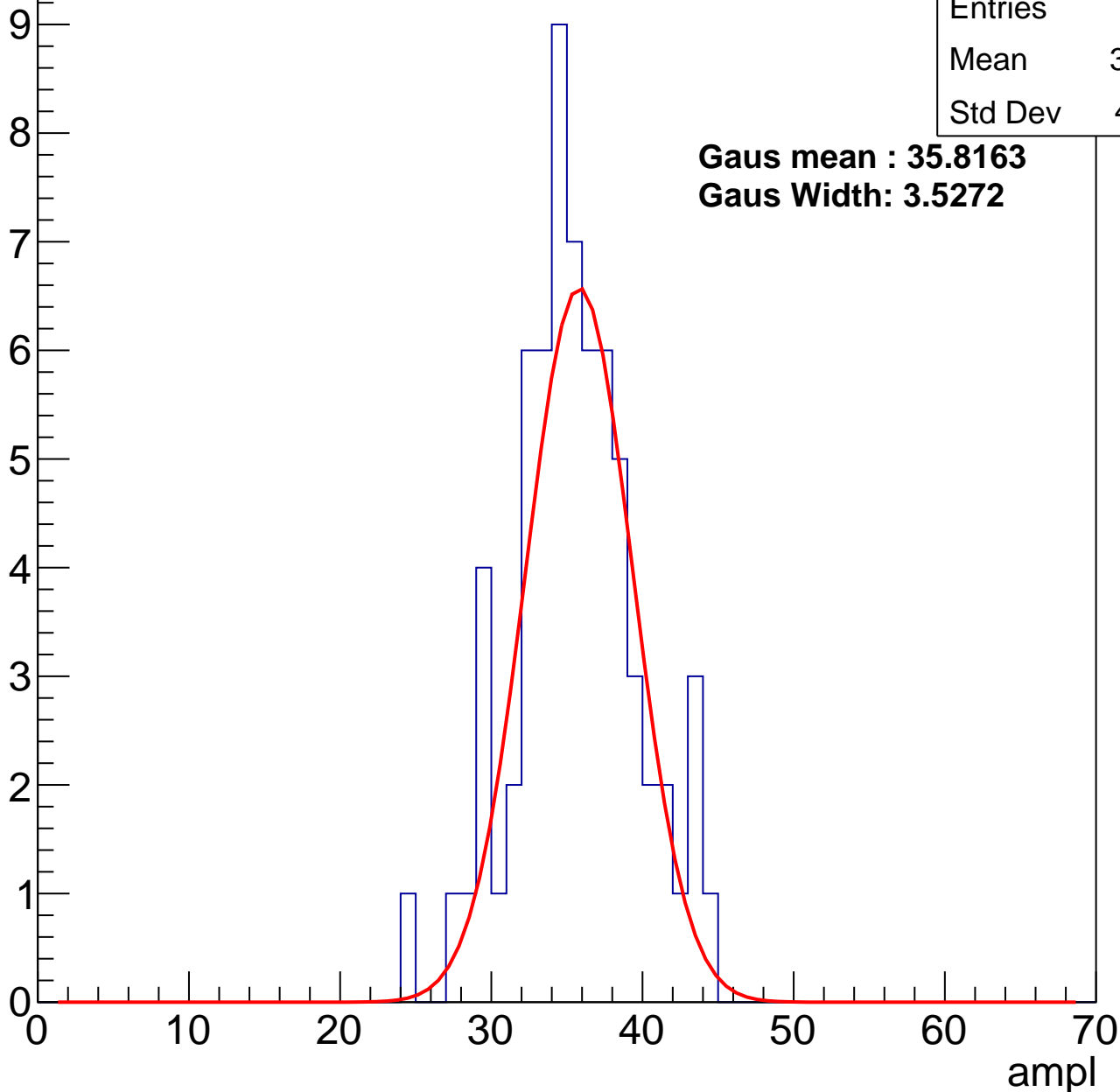
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.07
Std Dev	4.031

Gaus mean : 35.8163

Gaus Width: 3.5272



B1L100S, U19-ch12, adc2

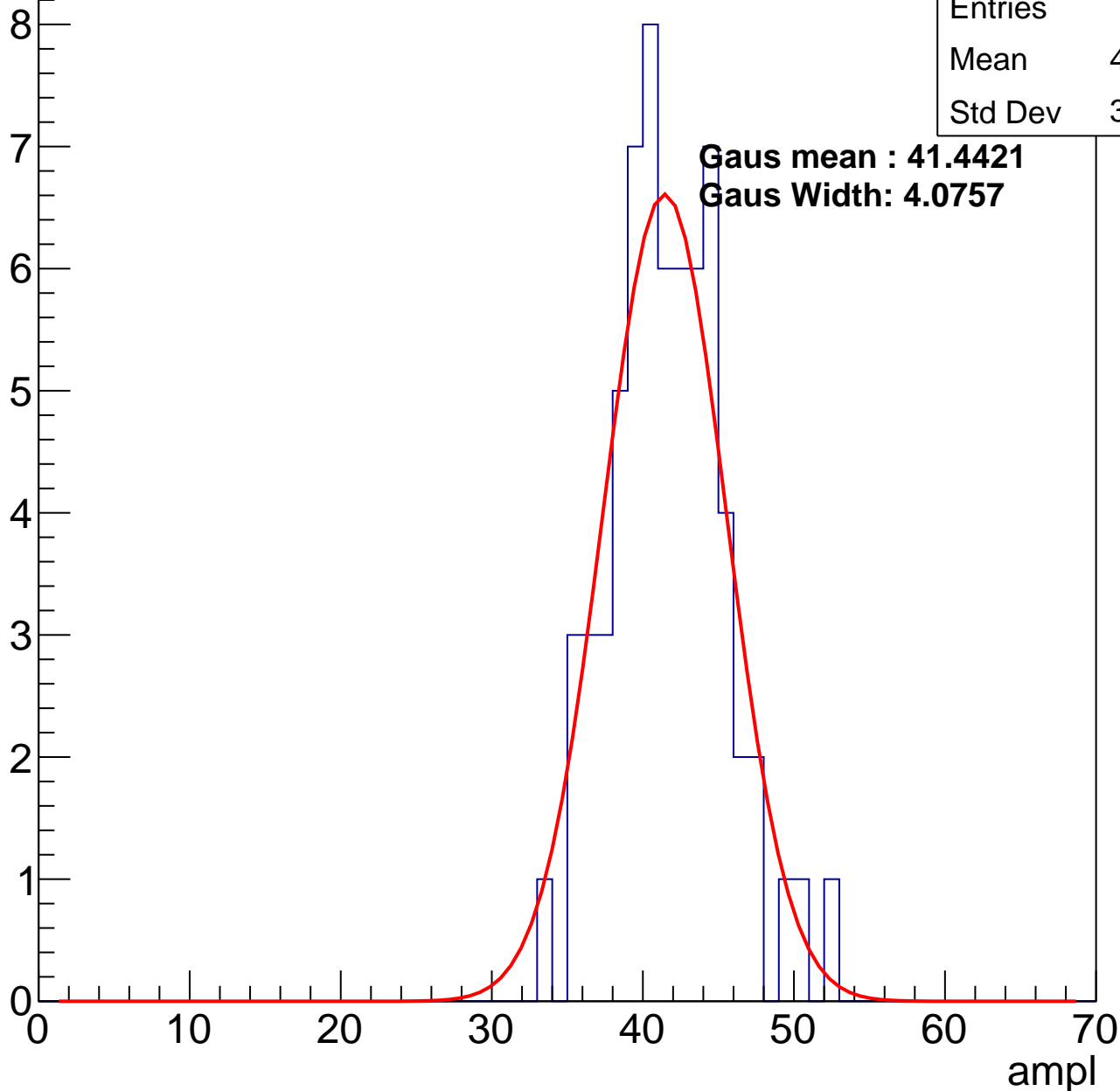
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.23
Std Dev	3.729

Gaus mean : 41.4421

Gaus Width: 4.0757

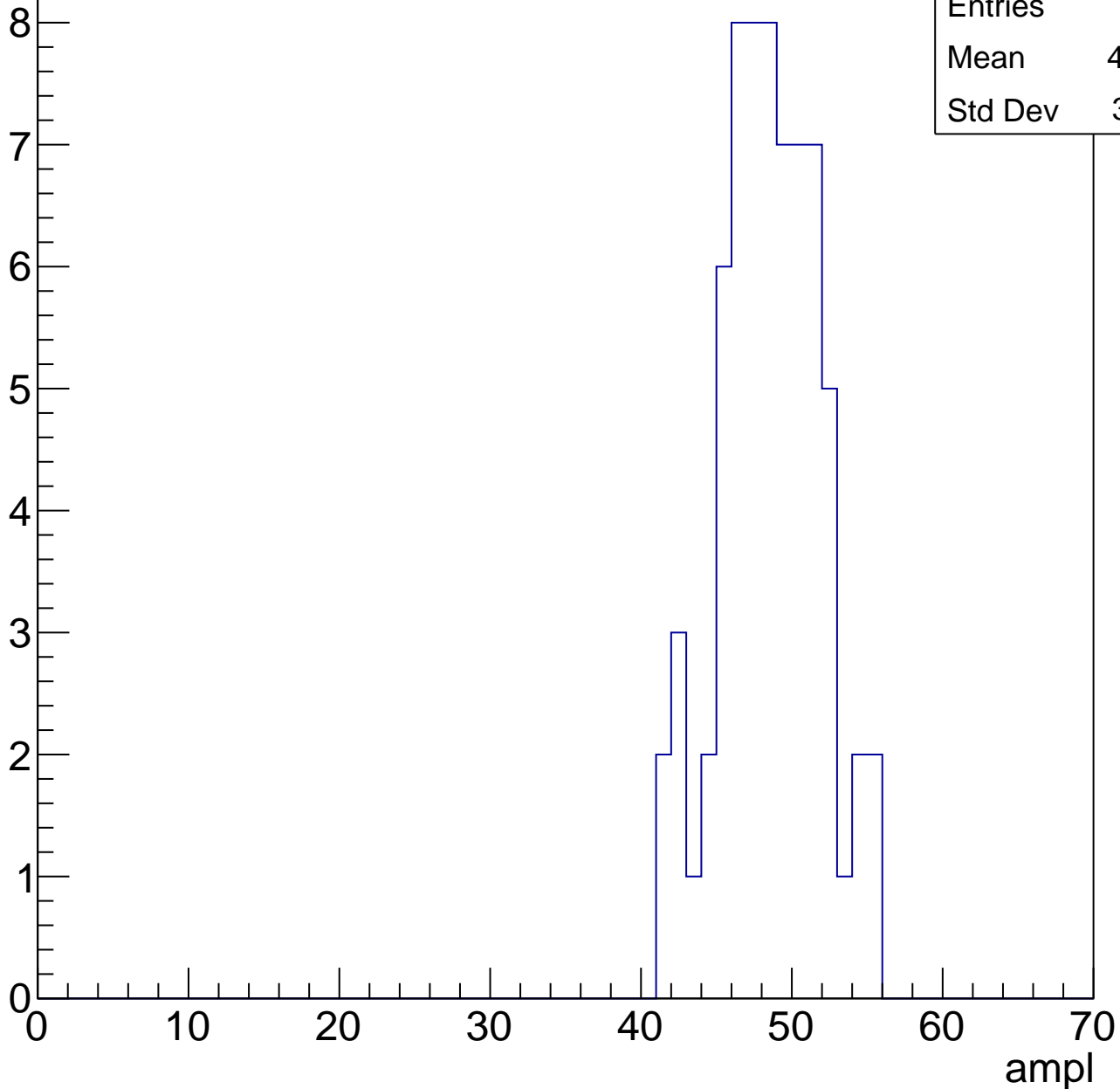


B1L100S, U19-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	48.09
Std Dev	3.251

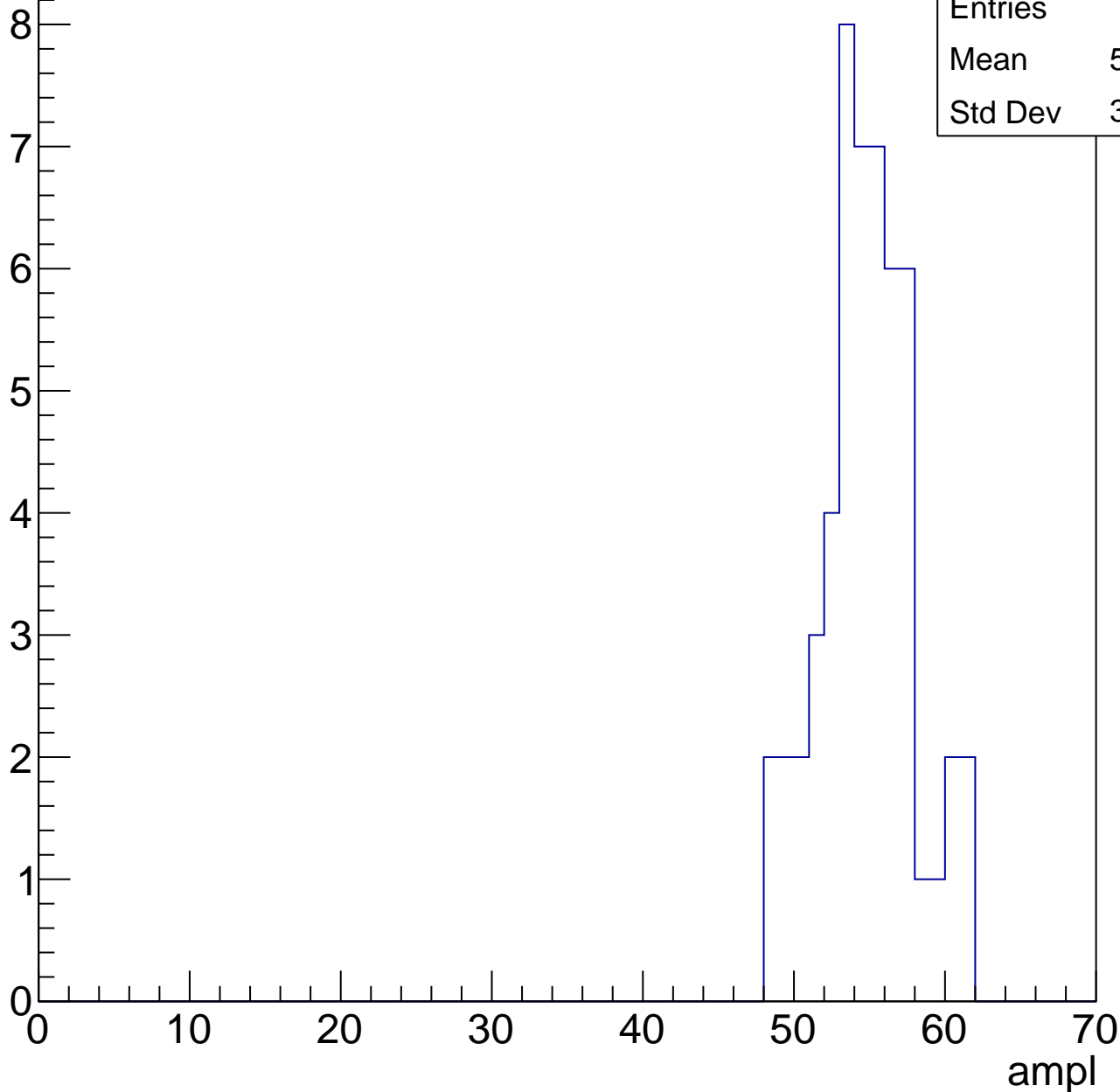


B1L100S, U19-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

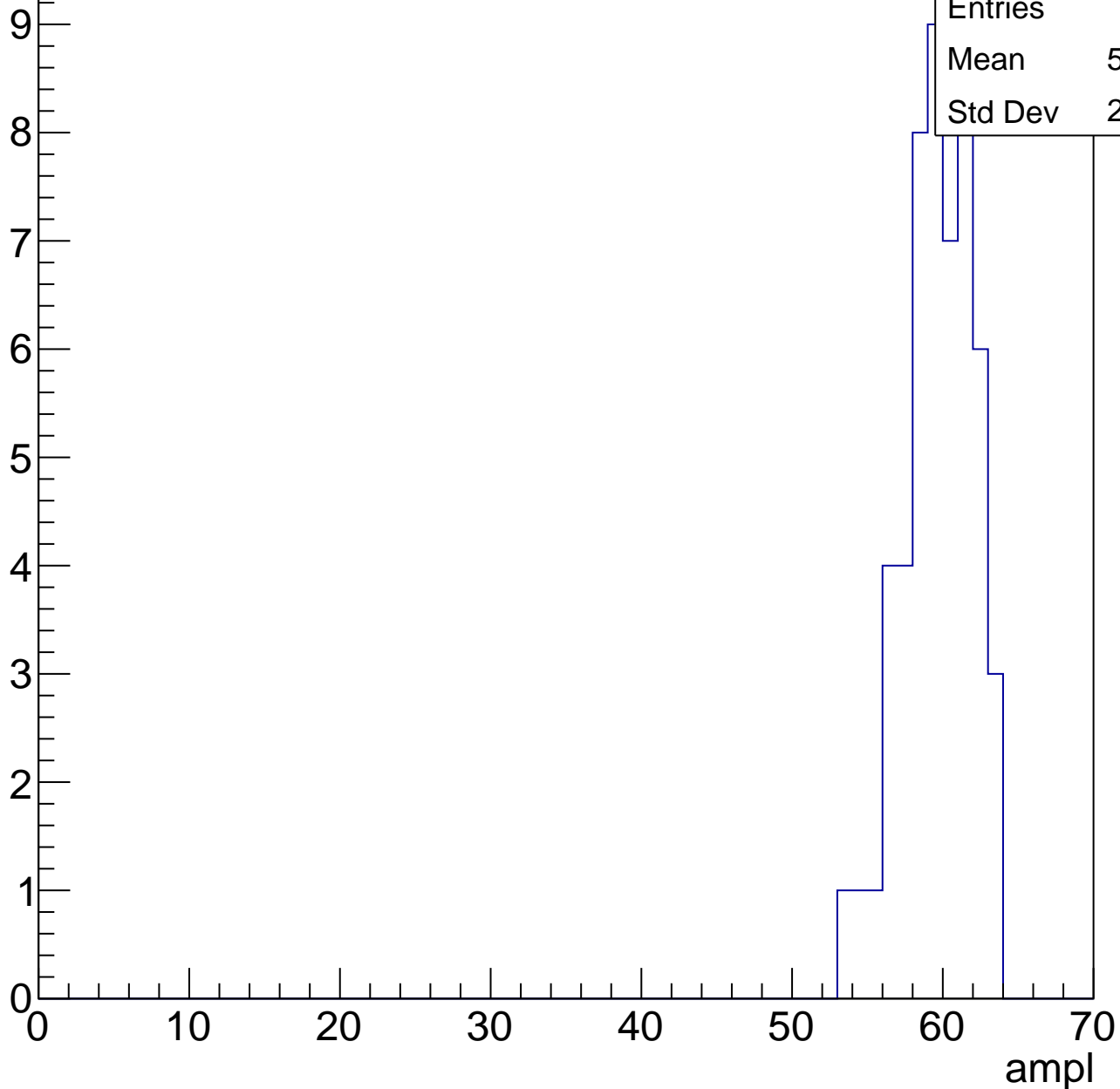
Entries	53
Mean	54.32
Std Dev	3.058



B1L100S, U19-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



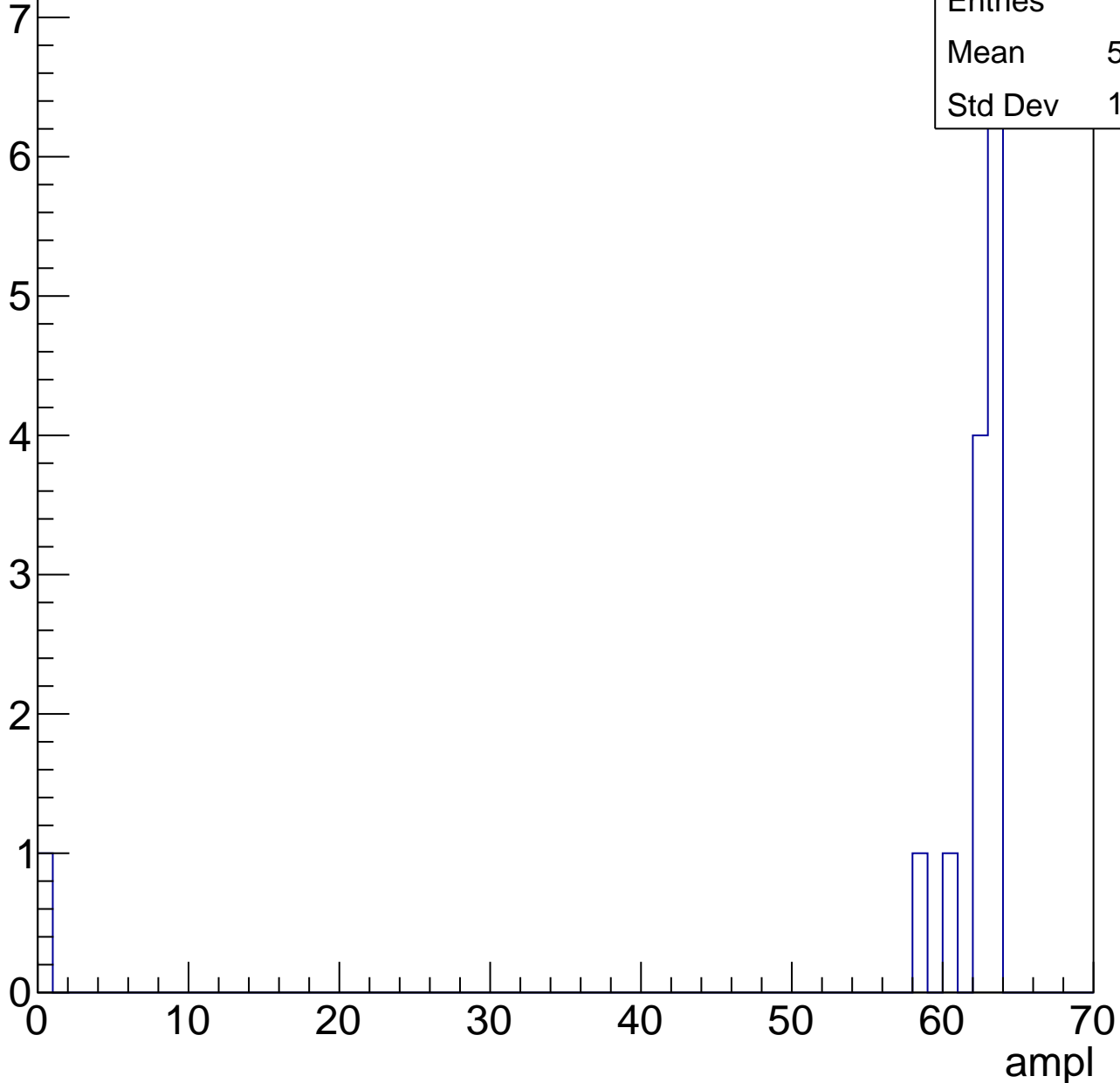
Entries	52
Mean	59.19
Std Dev	2.304

B1L100S, U19-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	57.64
Std Dev	16.05



B1L100S, U19-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch13, adc0

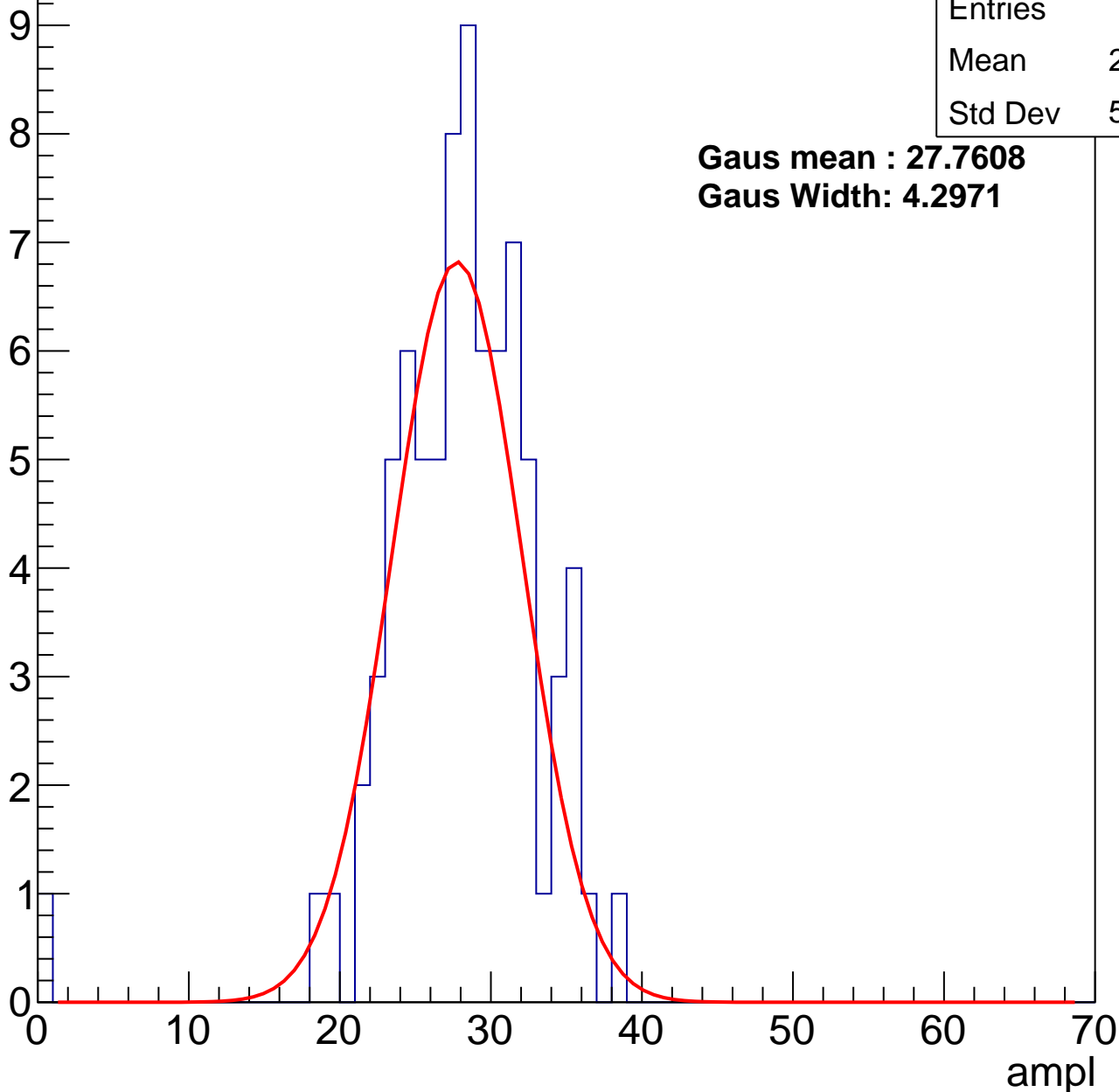
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	27.59
Std Dev	5.145

Gaus mean : 27.7608

Gaus Width: 4.2971



B1L100S, U19-ch13, adc1

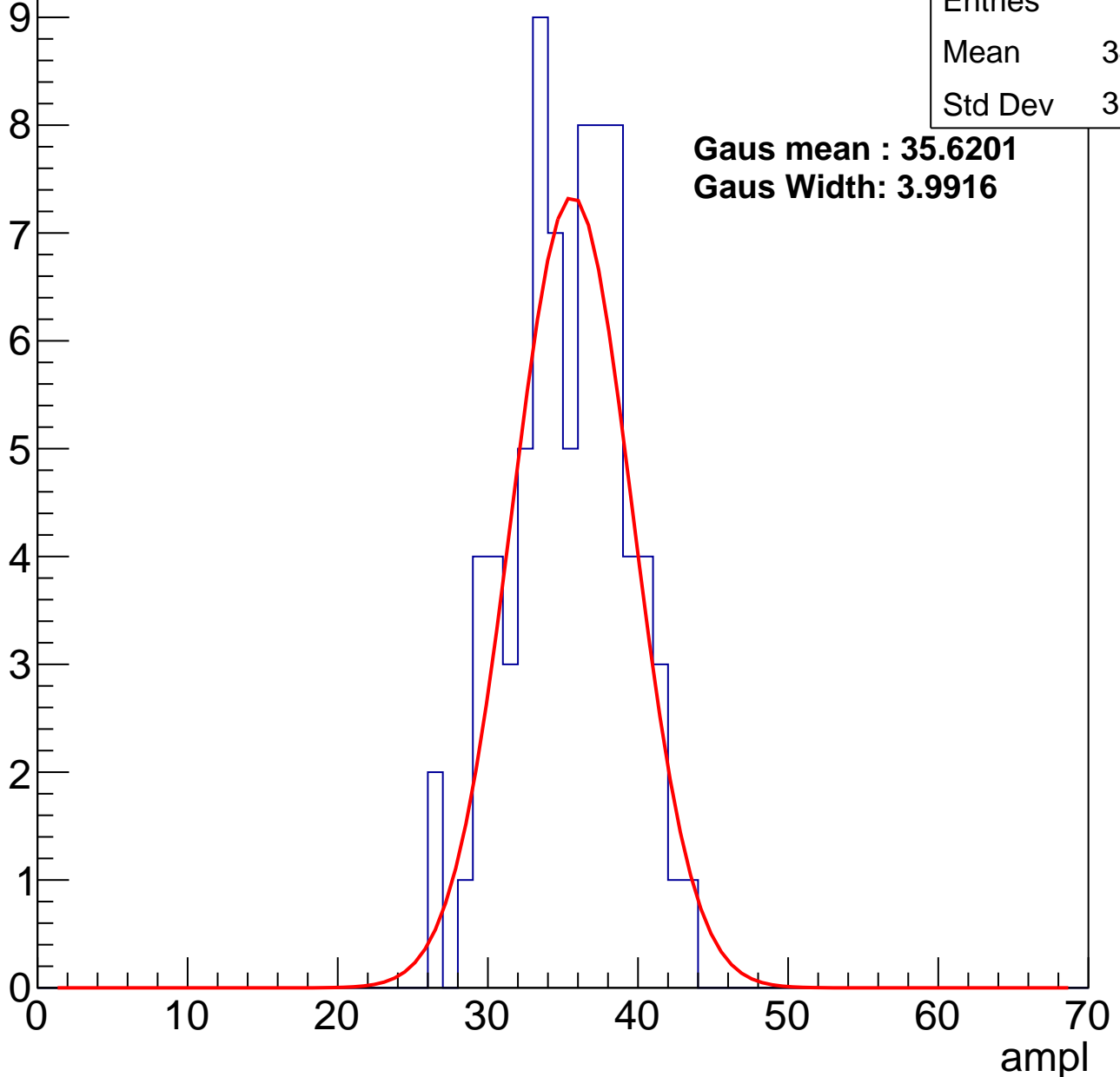
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	34.95
Std Dev	3.759

Gaus mean : 35.6201

Gaus Width: 3.9916



B1L100S, U19-ch13, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

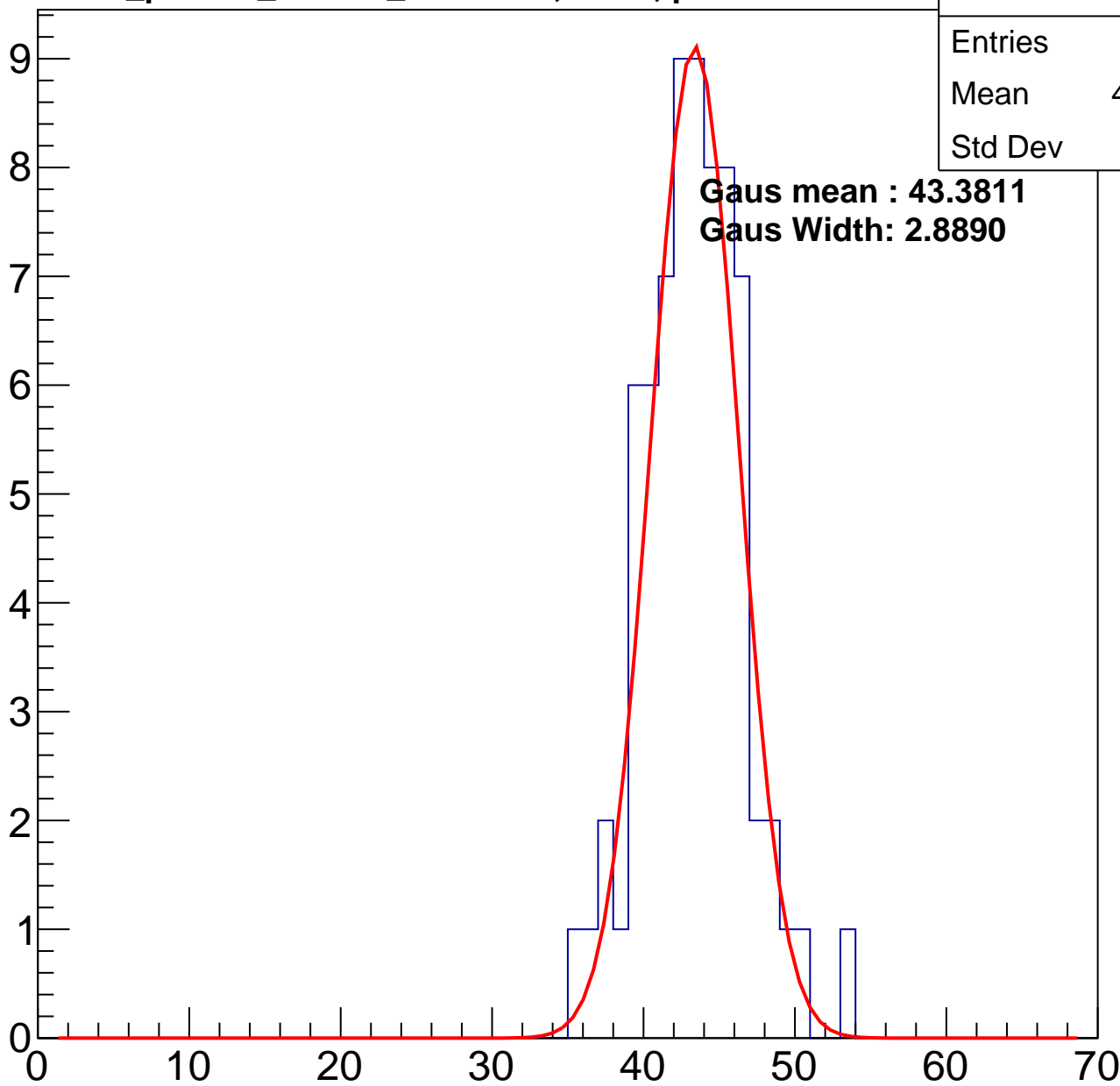
9
8
7
6
5
4
3
2
1
0

Entries	72
Mean	42.85
Std Dev	3.26

Gaus mean : 43.3811

Gaus Width: 2.8890

ampl

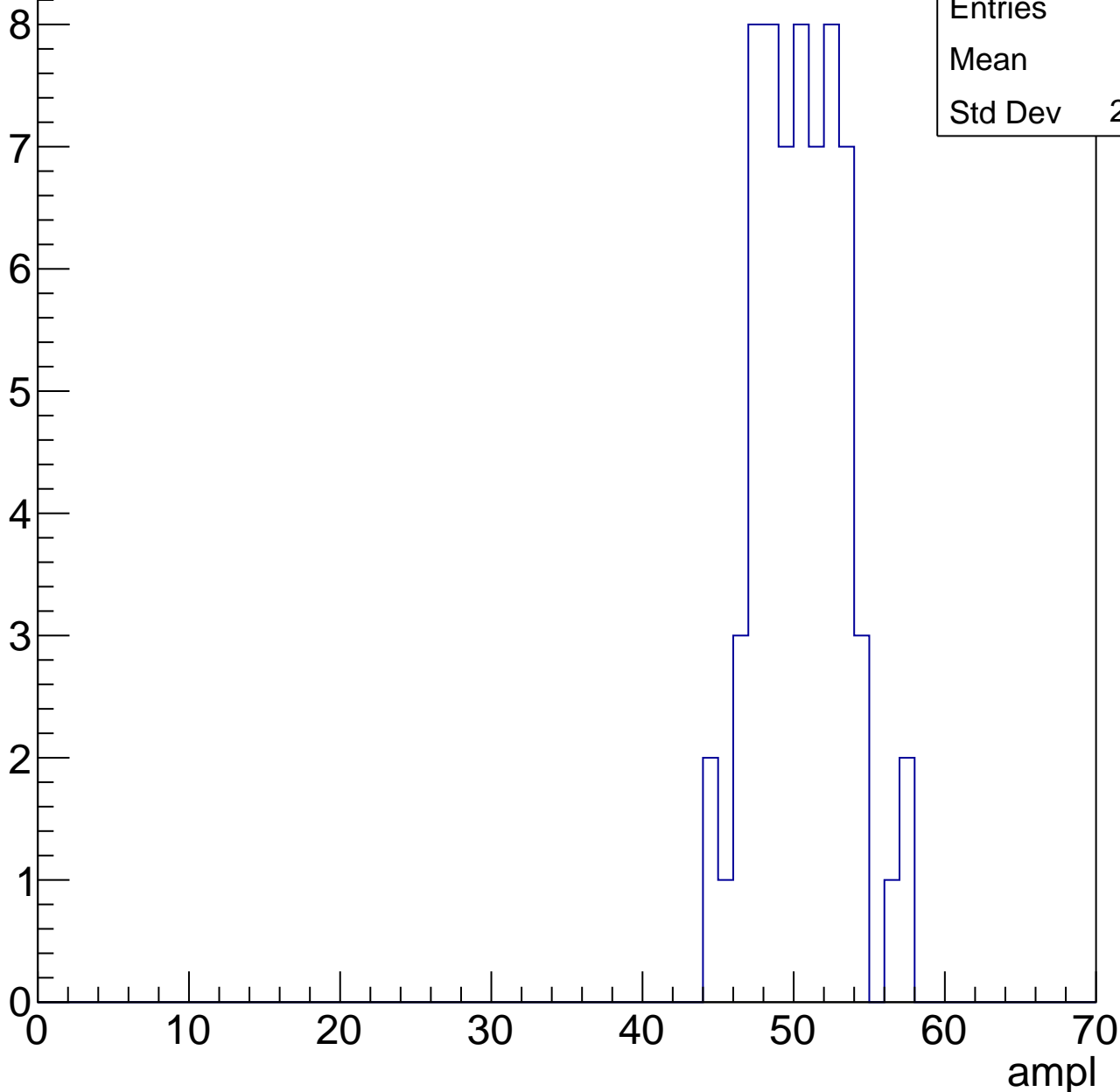


B1L100S, U19-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

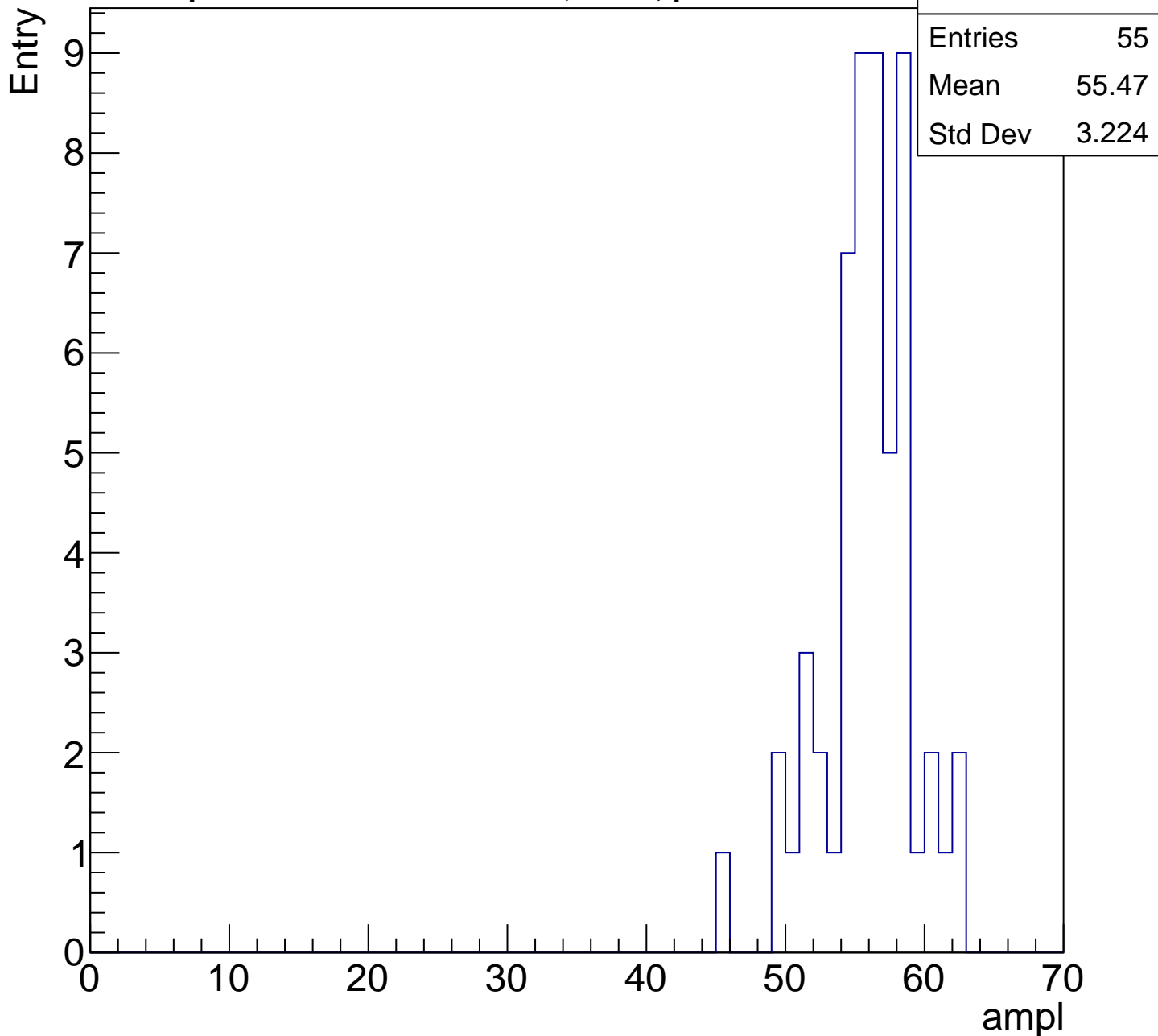
Entry

Entries	65
Mean	50
Std Dev	2.882



B1L100S, U19-ch13, adc4

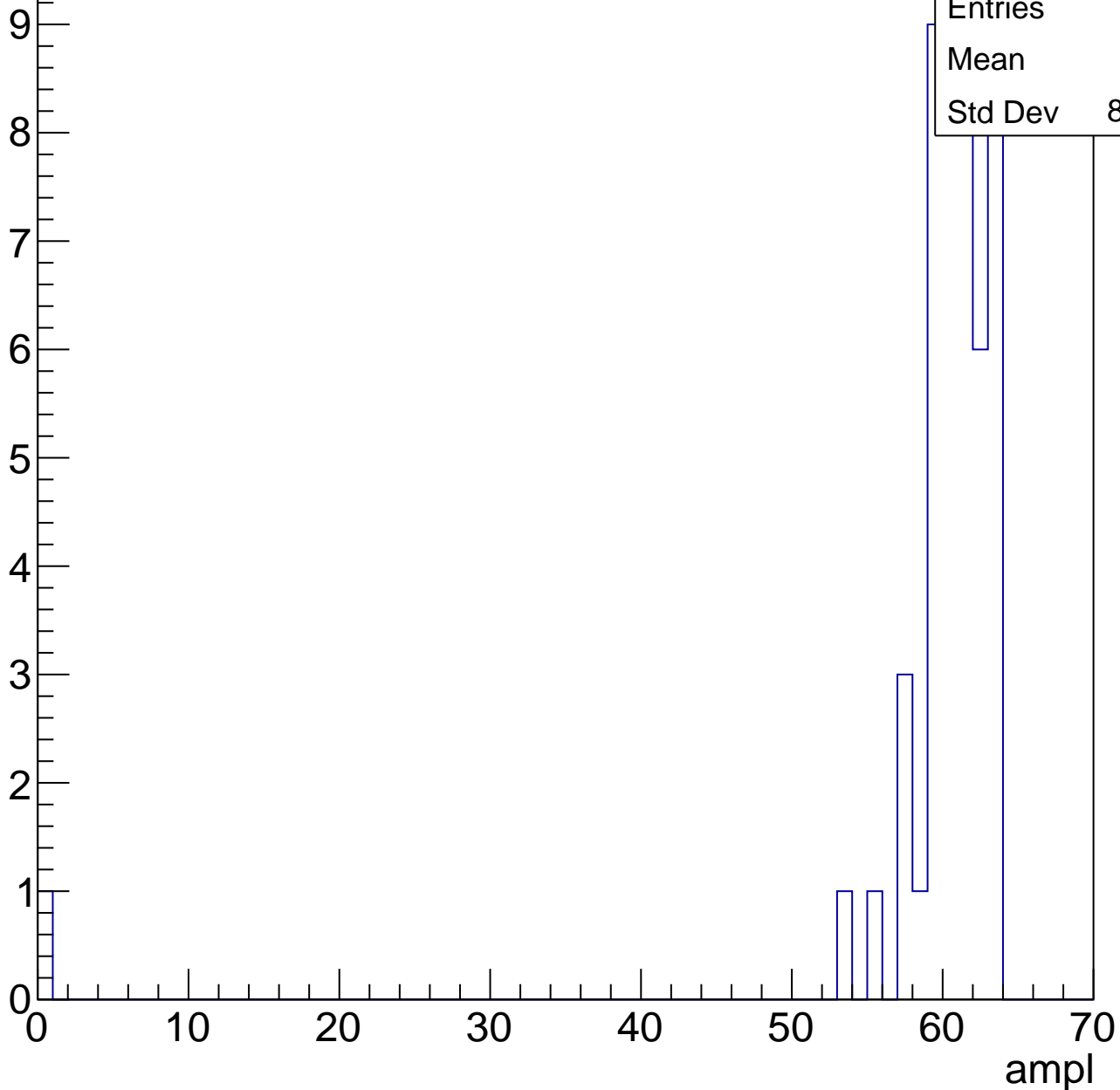
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch14, adc0

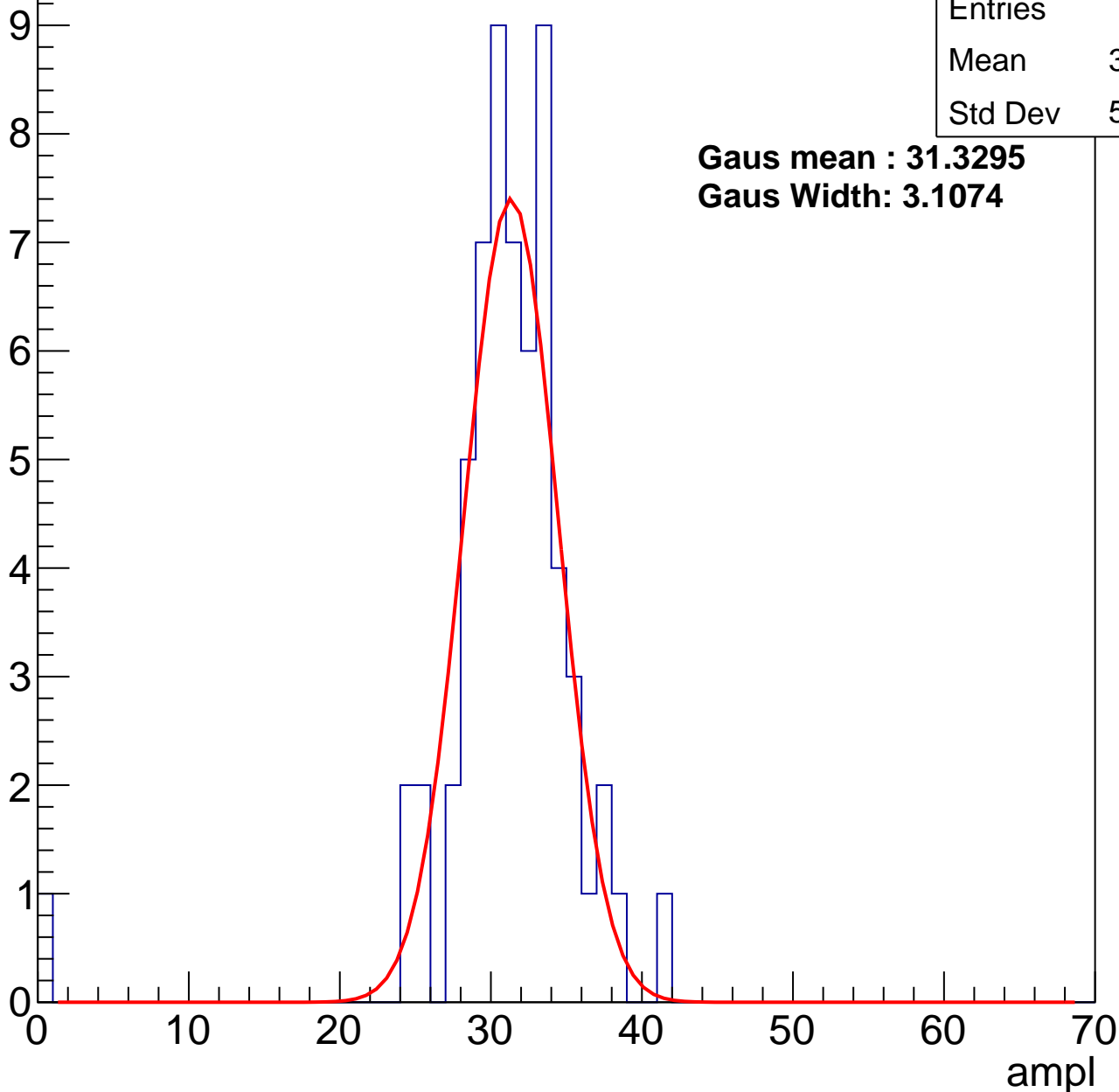
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.66
Std Dev	5.096

Gaus mean : 31.3295

Gaus Width: 3.1074



B1L100S, U19-ch14, adc1

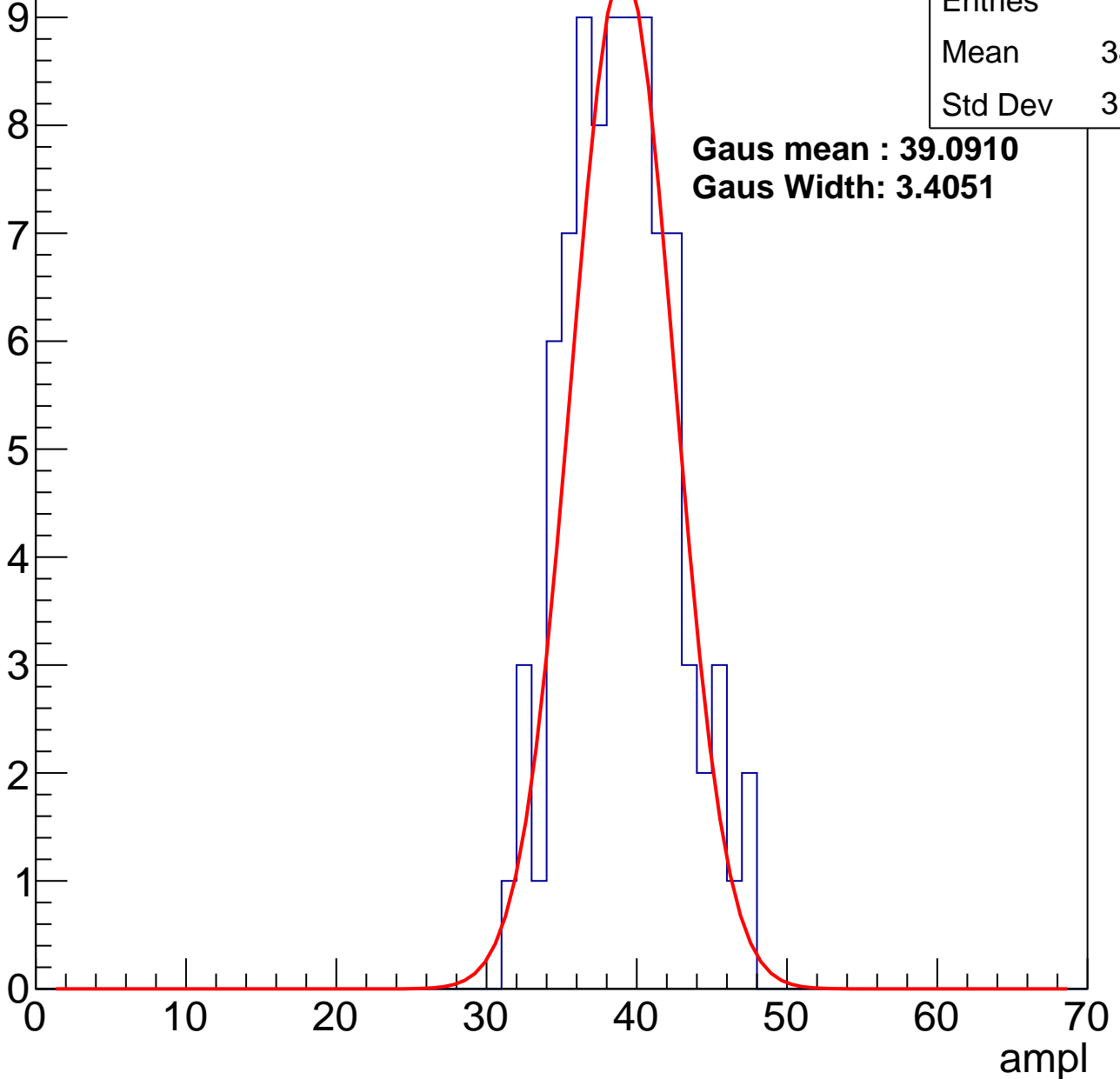
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	38.56
Std Dev	3.552

Gaus mean : 39.0910

Gaus Width: 3.4051



B1L100S, U19-ch14, adc2

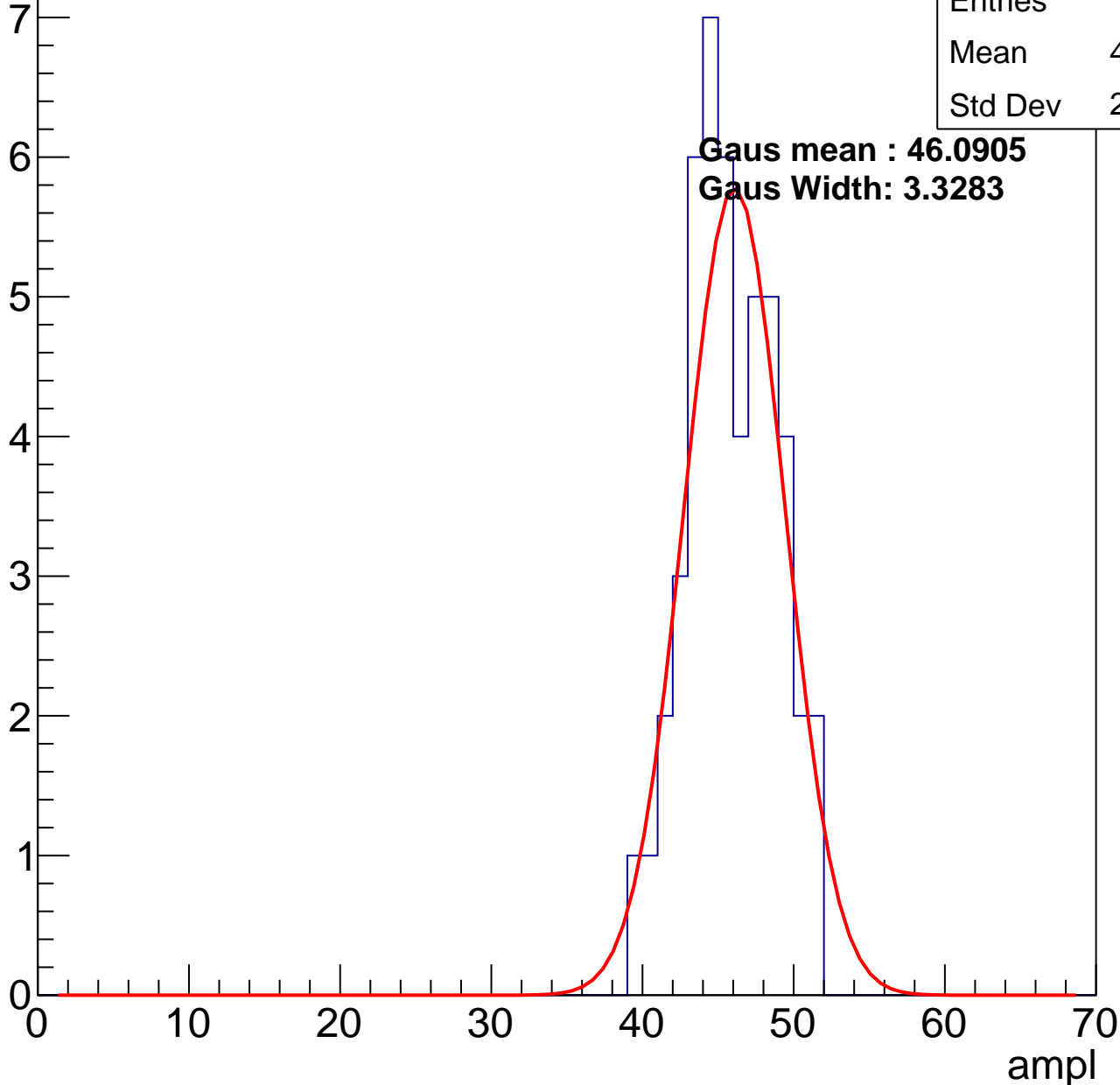
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	45.42
Std Dev	2.878

Gaus mean : 46.0905

Gaus Width: 3.3283

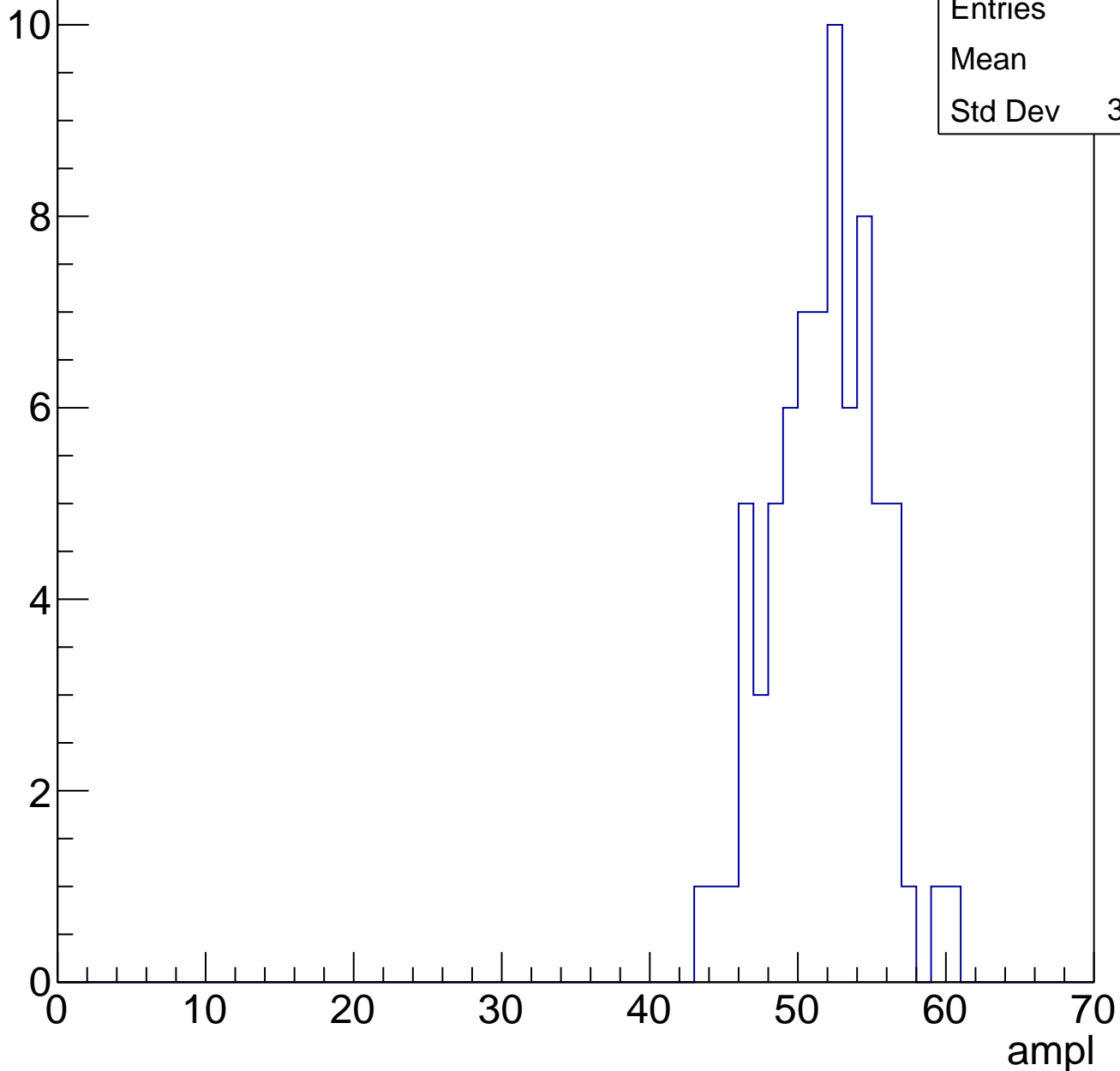


B1L100S, U19-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

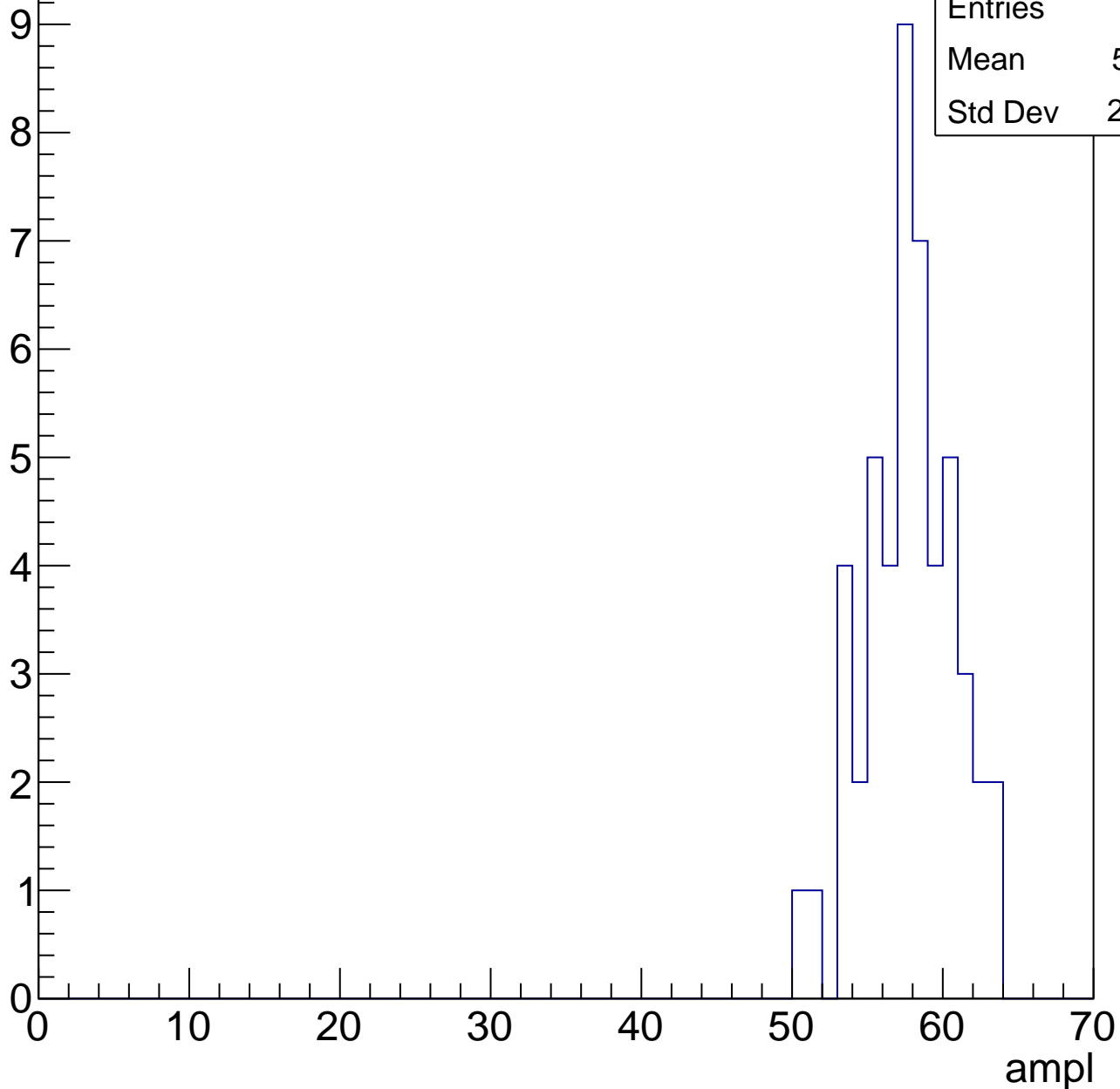
Entries	73
Mean	51.3
Std Dev	3.479



B1L100S, U19-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

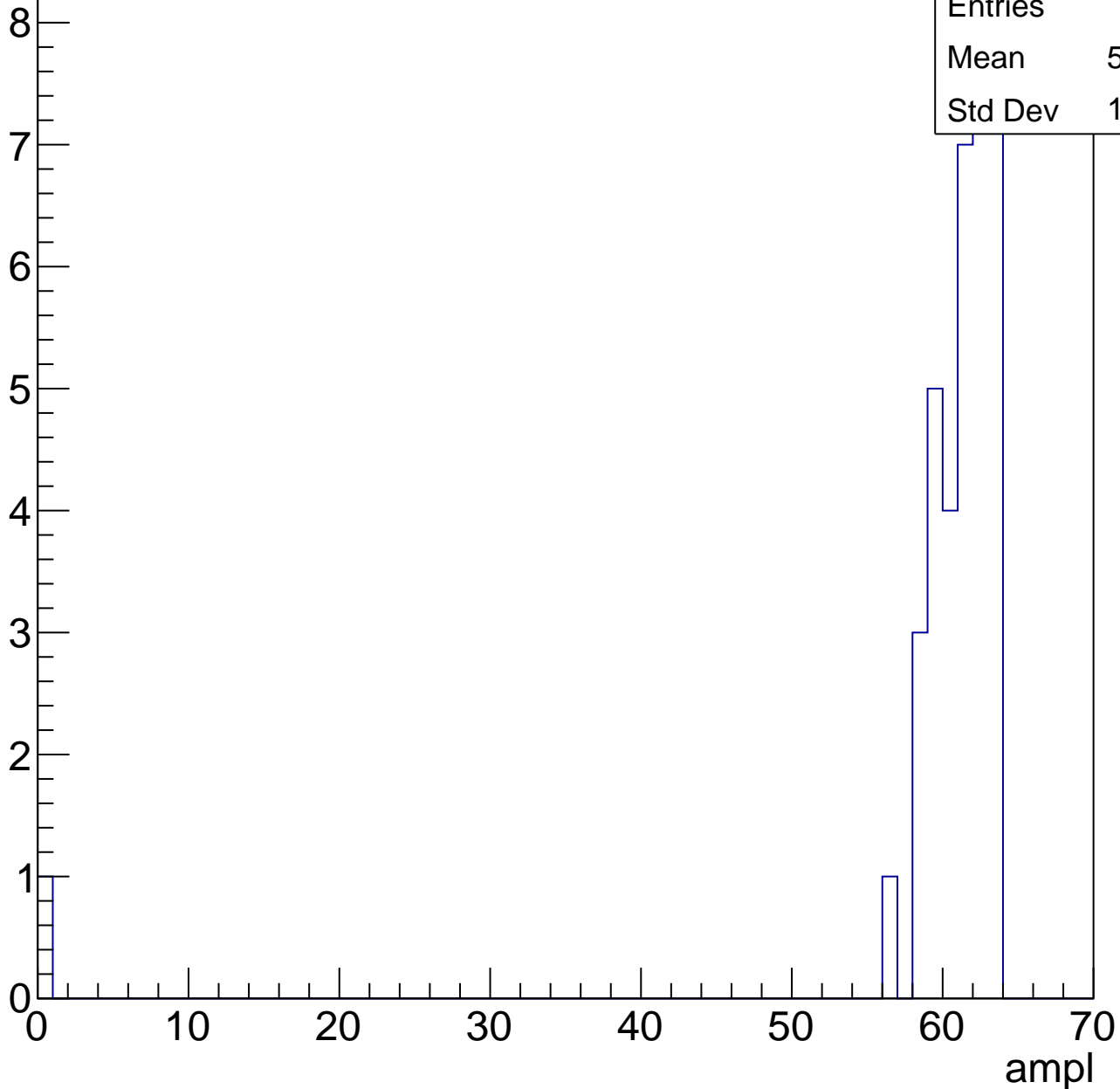


B1L100S, U19-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.24
Std Dev	10.03



B1L100S, U19-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch15, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	28.82
Std Dev	4.56

Gaus mean : 29.7279

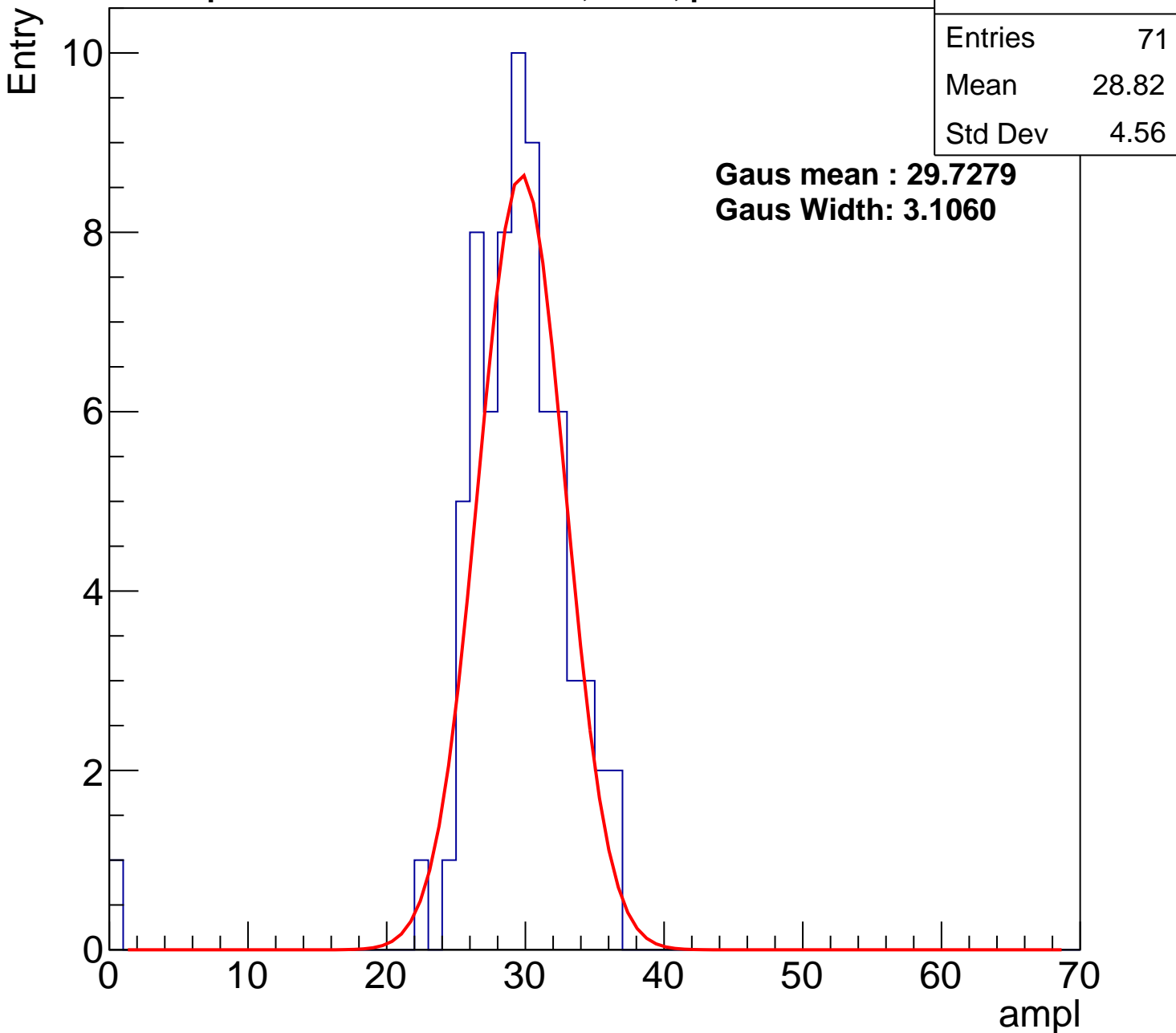
Gaus Width: 3.1060

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch15, adc1

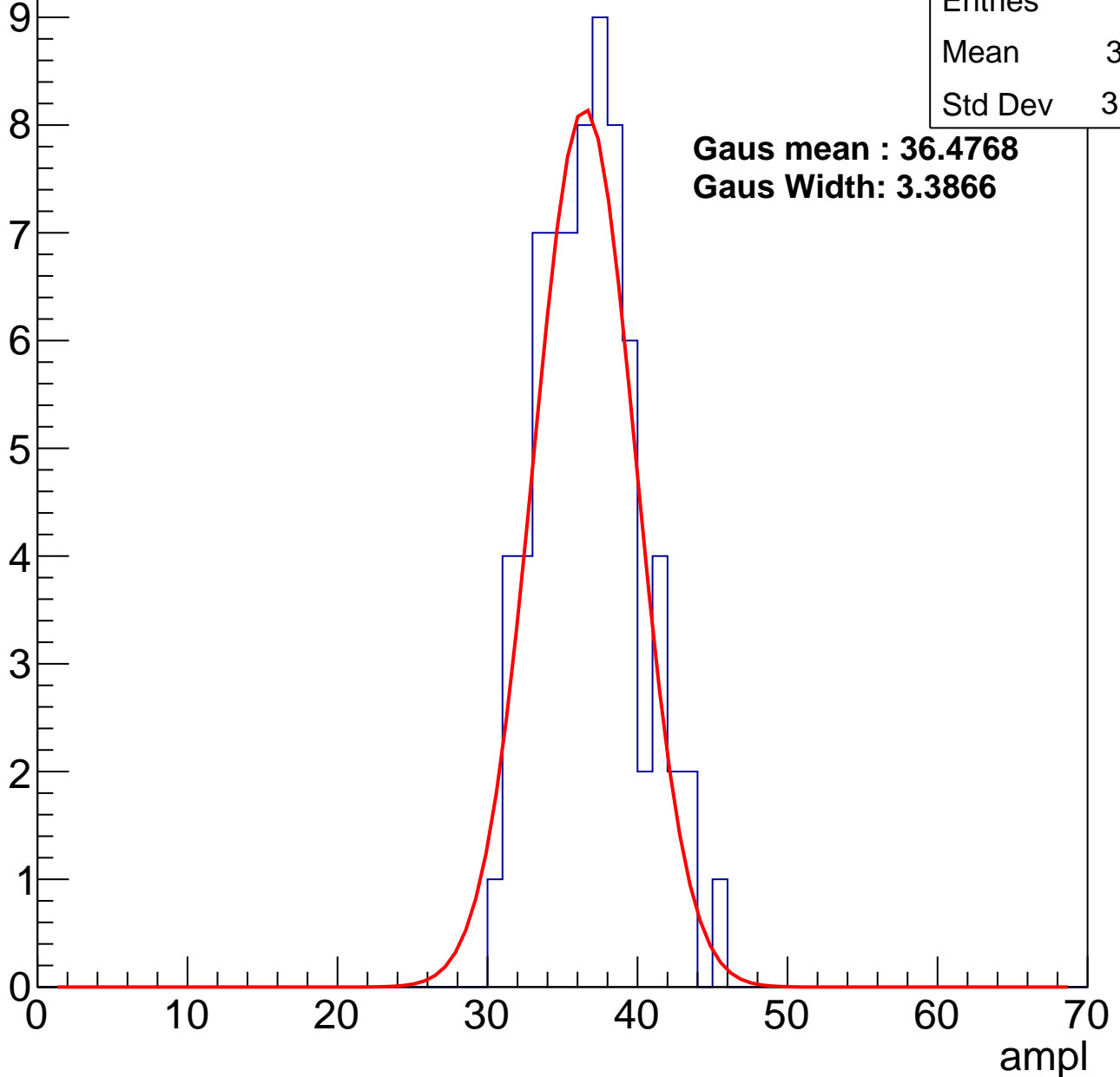
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.31
Std Dev	3.269

Gaus mean : 36.4768

Gaus Width: 3.3866



B1L100S, U19-ch15, adc2

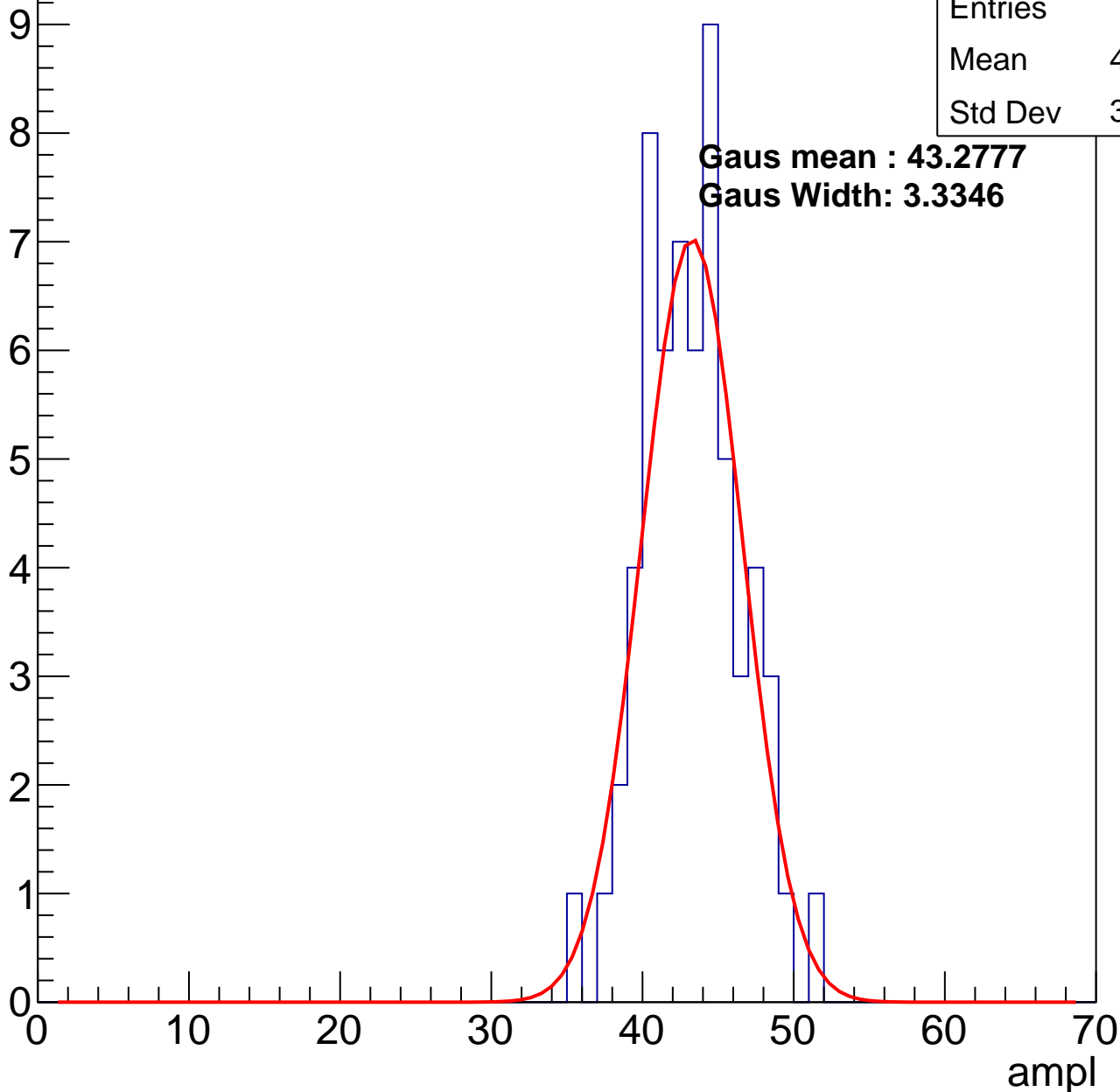
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.84
Std Dev	3.163

Gaus mean : 43.2777

Gaus Width: 3.3346



B1L100S, U19-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

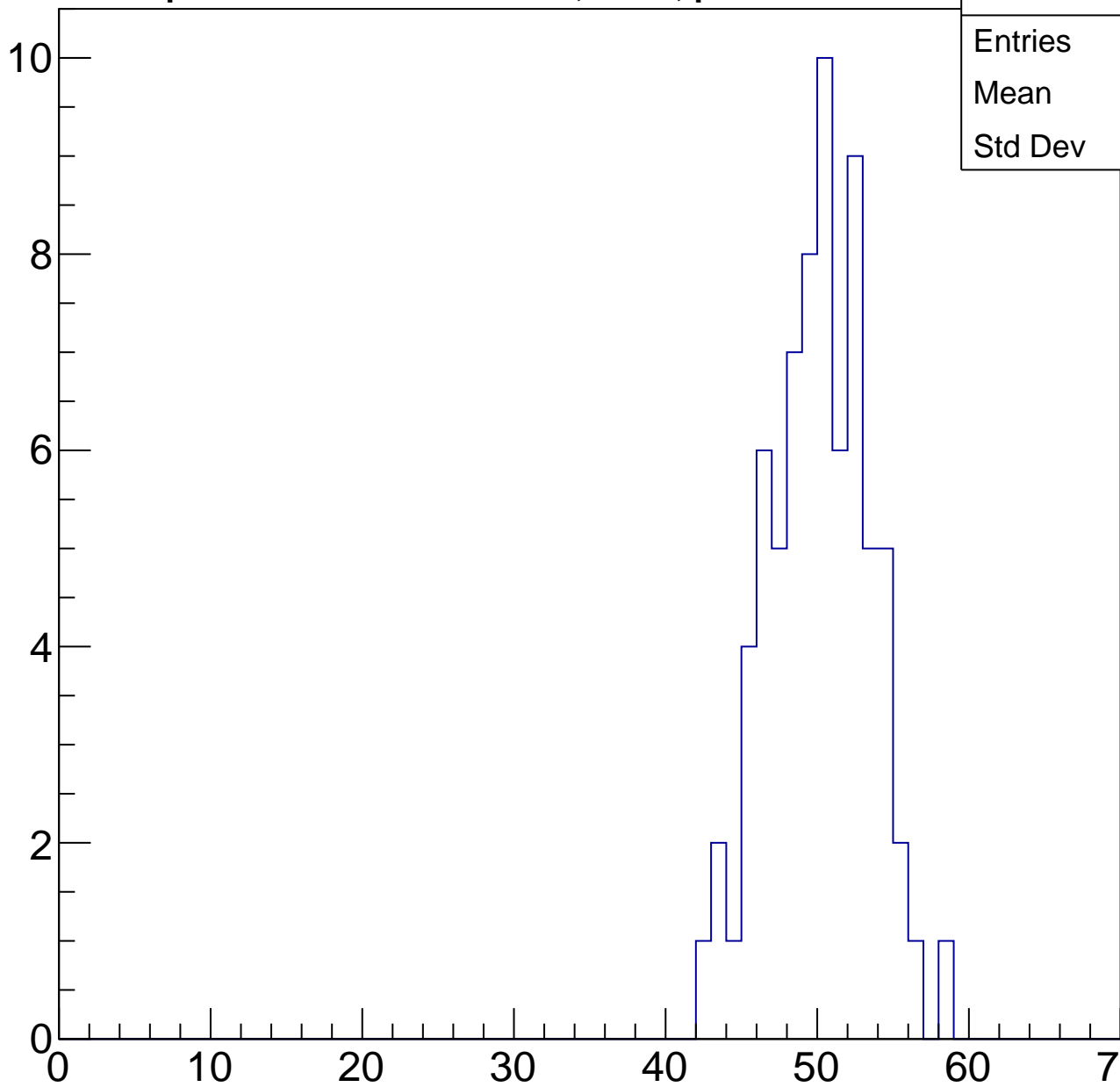
Entries	73
Mean	49.64
Std Dev	3.275

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

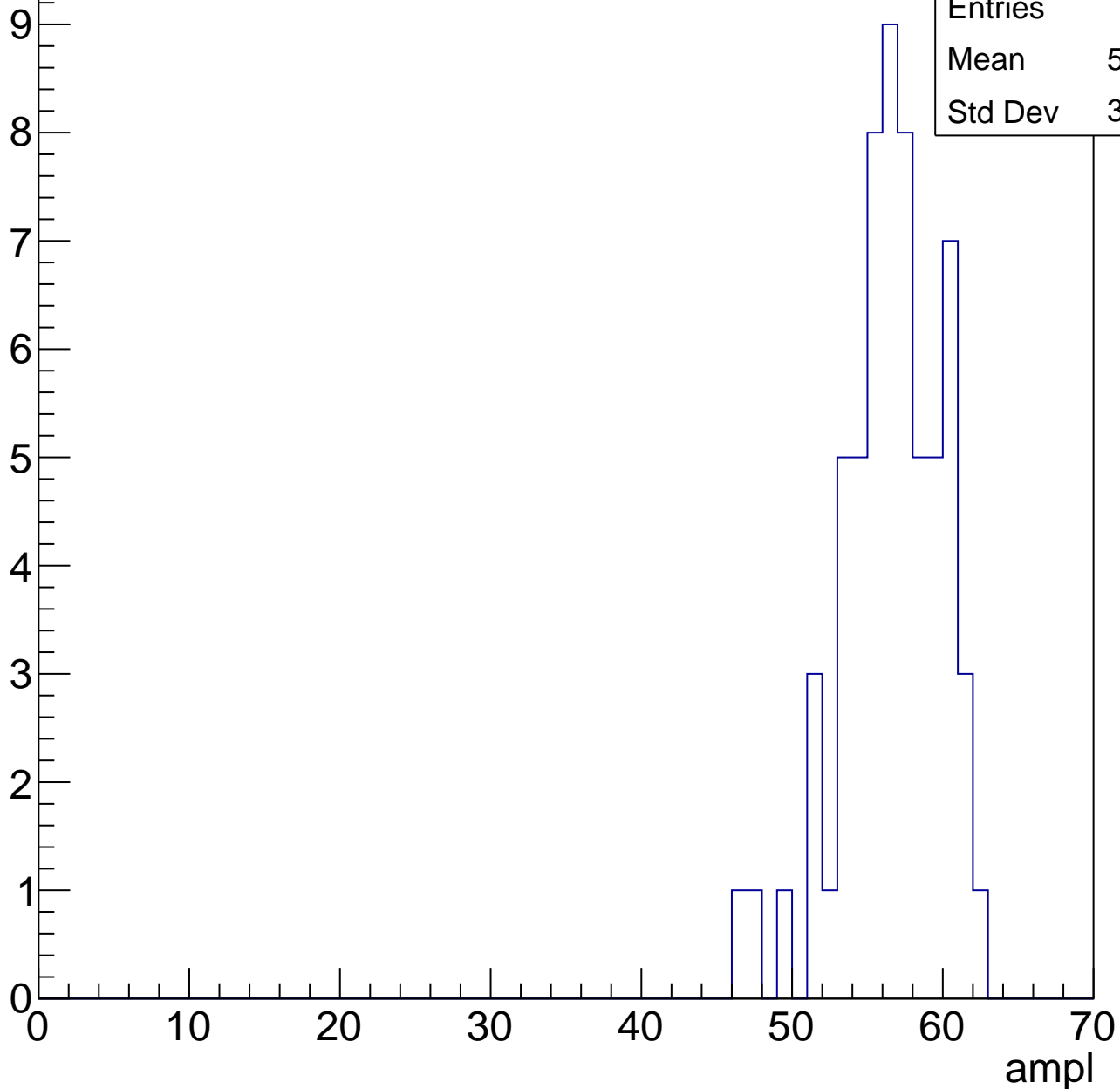
ampl



B1L100S, U19-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



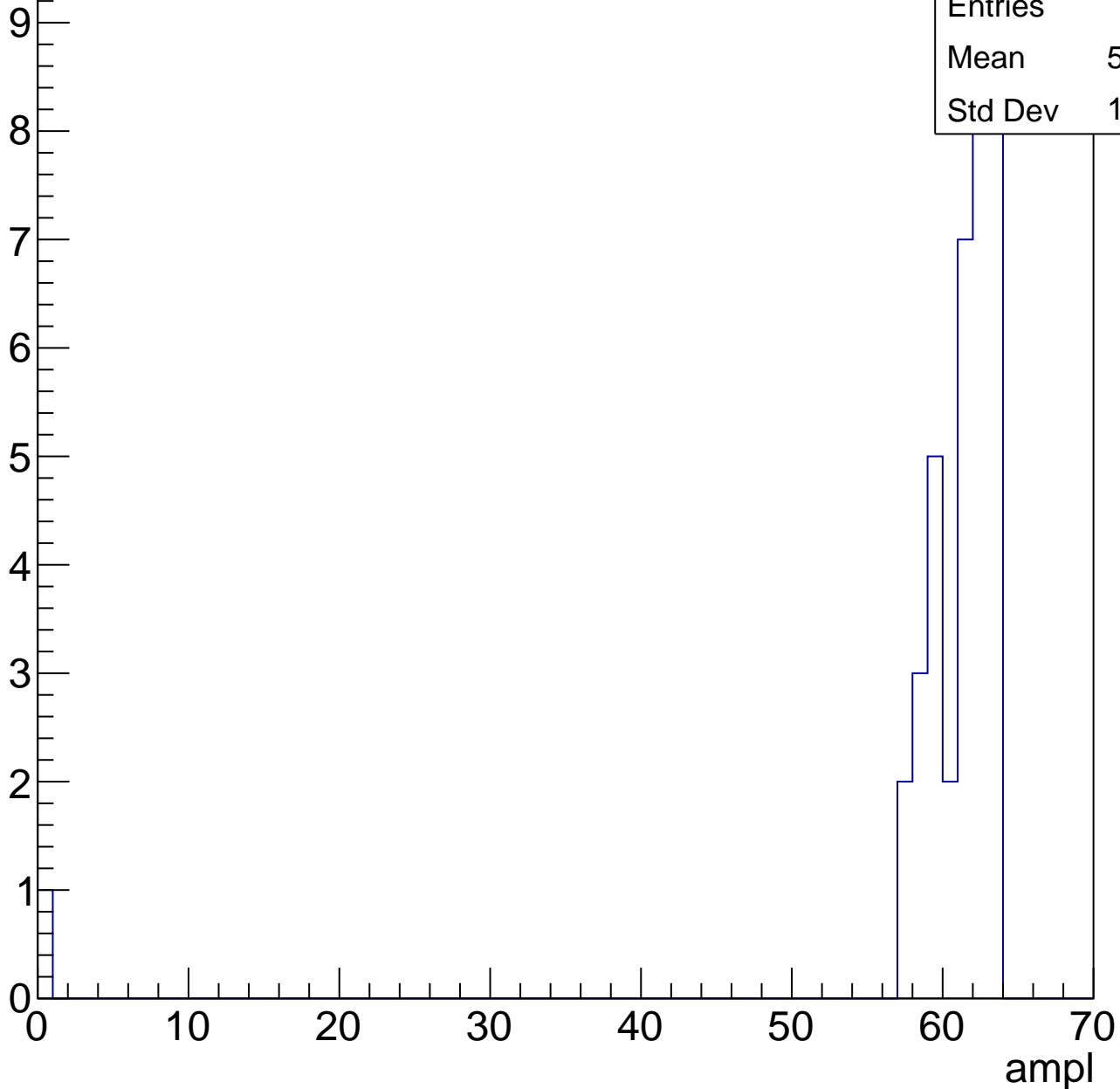
Entries	63
Mean	56.06
Std Dev	3.309

B1L100S, U19-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

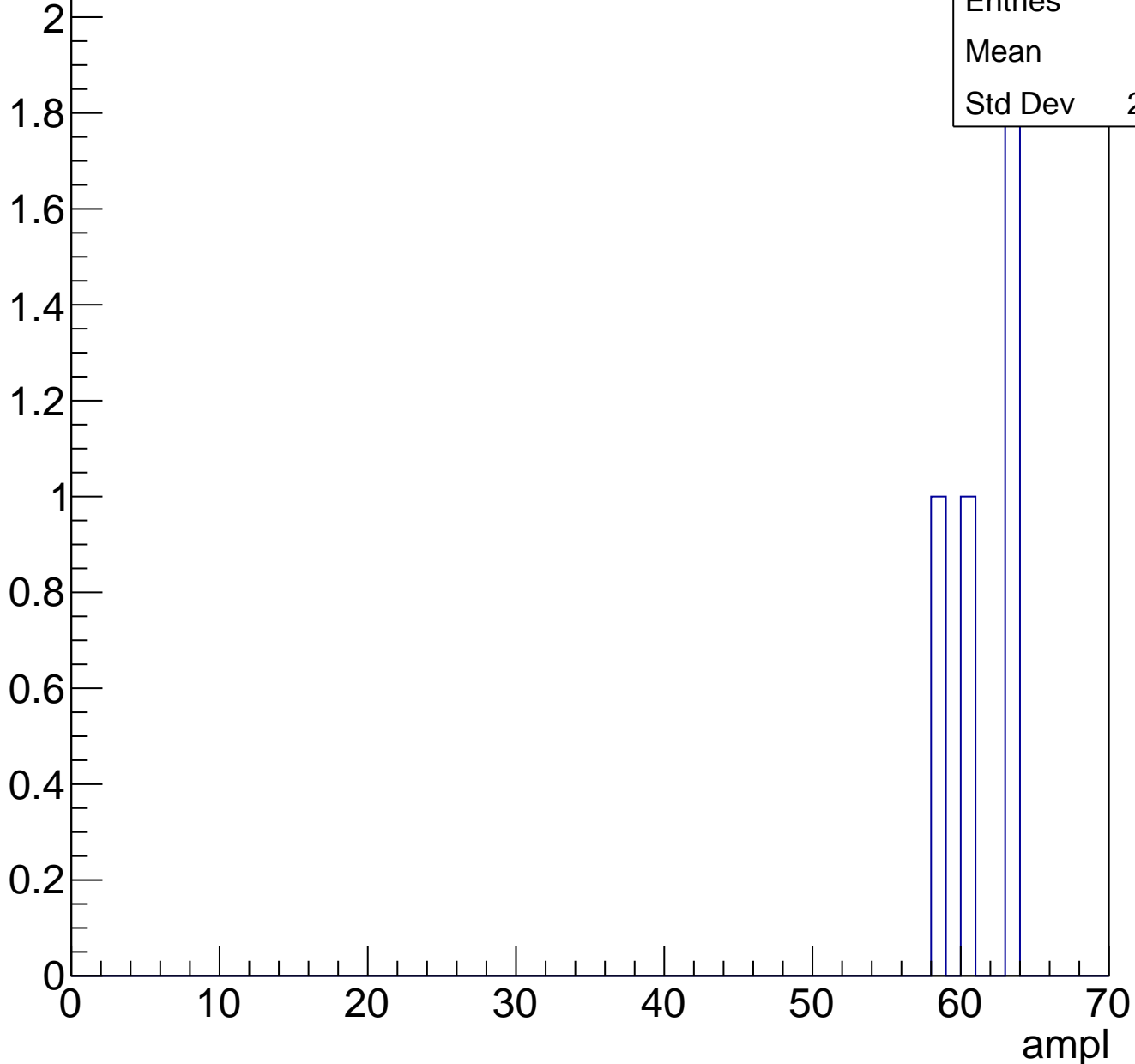
Entries	37
Mean	59.24
Std Dev	10.04



B1L100S, U19-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch16, adc0

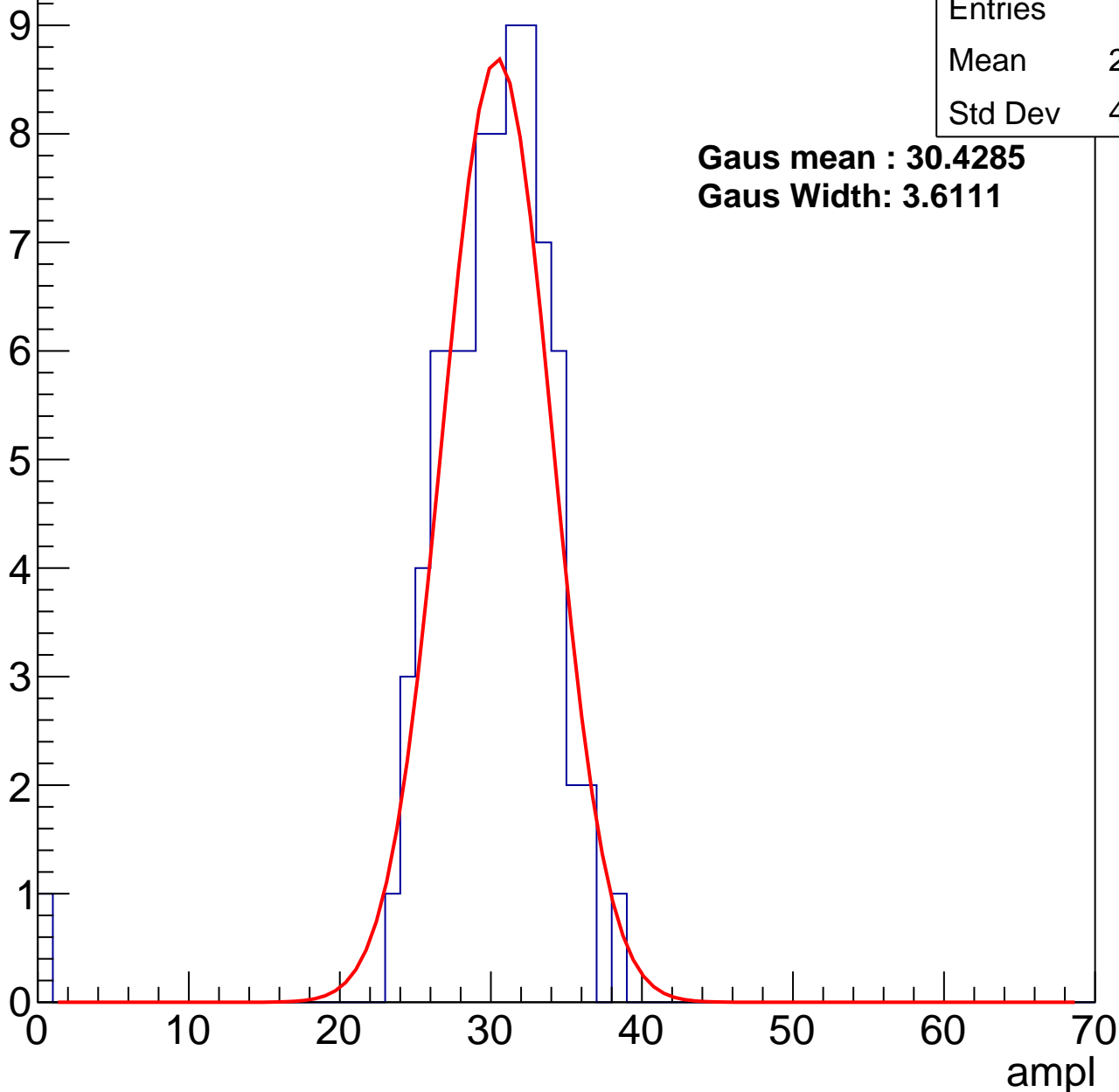
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	29.56
Std Dev	4.657

Gaus mean : 30.4285

Gaus Width: 3.6111



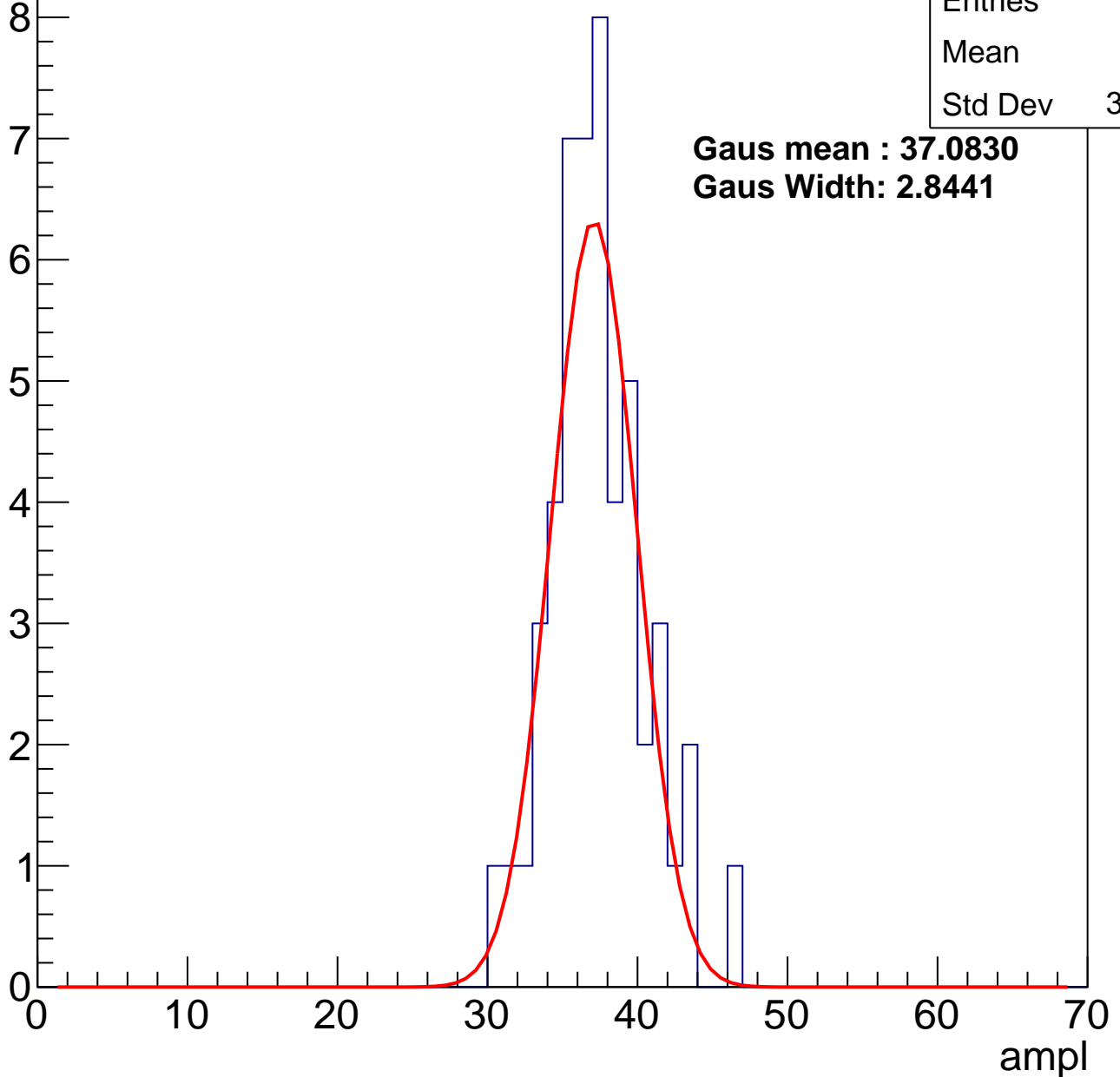
B1L100S, U19-ch16, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	36.9
Std Dev	3.151

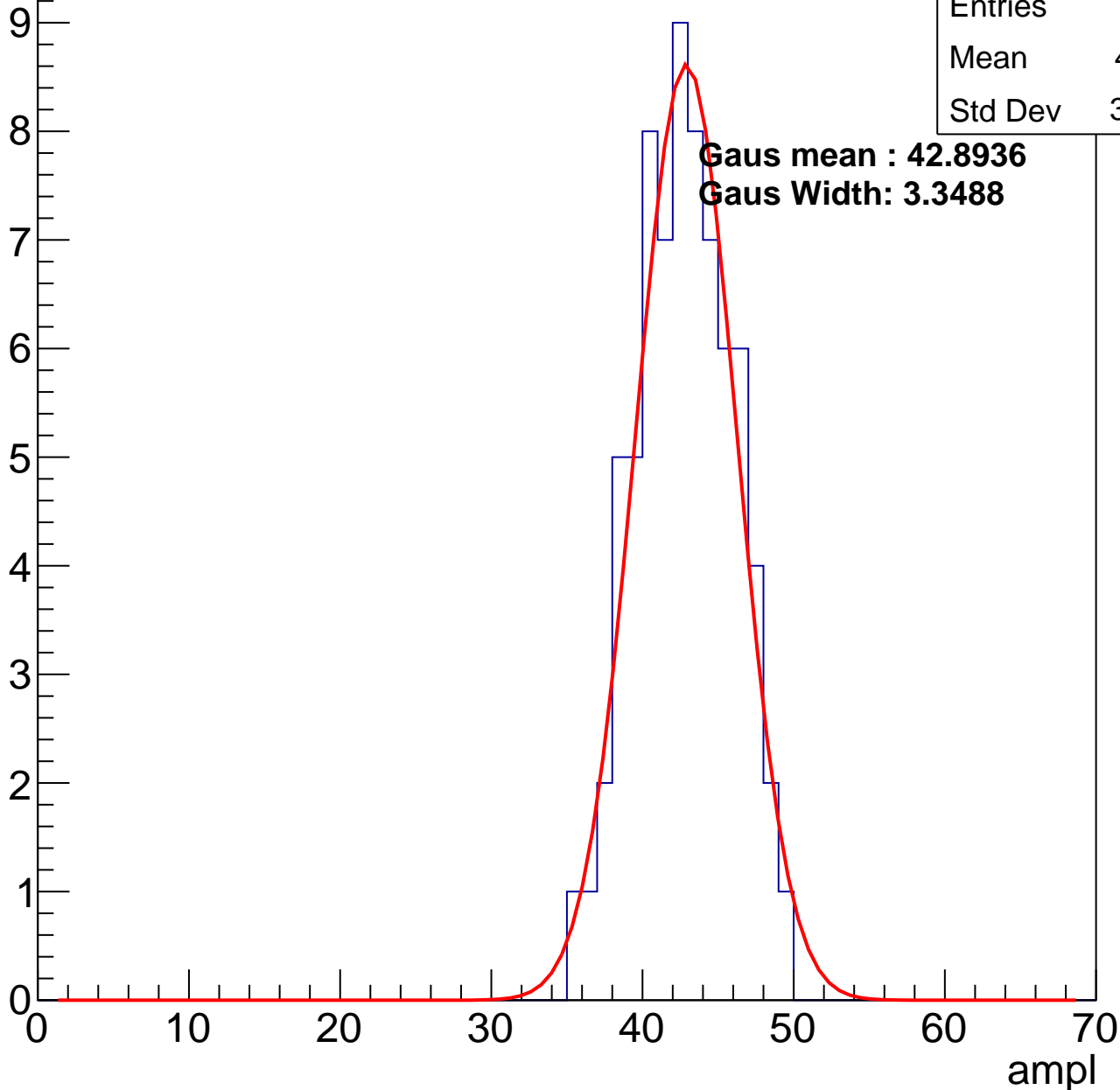
Gaus mean : 37.0830
Gaus Width: 2.8441



B1L100S, U19-ch16, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

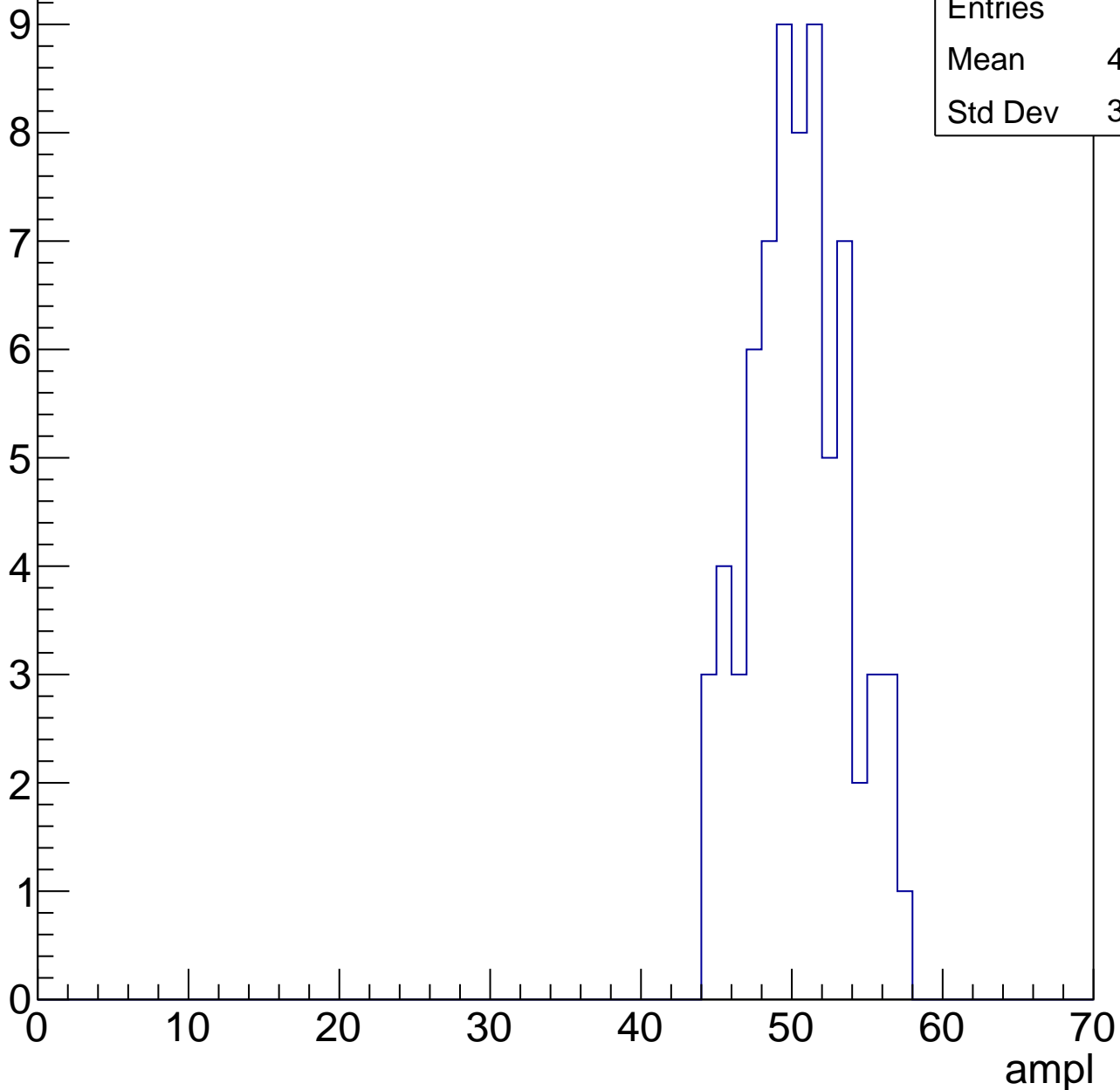


B1L100S, U19-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

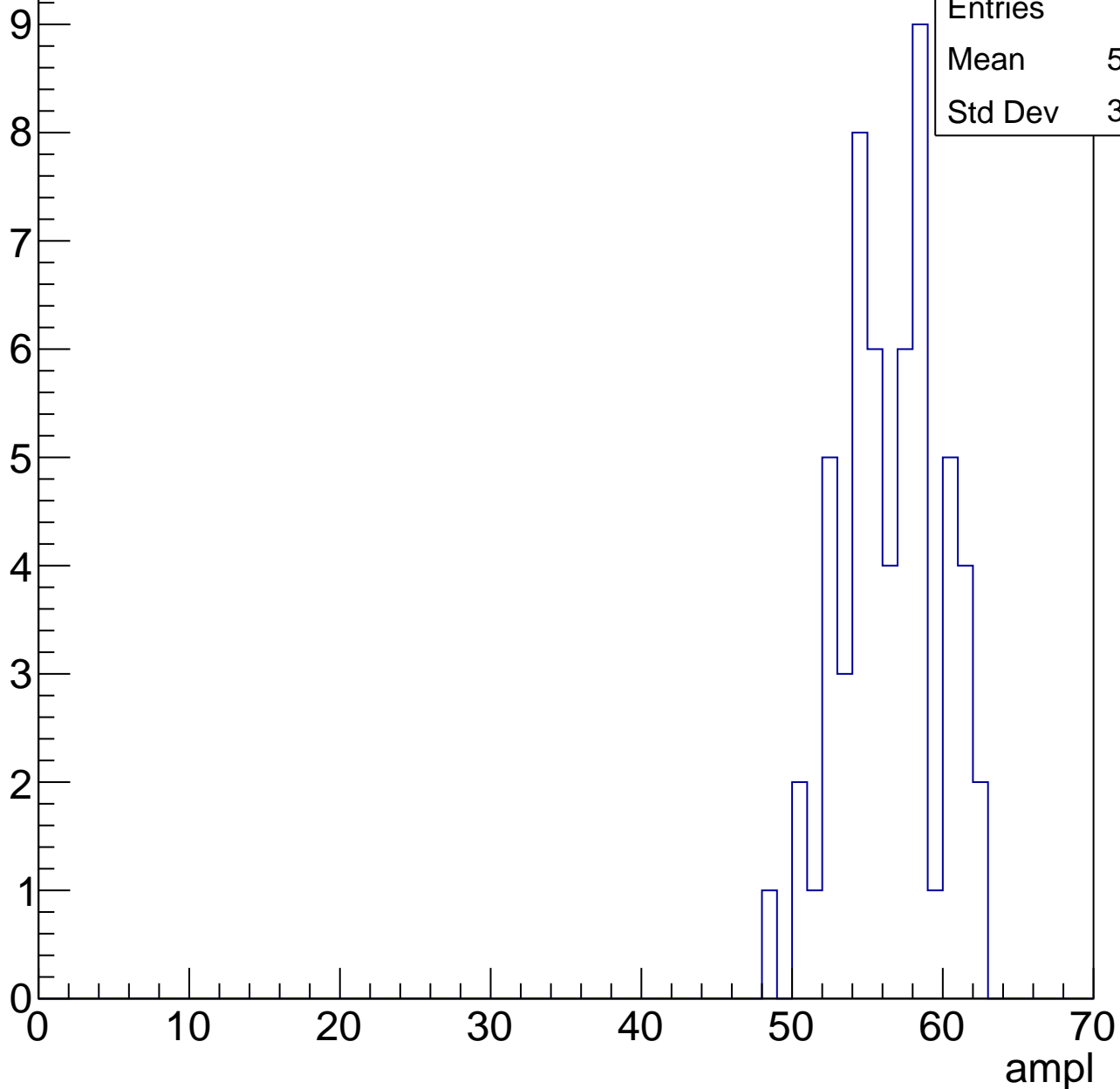
Entries	70
Mean	49.96
Std Dev	3.169



B1L100S, U19-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

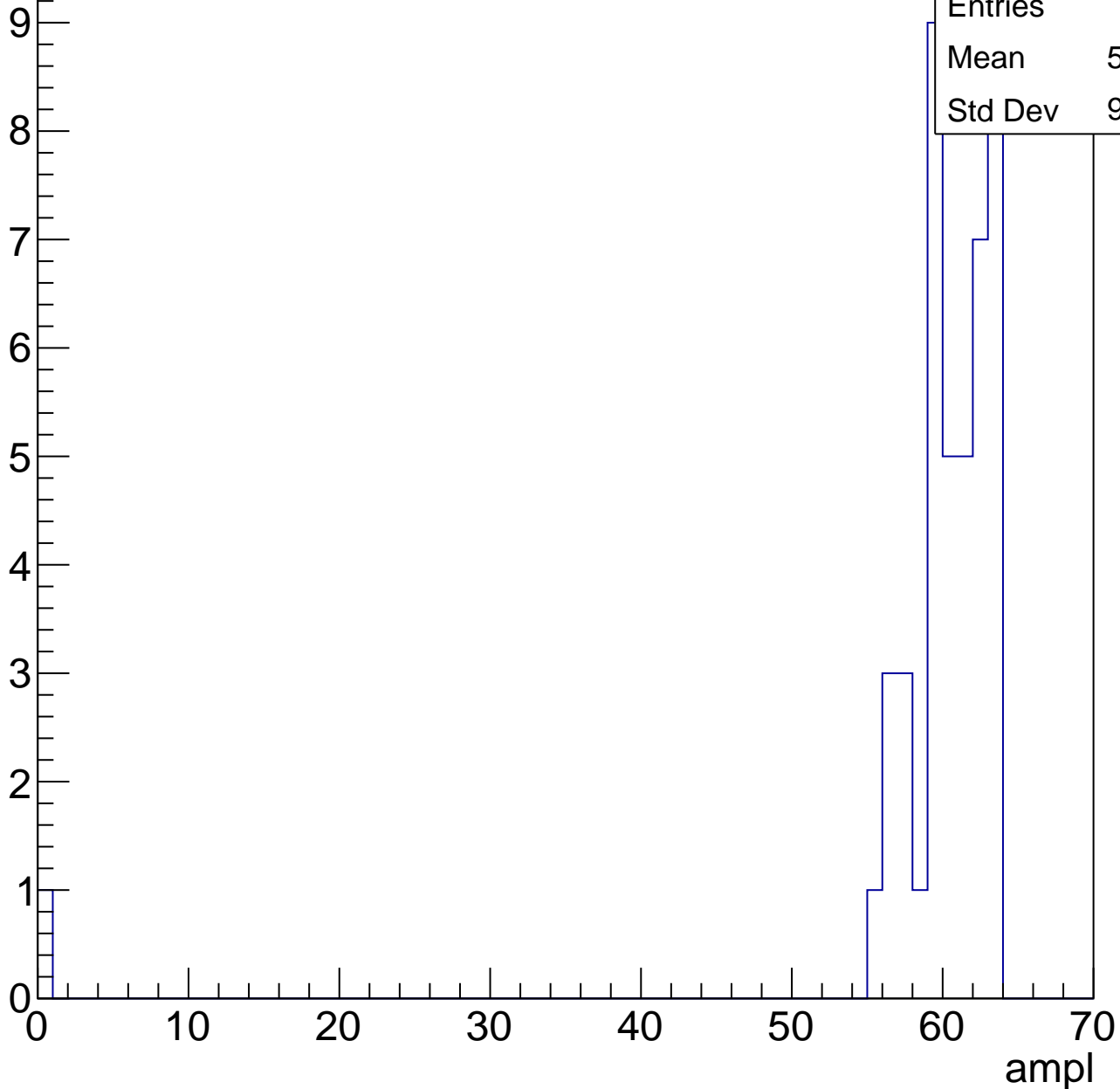


Entries	57
Mean	56.05
Std Dev	3.268

B1L100S, U19-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

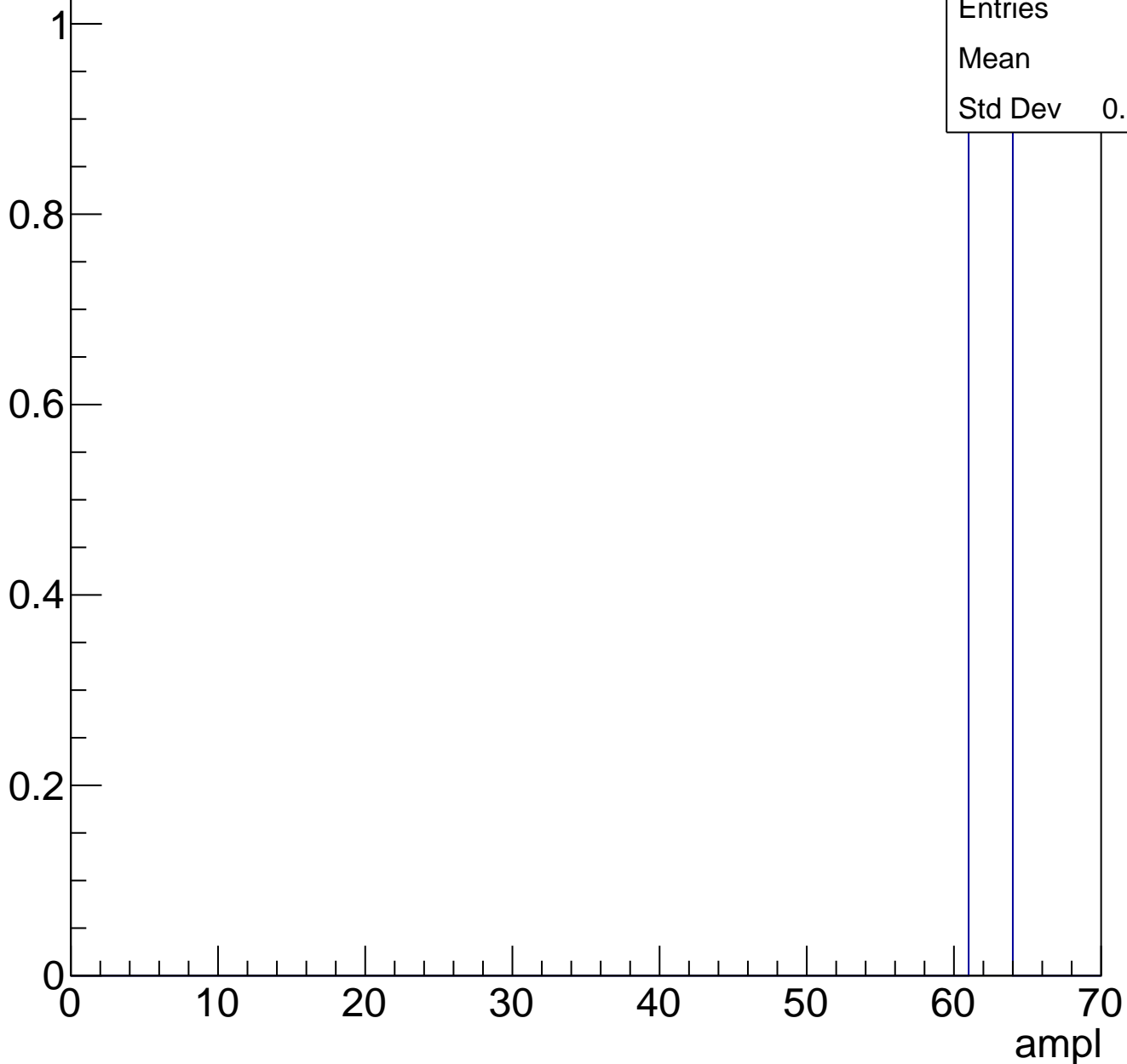
Entry



B1L100S, U19-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch17, adc0

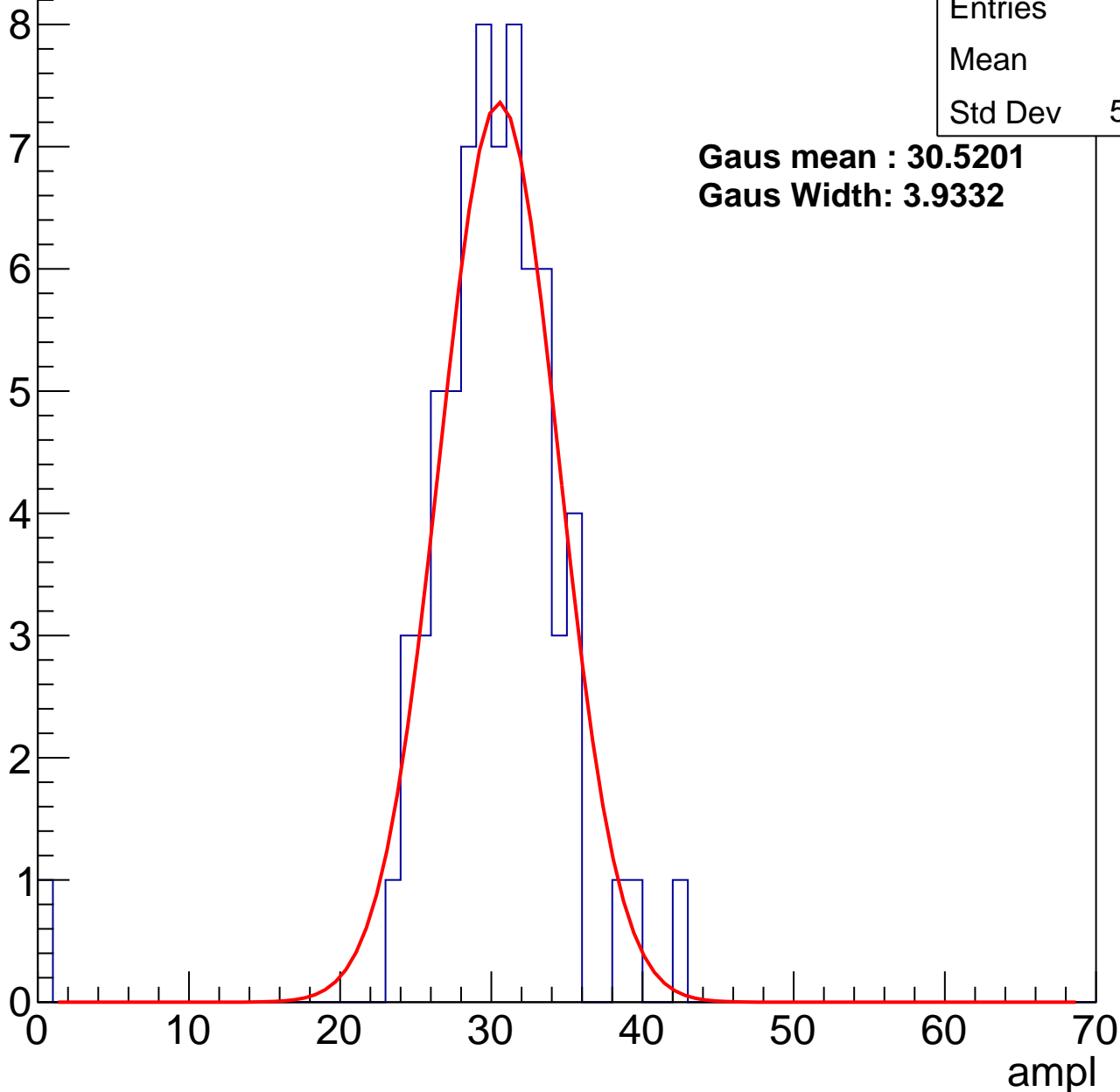
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.6
Std Dev	5.078

Gaus mean : 30.5201

Gaus Width: 3.9332



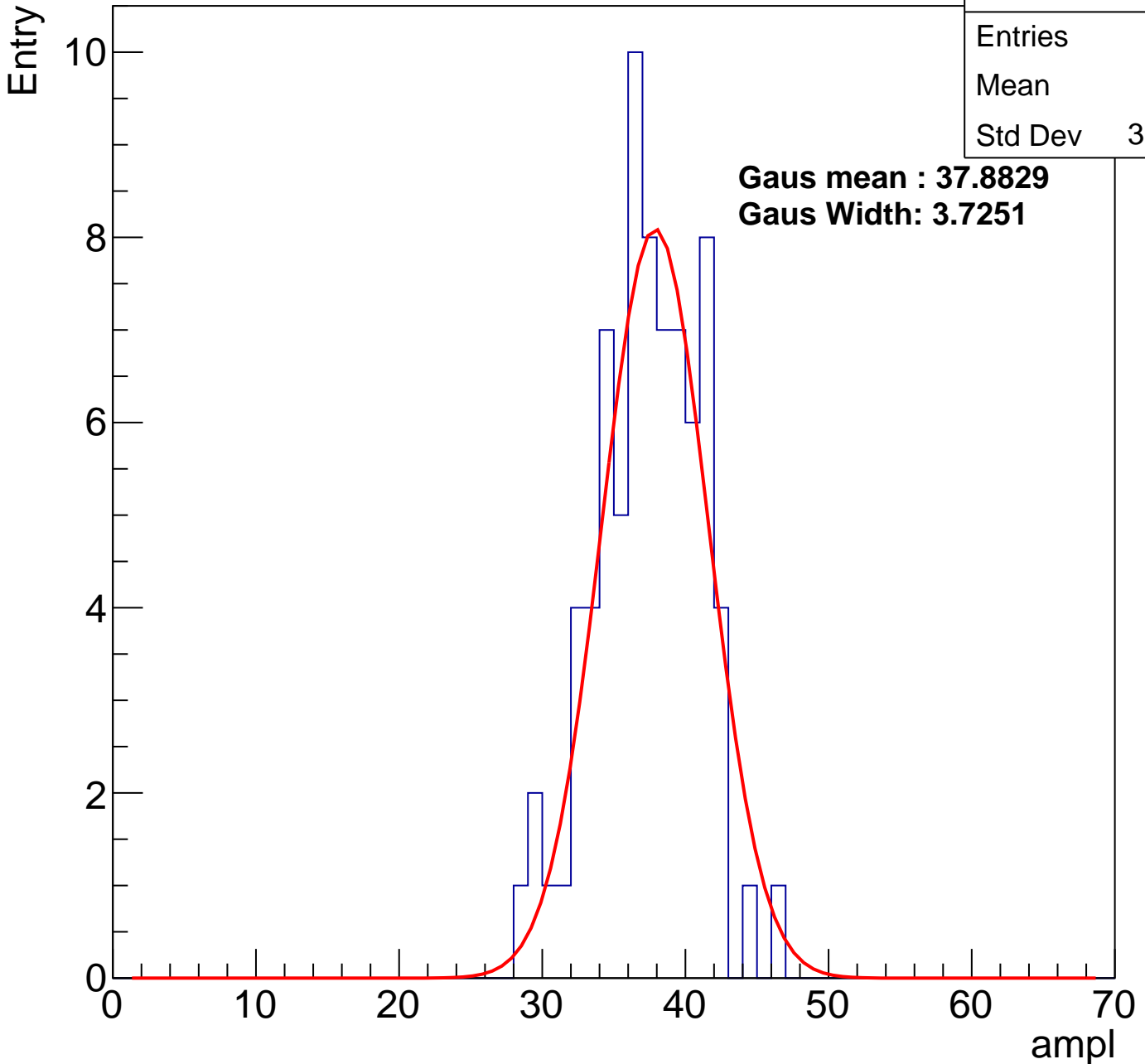
B1L100S, U19-ch17, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.9
Std Dev	3.599

Gaus mean : 37.8829

Gaus Width: 3.7251



B1L100S, U19-ch17, adc2

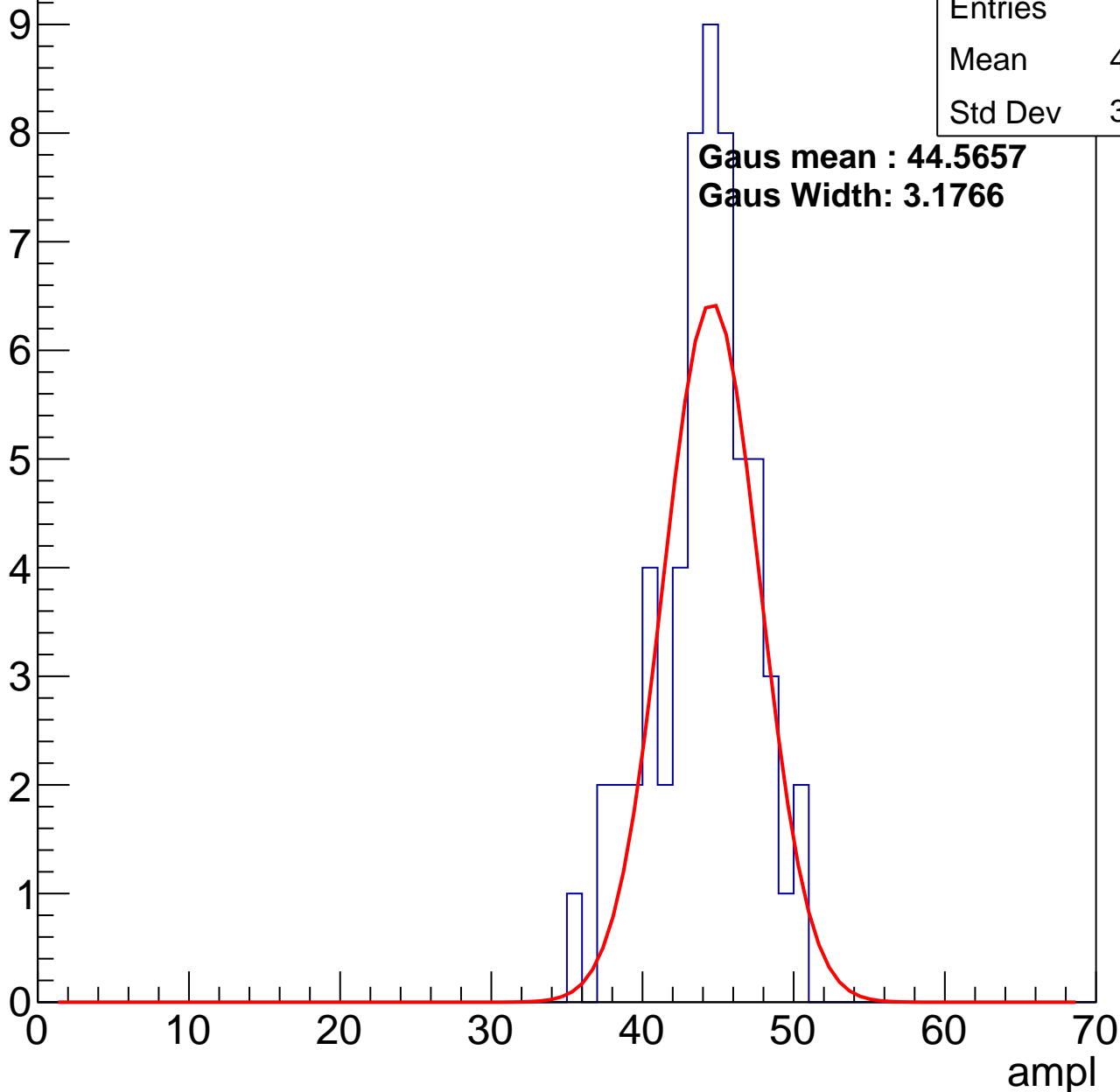
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.64
Std Dev	3.263

Gaus mean : 44.5657

Gaus Width: 3.1766



B1L100S, U19-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

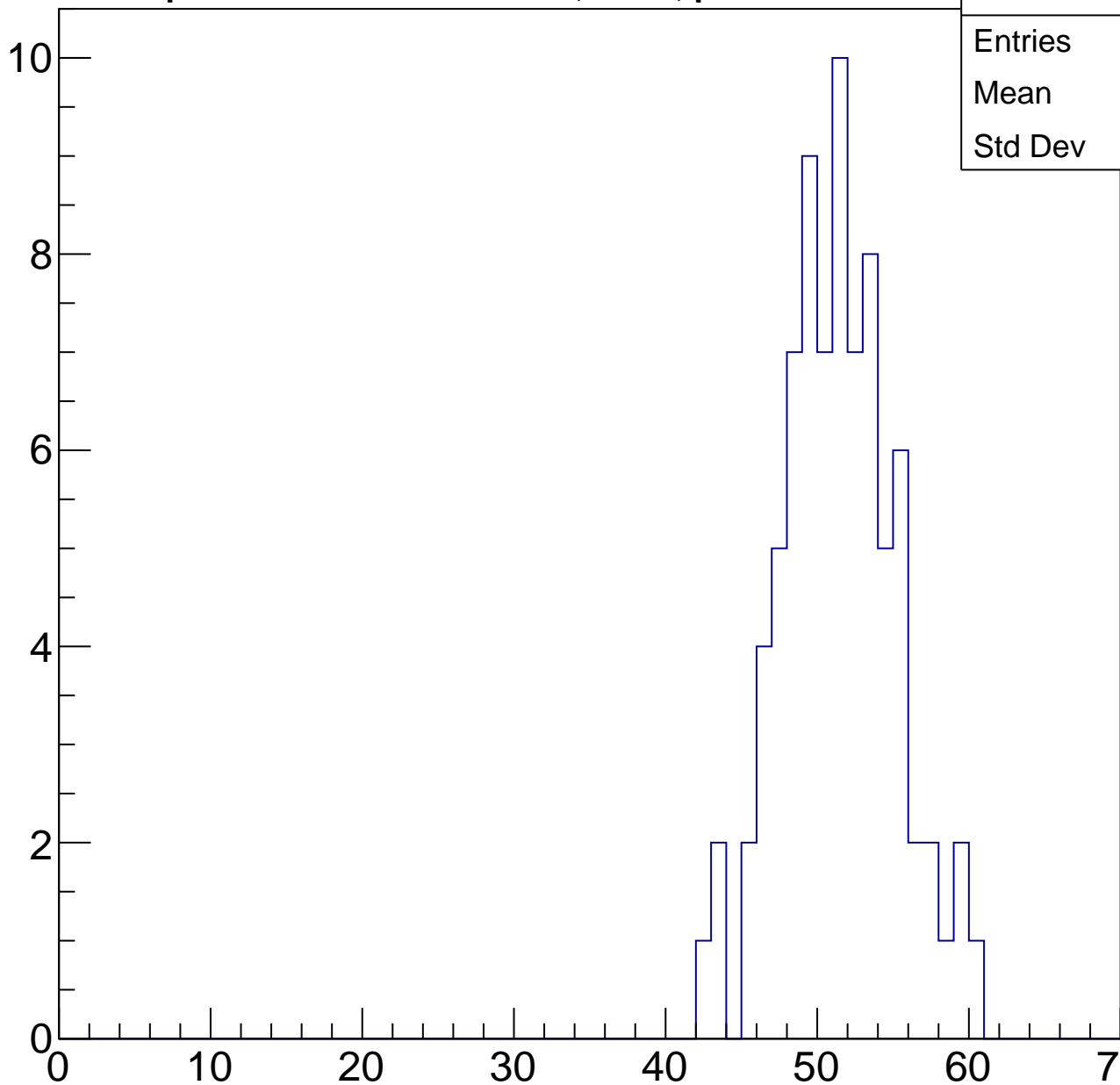
Entries	81
Mean	50.91
Std Dev	3.729

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

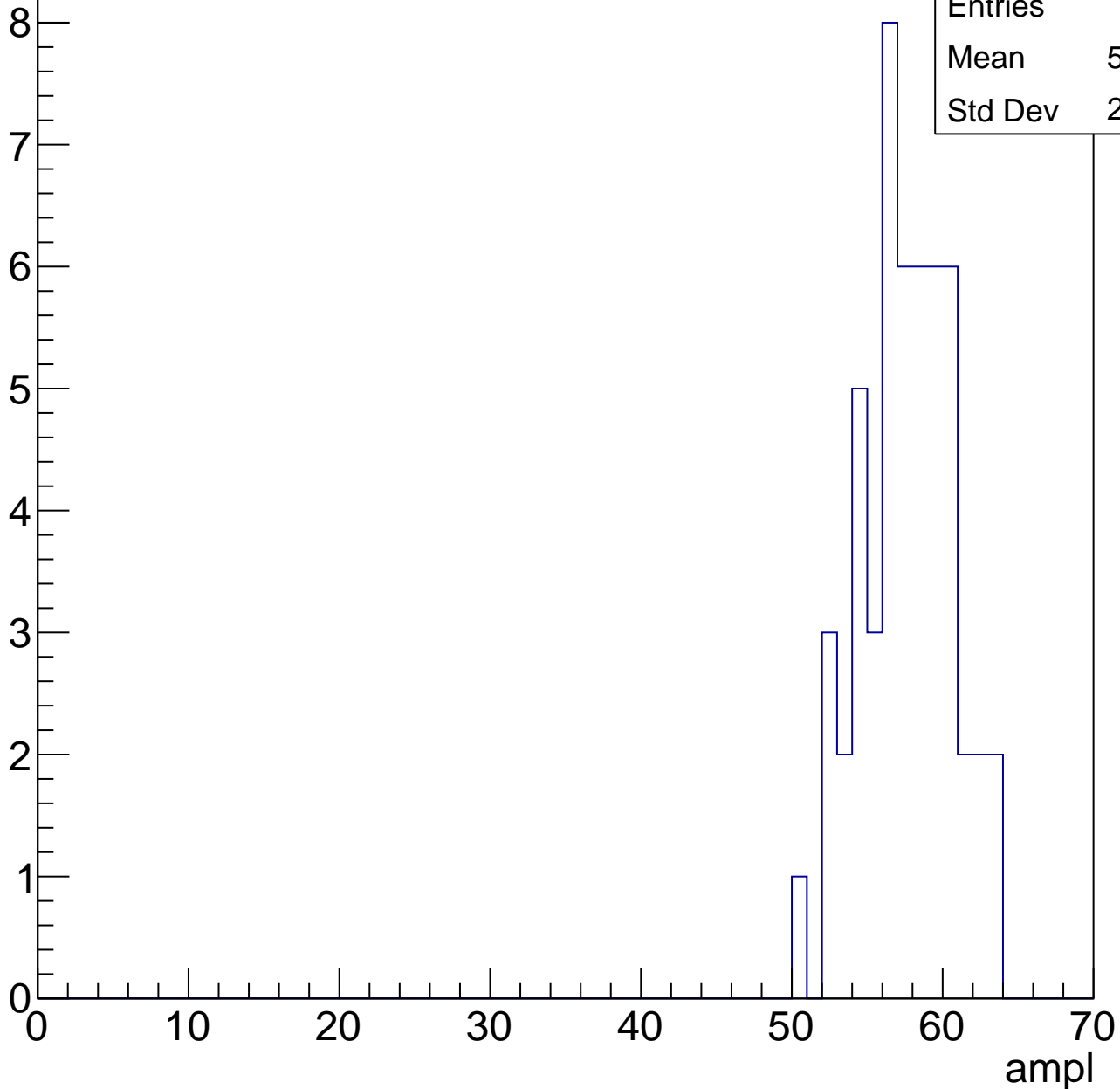


B1L100S, U19-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	57.13
Std Dev	2.968

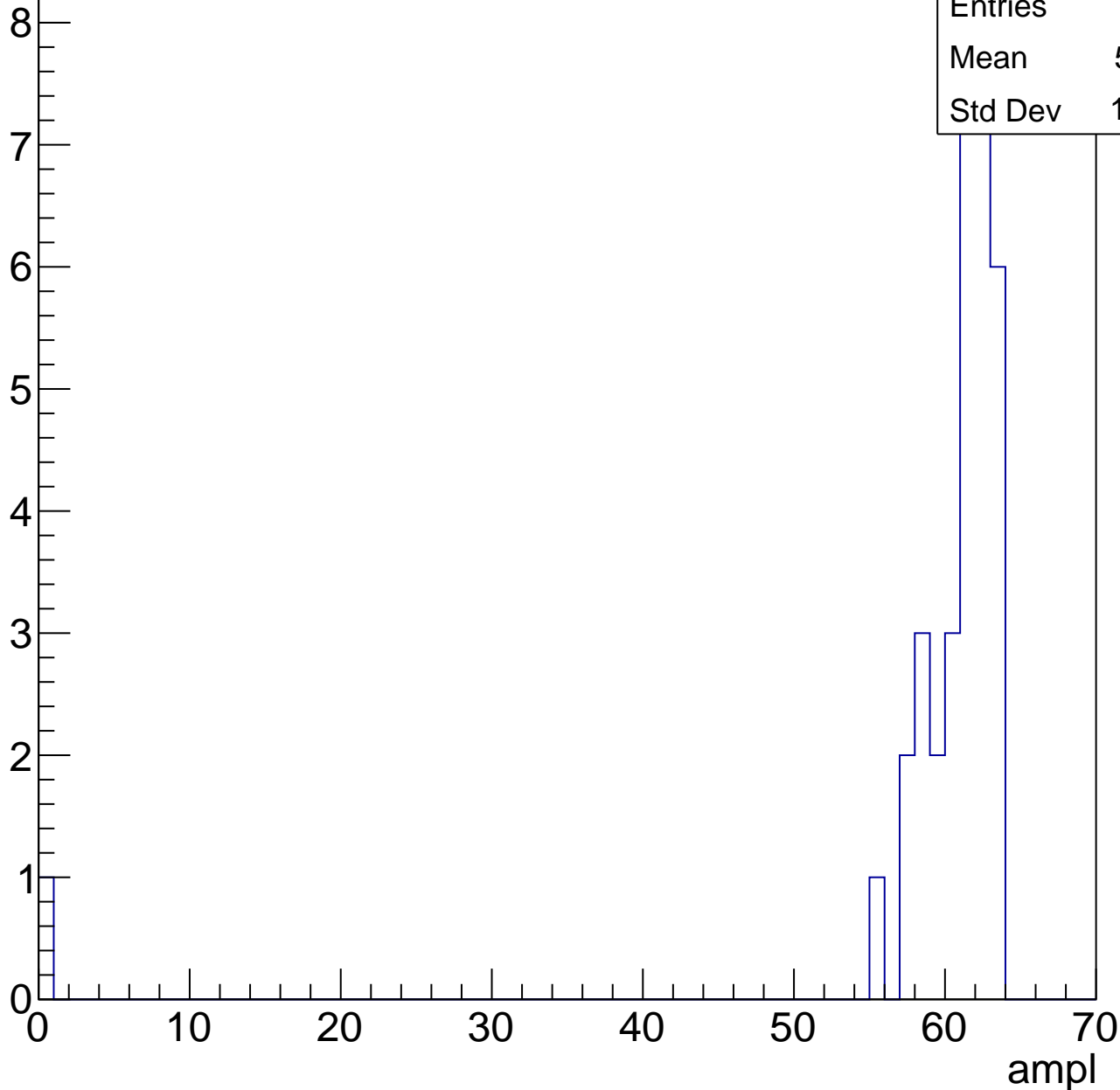


B1L100S, U19-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.91
Std Dev	10.45



B1L100S, U19-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch18, adc0

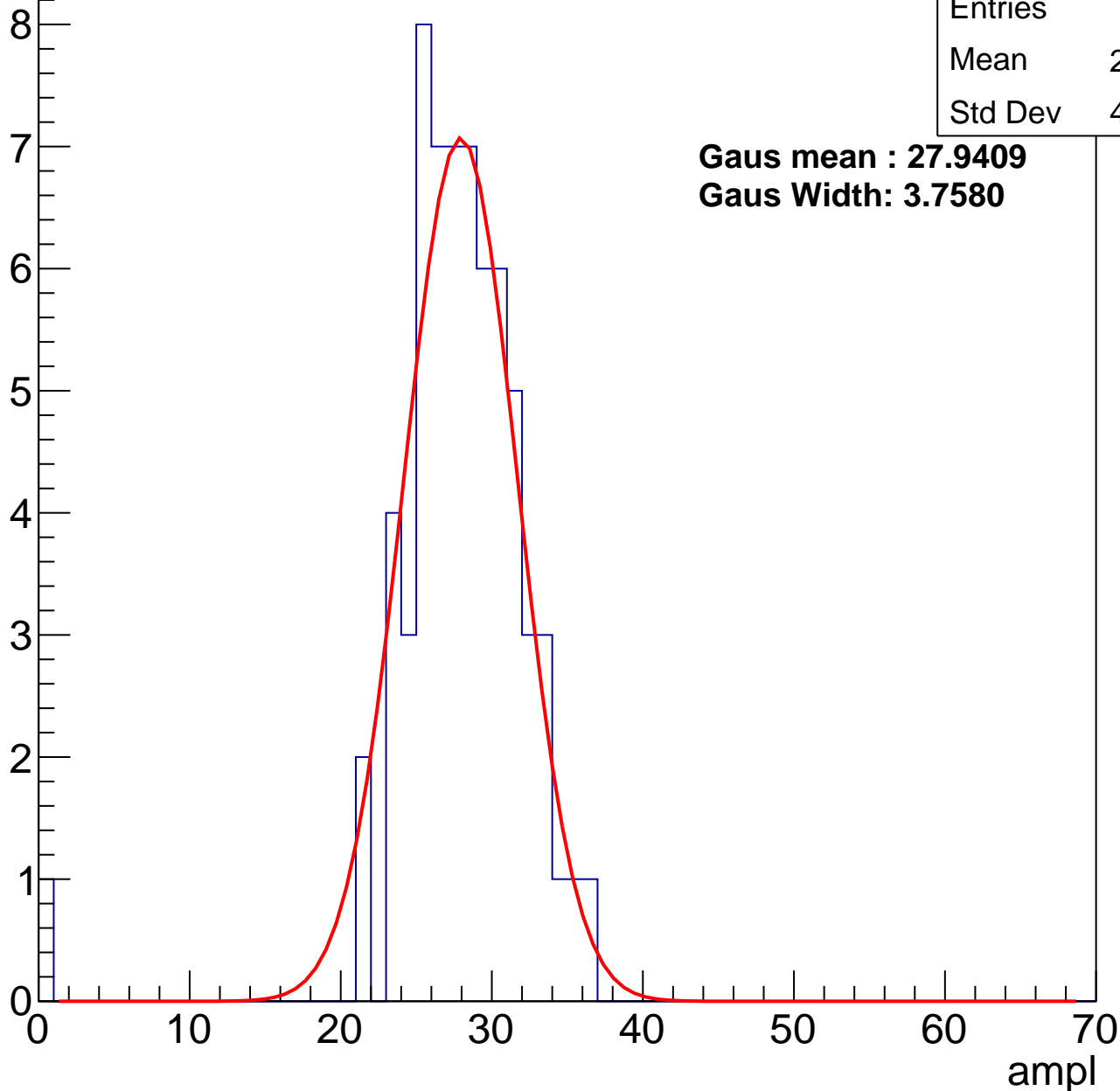
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.42
Std Dev	4.742

Gaus mean : 27.9409

Gaus Width: 3.7580



B1L100S, U19-ch18, adc1

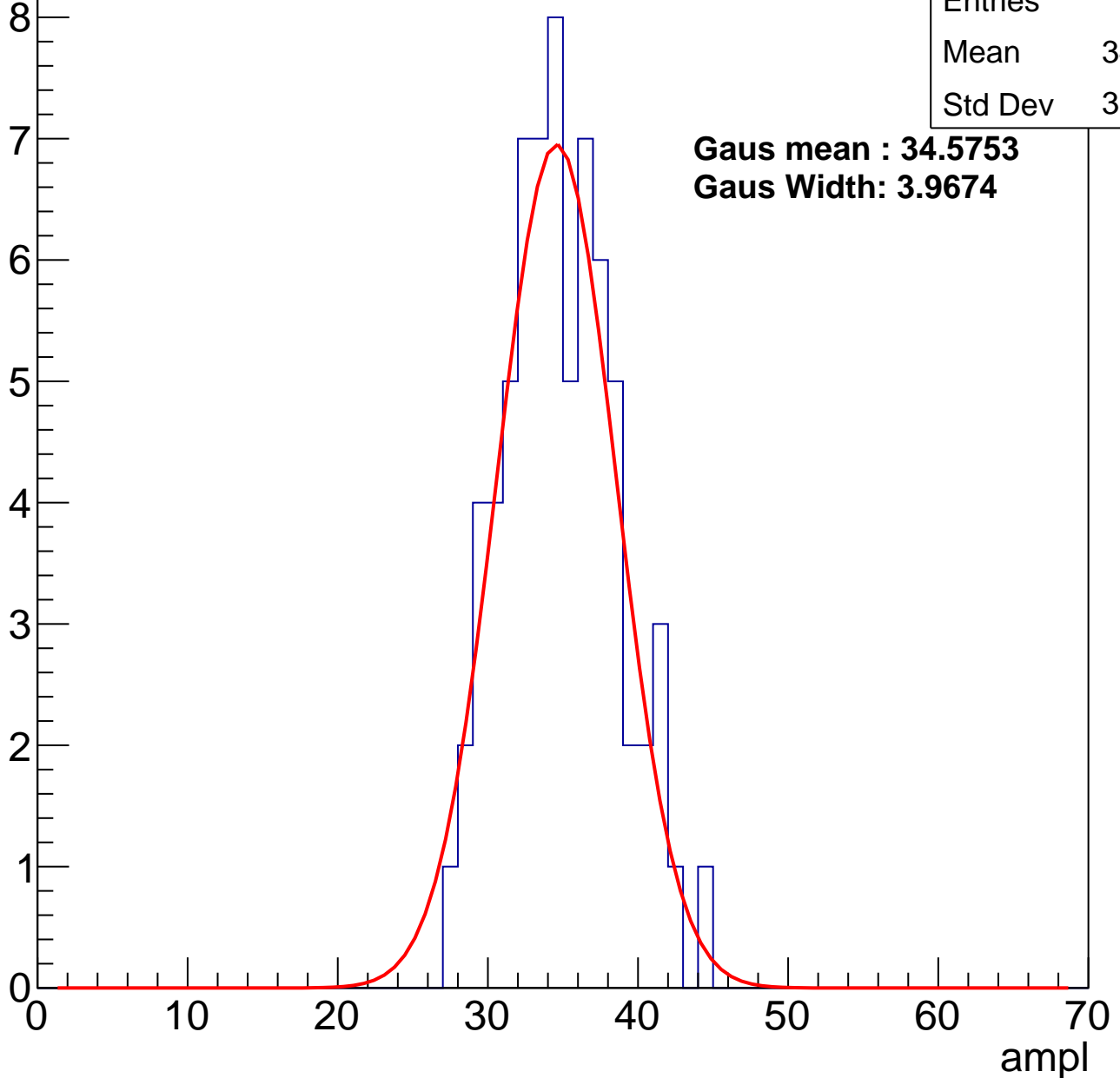
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.39
Std Dev	3.693

Gaus mean : 34.5753

Gaus Width: 3.9674



B1L100S, U19-ch18, adc2

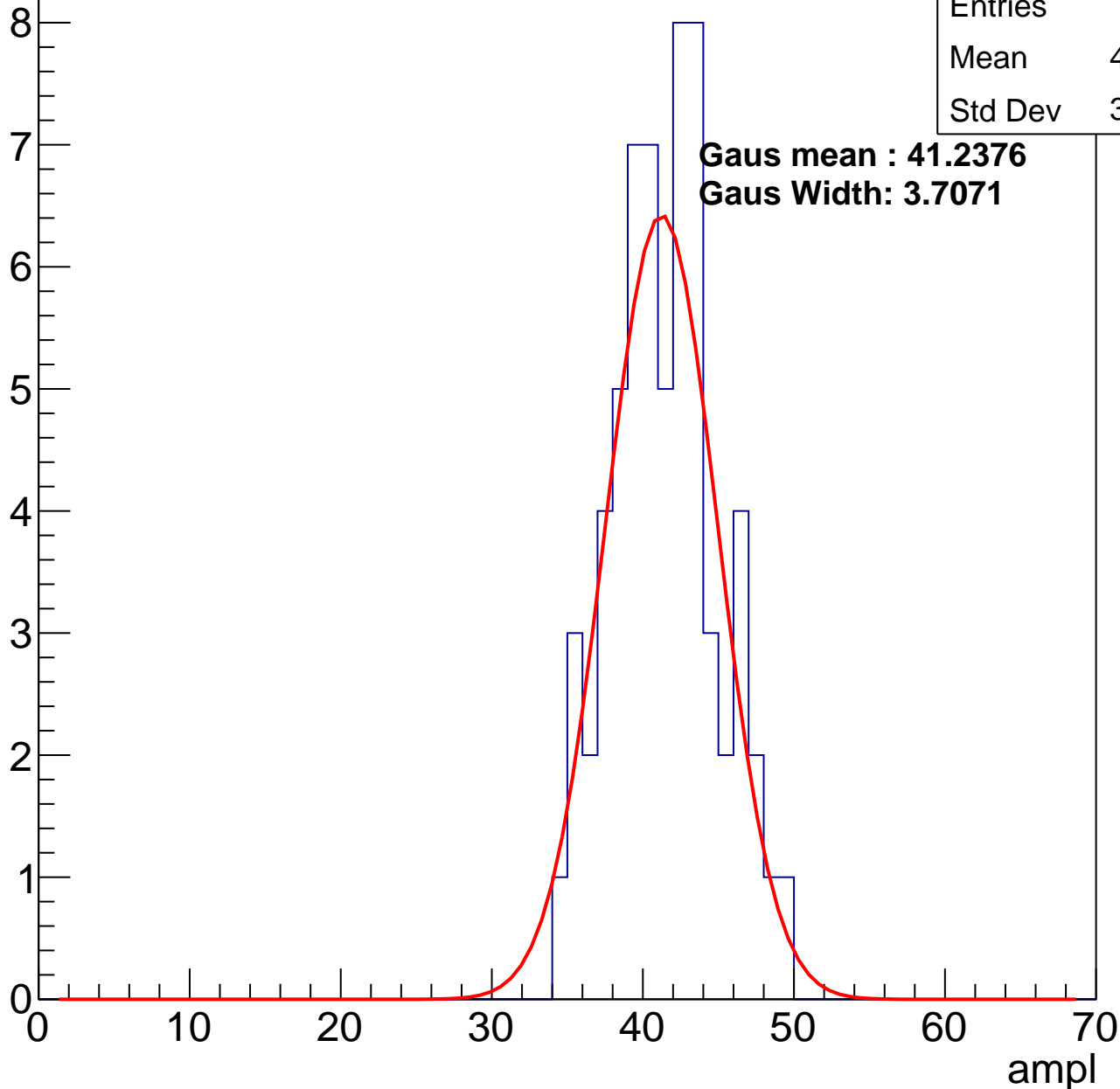
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	41.02
Std Dev	3.416

Gaus mean : 41.2376

Gaus Width: 3.7071

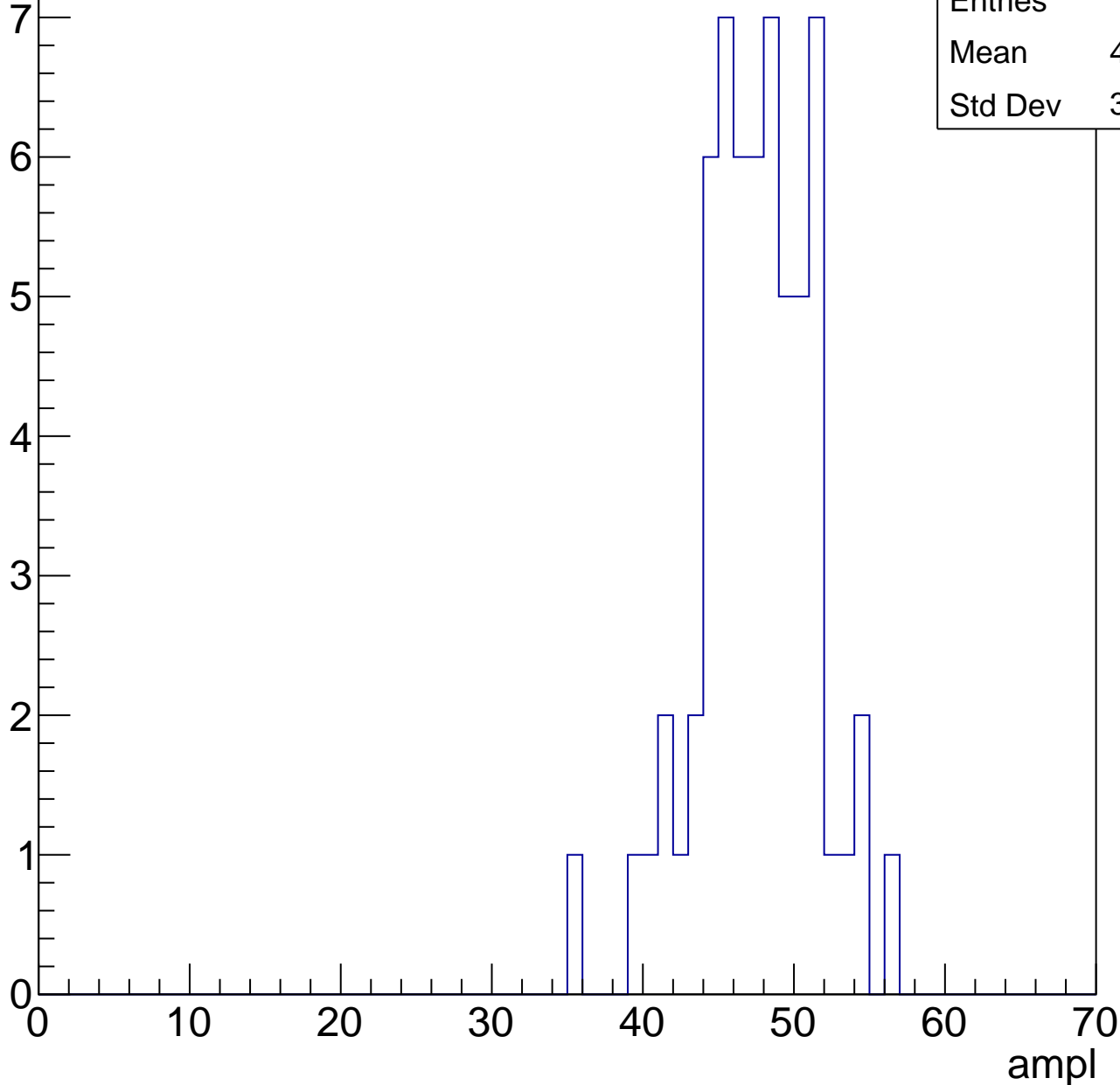


B1L100S, U19-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	47.06
Std Dev	3.805

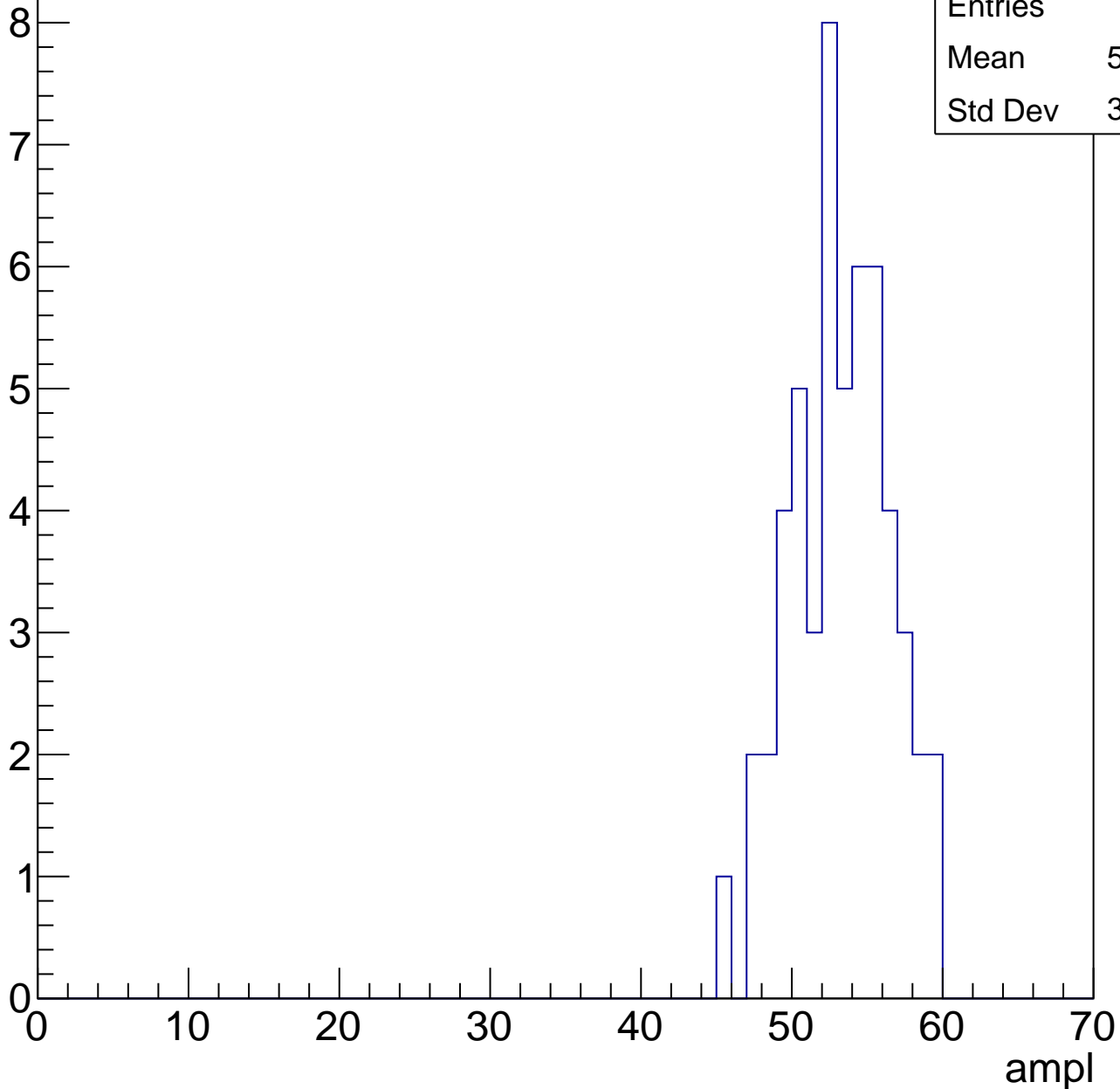


B1L100S, U19-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	52.79
Std Dev	3.218

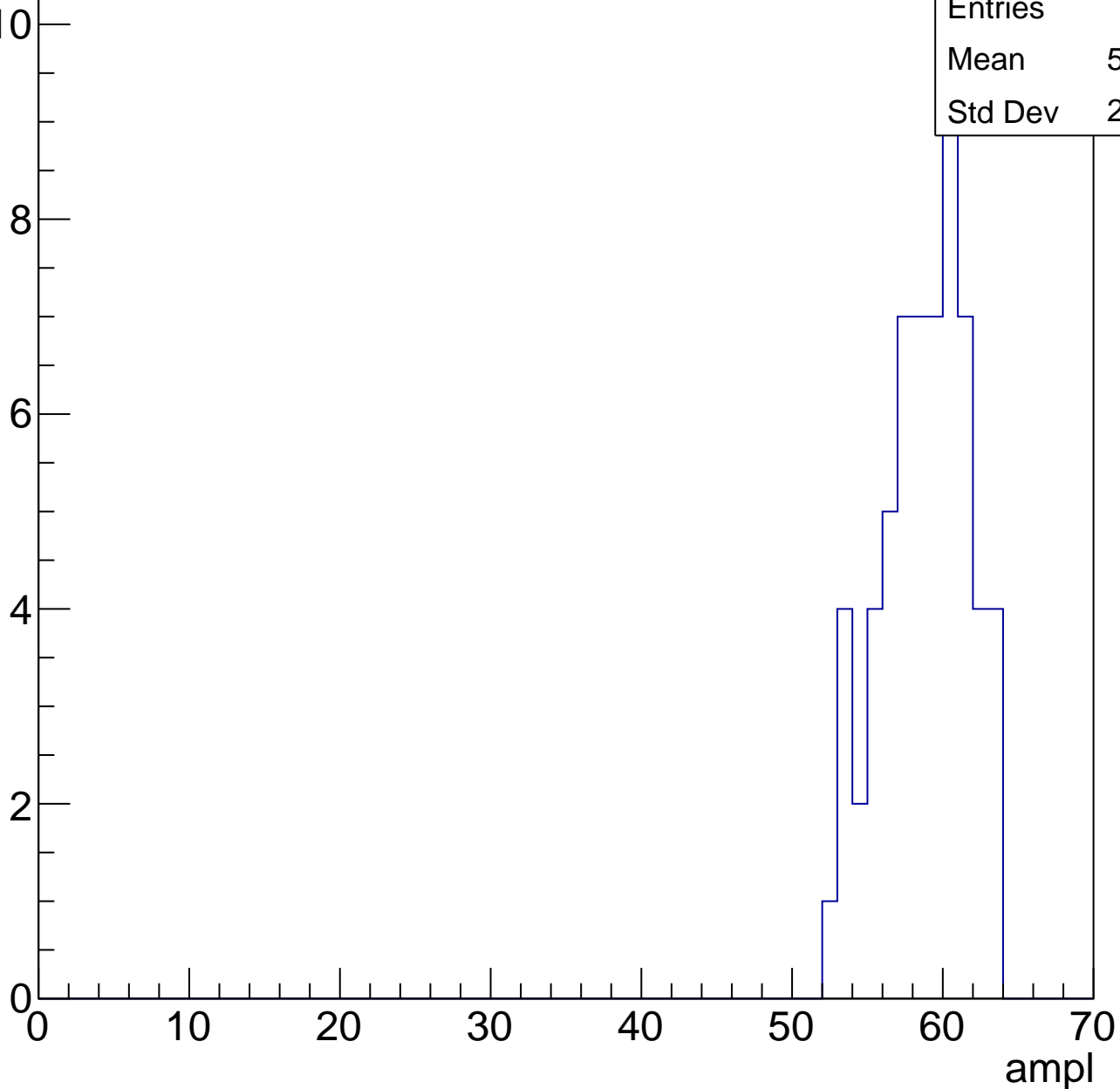


B1L100S, U19-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	58.34
Std Dev	2.834

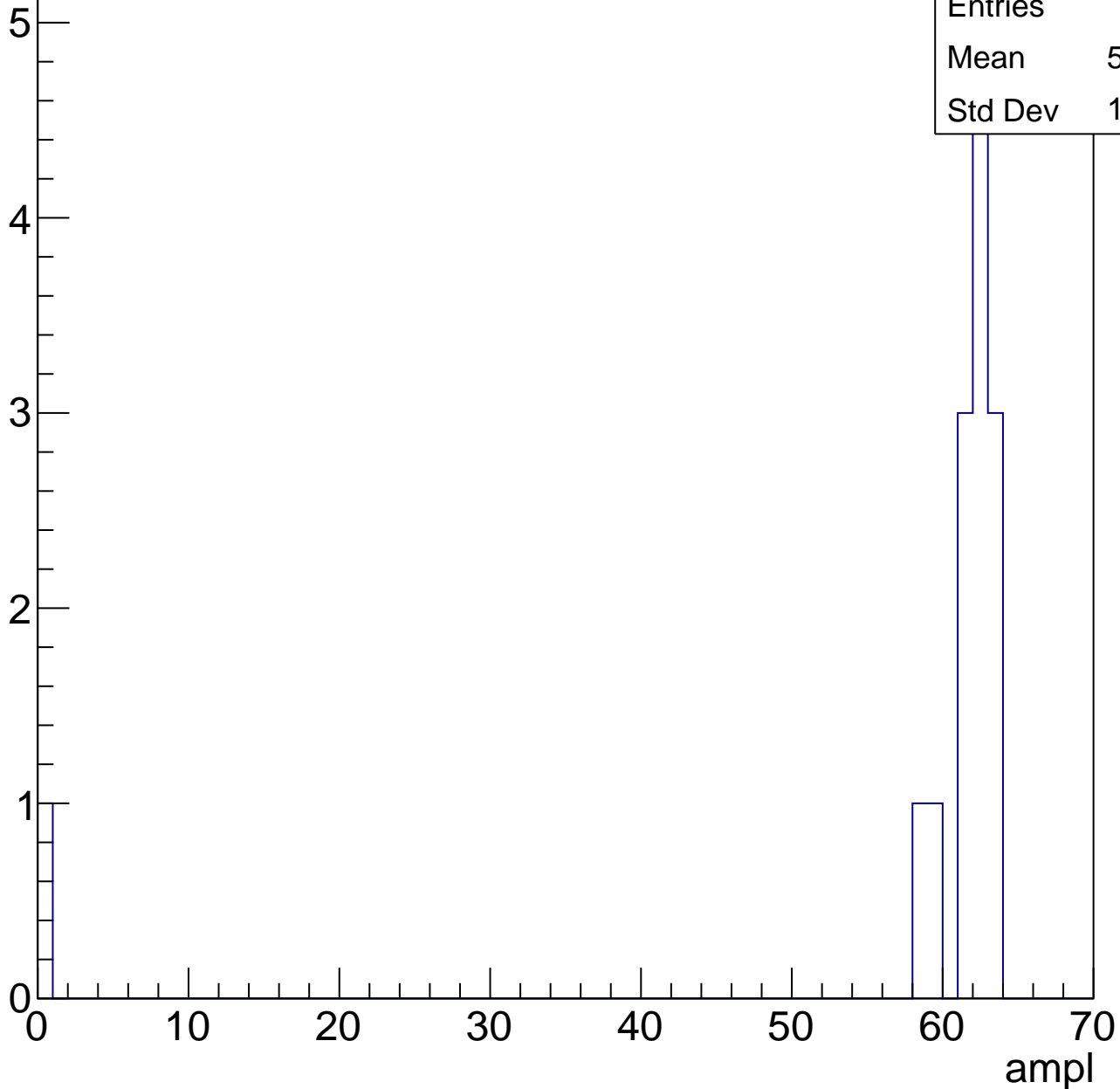


B1L100S, U19-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

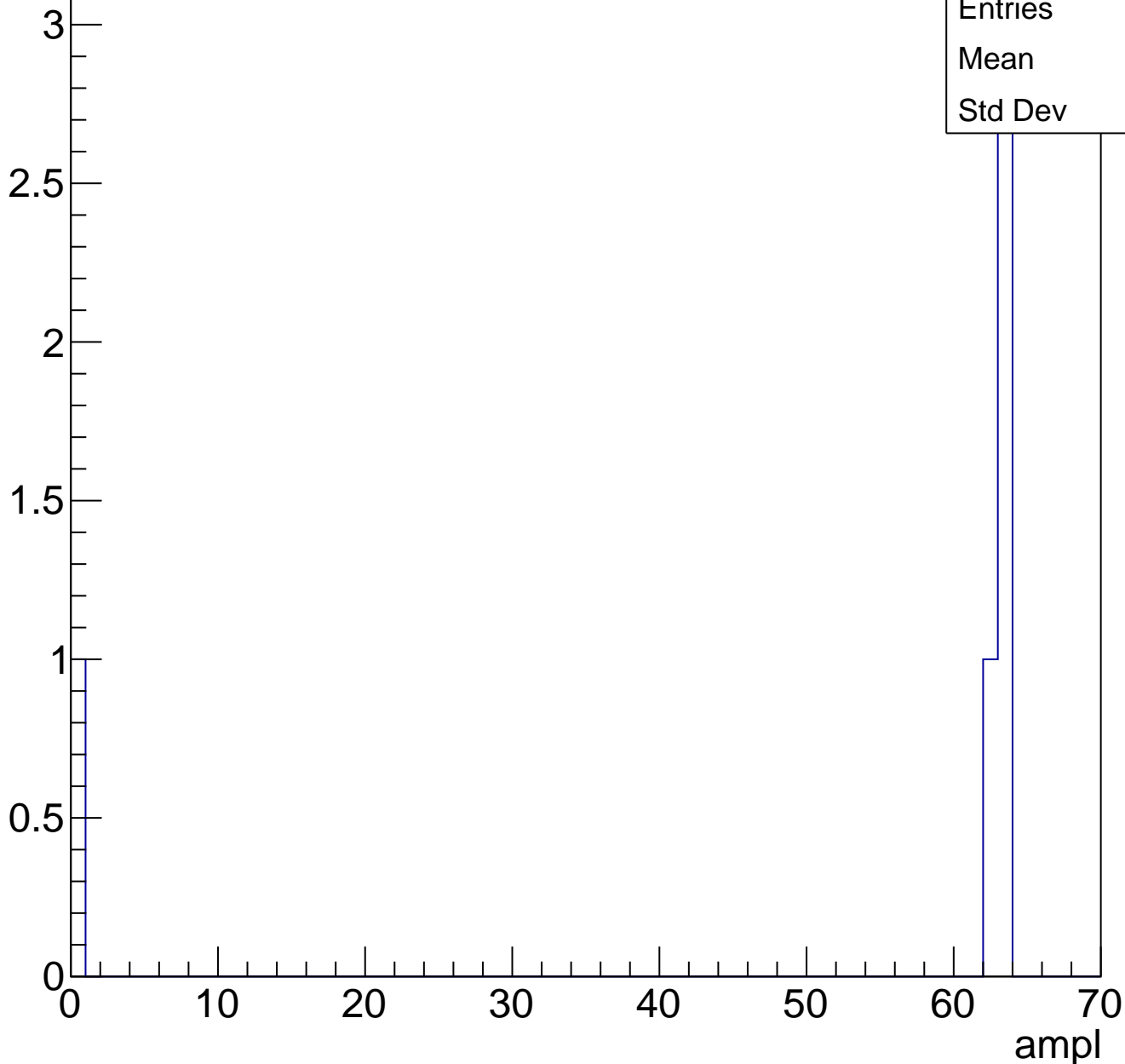
Entries	14
Mean	57.07
Std Dev	15.89



B1L100S, U19-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch19, adc0

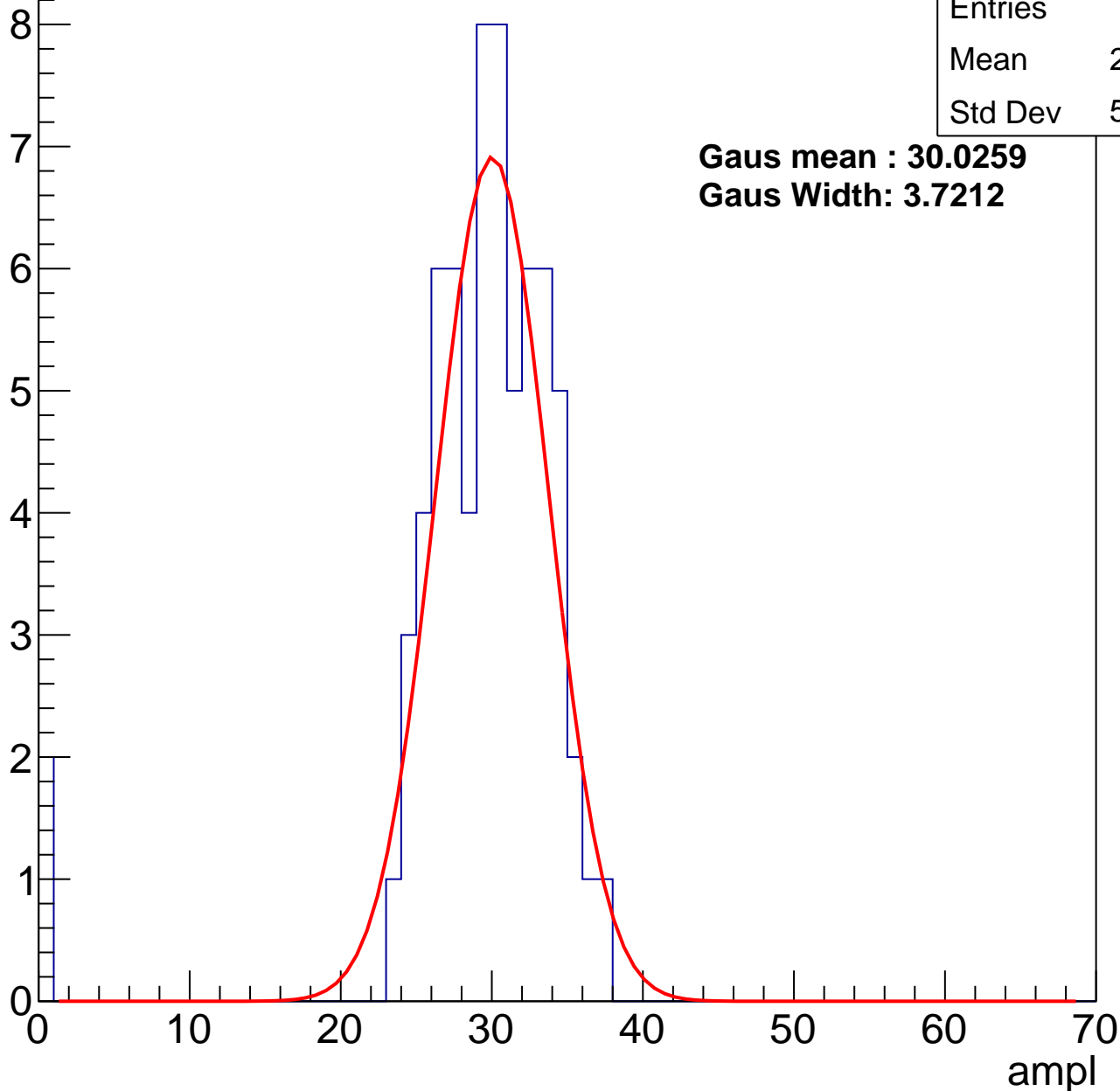
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.75
Std Dev	5.969

Gaus mean : 30.0259

Gaus Width: 3.7212



B1L100S, U19-ch19, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	36.78
Std Dev	3.413

Gaus mean : 37.2453

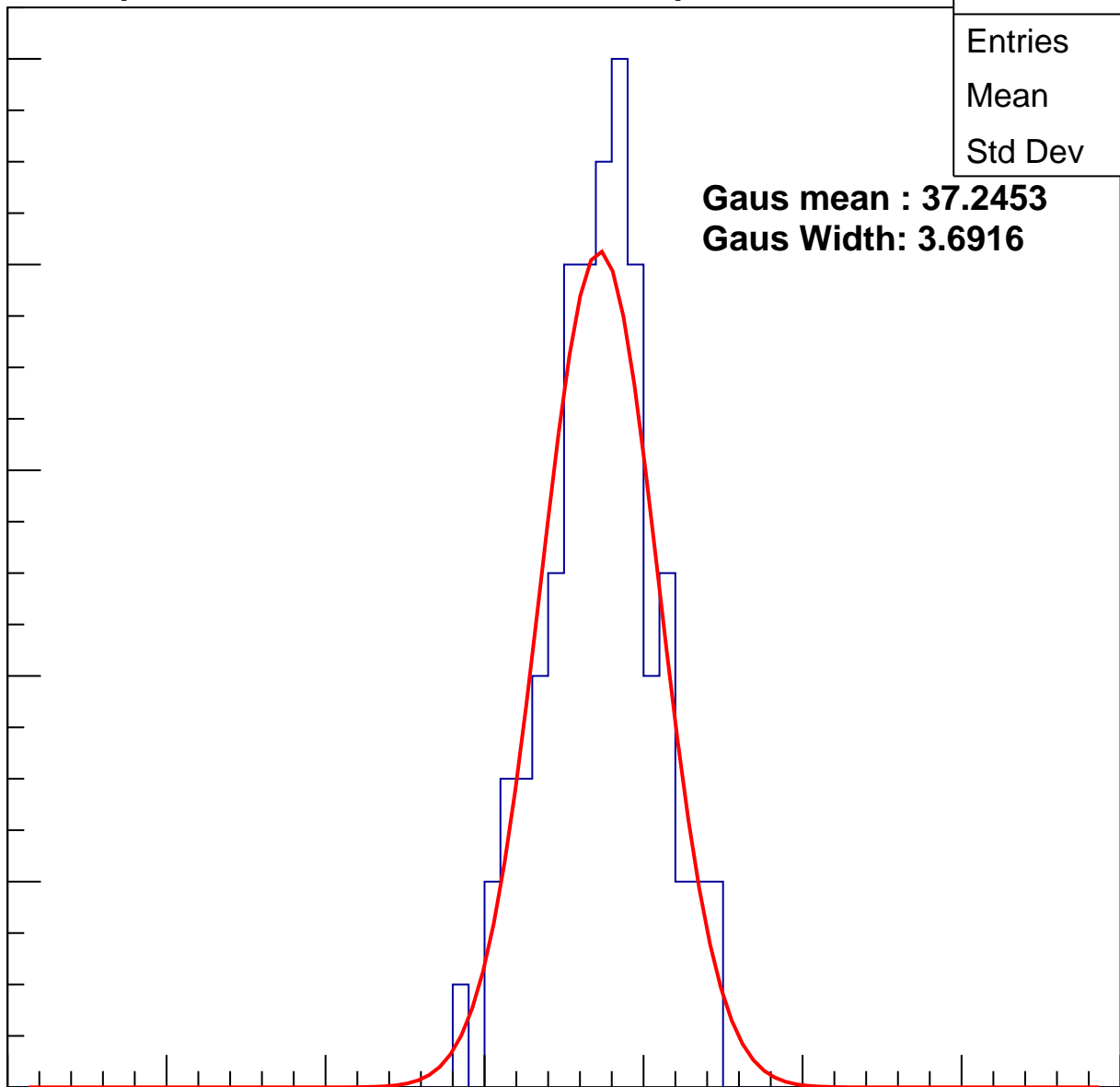
Gaus Width: 3.6916

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch19, adc2

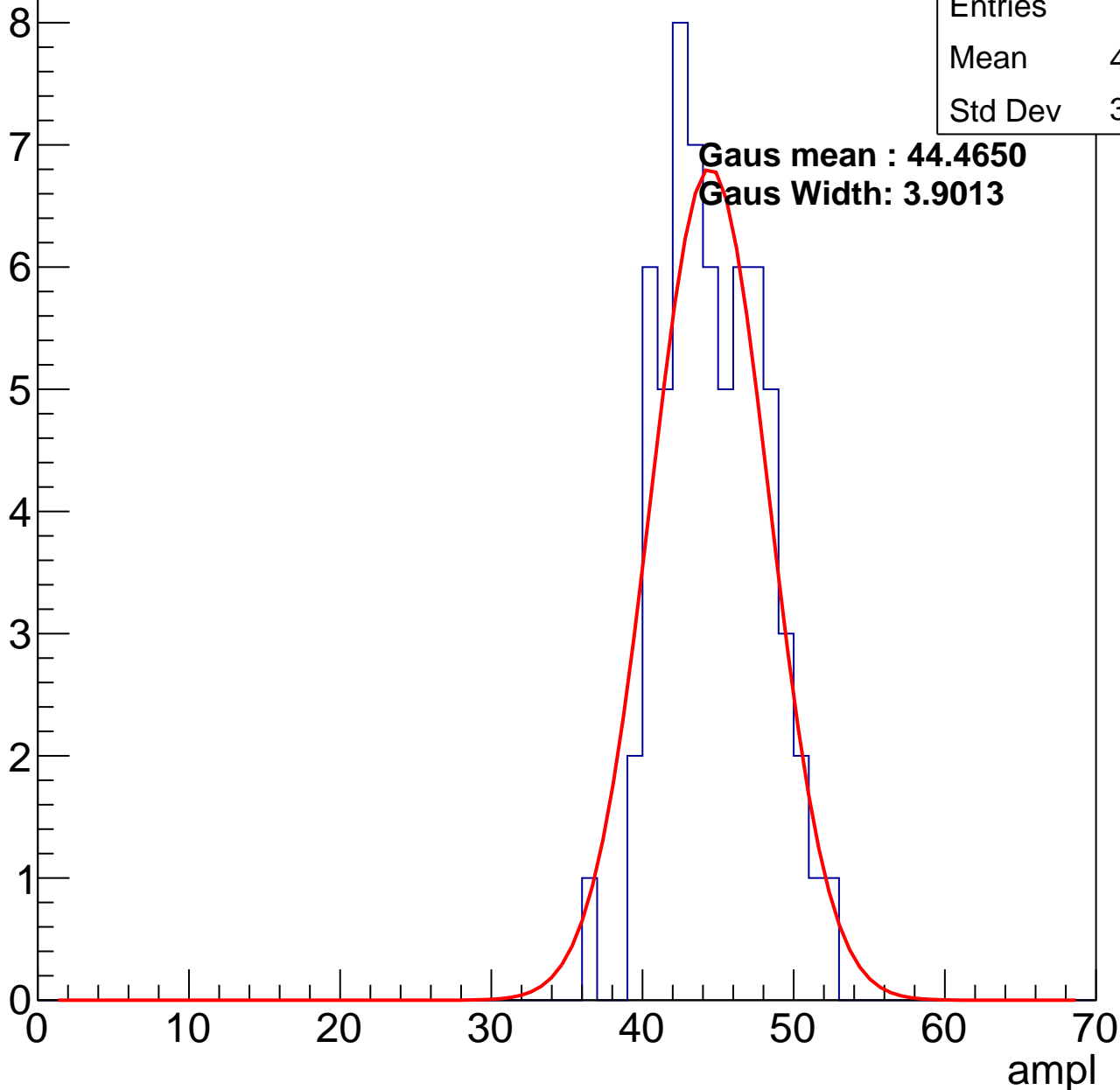
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.27
Std Dev	3.337

Gaus mean : 44.4650

Gaus Width: 3.9013



B1L100S, U19-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	50.64
Std Dev	3.508

Entry

10

8

6

4

2

0

0

10

20

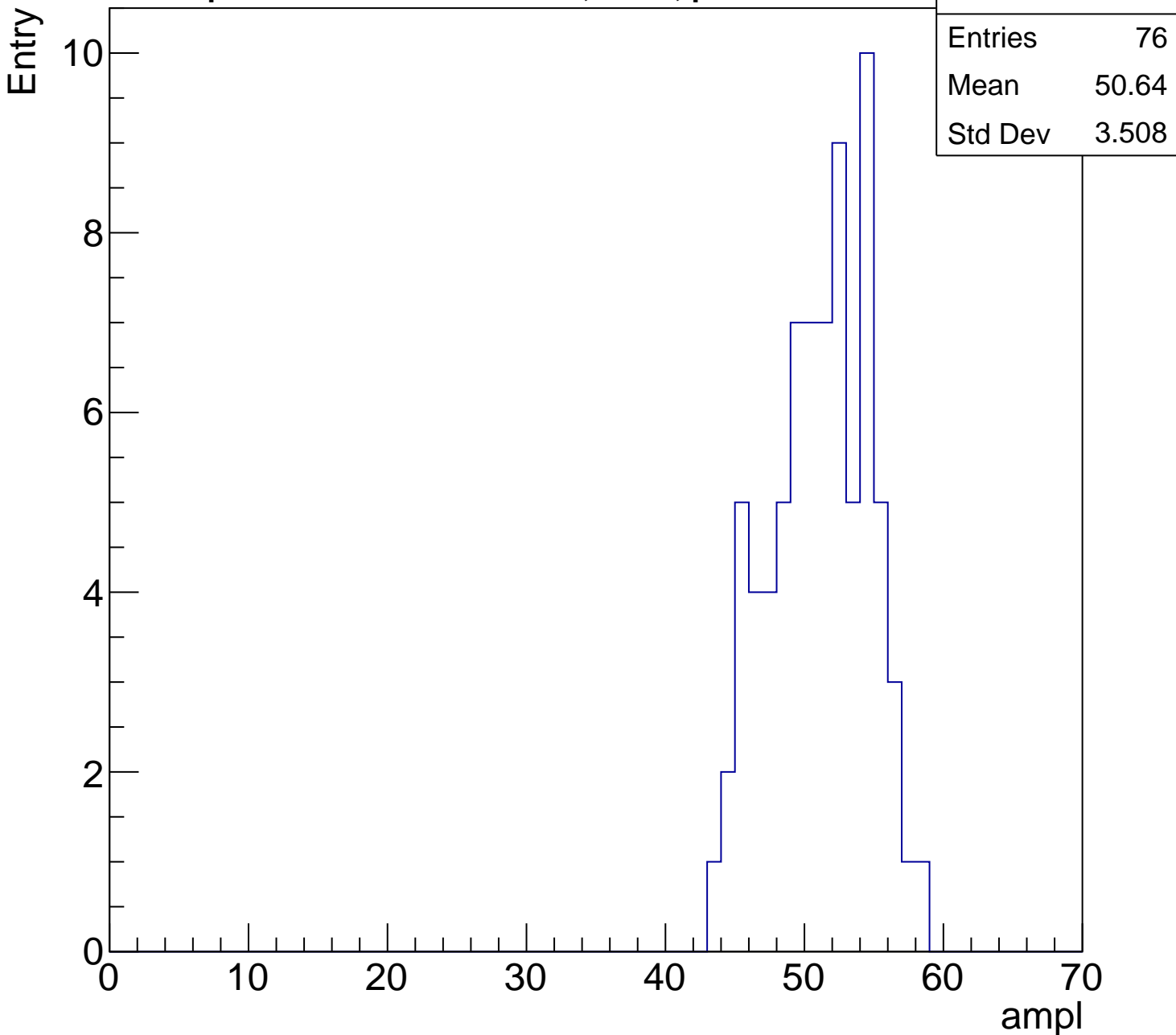
30

40

50

60

ampl

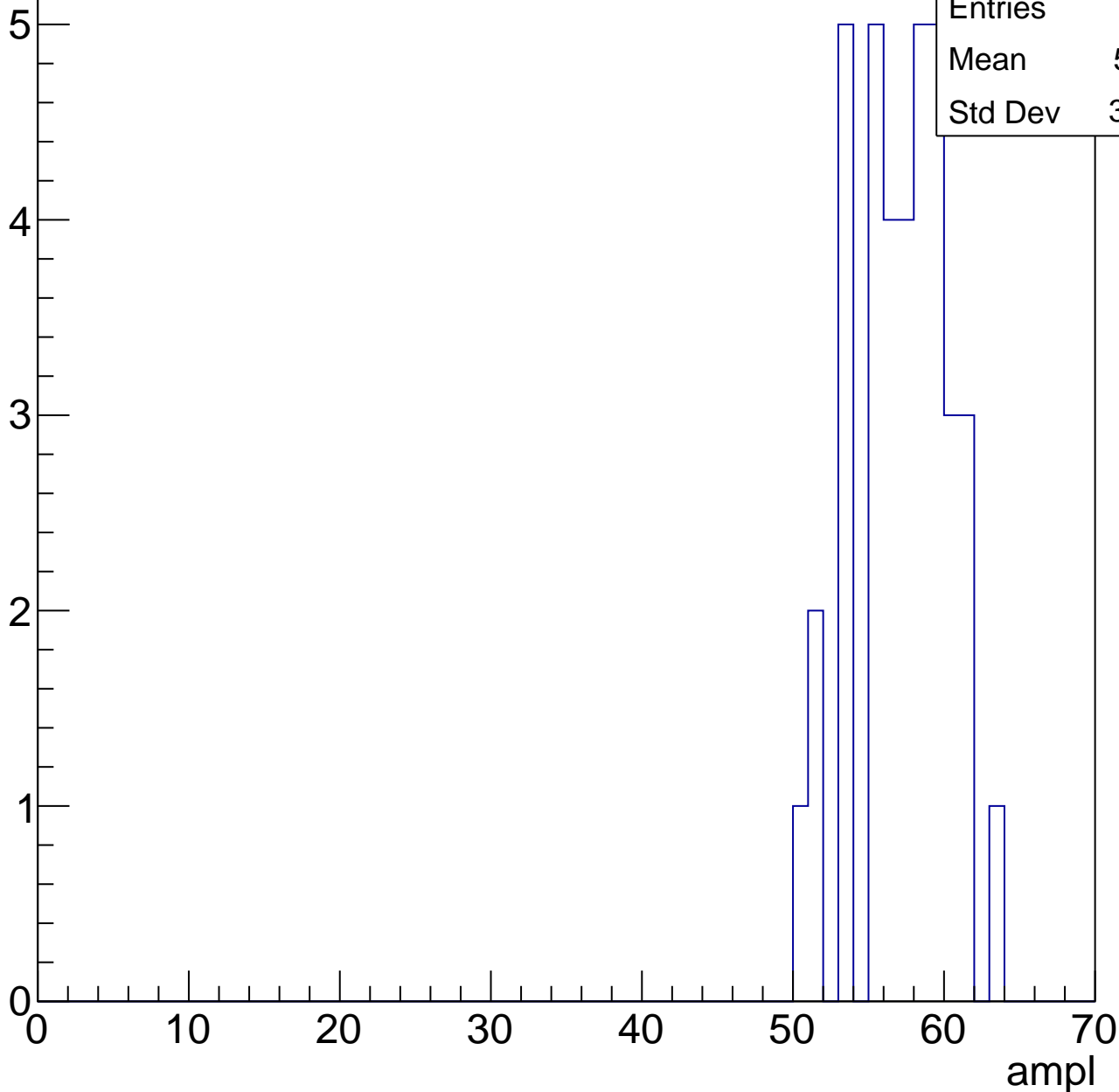


B1L100S, U19-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	56.71
Std Dev	3.069

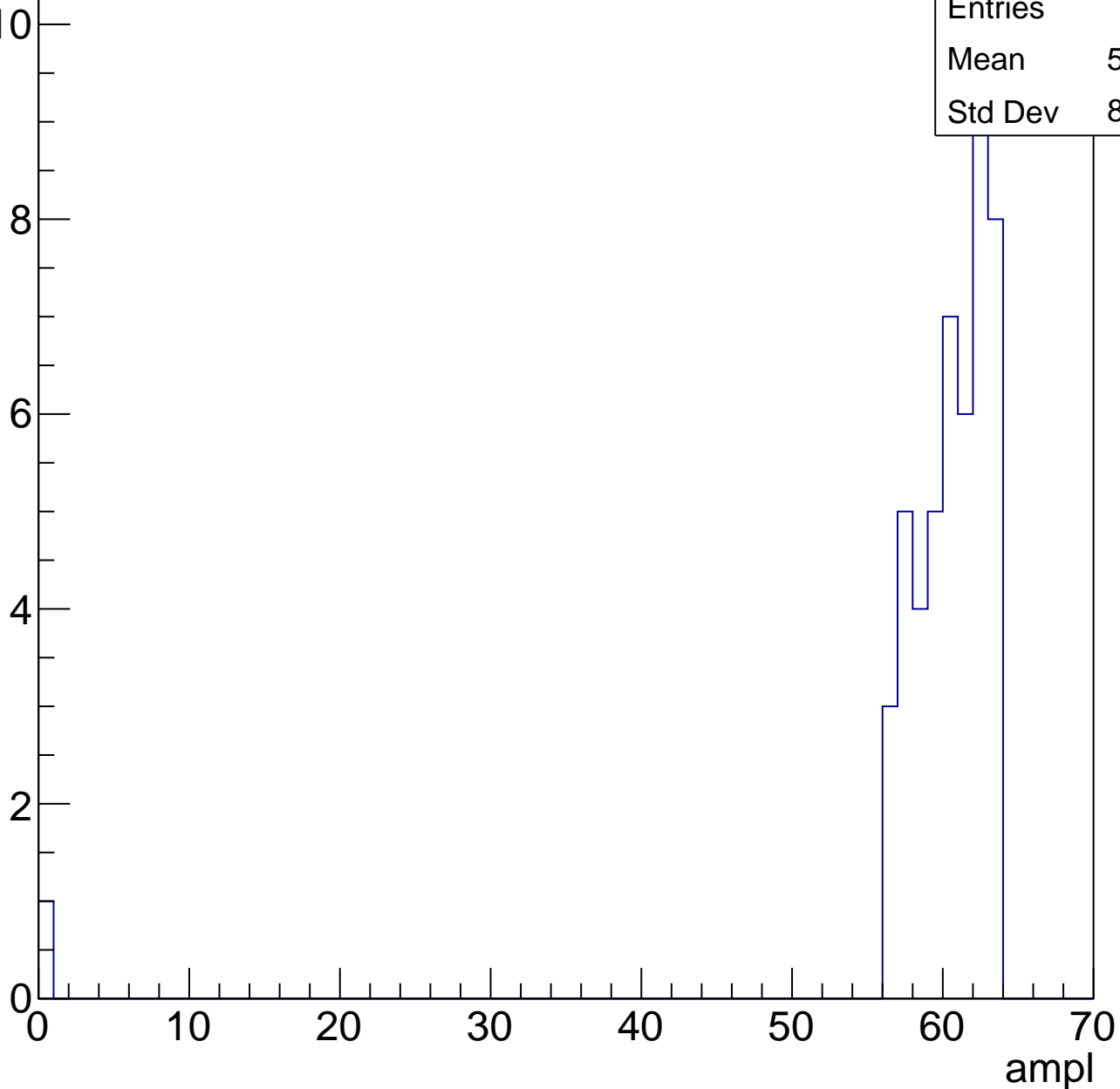


B1L100S, U19-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.98
Std Dev	8.784



B1L100S, U19-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch20, adc0

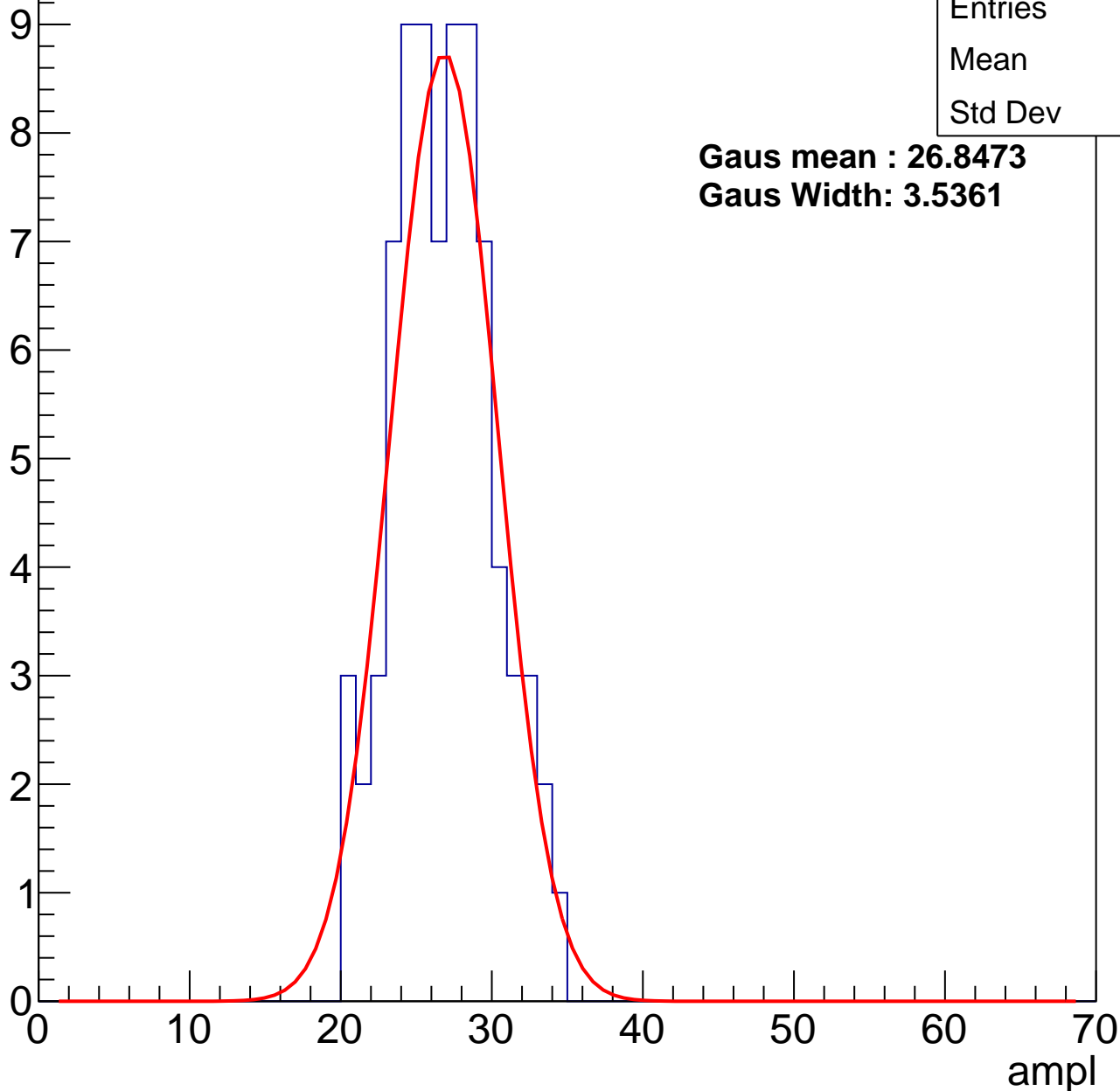
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	26.4
Std Dev	3.24

Gaus mean : 26.8473

Gaus Width: 3.5361



B1L100S, U19-ch20, adc1

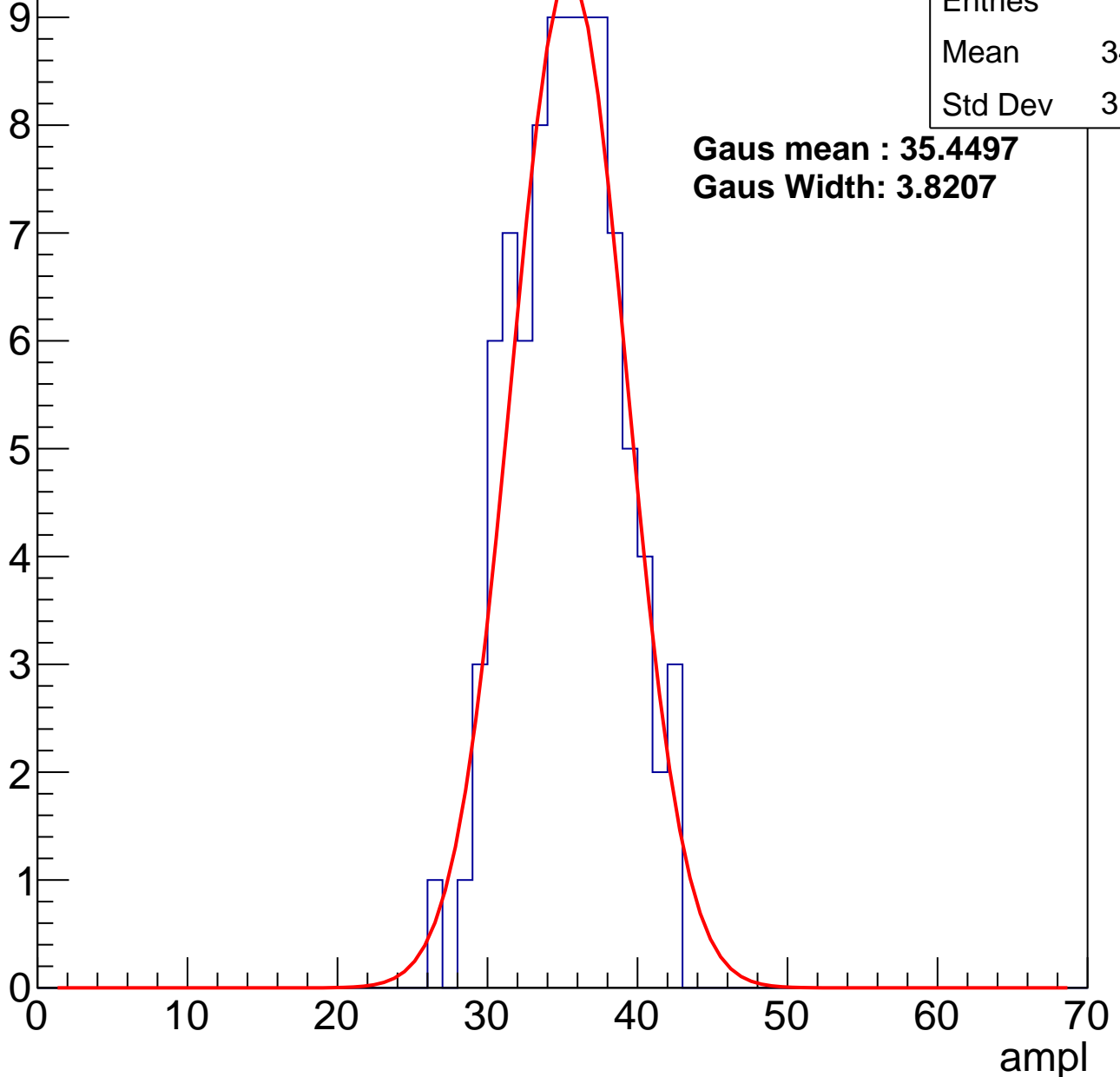
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	34.84
Std Dev	3.515

Gaus mean : 35.4497

Gaus Width: 3.8207



B1L100S, U19-ch20, adc2

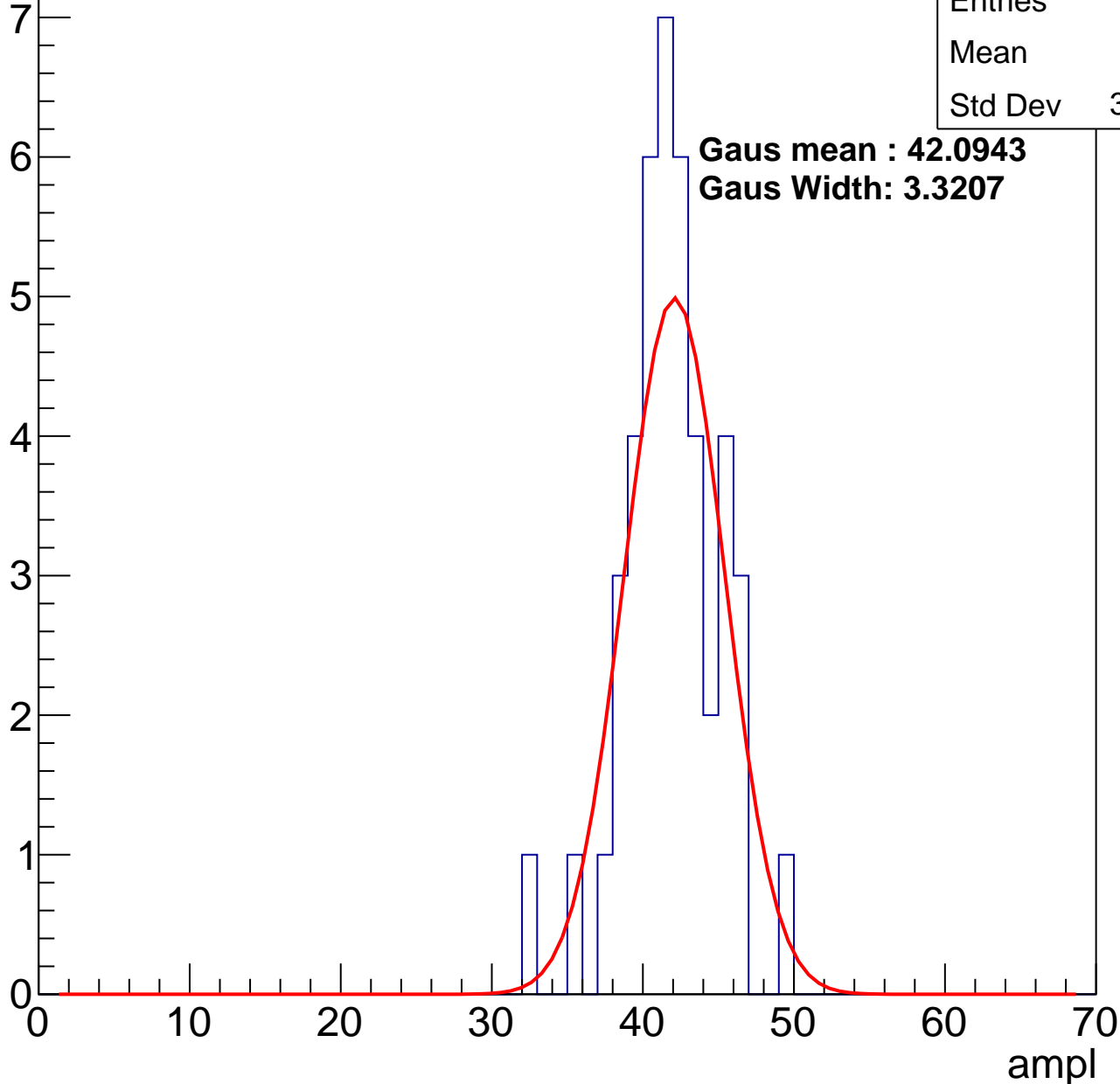
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	41.4
Std Dev	3.119

Gaus mean : 42.0943

Gaus Width: 3.3207

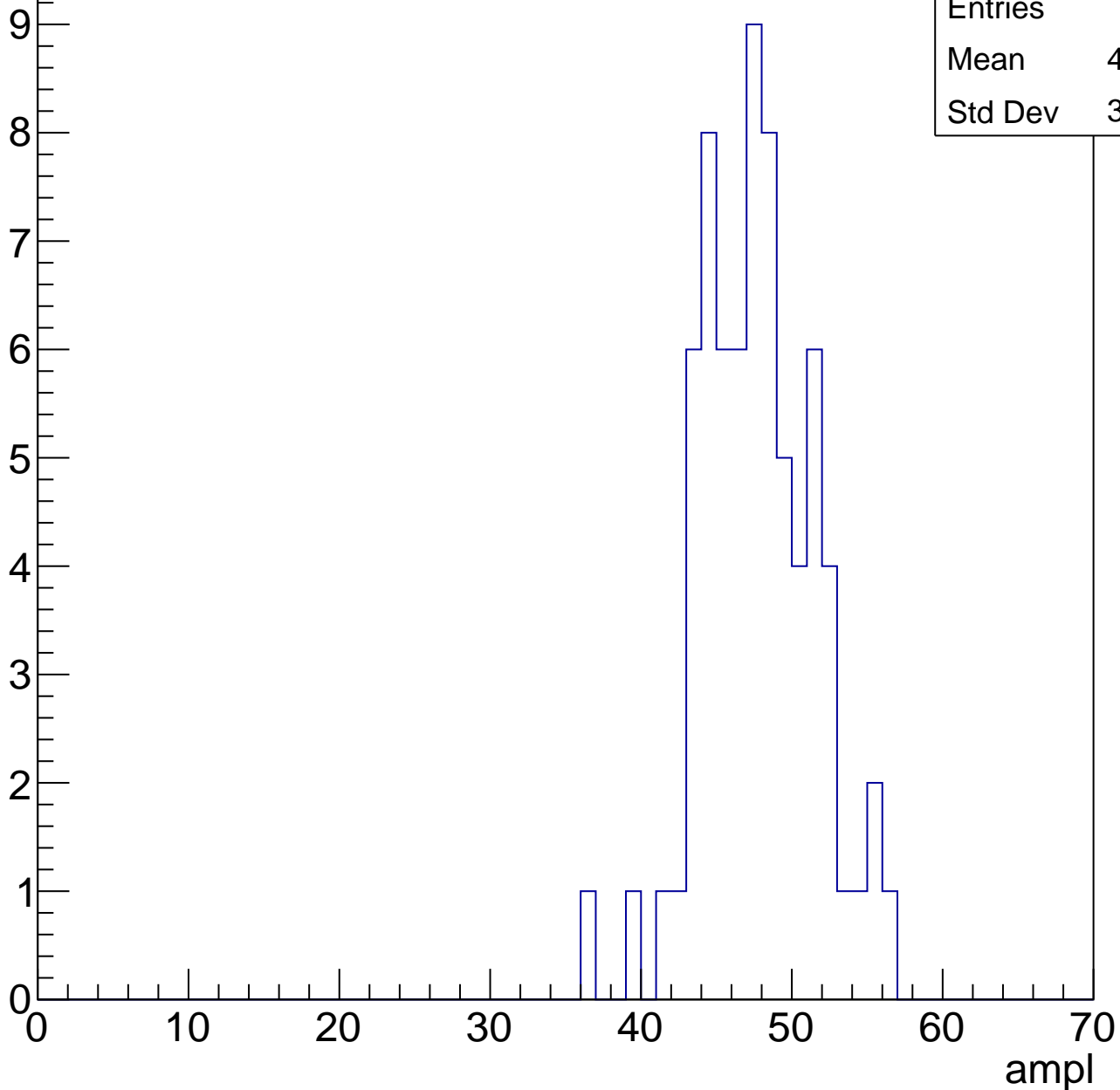


B1L100S, U19-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	47.23
Std Dev	3.742

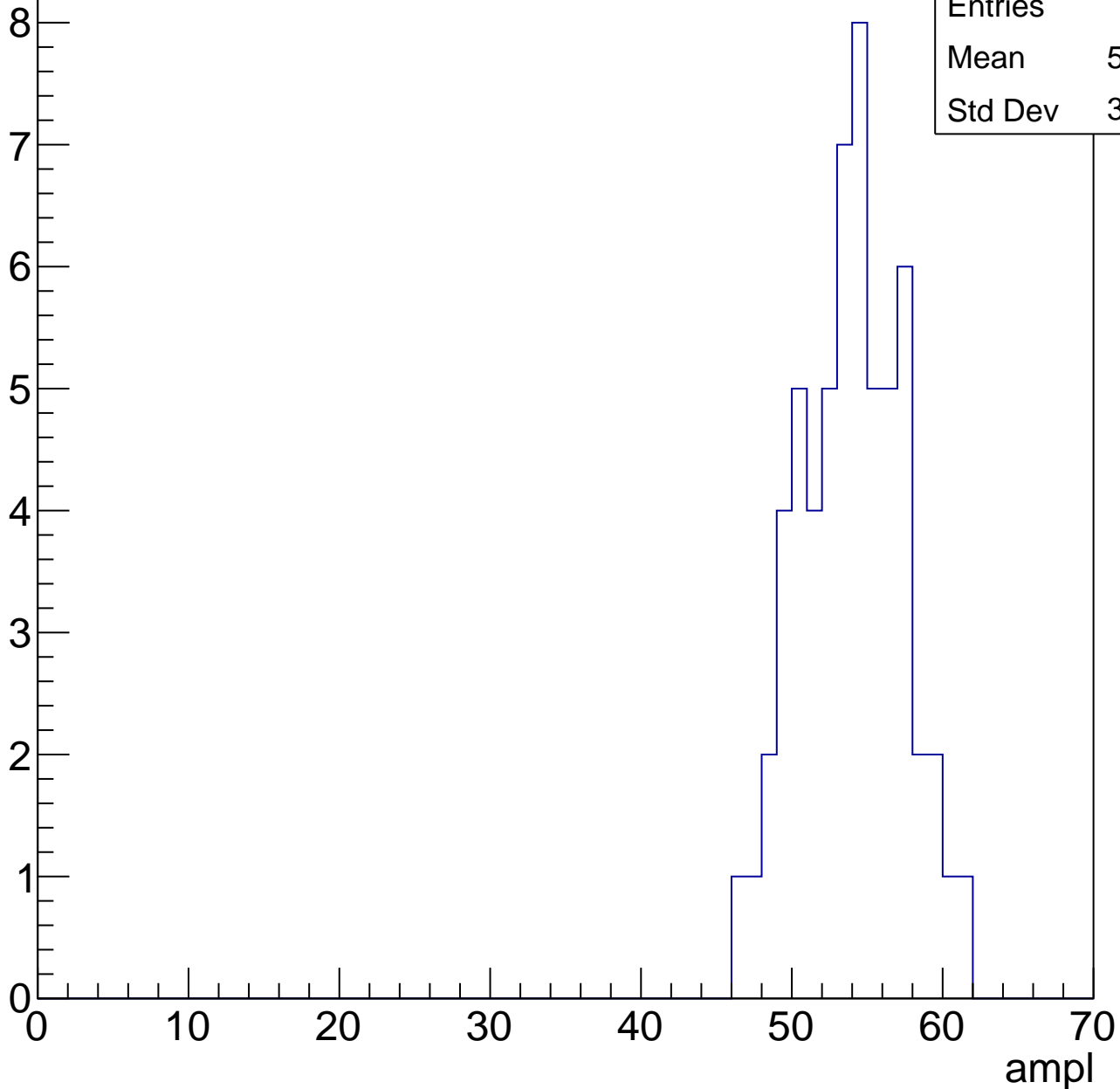


B1L100S, U19-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

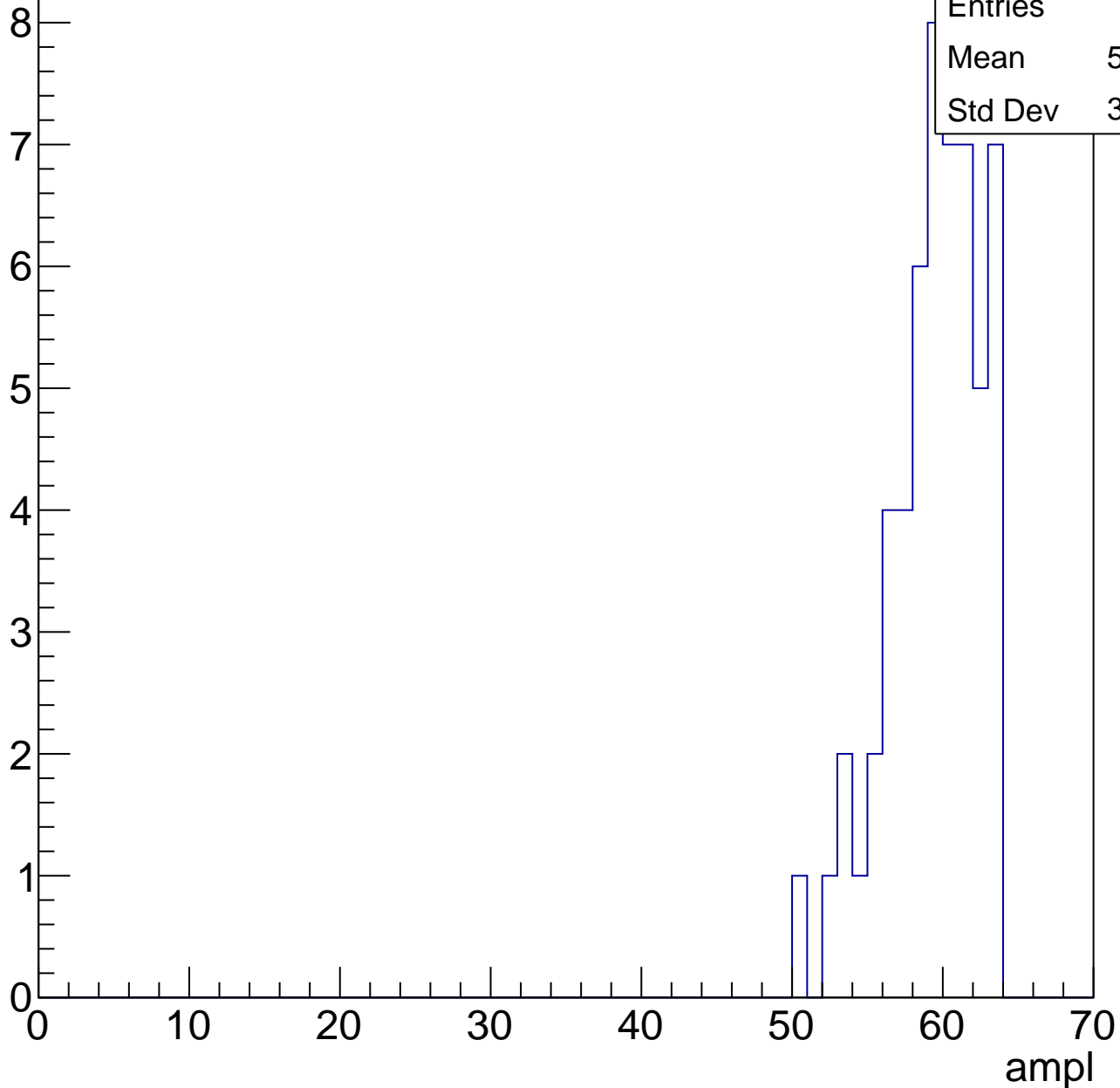
Entries	59
Mean	53.46
Std Dev	3.336



B1L100S, U19-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

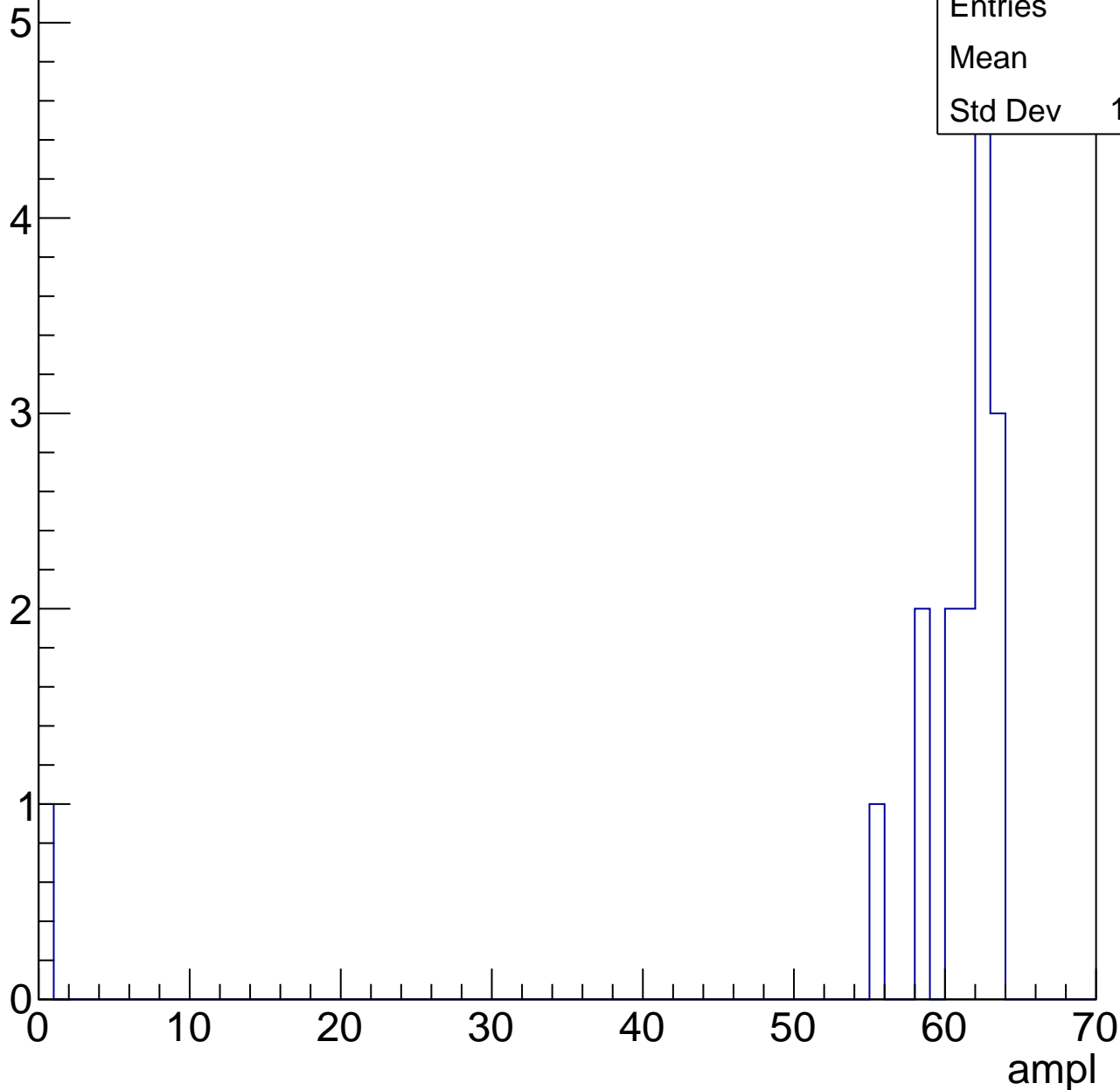


B1L100S, U19-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	57
Std Dev	14.87



B1L100S, U19-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L100S, U19-ch21, adc0

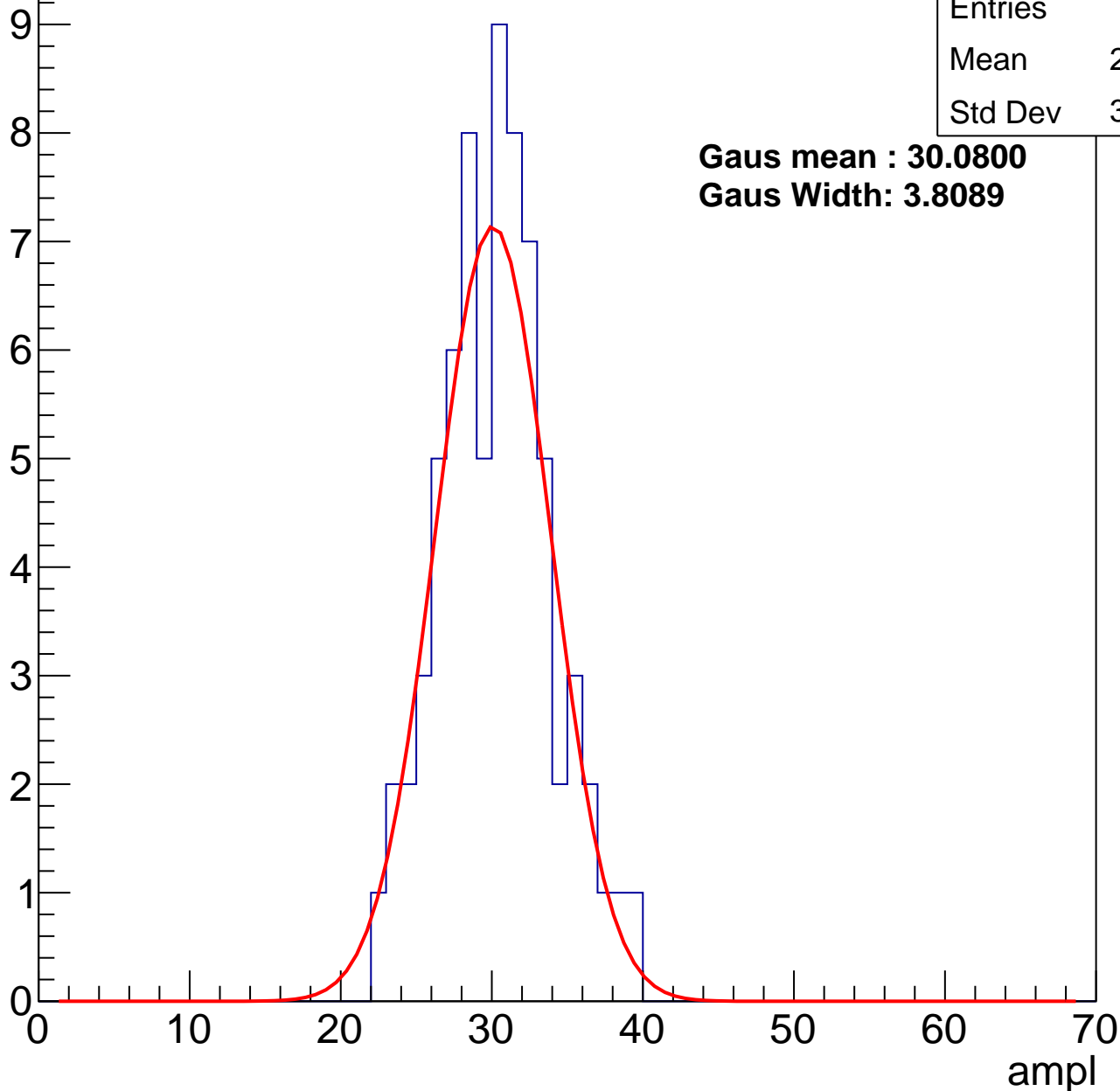
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.83
Std Dev	3.623

Gaus mean : 30.0800

Gaus Width: 3.8089



B1L100S, U19-ch21, adc1

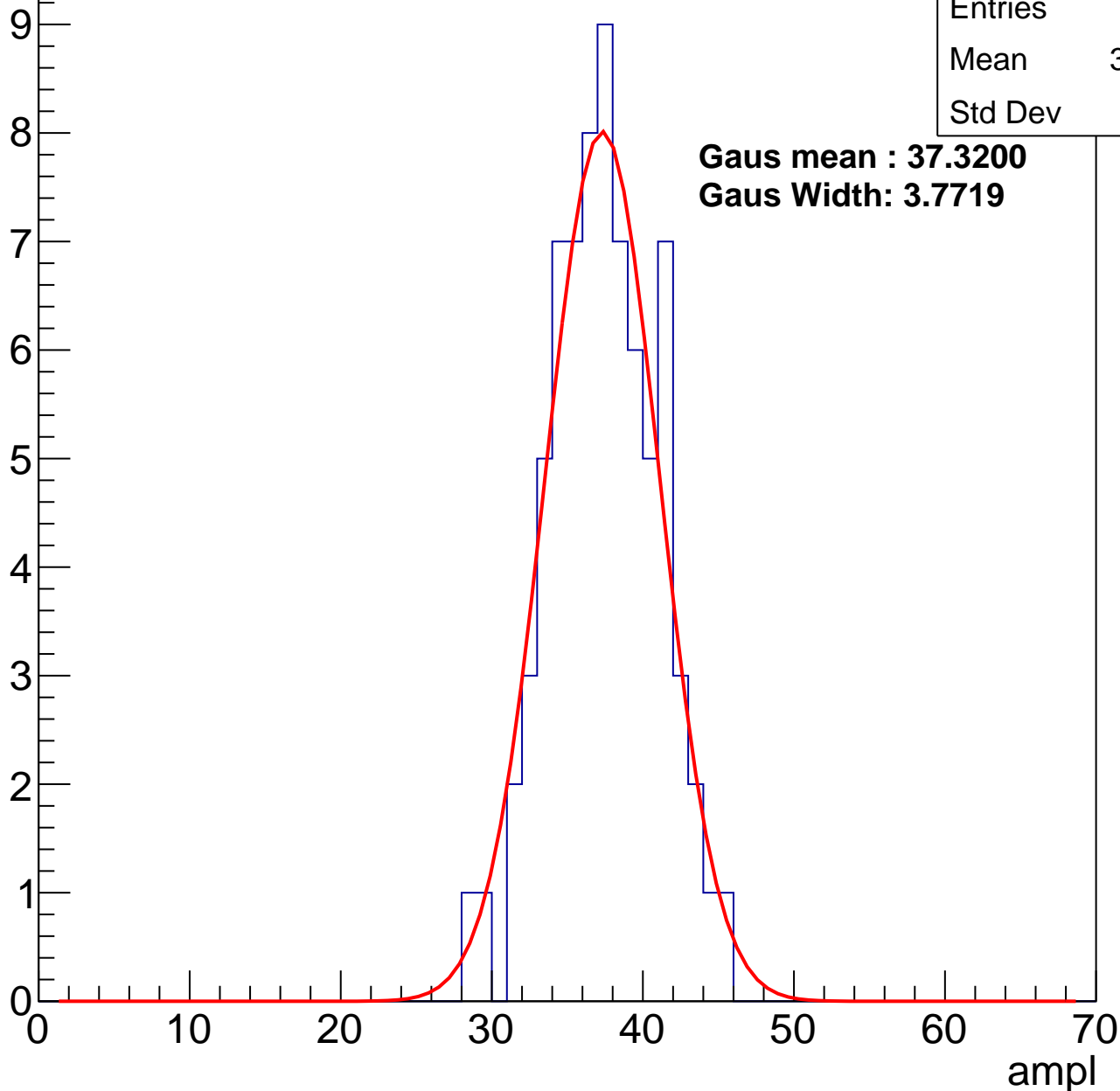
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.96
Std Dev	3.5

Gaus mean : 37.3200

Gaus Width: 3.7719



B1L100S, U19-ch21, adc2

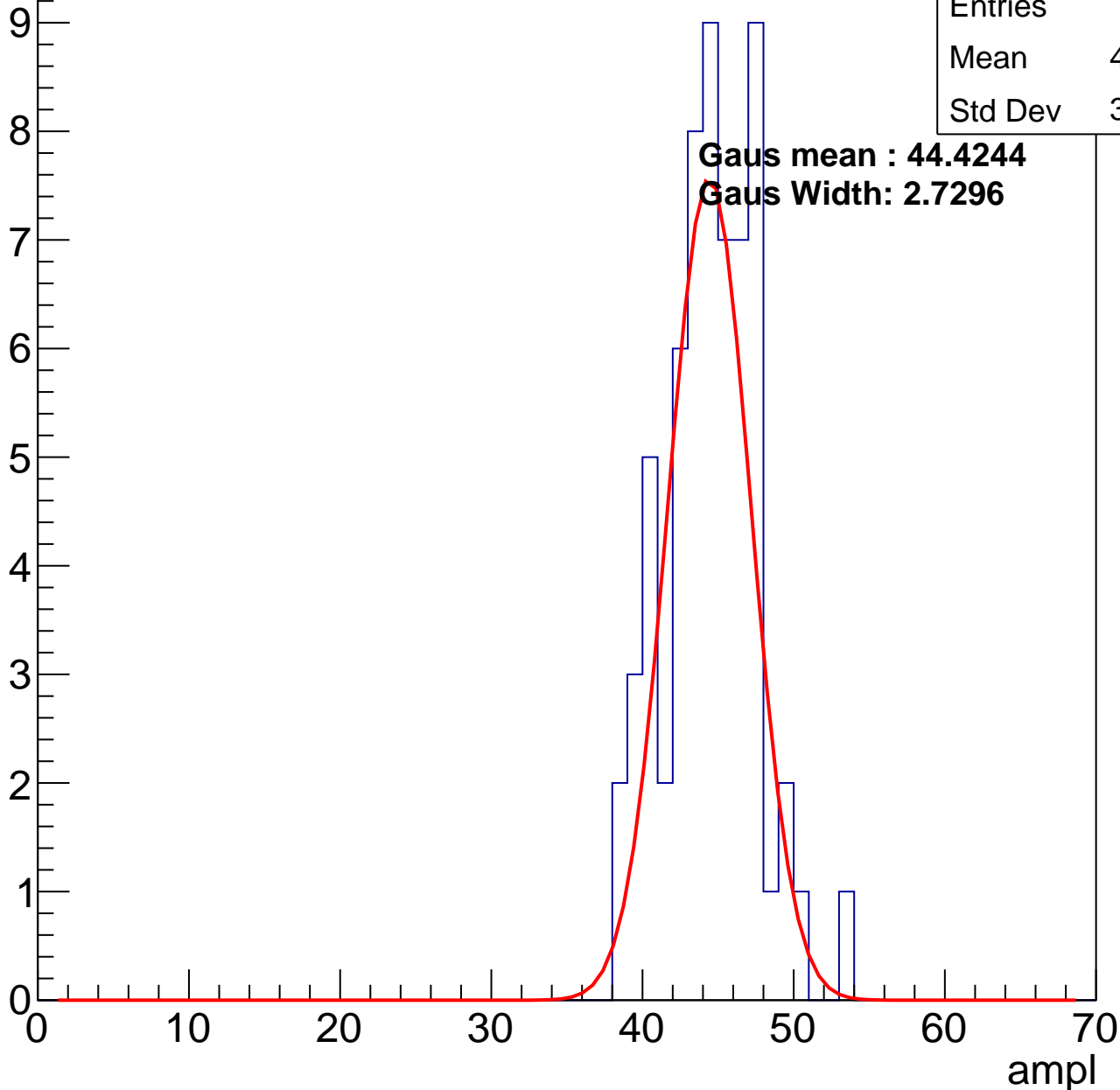
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.06
Std Dev	3.023

Gaus mean : 44.4244

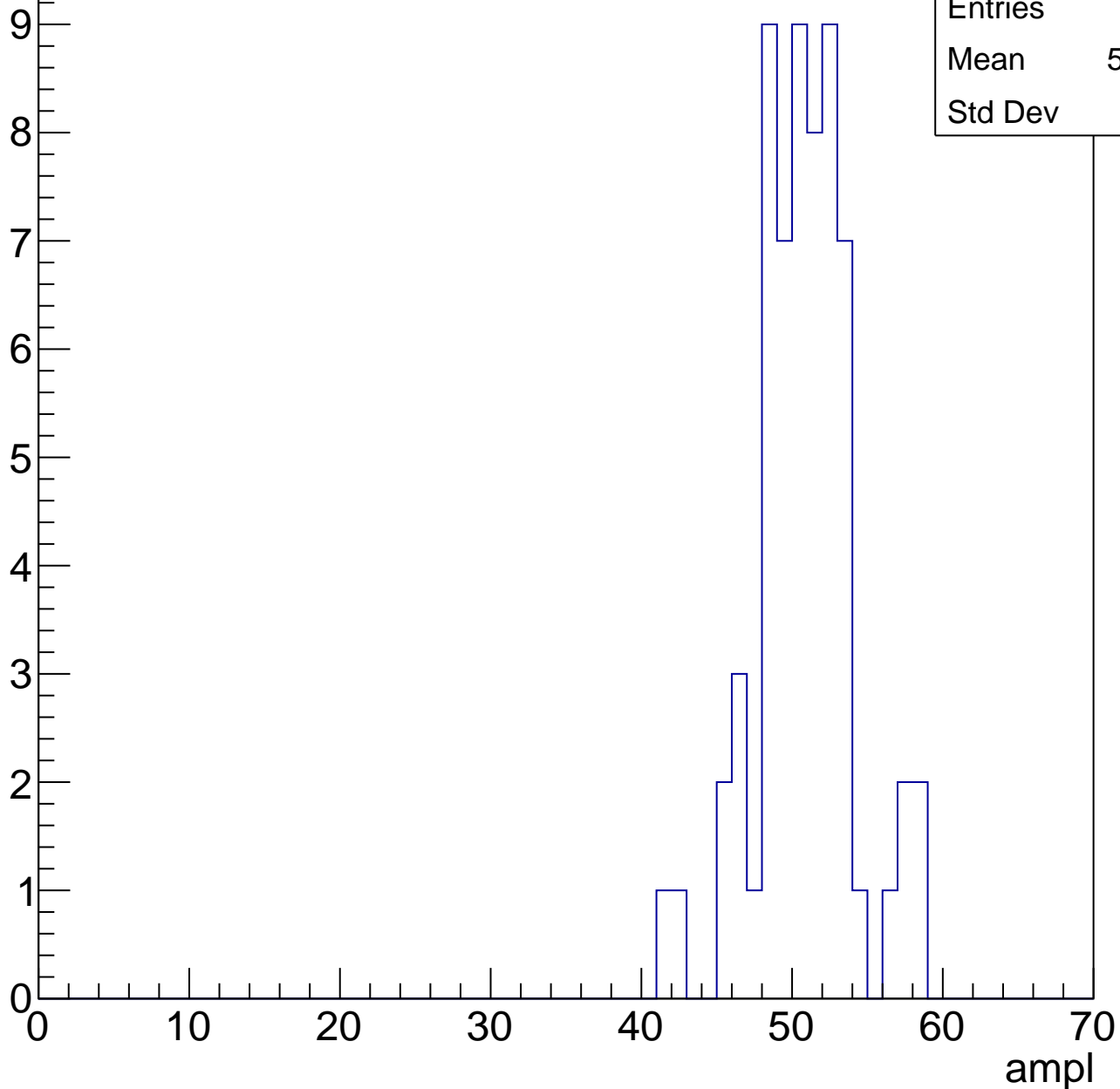
Gaus Width: 2.7296



B1L100S, U19-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



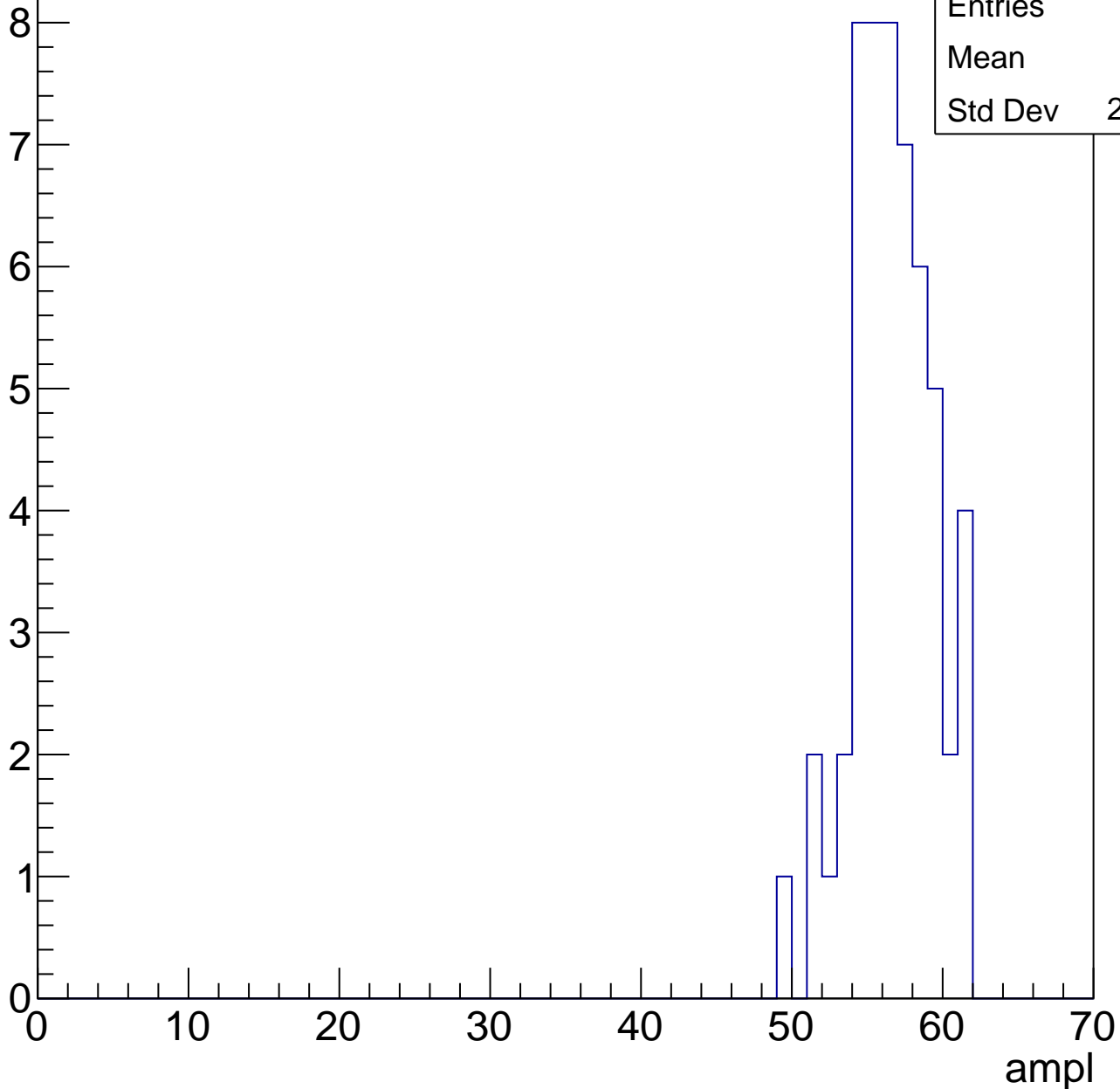
Entries	63
Mean	50.32
Std Dev	3.27

B1L100S, U19-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.2
Std Dev	2.648

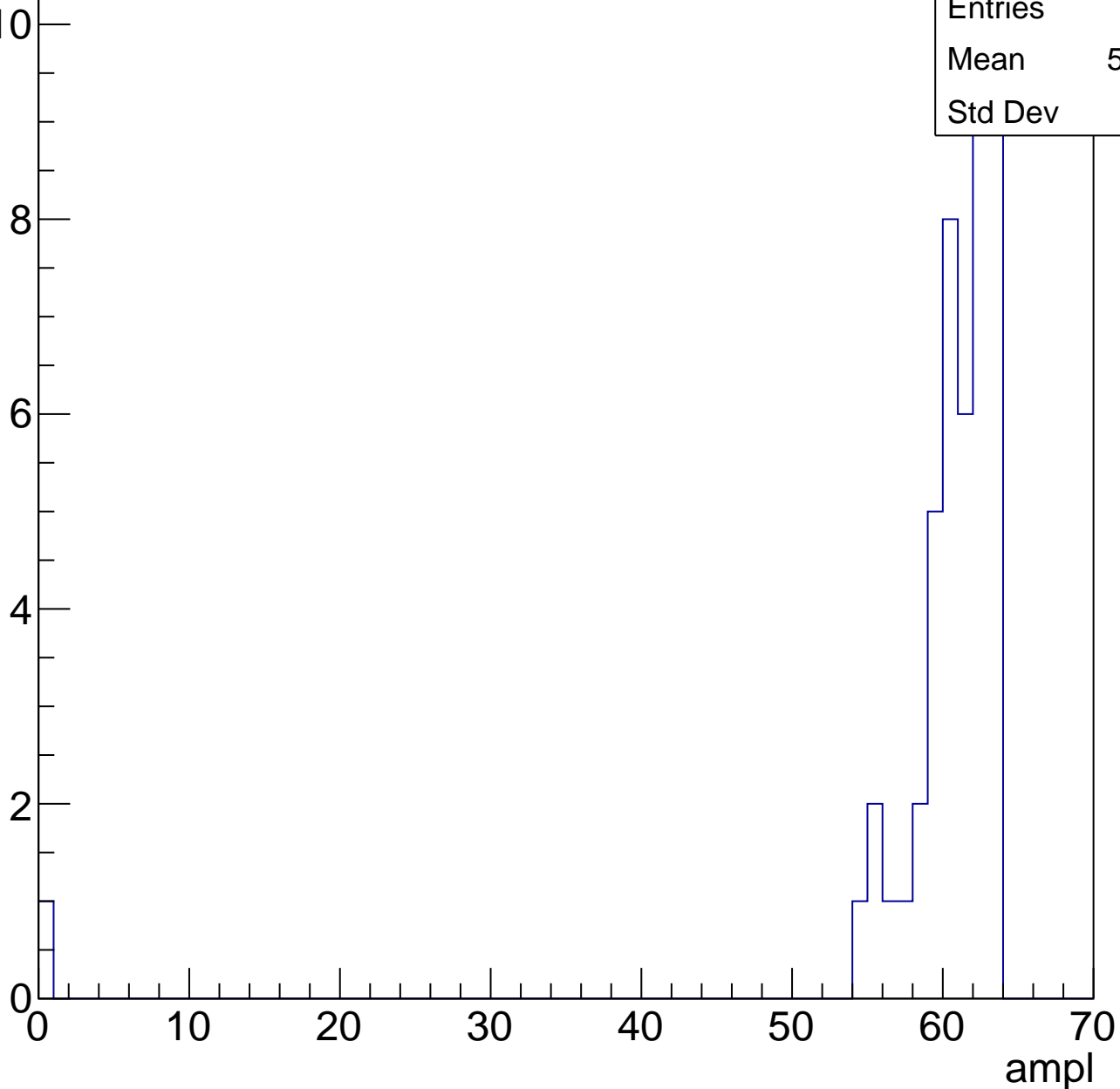


B1L100S, U19-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.15
Std Dev	9.11



B1L100S, U19-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L100S, U19-ch22, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	31
Std Dev	3.286

Gaus mean : 30.8638

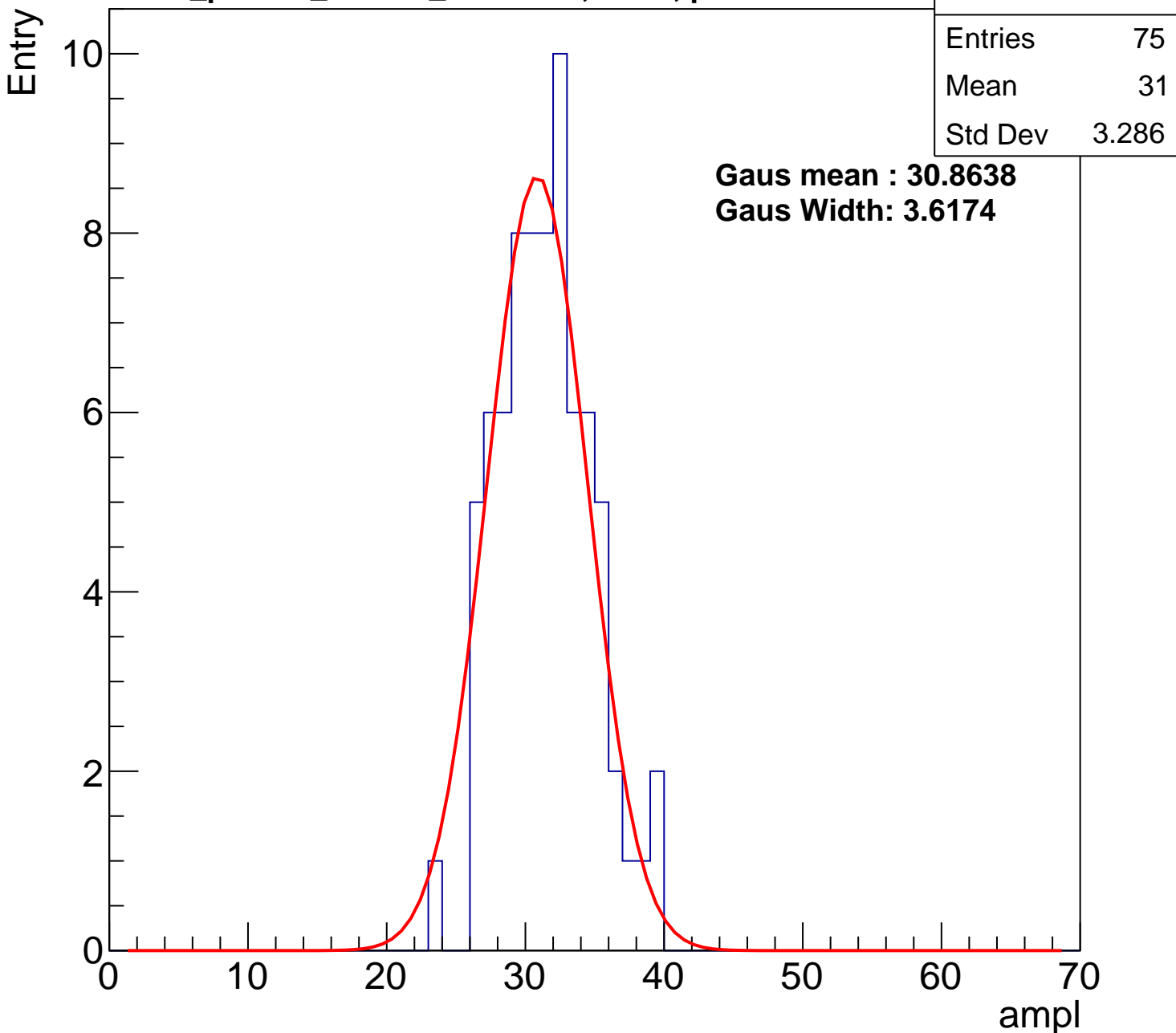
Gaus Width: 3.6174

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch22, adc1

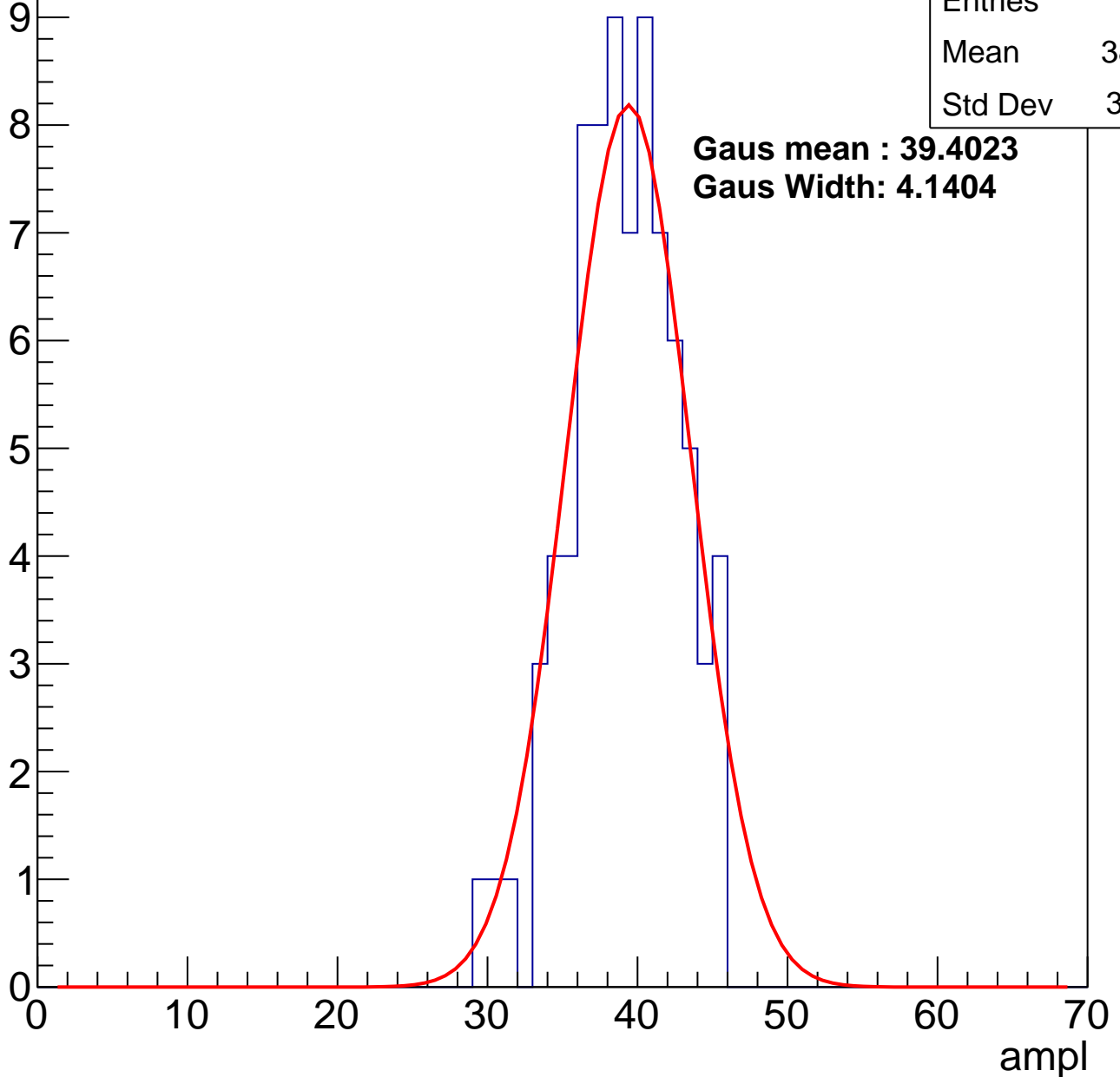
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	38.62
Std Dev	3.551

Gaus mean : 39.4023

Gaus Width: 4.1404



B1L100S, U19-ch22, adc2

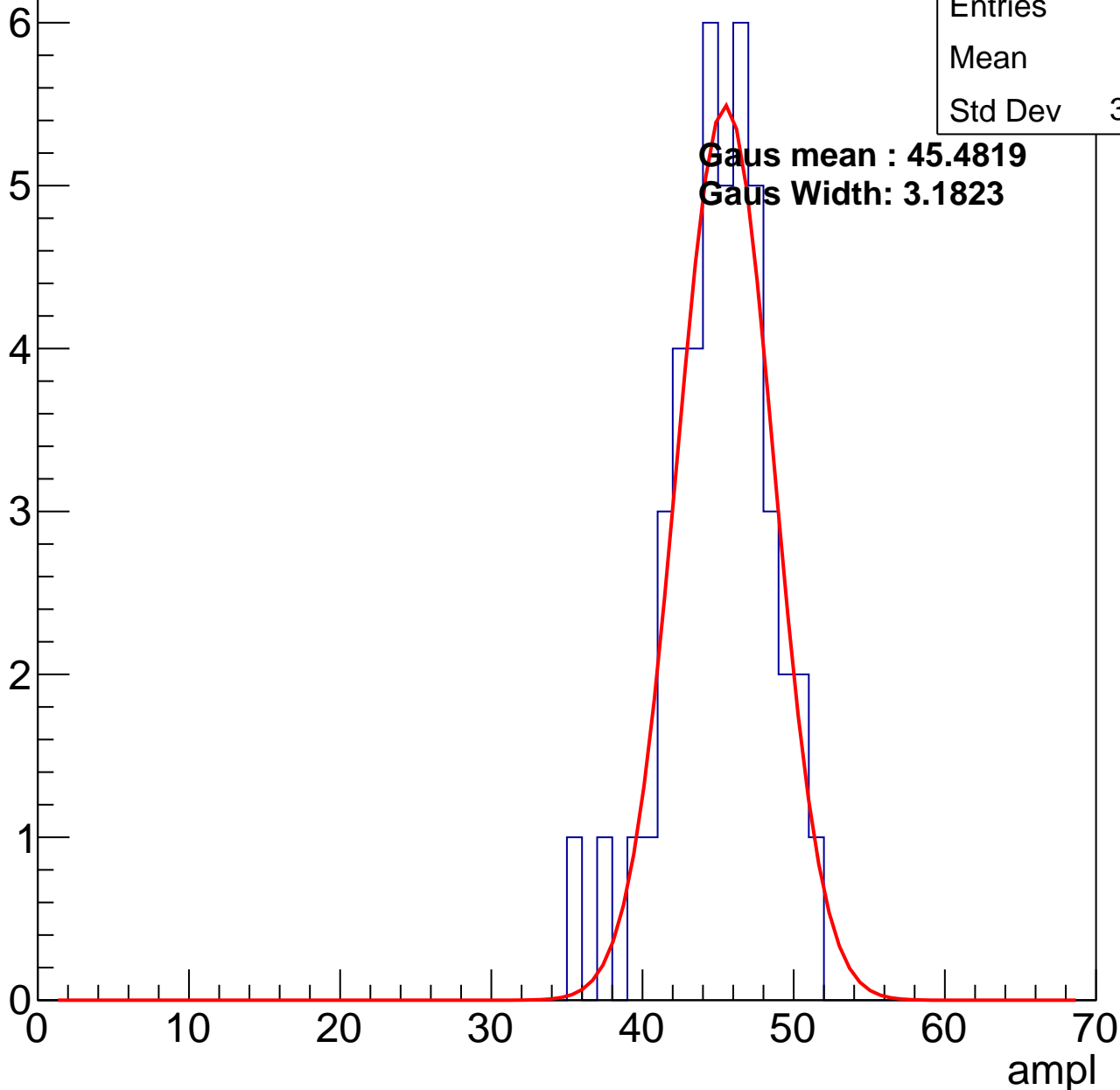
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	44.6
Std Dev	3.316

Gaus mean : 45.4819

Gaus Width: 3.1823

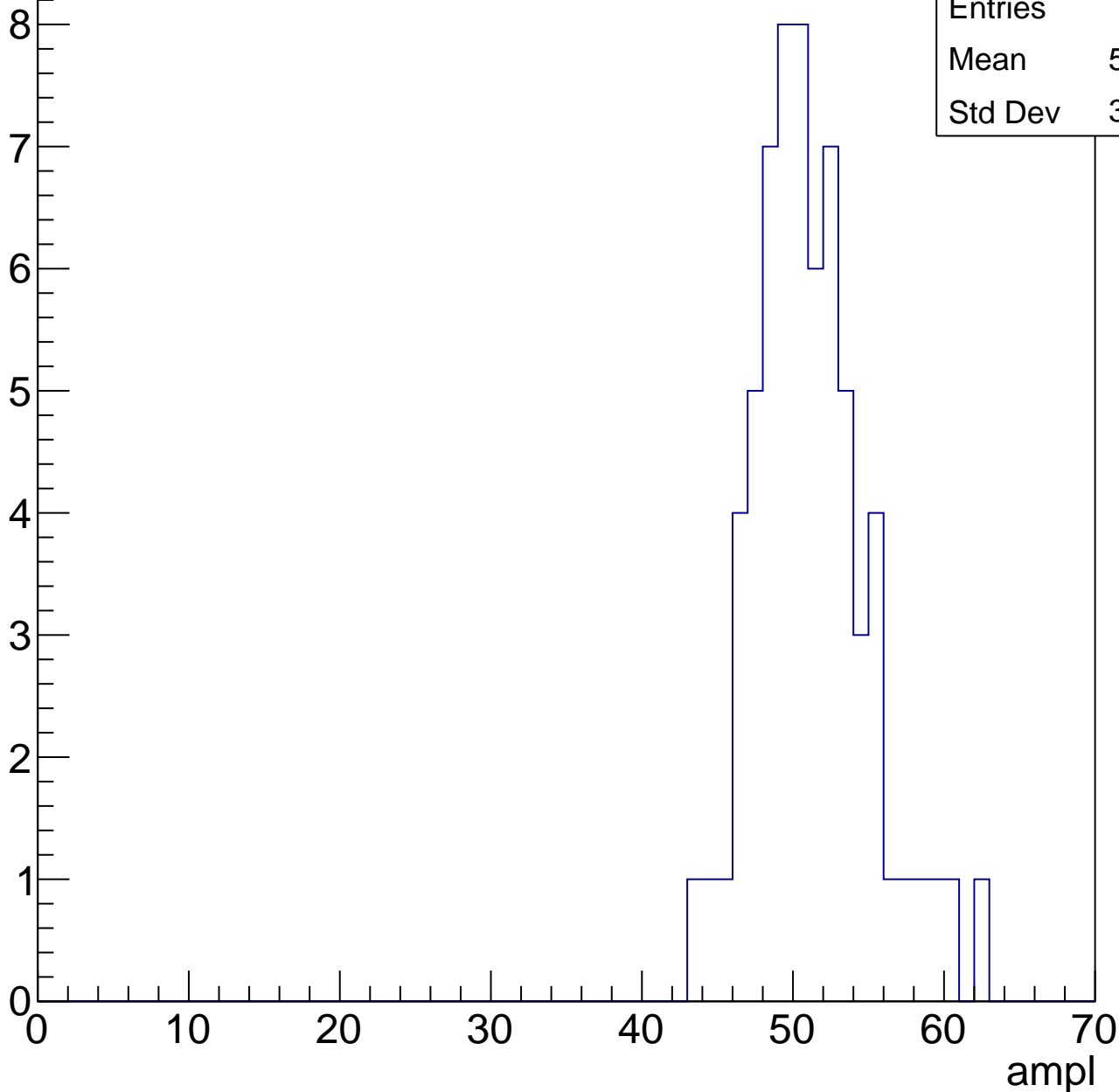


B1L100S, U19-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	50.73
Std Dev	3.736



B1L100S, U19-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

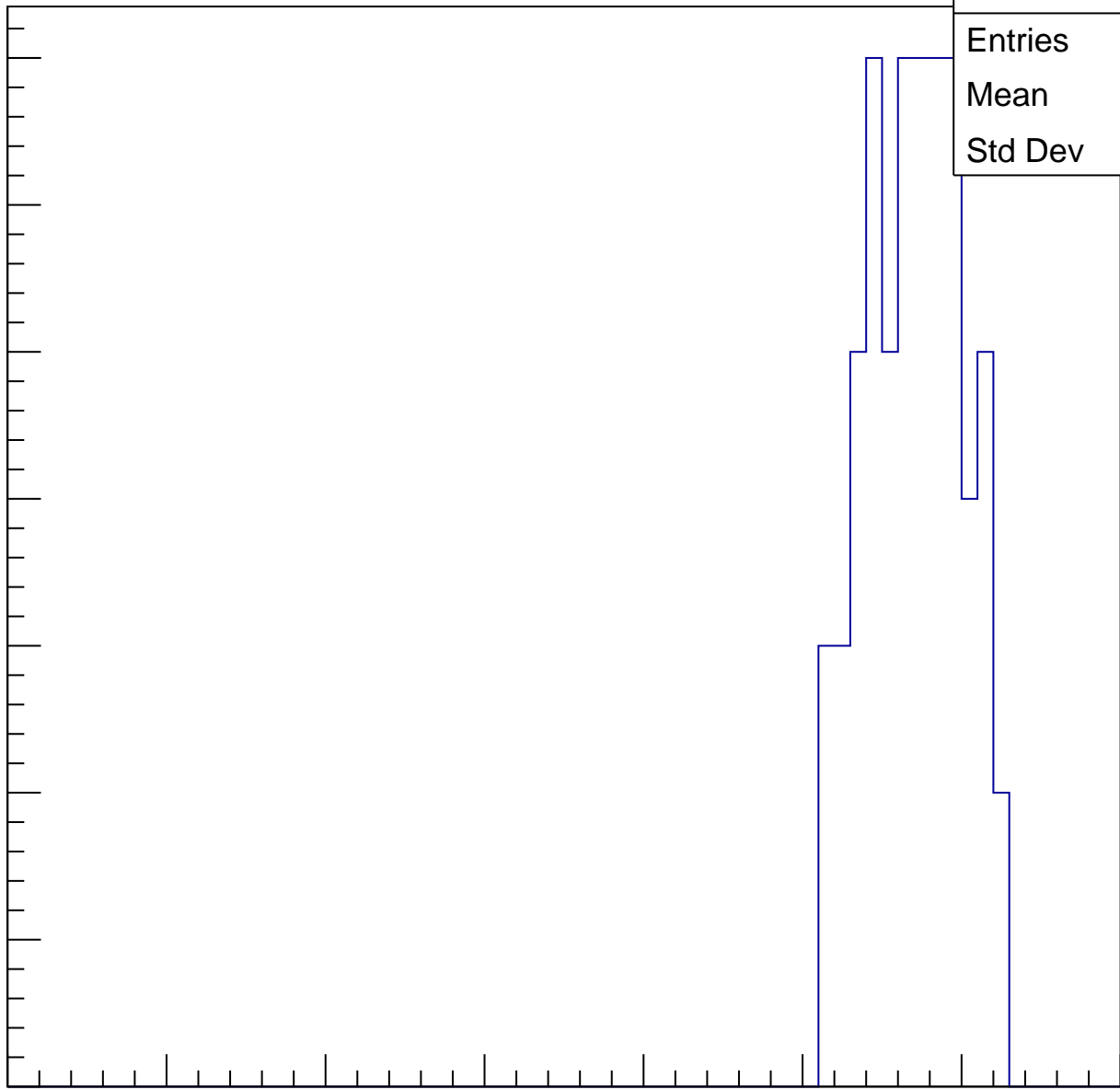
Entry

7
6
5
4
3
2
1
0

Entries	62
Mean	56.55
Std Dev	2.955

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

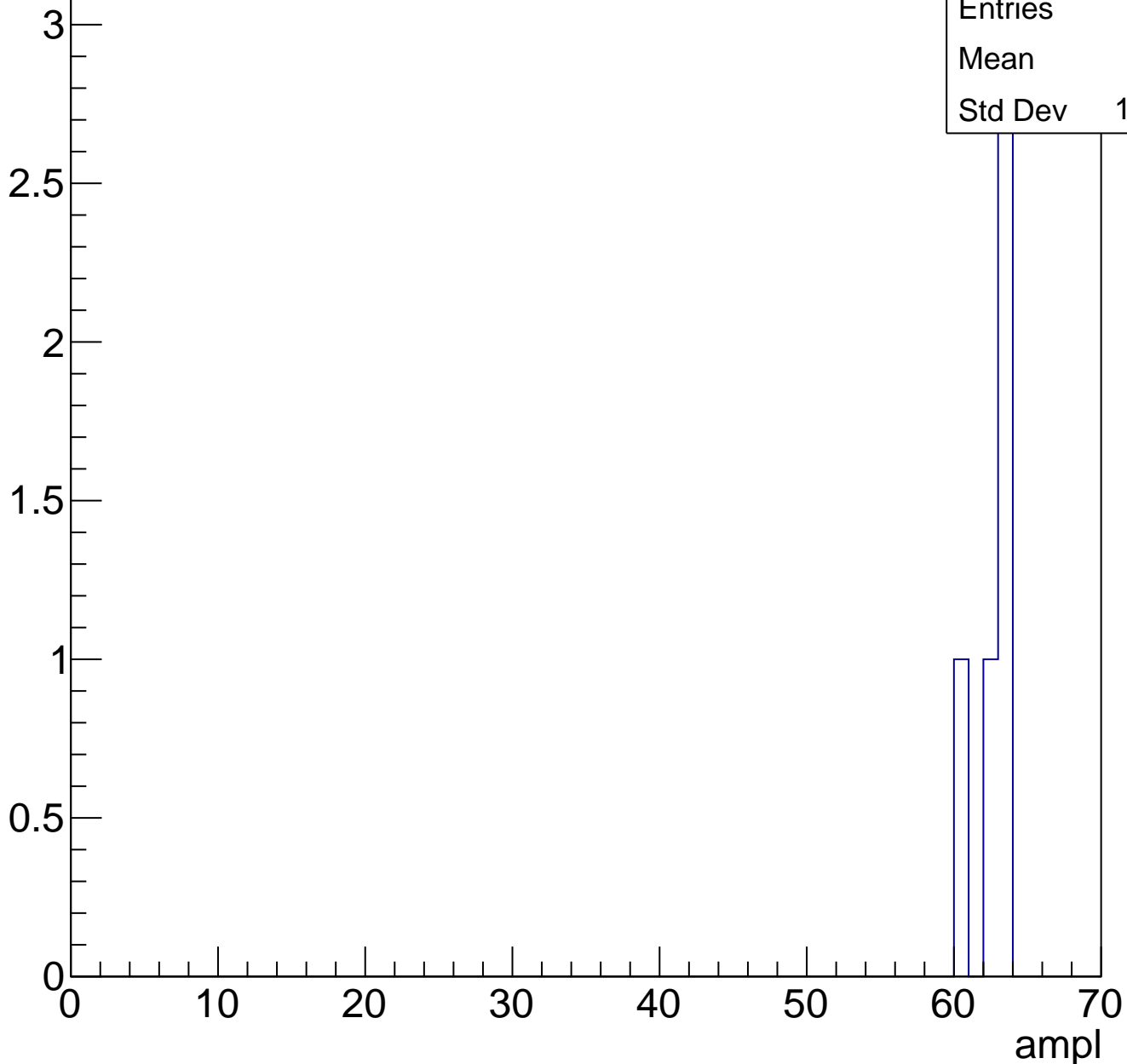
Entries	32
Mean	58.53
Std Dev	10.76

ampl

B1L100S, U19-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch23, adc0

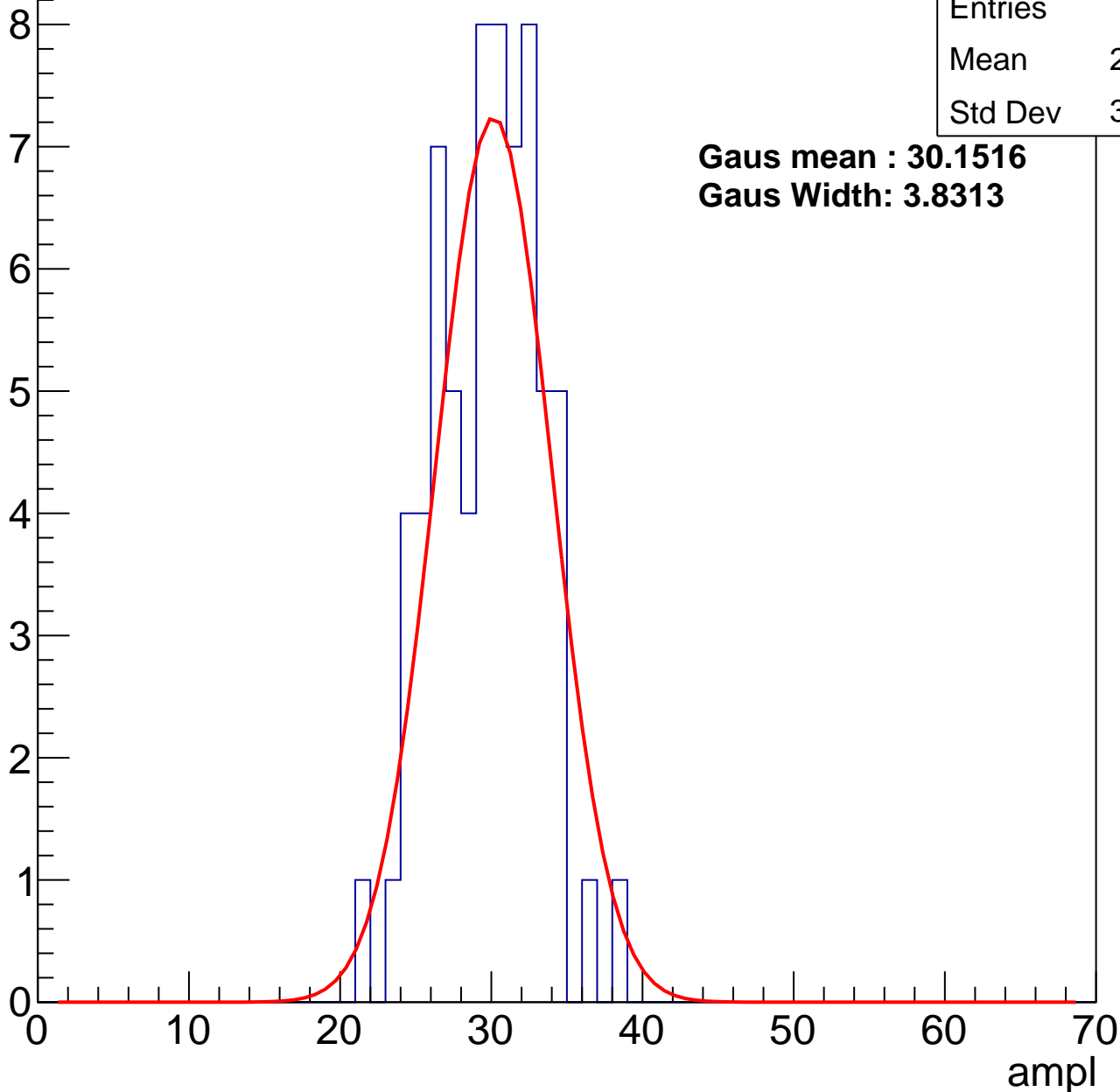
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.32
Std Dev	3.377

Gaus mean : 30.1516

Gaus Width: 3.8313



B1L100S, U19-ch23, adc1

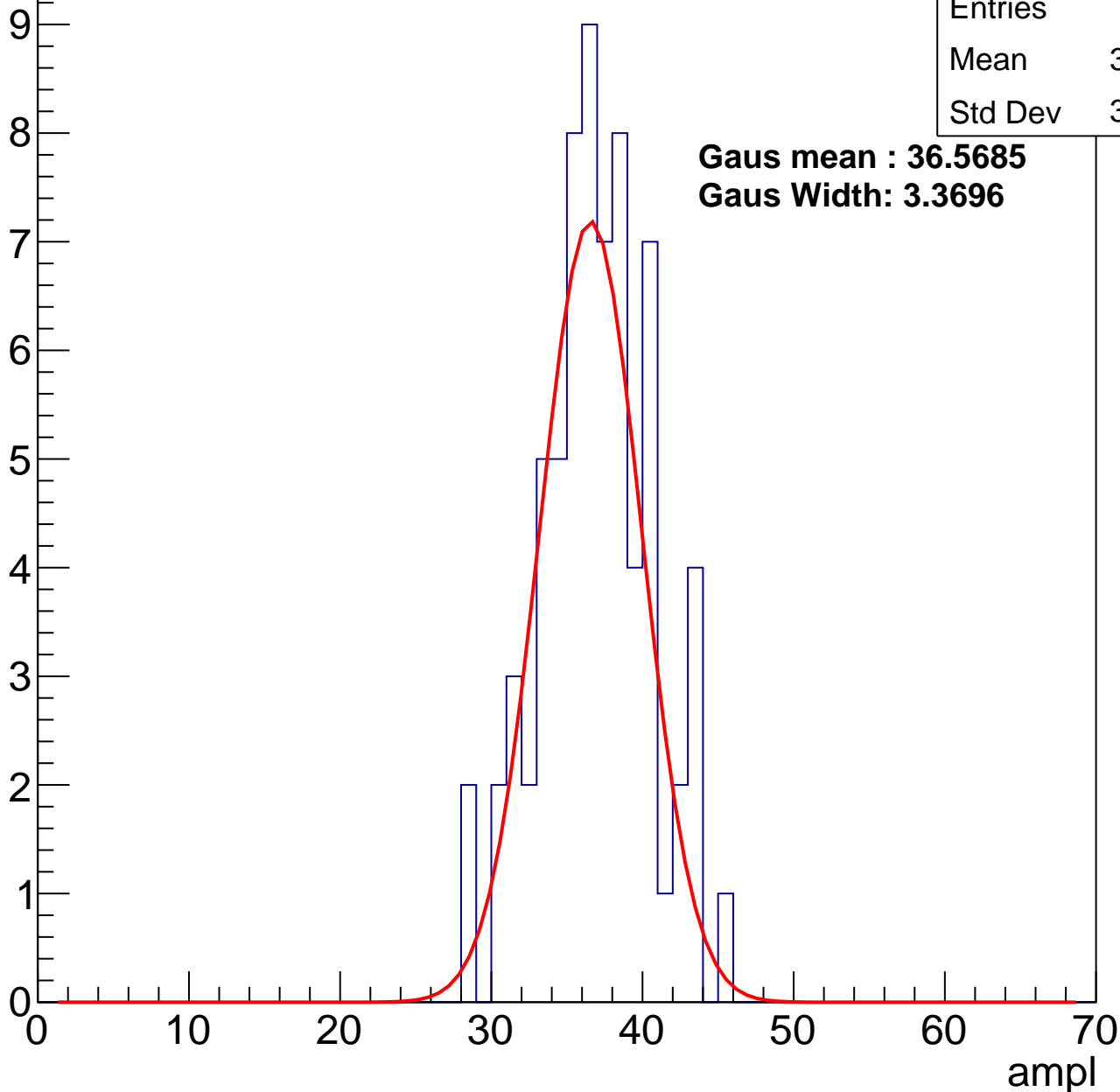
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.47
Std Dev	3.636

Gaus mean : 36.5685

Gaus Width: 3.3696



B1L100S, U19-ch23, adc2

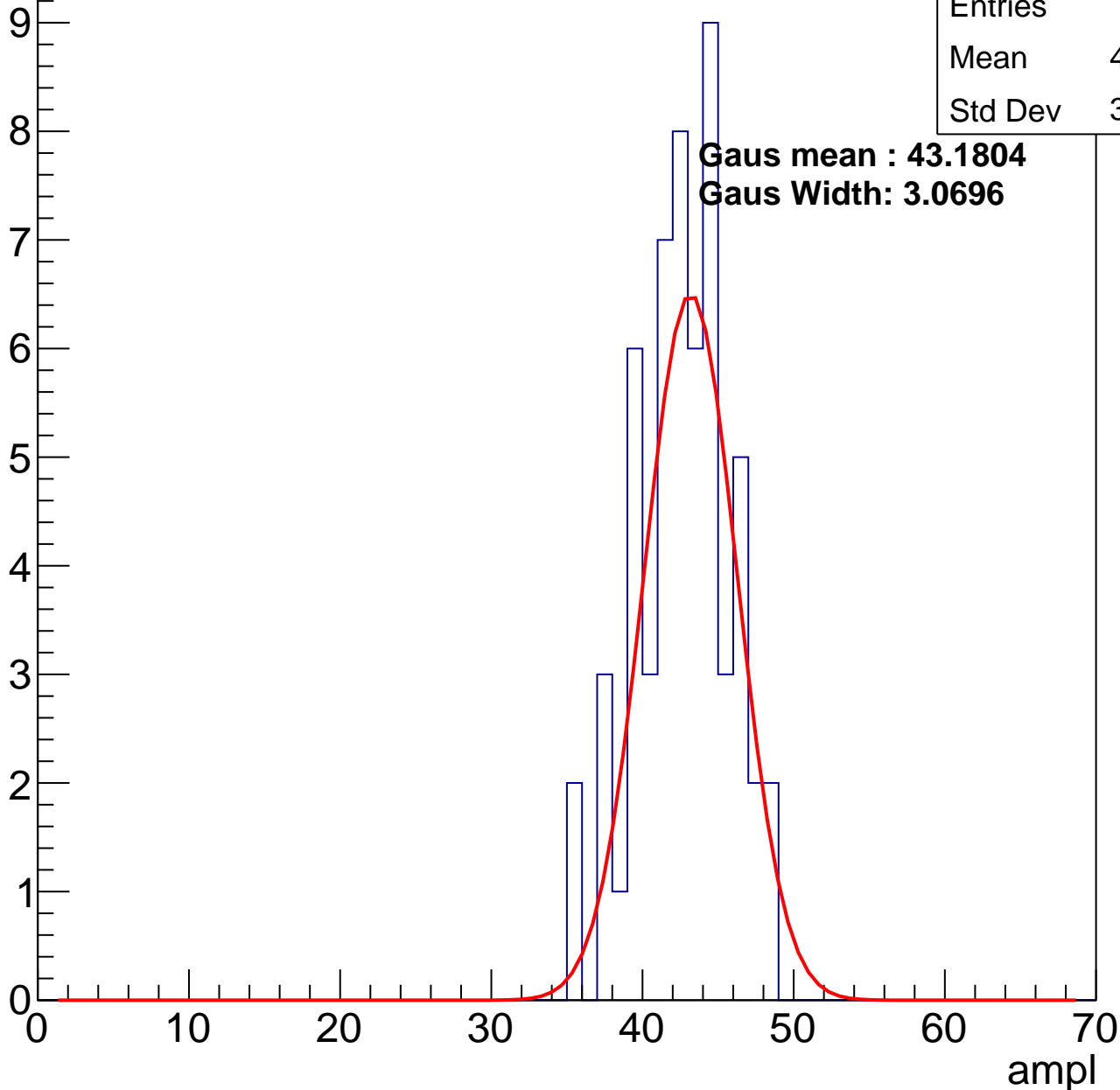
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.19
Std Dev	3.052

Gaus mean : 43.1804

Gaus Width: 3.0696

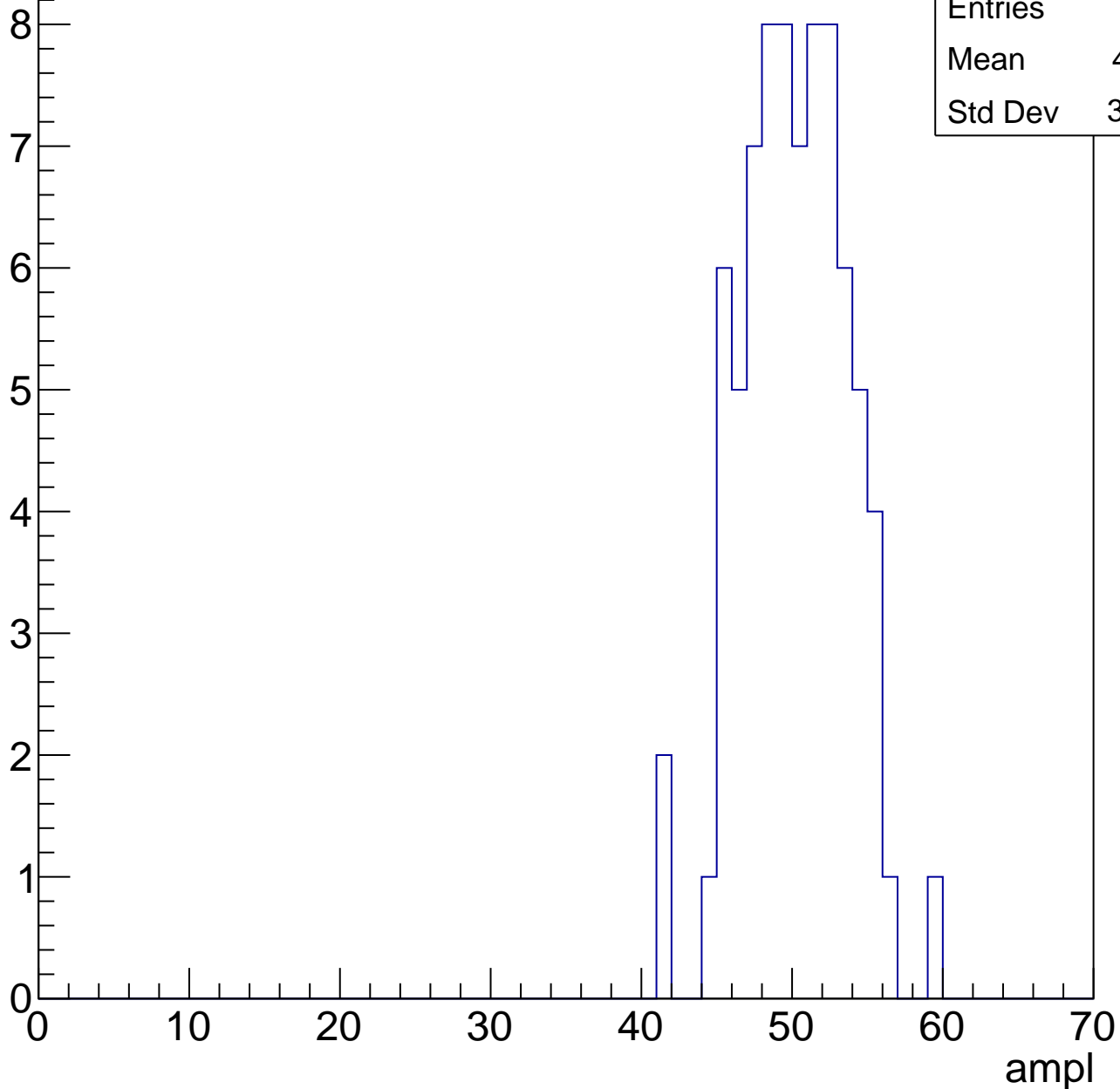


B1L100S, U19-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

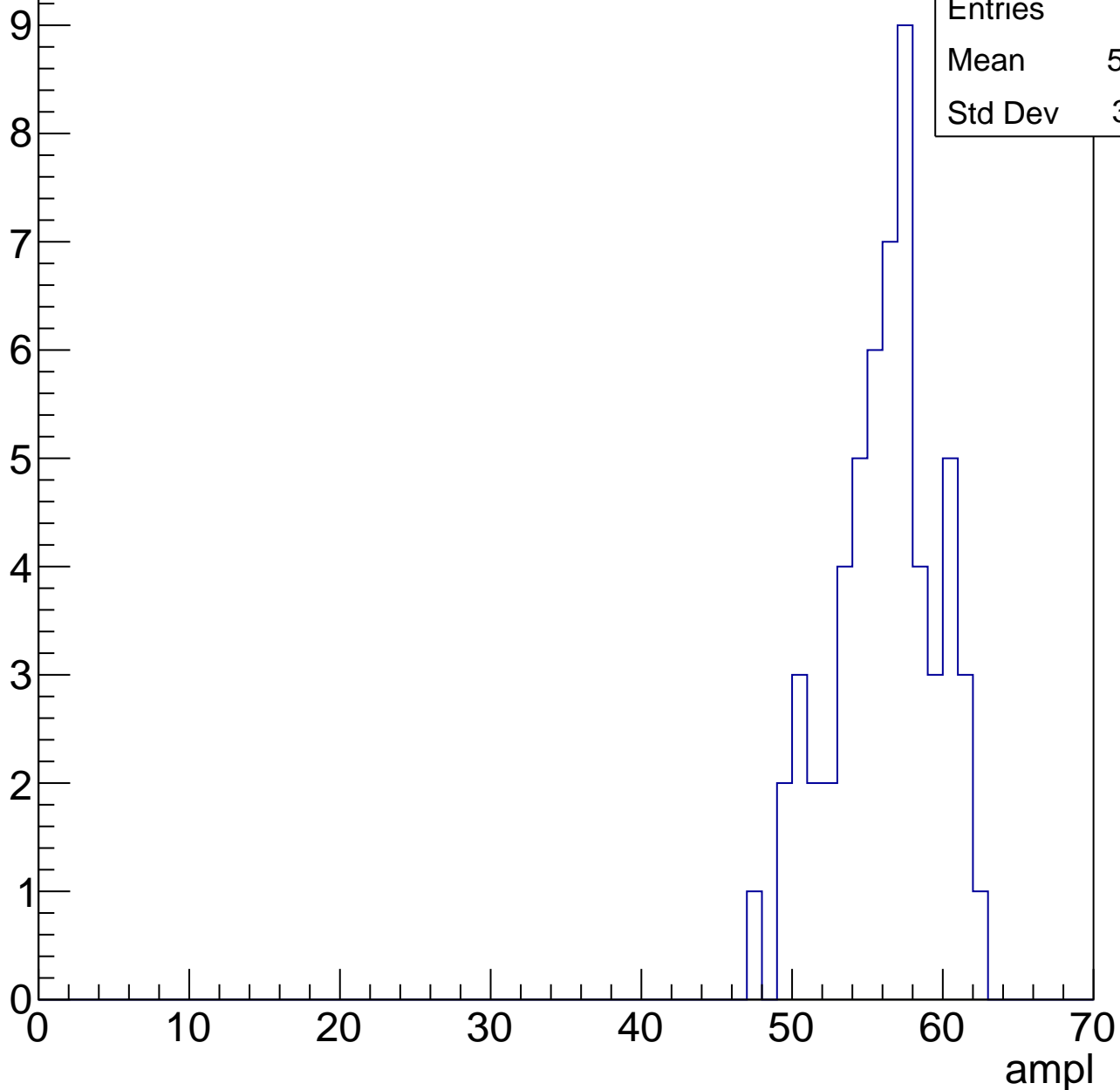
Entries	77
Mean	49.71
Std Dev	3.449



B1L100S, U19-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

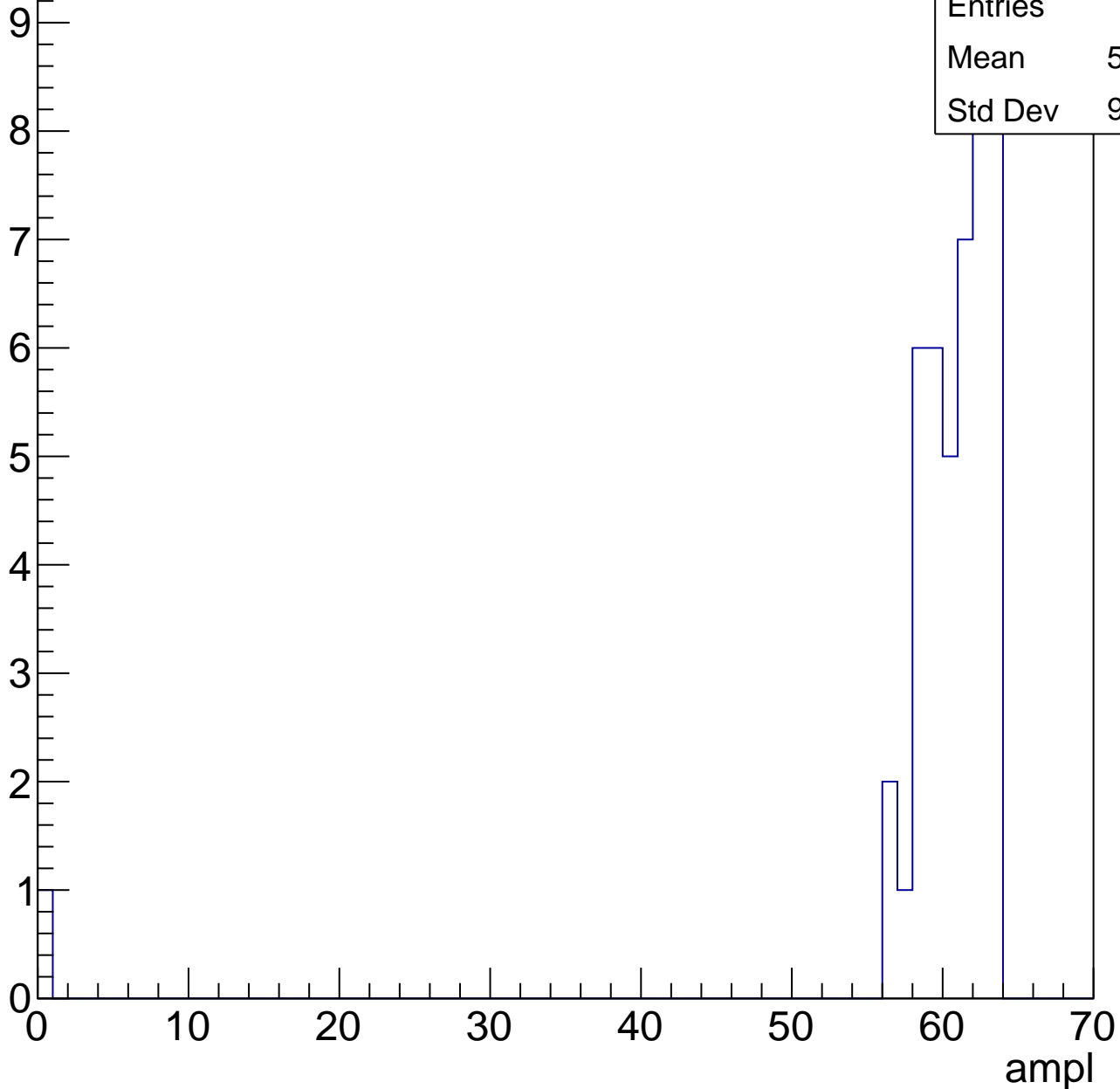


B1L100S, U19-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	59.13
Std Dev	9.137



B1L100S, U19-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch24, adc0

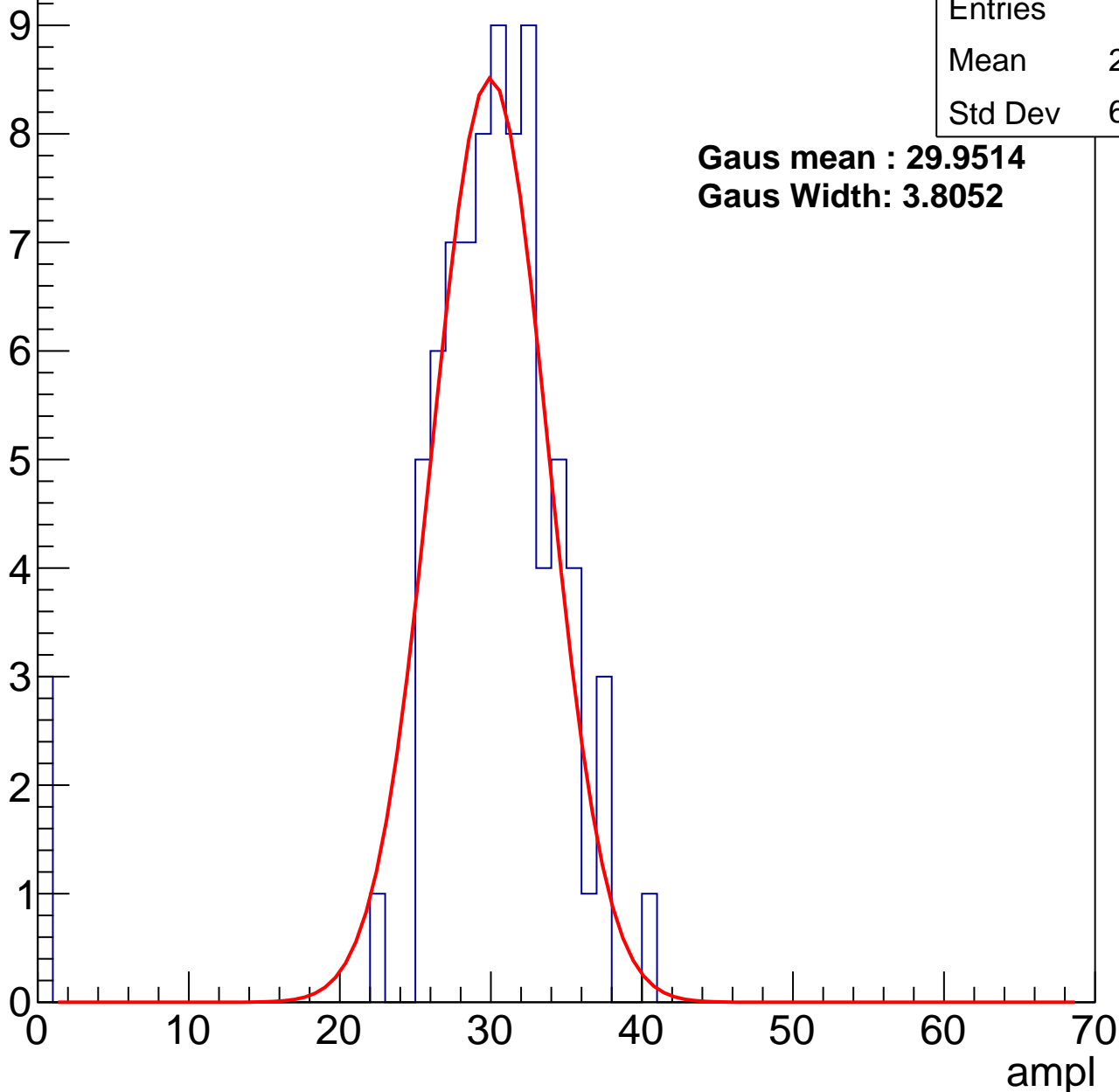
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	29.07
Std Dev	6.627

Gaus mean : 29.9514

Gaus Width: 3.8052



B1L100S, U19-ch24, adc1

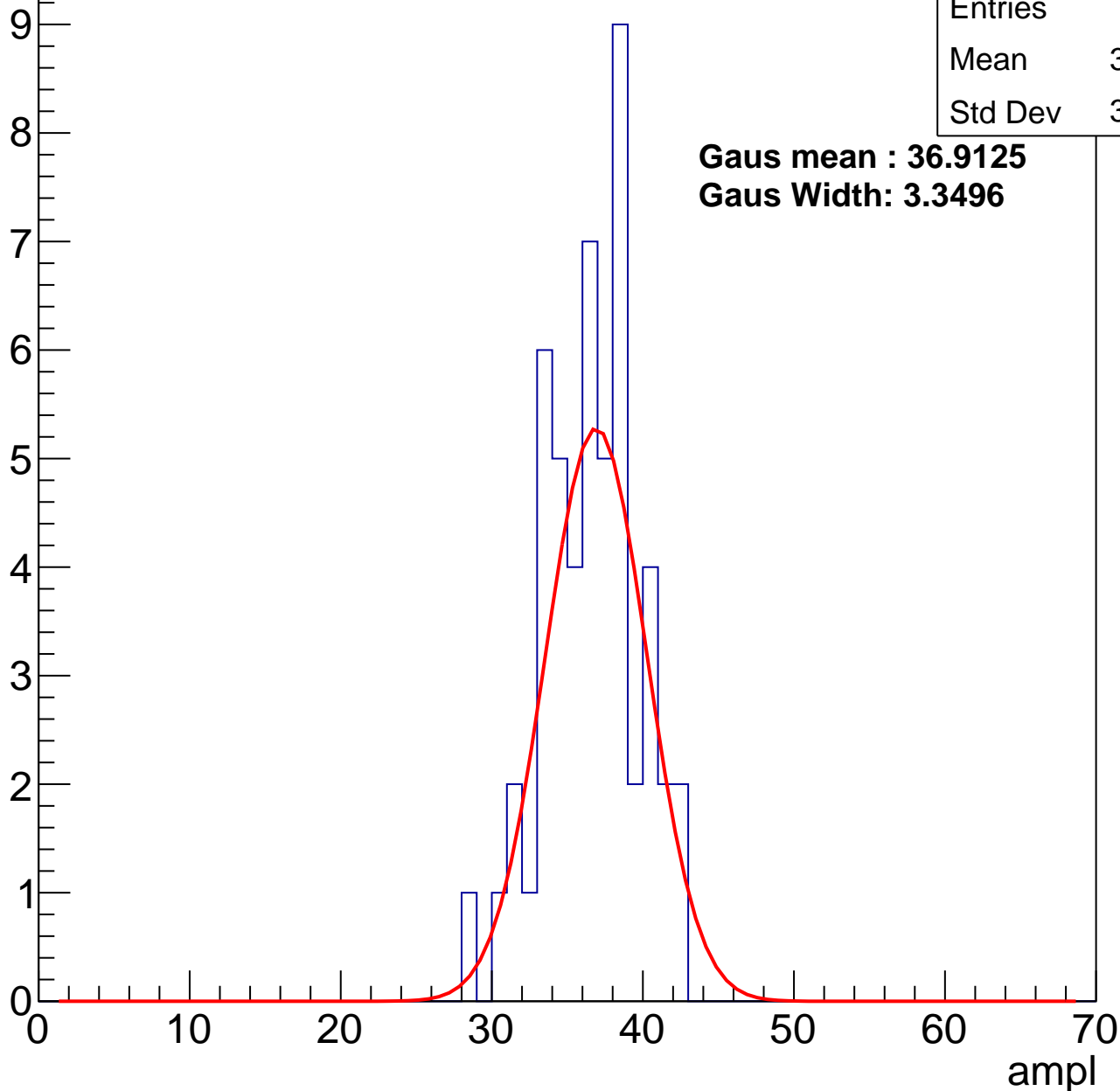
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	36.14
Std Dev	3.093

Gaus mean : 36.9125

Gaus Width: 3.3496



B1L100S, U19-ch24, adc2

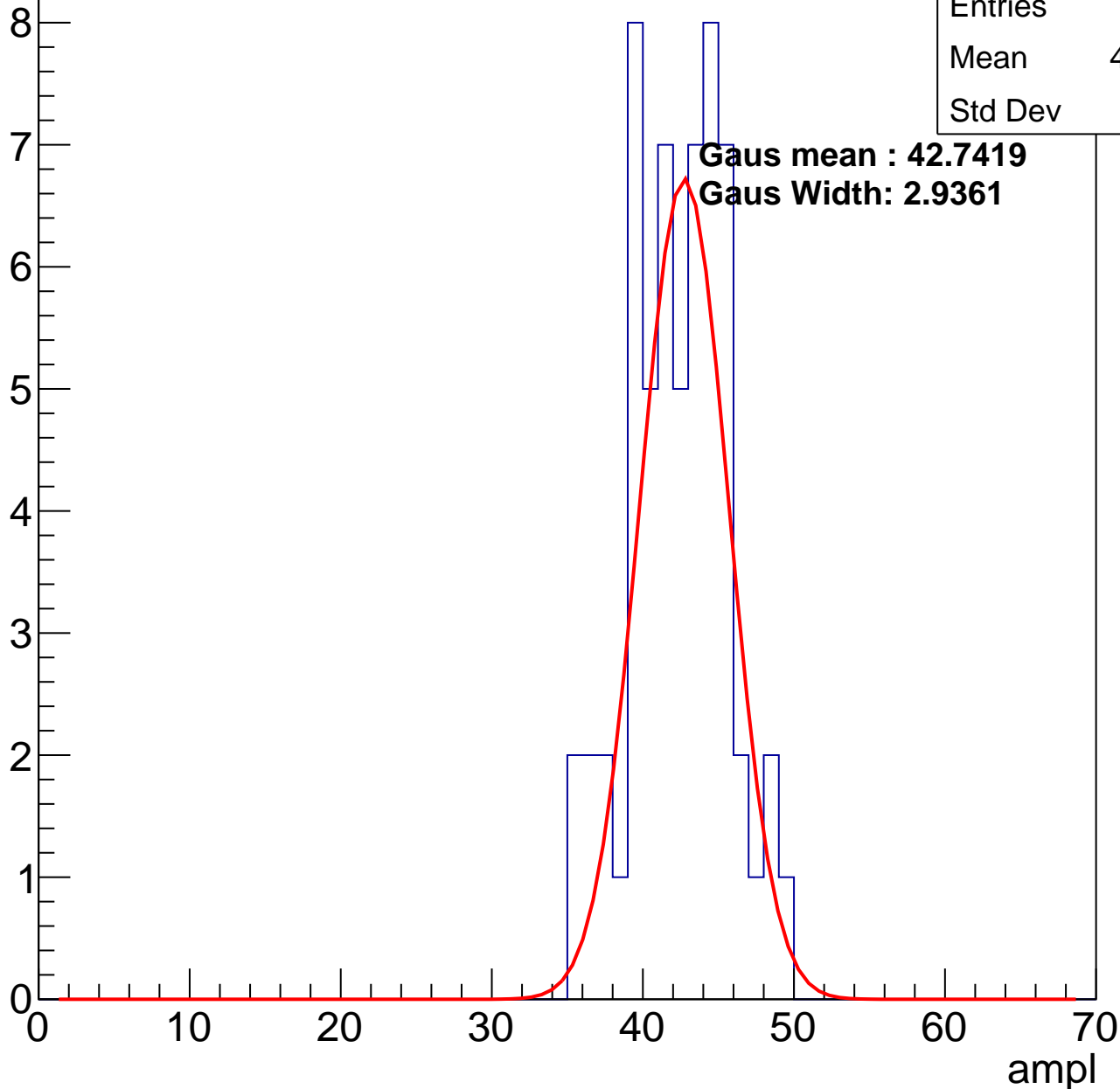
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.92
Std Dev	3.2

Gaus mean : 42.7419

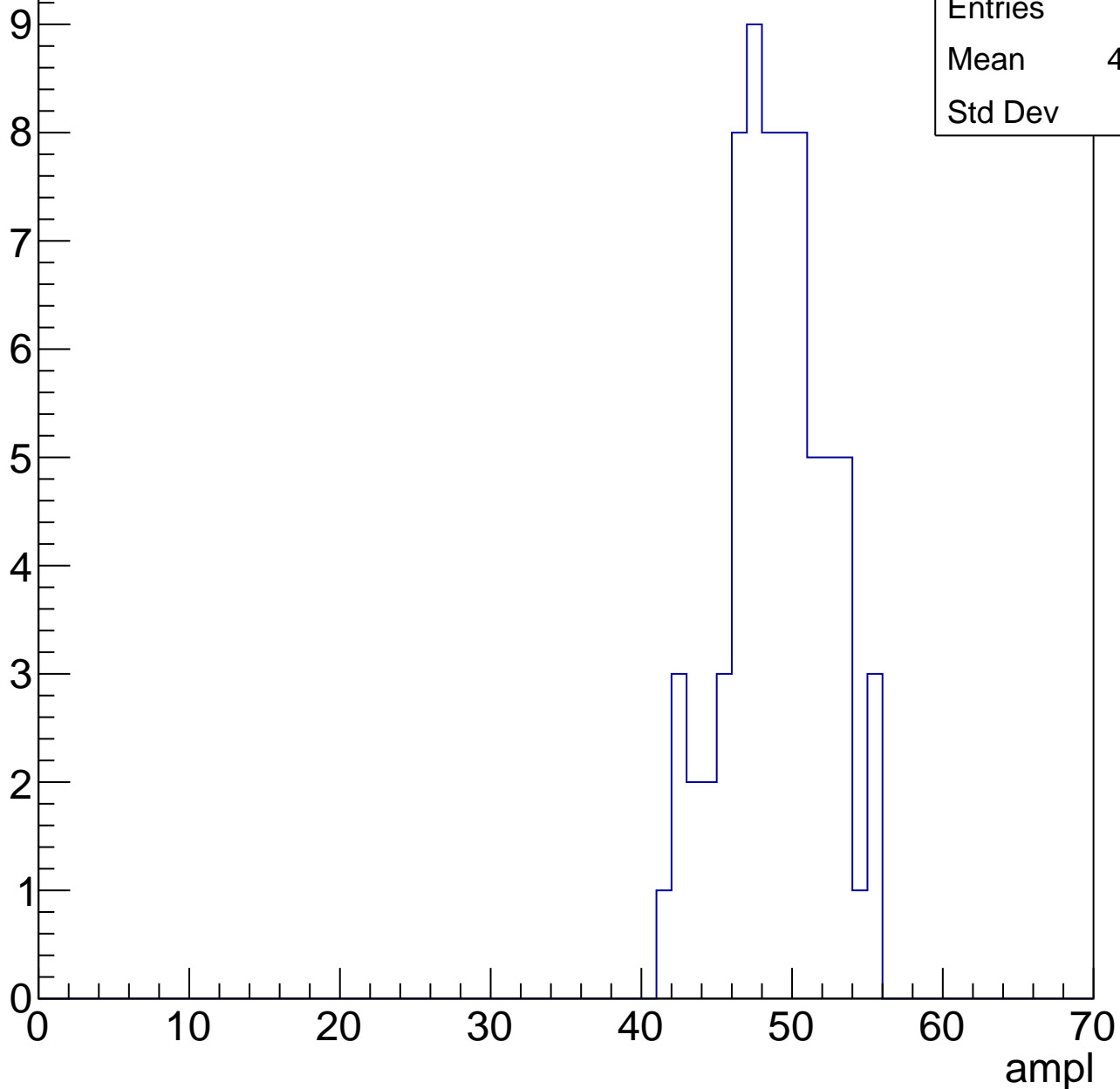
Gaus Width: 2.9361



B1L100S, U19-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



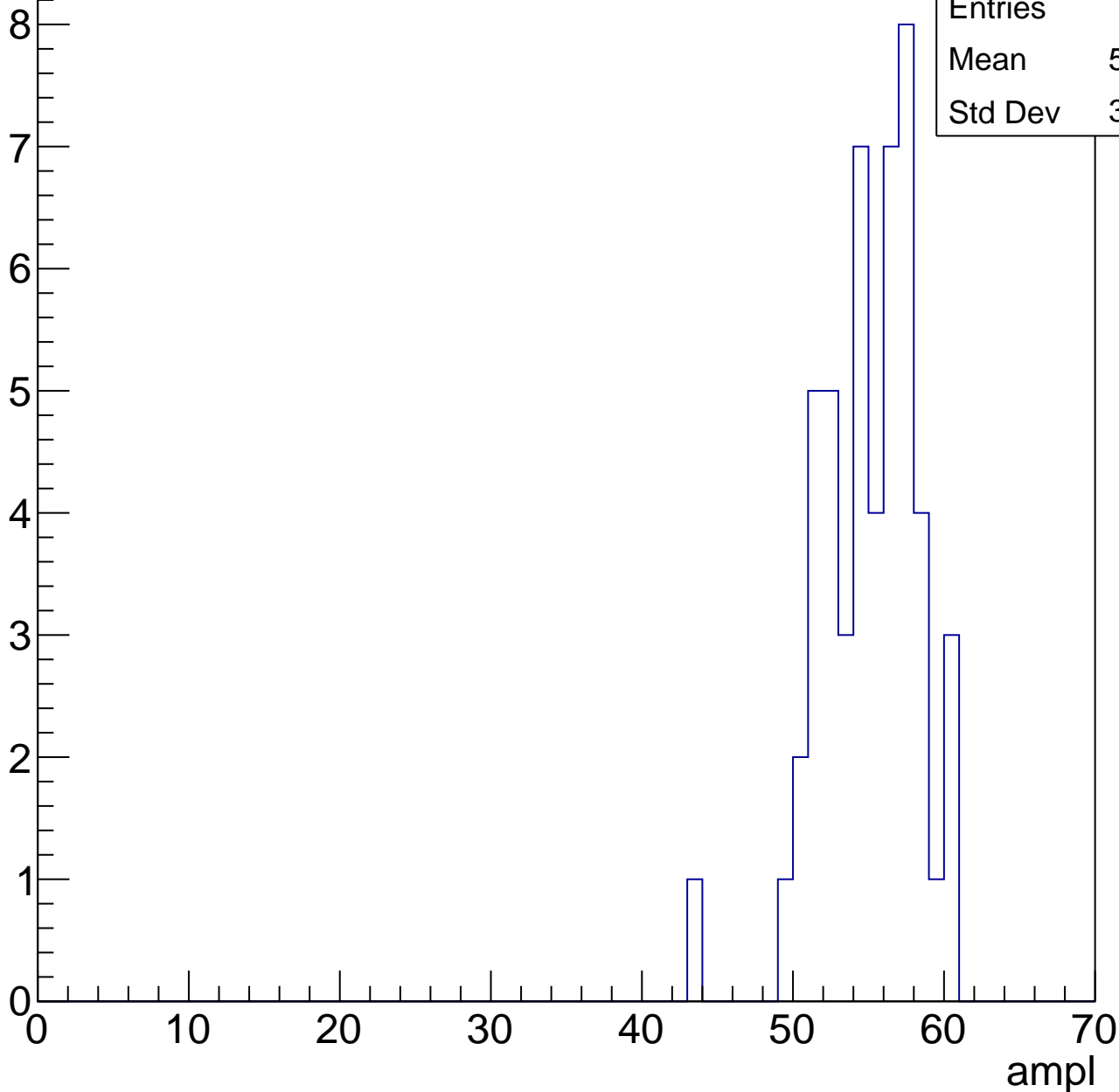
Entries	71
Mean	48.48
Std Dev	3.28

B1L100S, U19-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	54.57
Std Dev	3.225

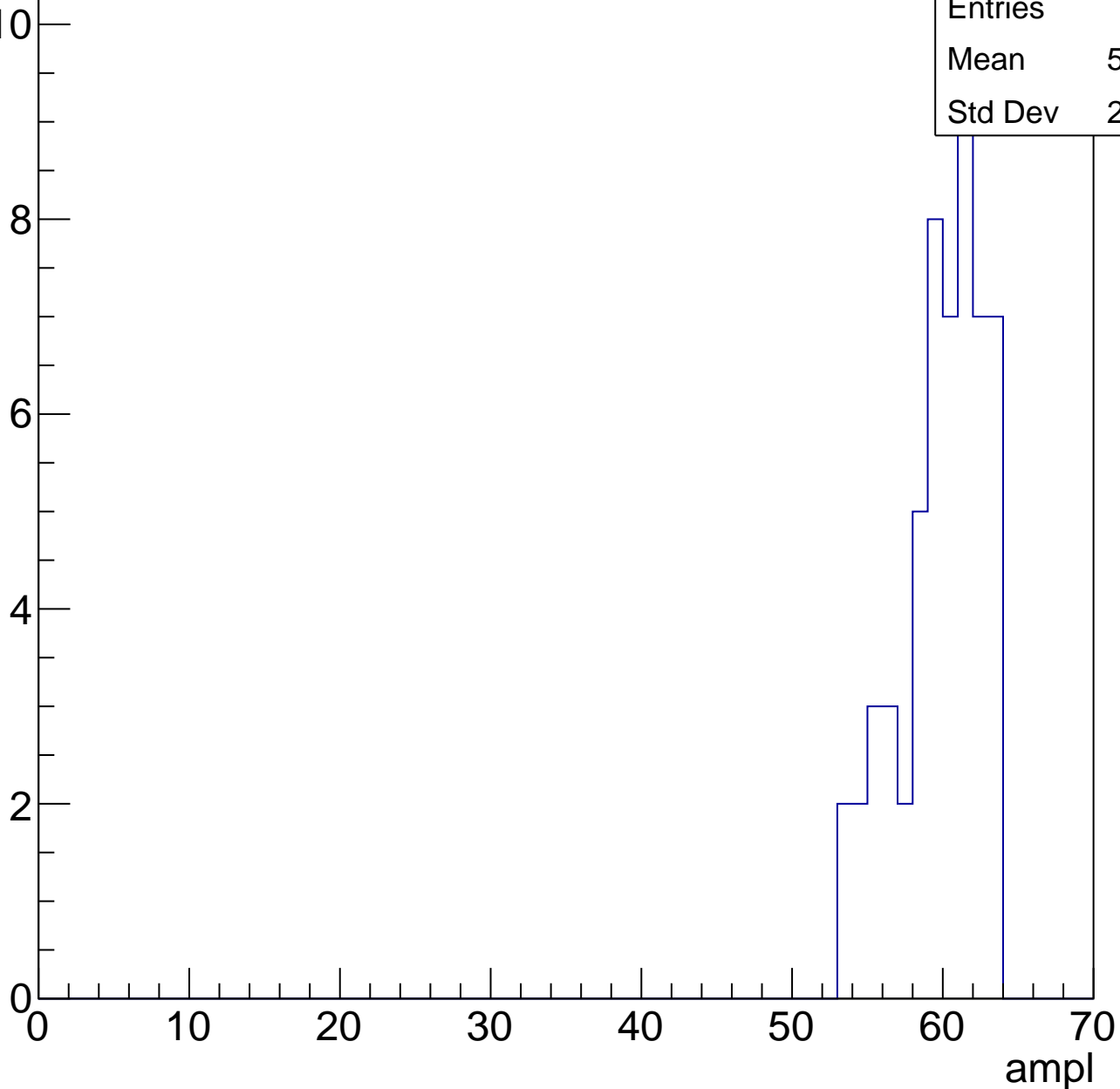


B1L100S, U19-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

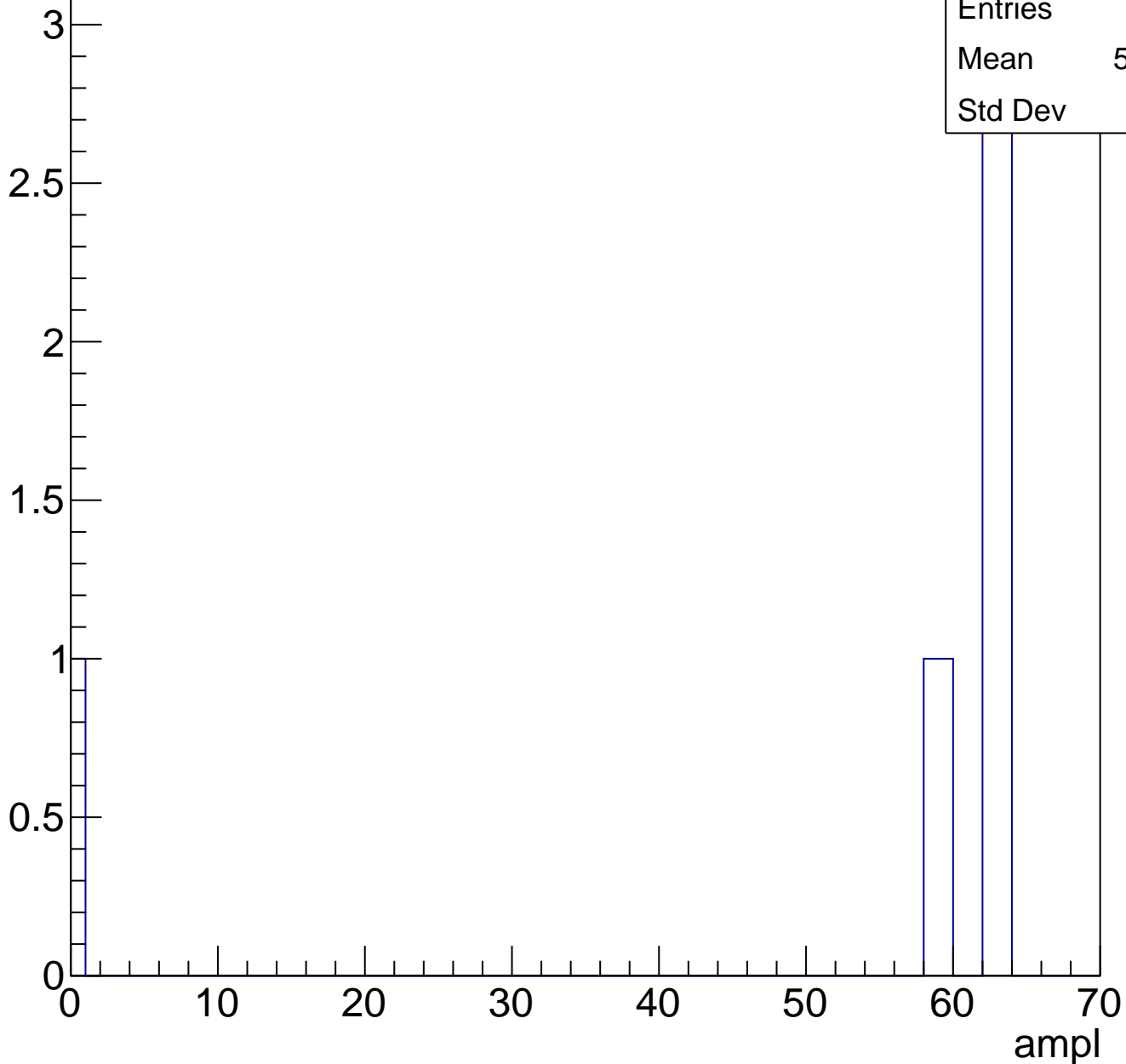
Entries	56
Mean	59.43
Std Dev	2.744



B1L100S, U19-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch25, adc0

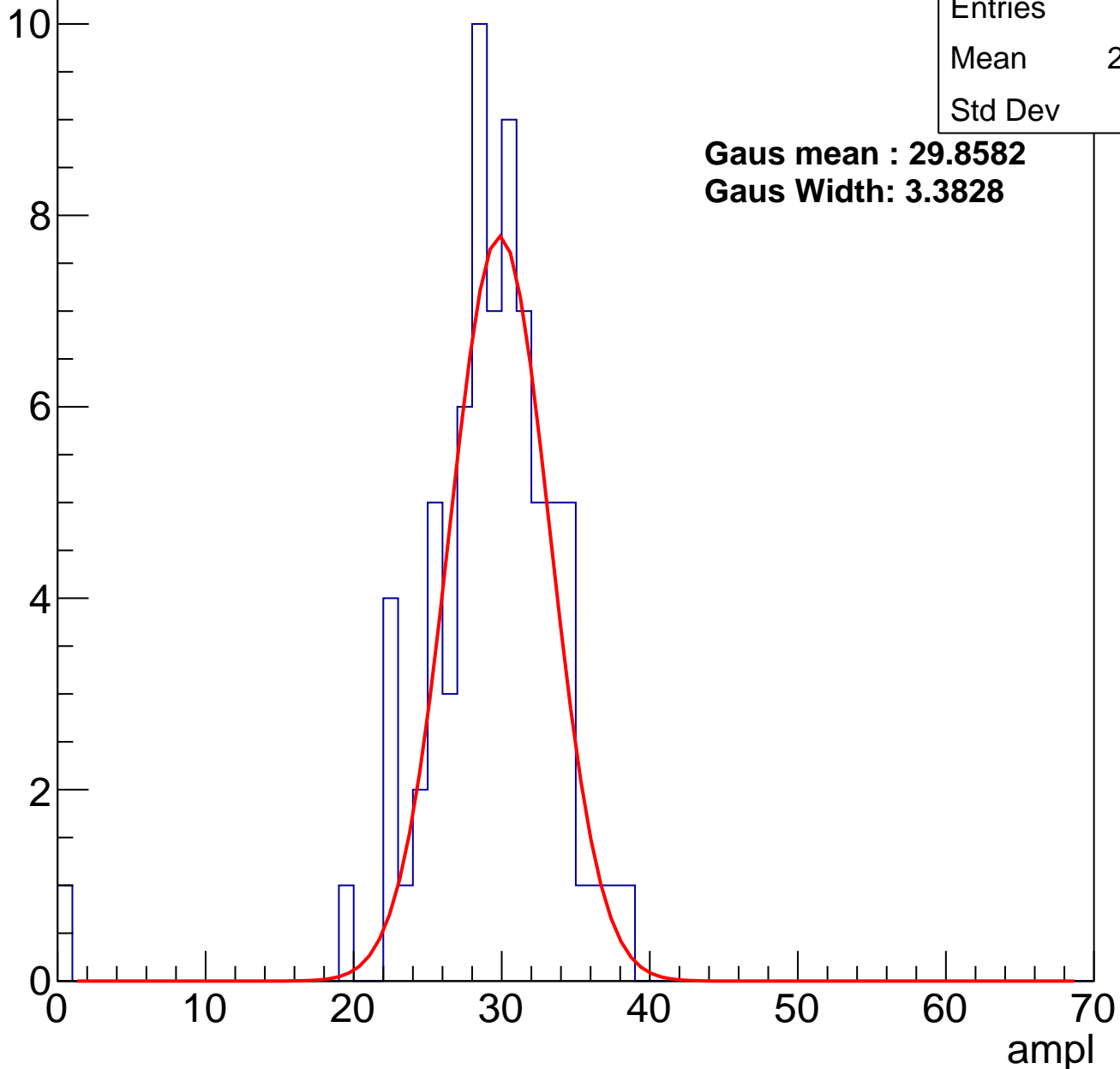
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	28.72
Std Dev	5

Gaus mean : 29.8582

Gaus Width: 3.3828

Entry



B1L100S, U19-ch25, adc1

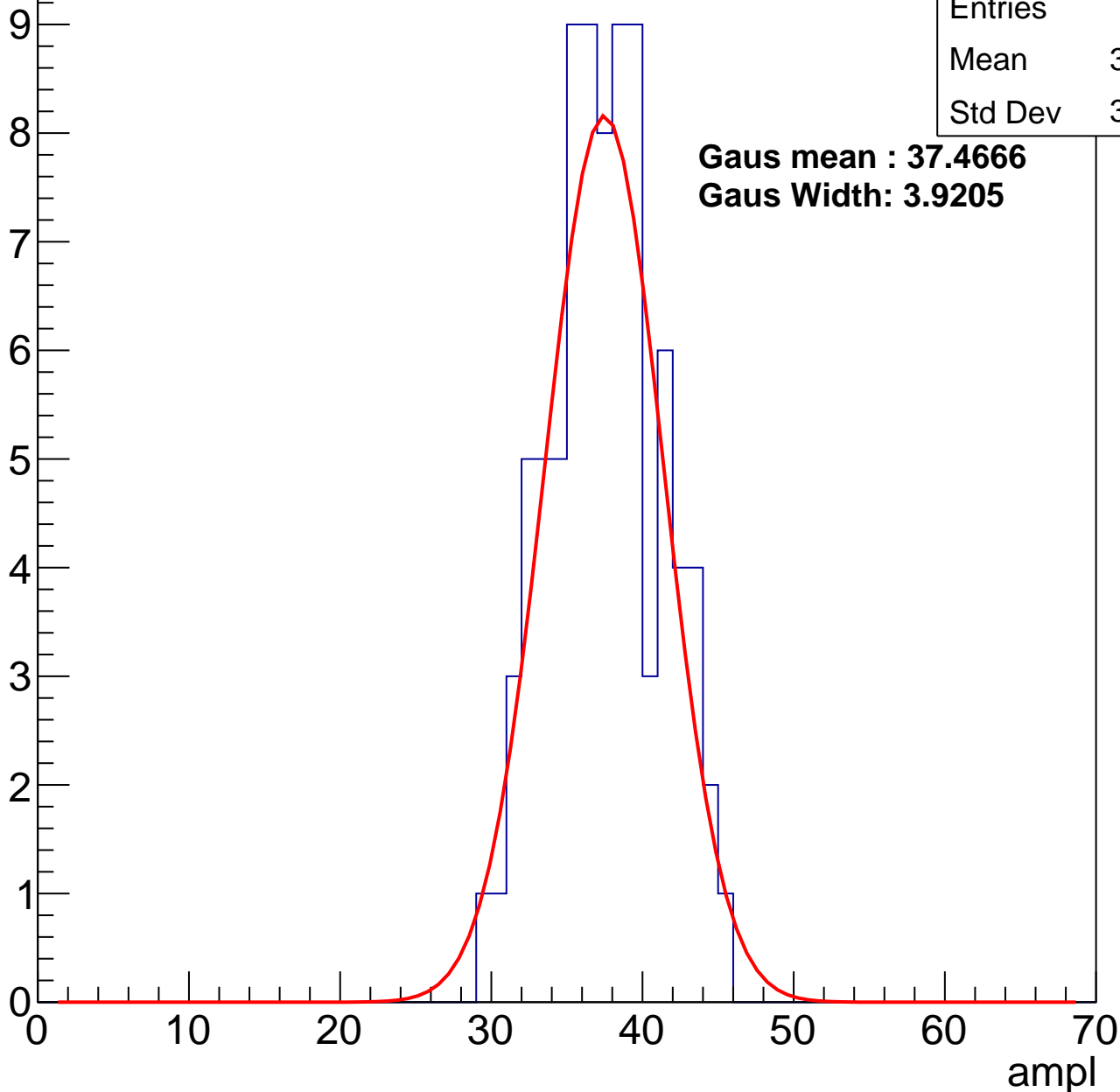
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	37.07
Std Dev	3.602

Gaus mean : 37.4666

Gaus Width: 3.9205



B1L100S, U19-ch25, adc2

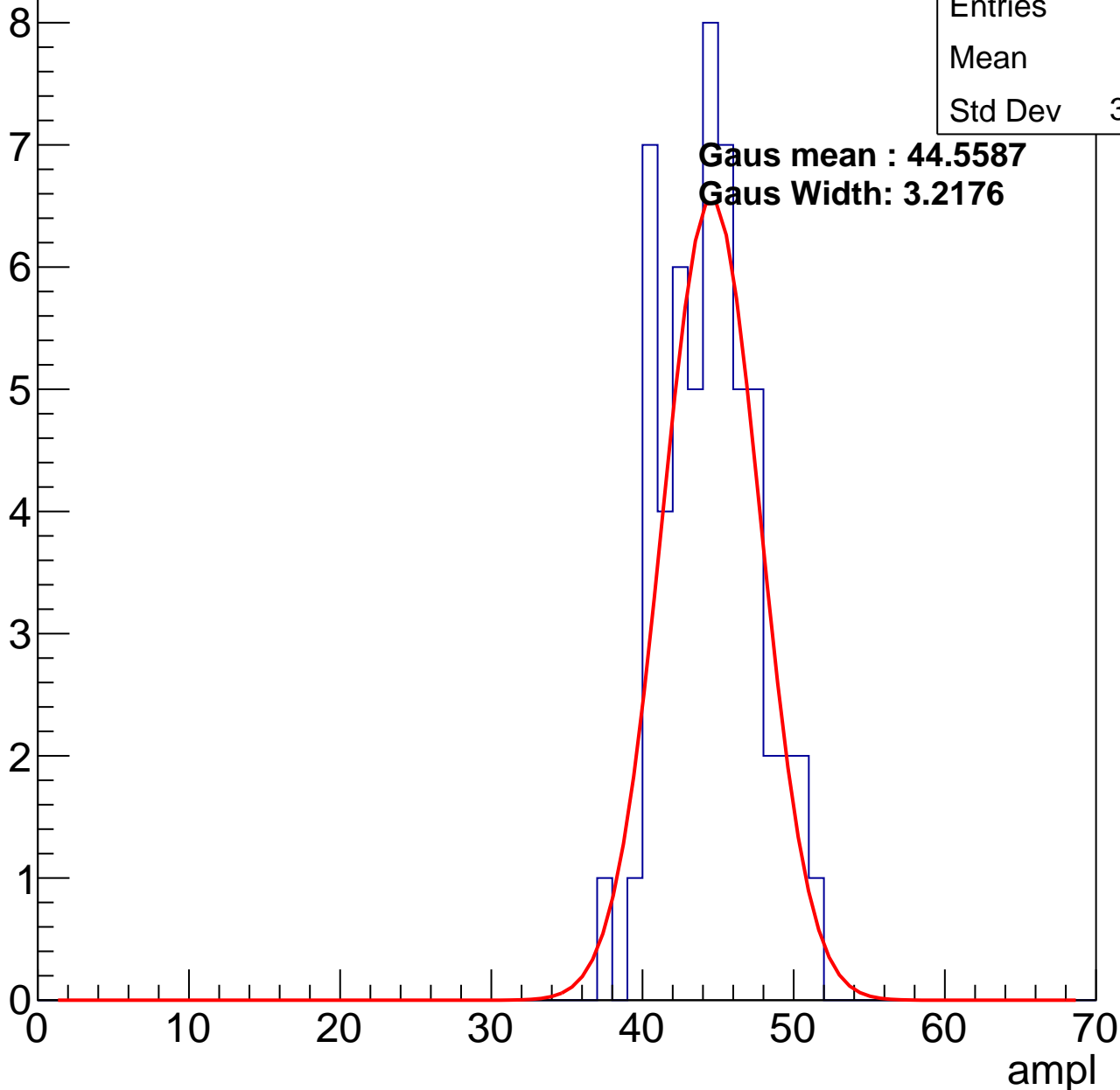
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44
Std Dev	3.065

Gaus mean : 44.5587

Gaus Width: 3.2176



B1L100S, U19-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	50.24
Std Dev	2.96

Entry

10

8

6

4

2

0

0

10

20

30

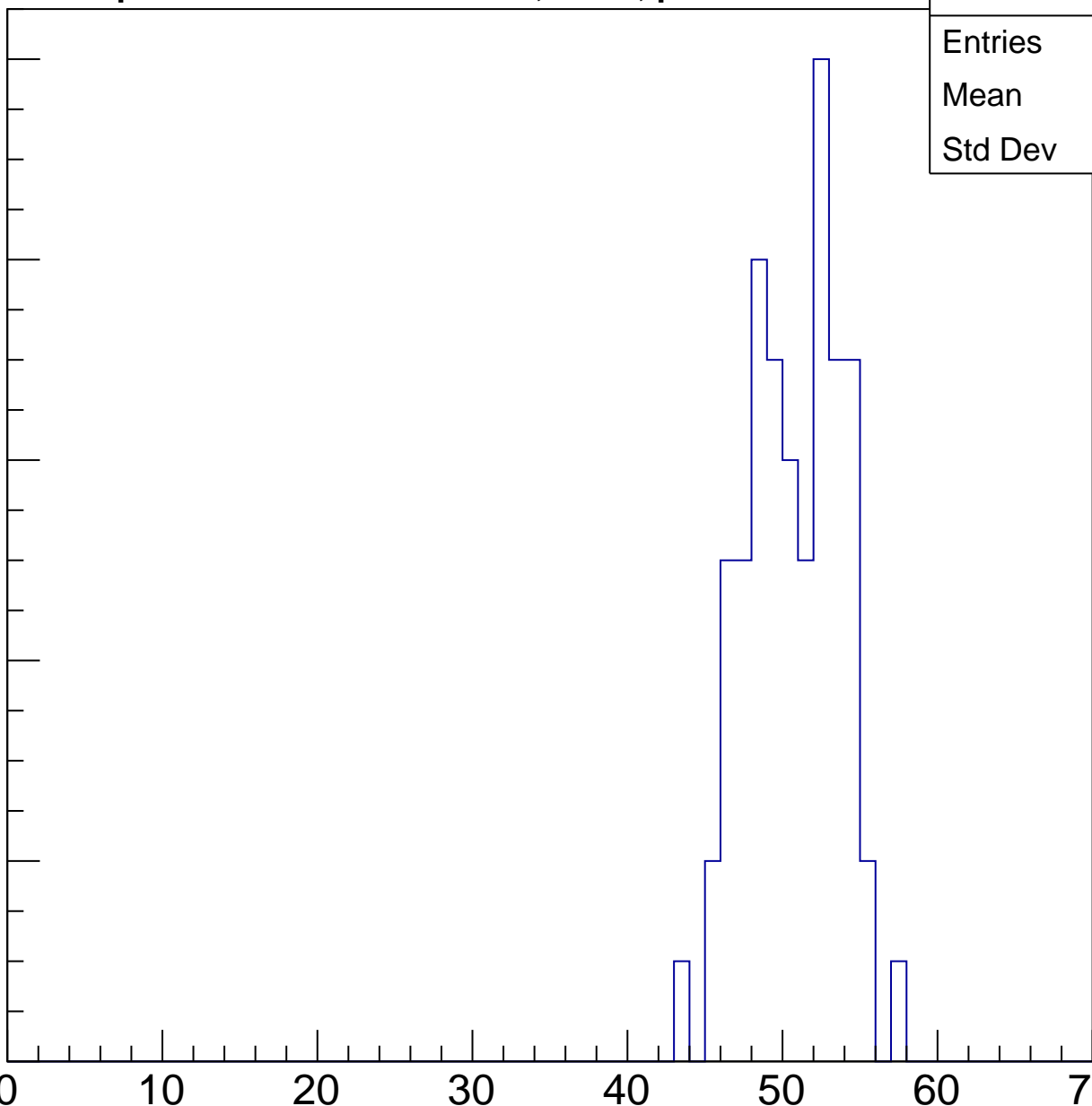
40

50

60

70

ampl



B1L100S, U19-ch25, adc4

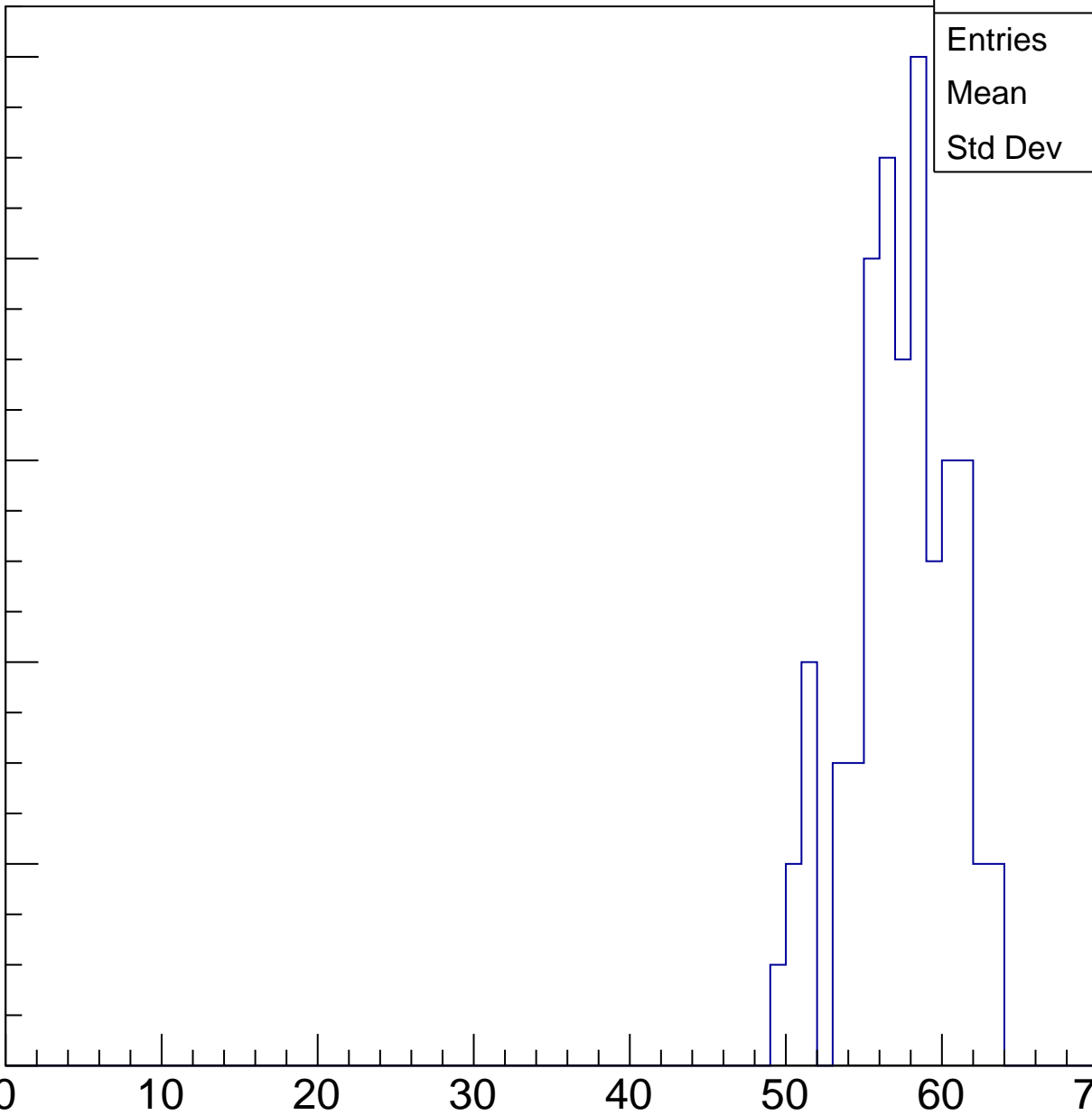
calib_packv5_042523_0143.root, FC#4, port A2

Entry

10
8
6
4
2
0

Entries	68
Mean	56.88
Std Dev	3.261

ampl

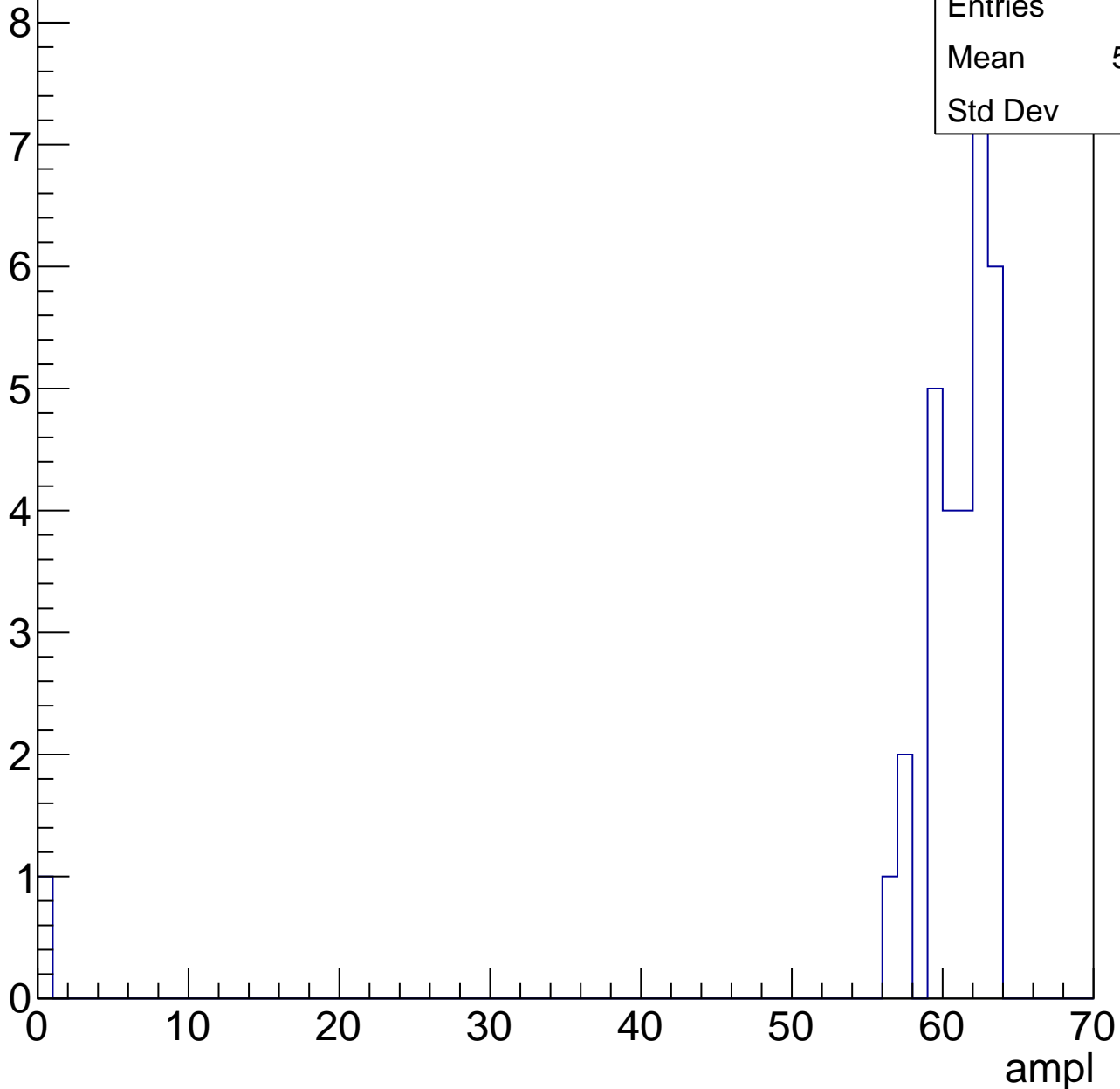


B1L100S, U19-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.81
Std Dev	10.9



B1L100S, U19-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch26, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.51
Std Dev	4.592

Gaus mean : 30.3114

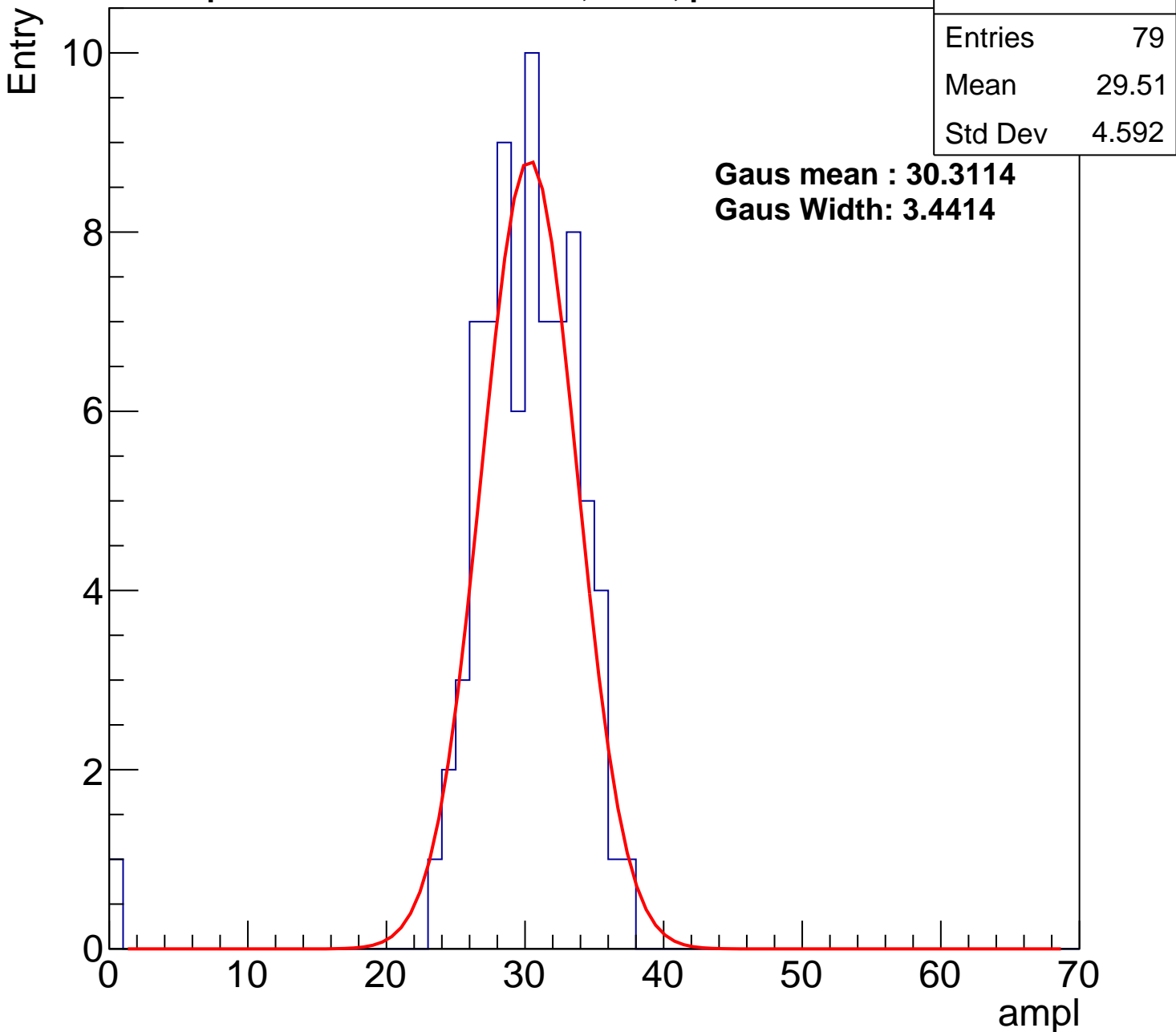
Gaus Width: 3.4414

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



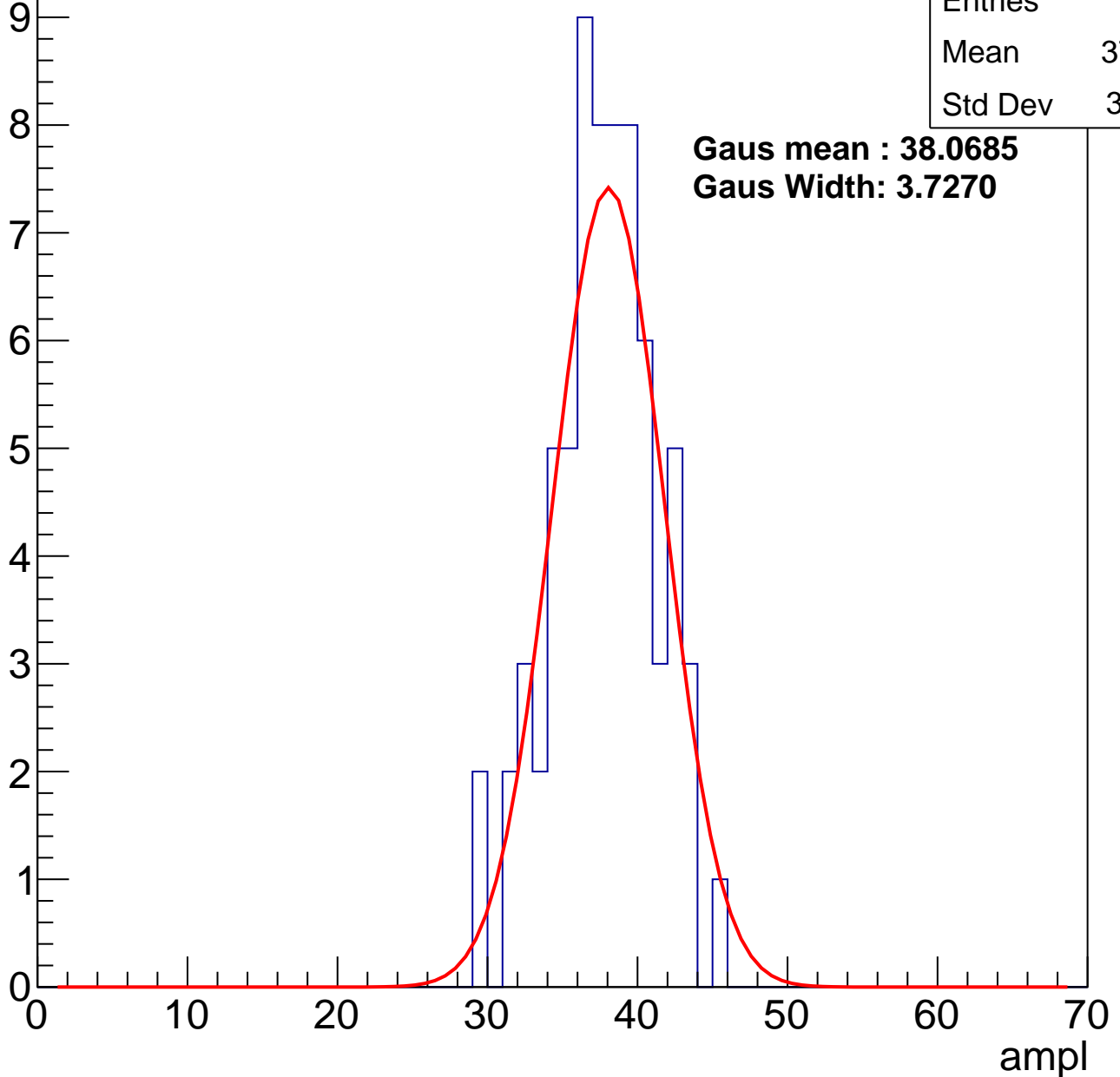
B1L100S, U19-ch26, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.29
Std Dev	3.411

Gaus mean : 38.0685
Gaus Width: 3.7270



B1L100S, U19-ch26, adc2

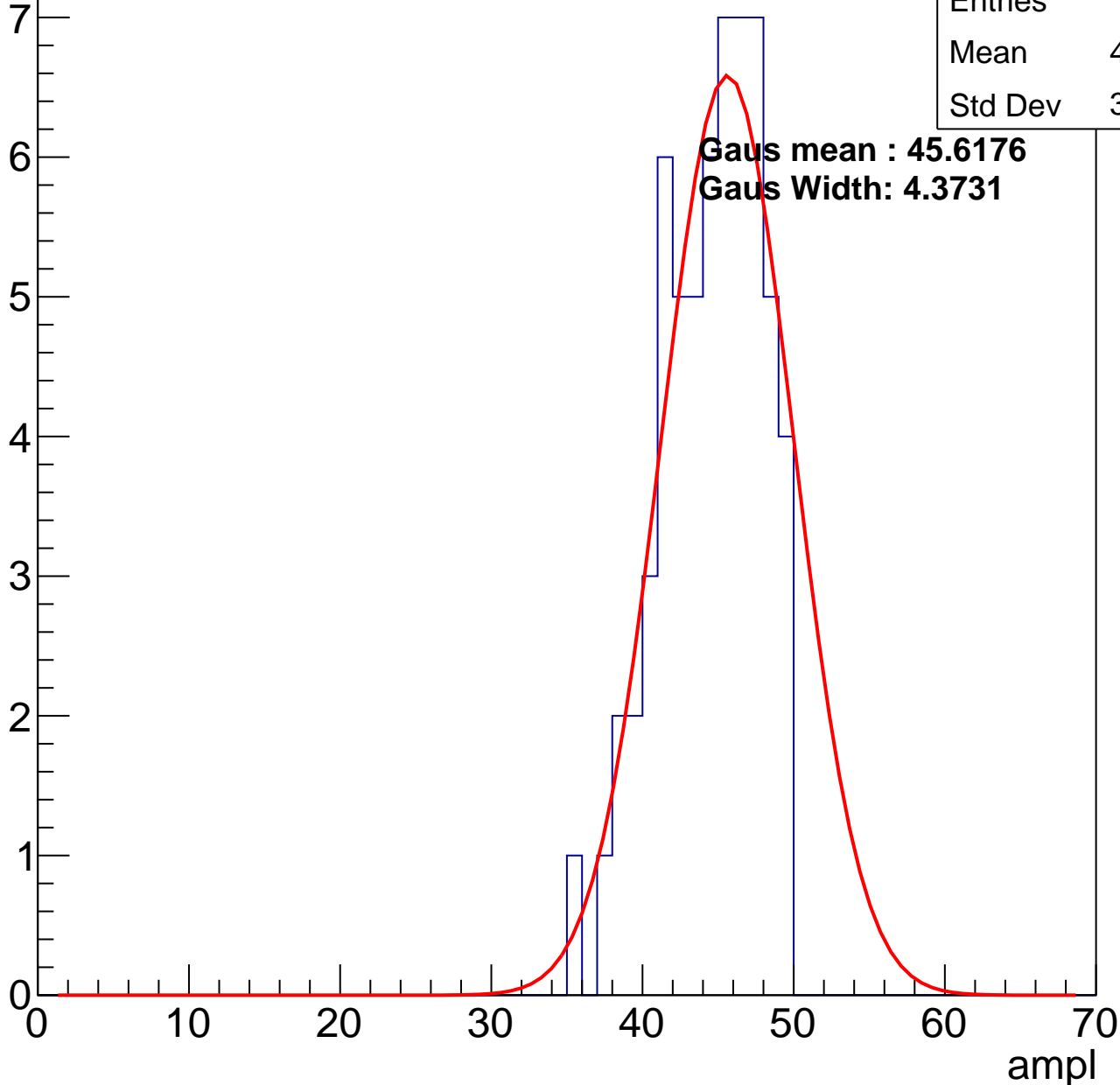
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.98
Std Dev	3.282

Gaus mean : 45.6176

Gaus Width: 4.3731

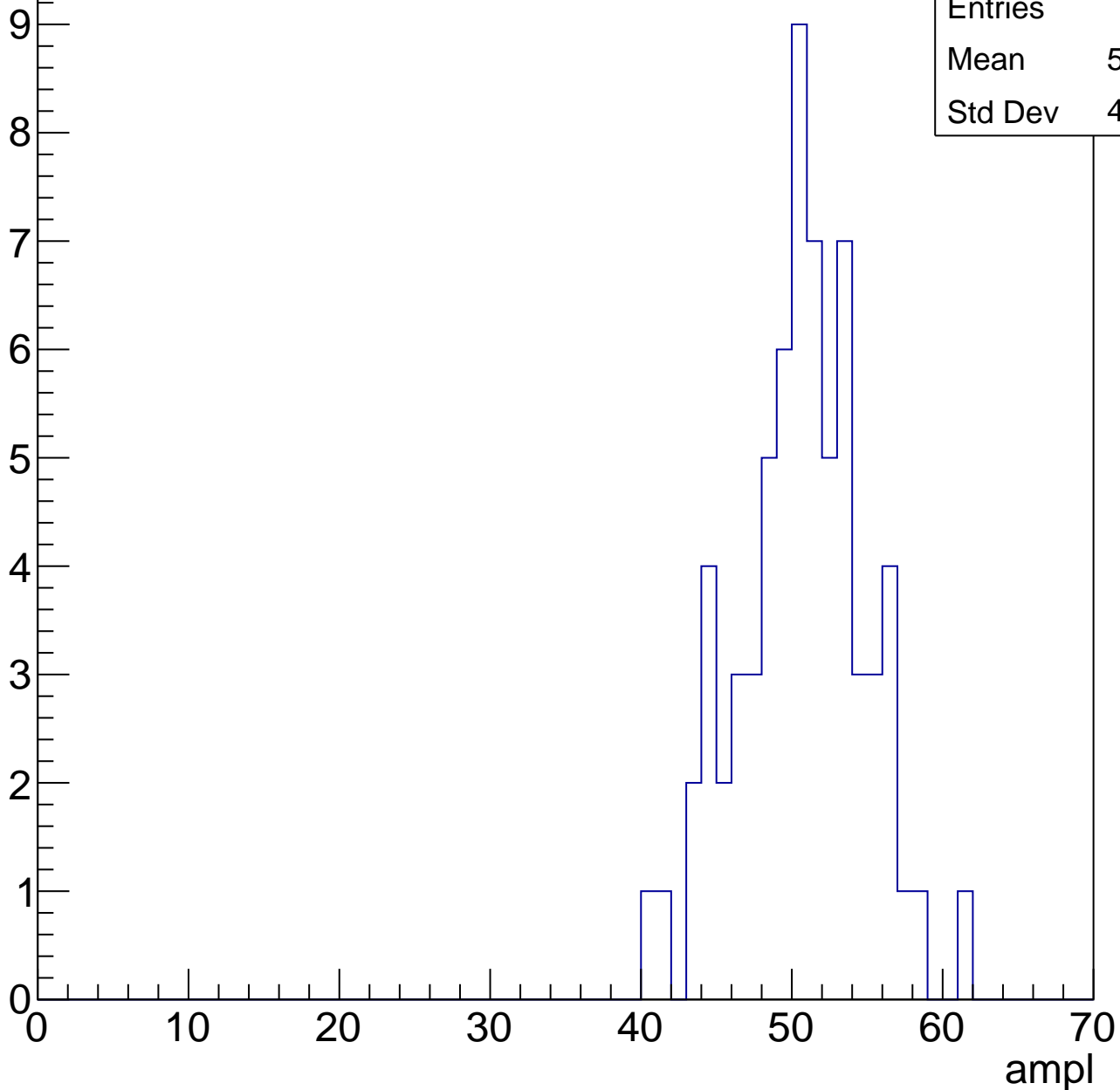


B1L100S, U19-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.16
Std Dev	4.136

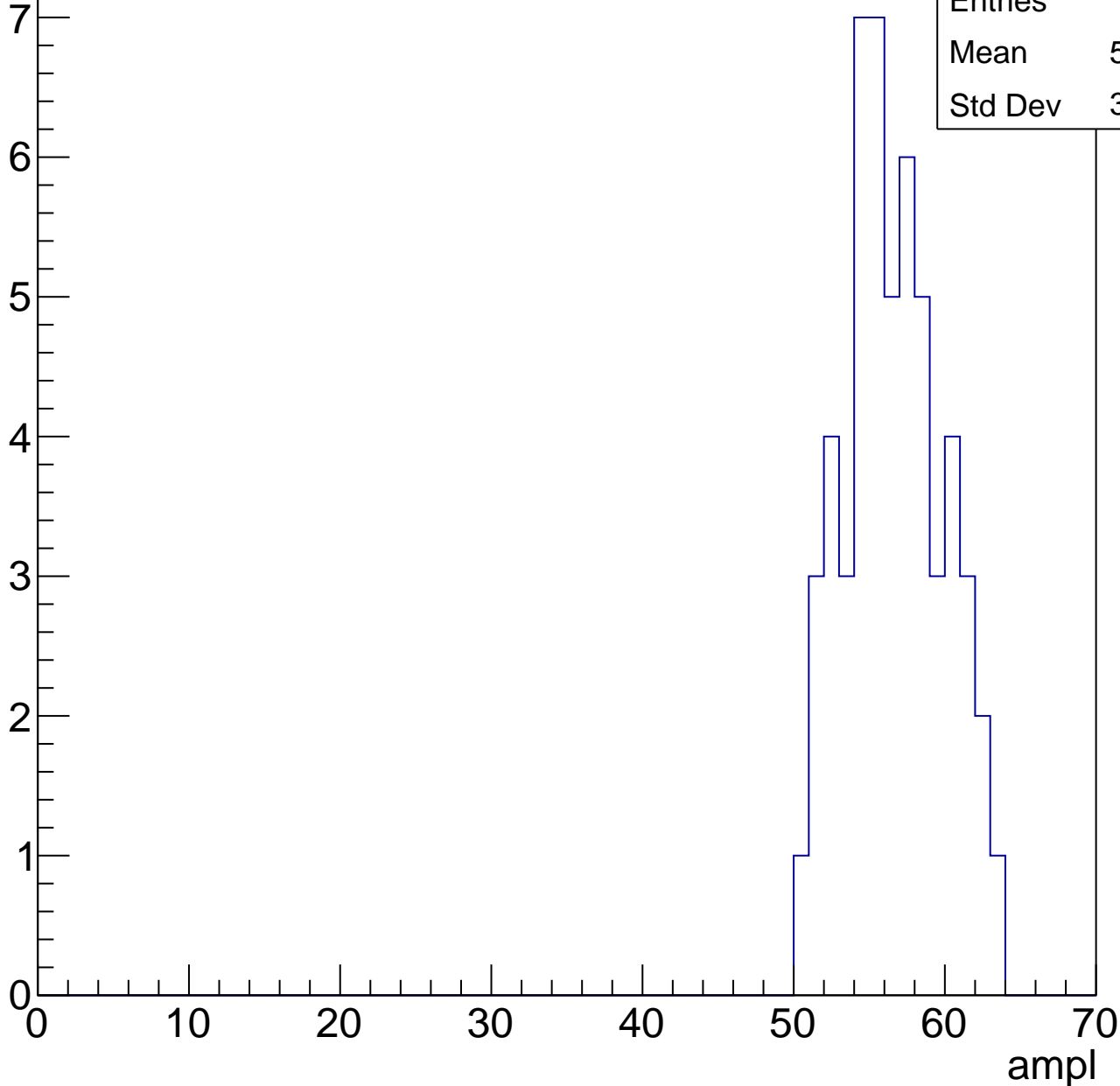


B1L100S, U19-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.15
Std Dev	3.188



B1L100S, U19-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	42
Mean	58.74
Std Dev	9.457

0

1

2

3

4

5

6

7

B1L100S, U19-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch27, adc0

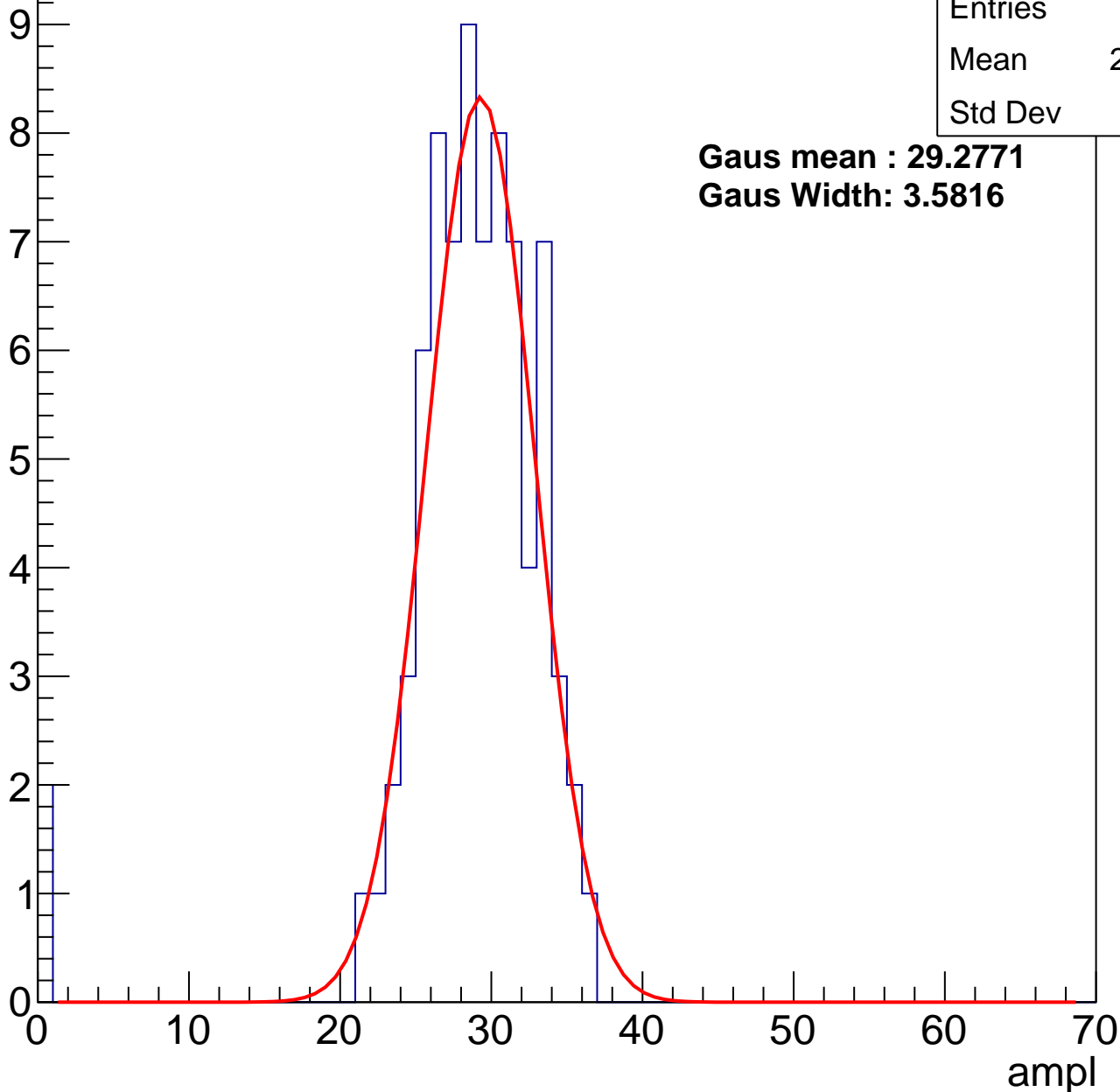
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.04
Std Dev	5.61

Gaus mean : 29.2771

Gaus Width: 3.5816



B1L100S, U19-ch27, adc1

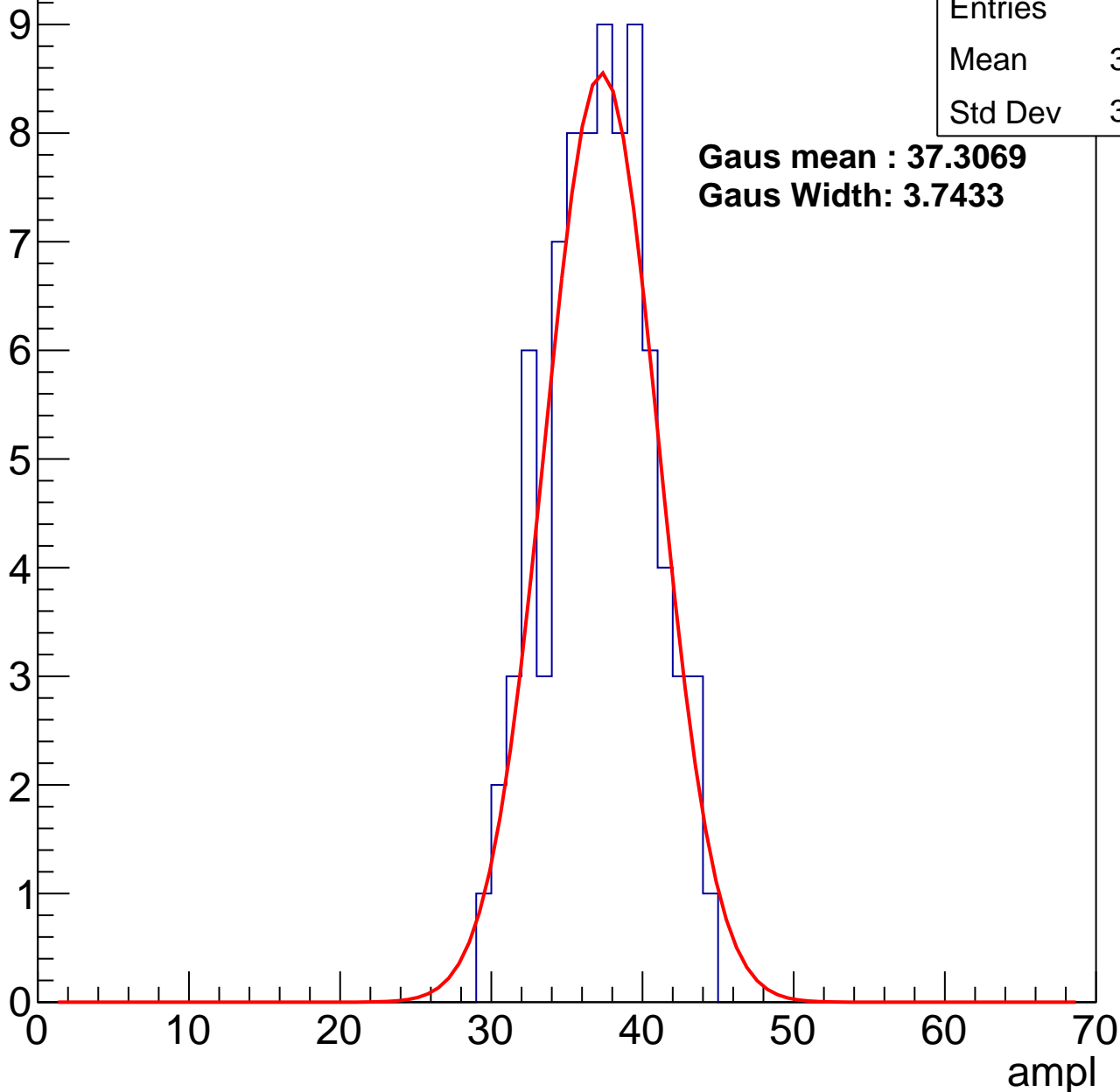
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.67
Std Dev	3.435

Gaus mean : 37.3069

Gaus Width: 3.7433



B1L100S, U19-ch27, adc2

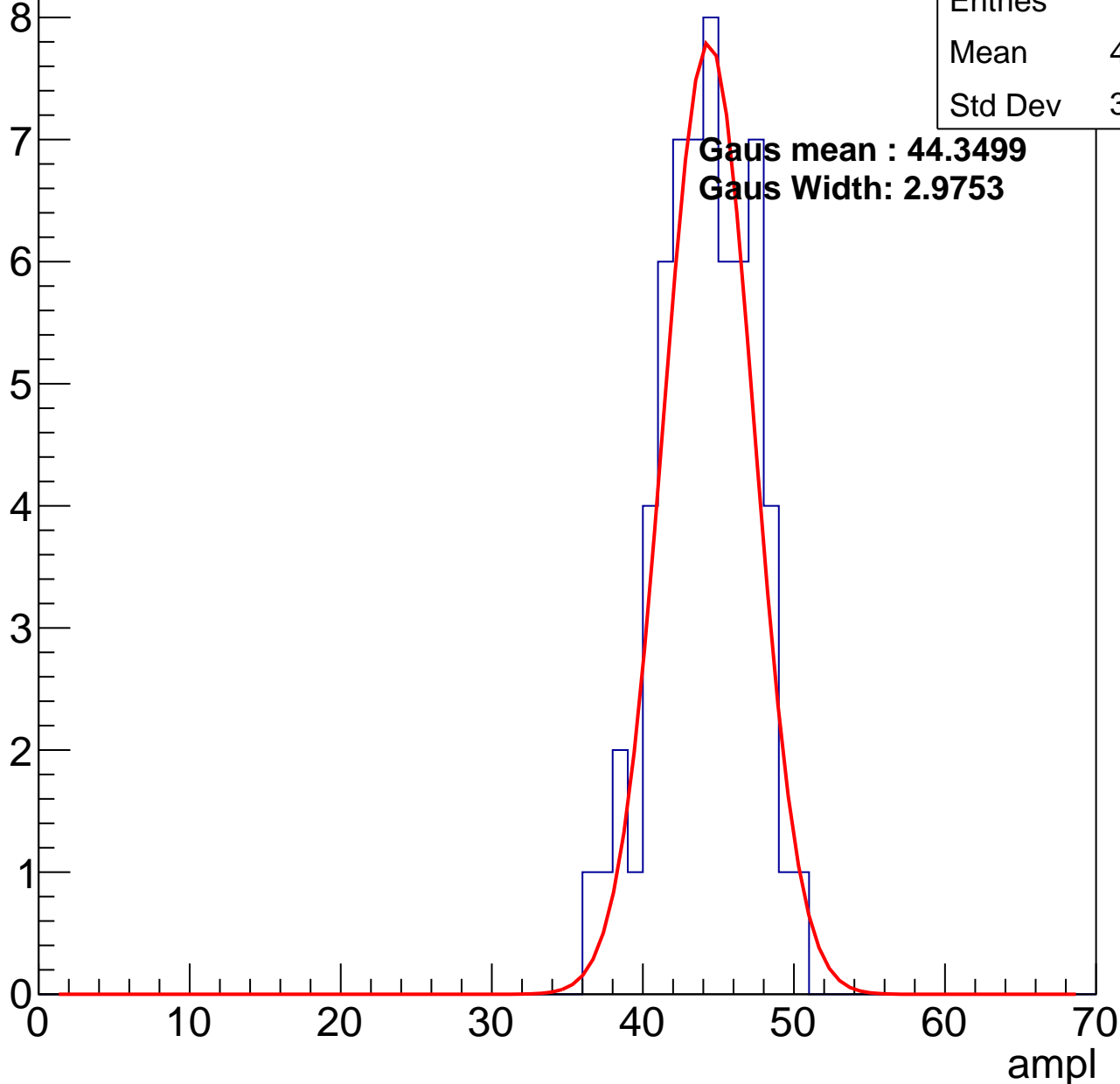
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.66
Std Dev	3.042

Gaus mean : 44.3499

Gaus Width: 2.9753

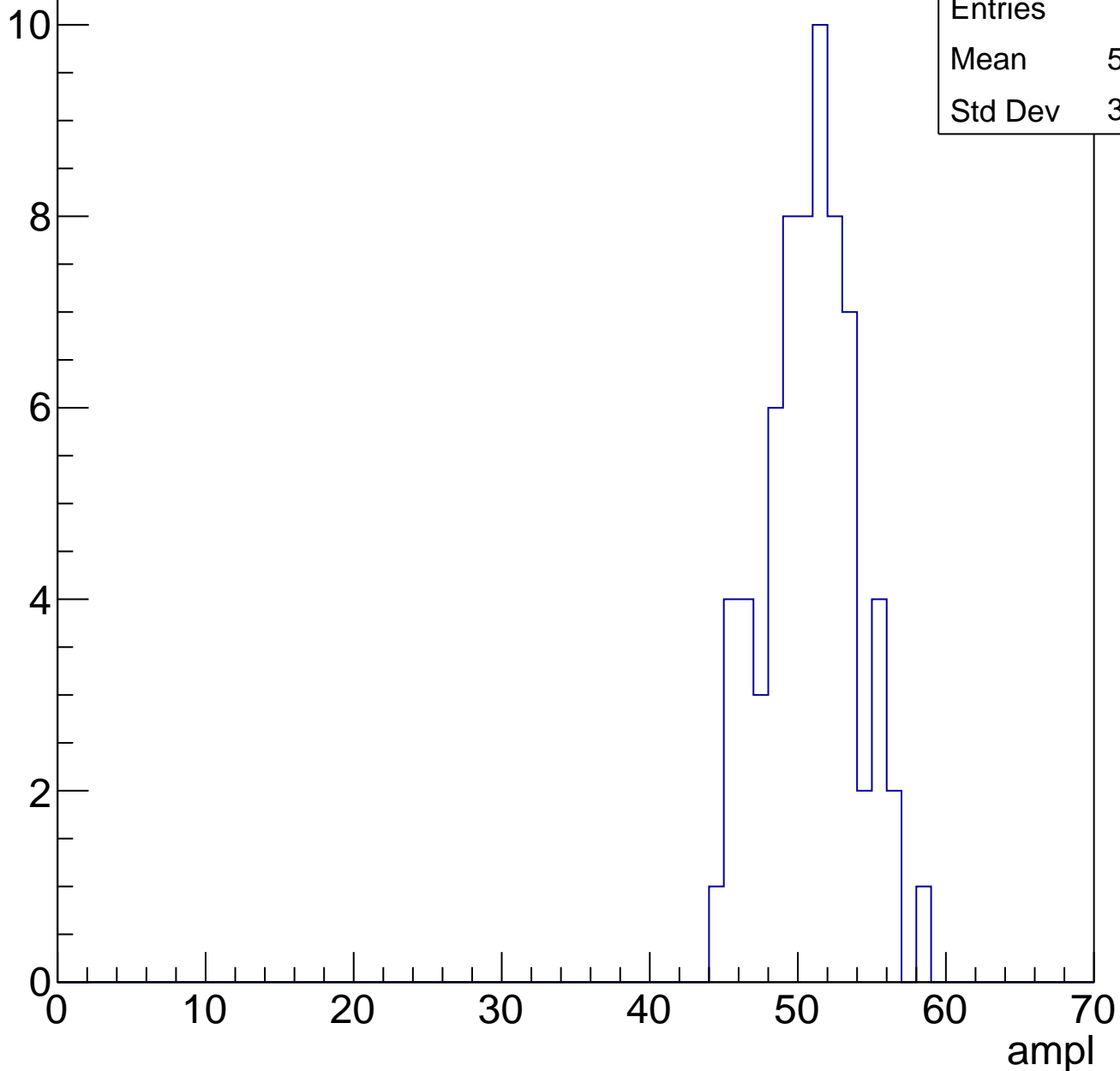


B1L100S, U19-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	50.35
Std Dev	3.028

Entry

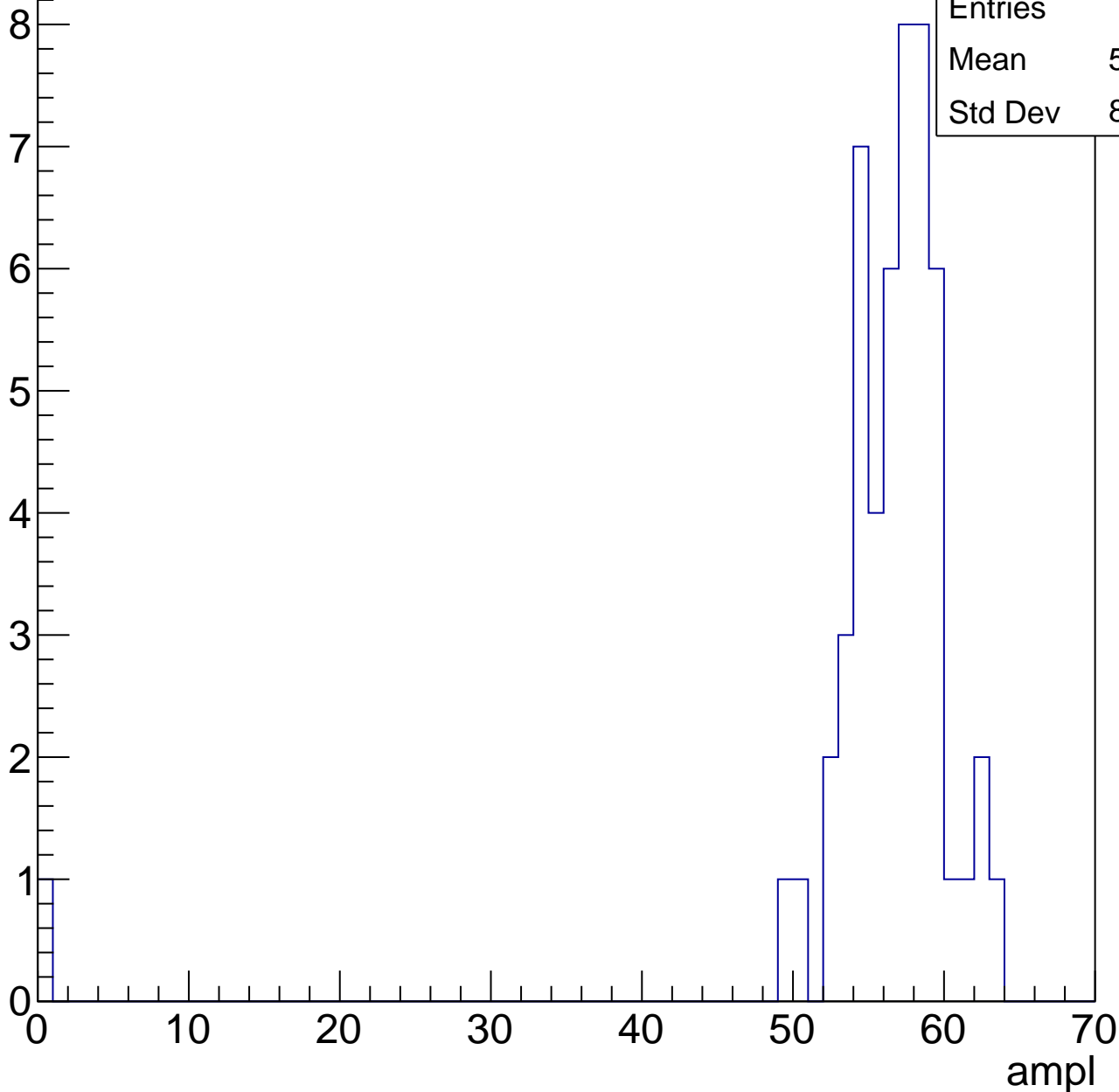


B1L100S, U19-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.35
Std Dev	8.258



B1L100S, U19-ch27, adc5

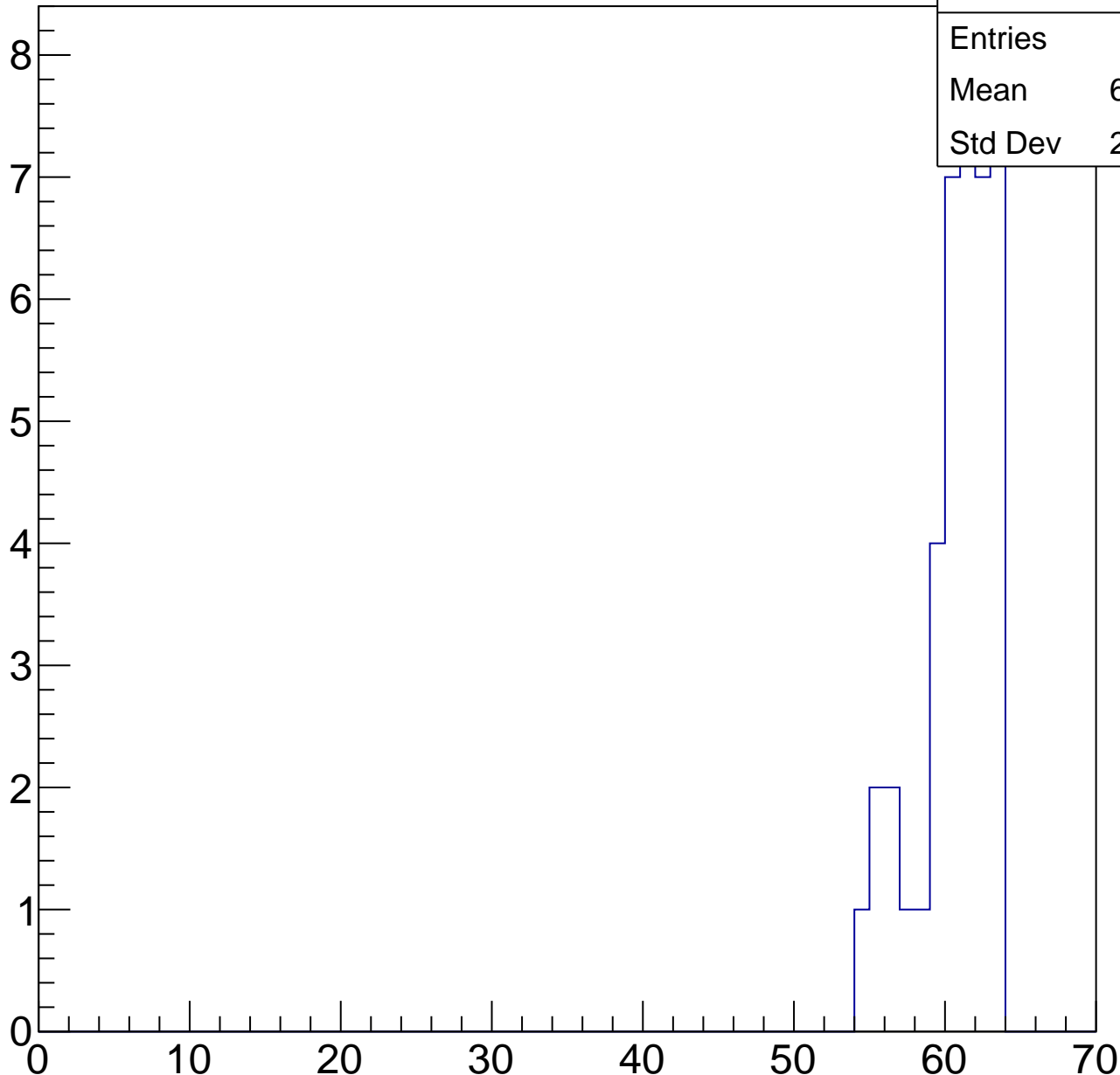
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.32
Std Dev	2.414

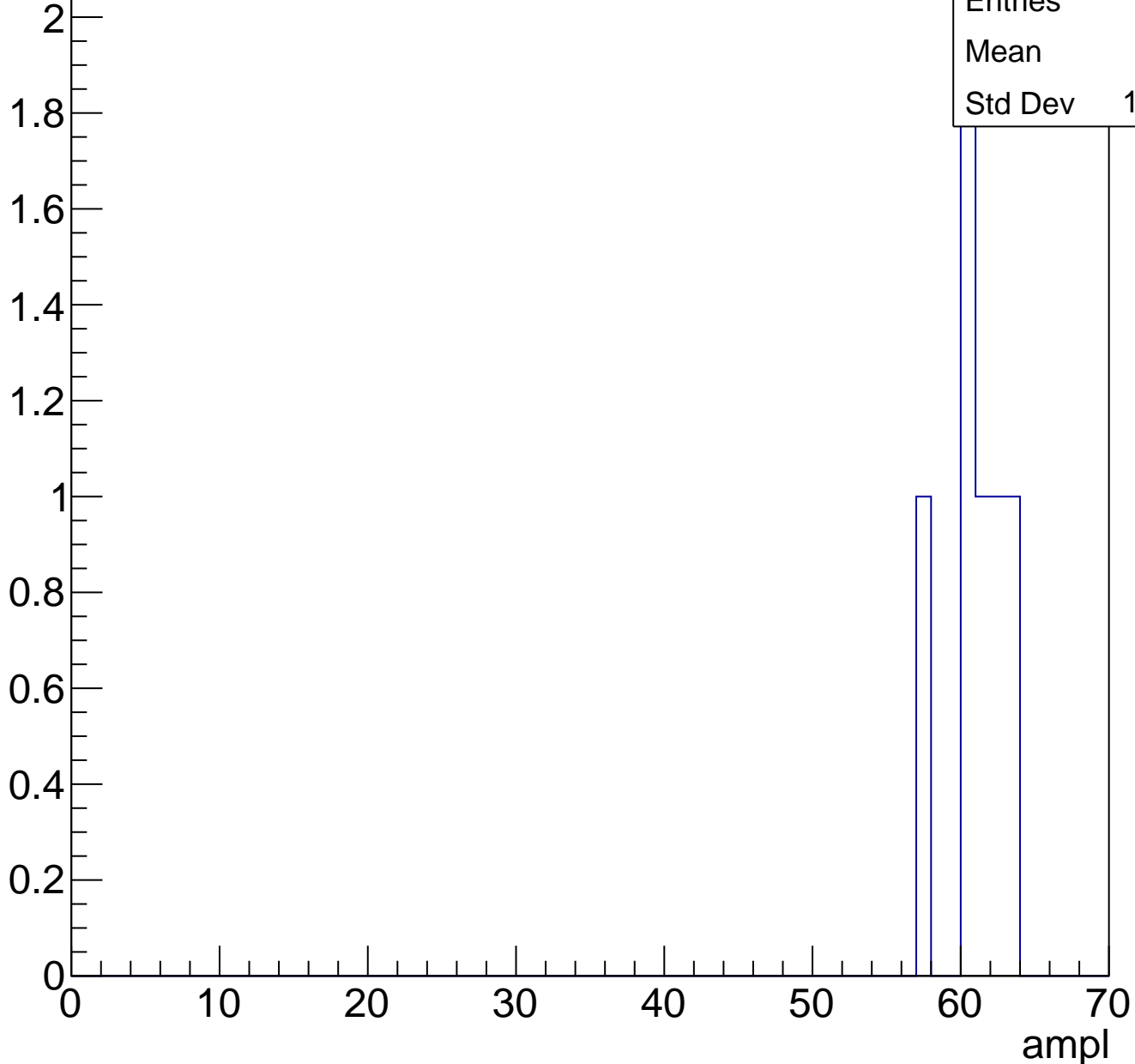
ampl



B1L100S, U19-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch28, adc0

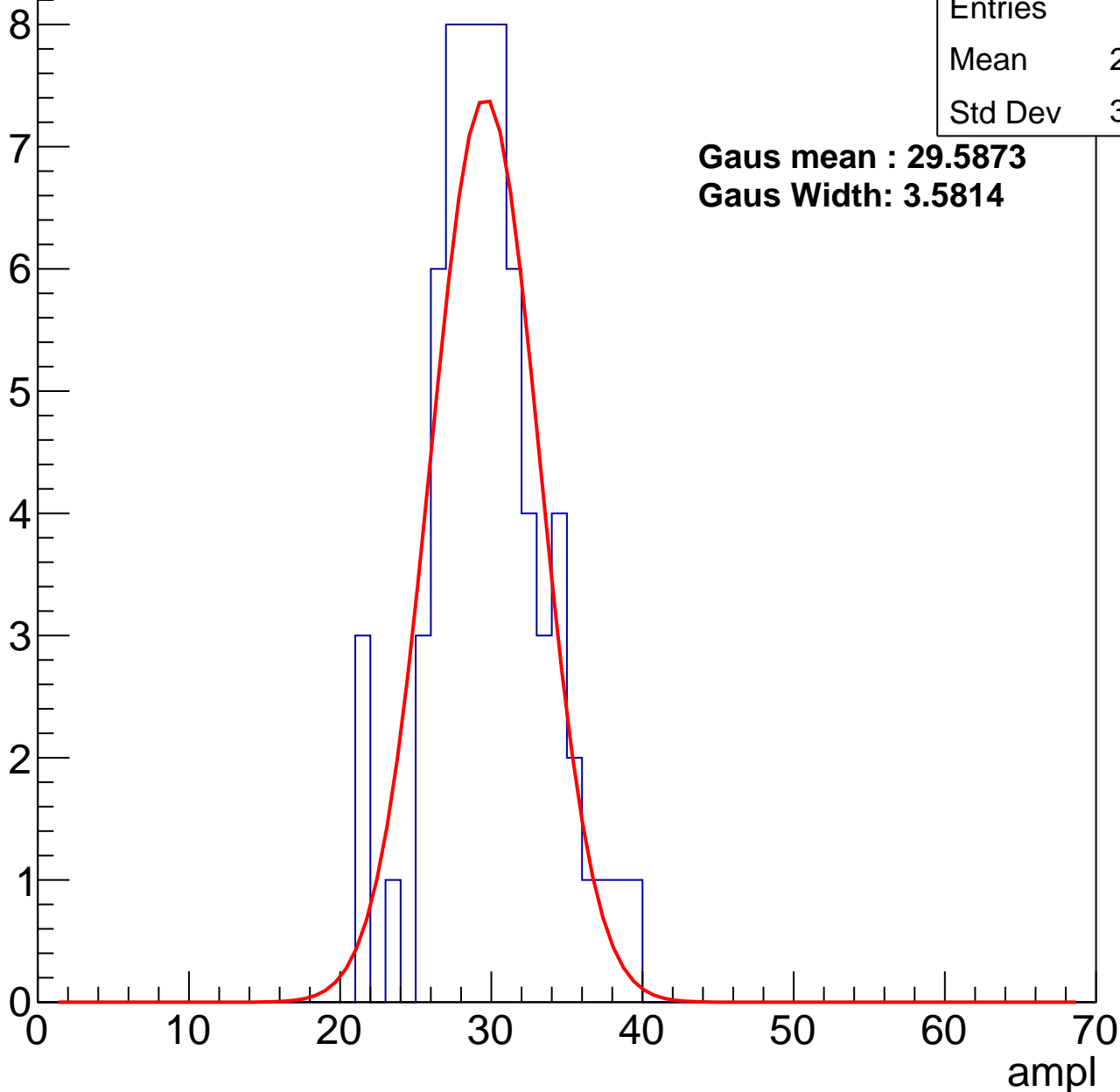
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.38
Std Dev	3.722

Gaus mean : 29.5873

Gaus Width: 3.5814



B1L100S, U19-ch28, adc1

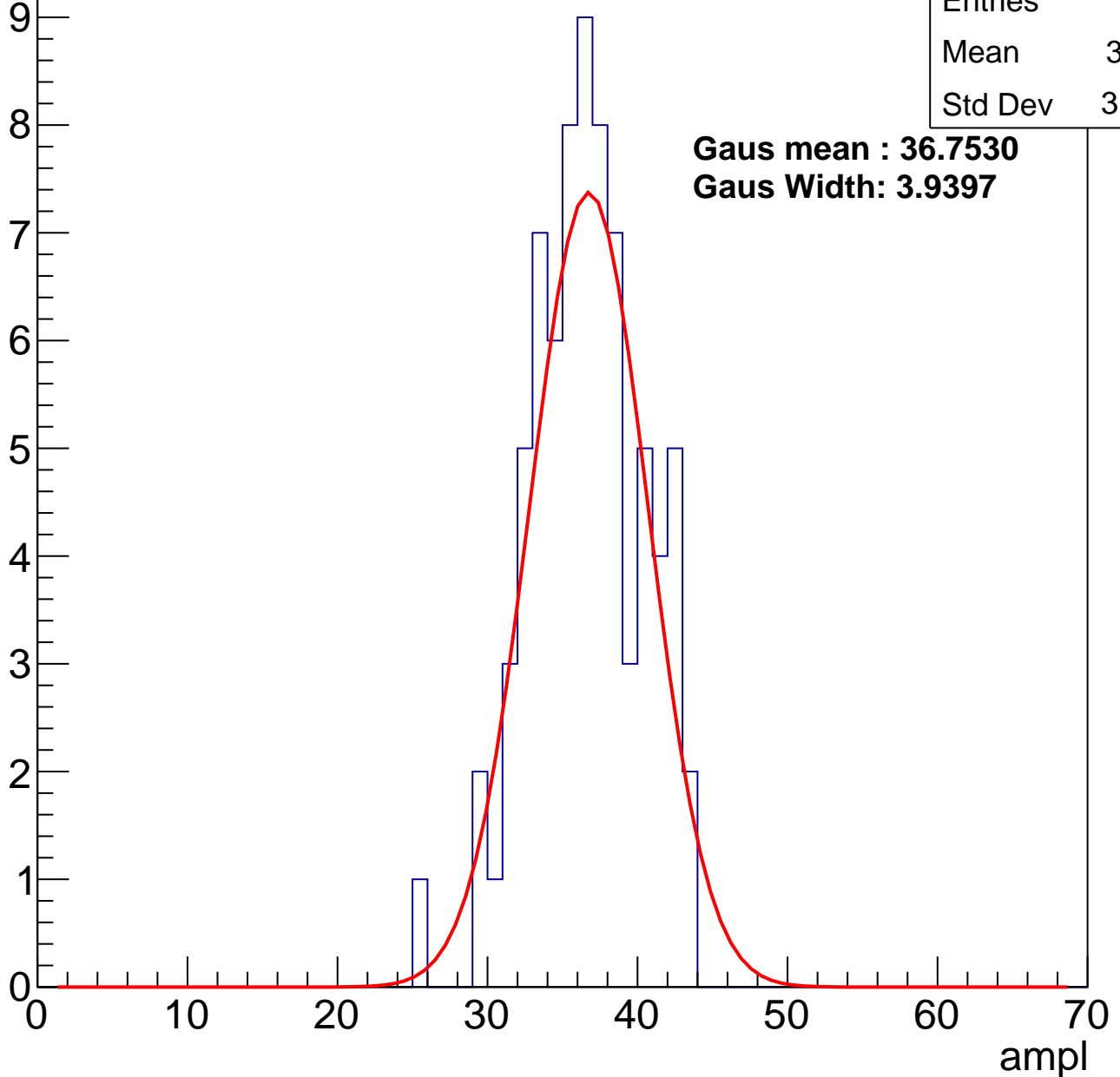
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.11
Std Dev	3.673

Gaus mean : 36.7530

Gaus Width: 3.9397

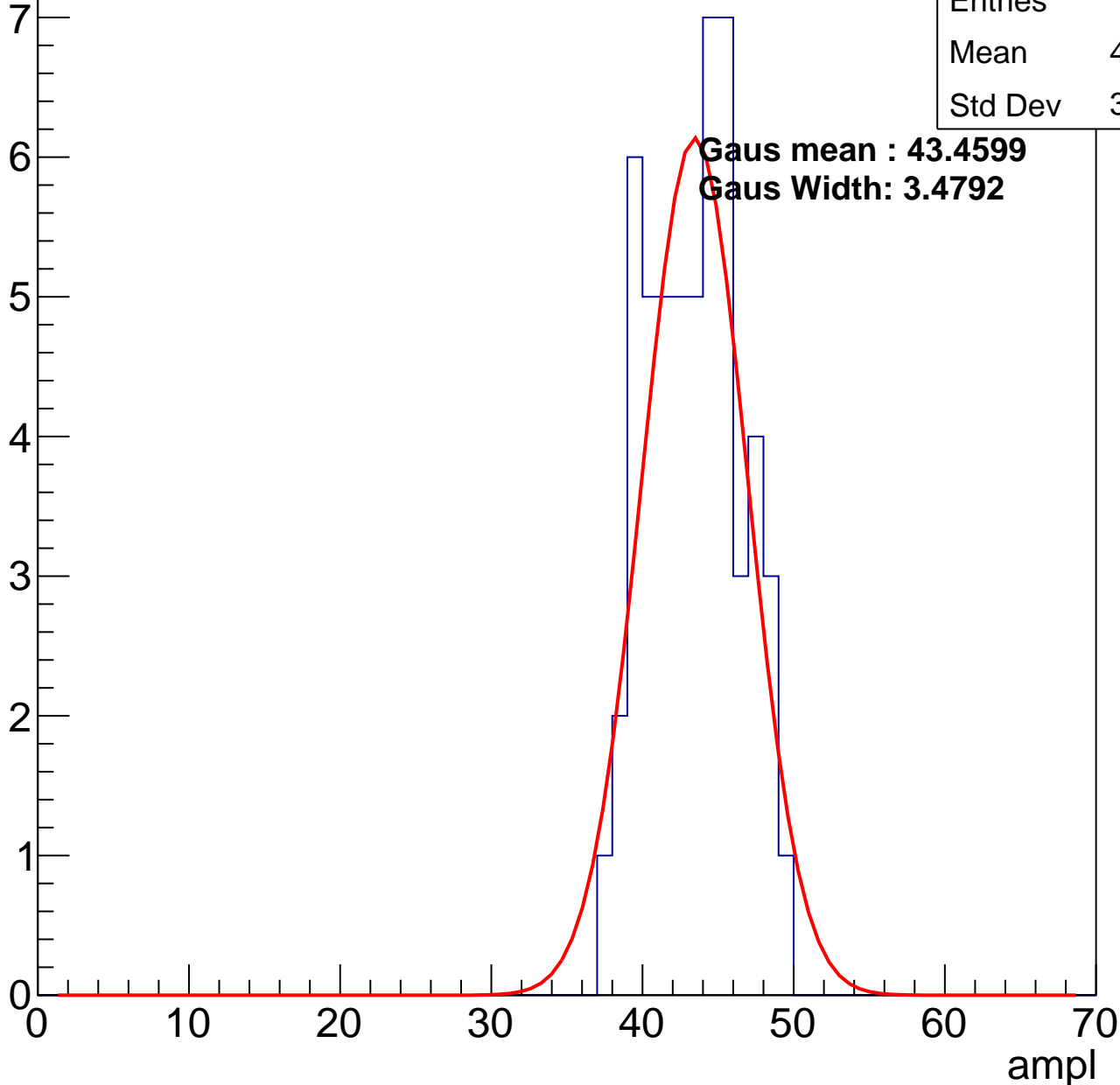


B1L100S, U19-ch28, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	42.94
Std Dev	3.009

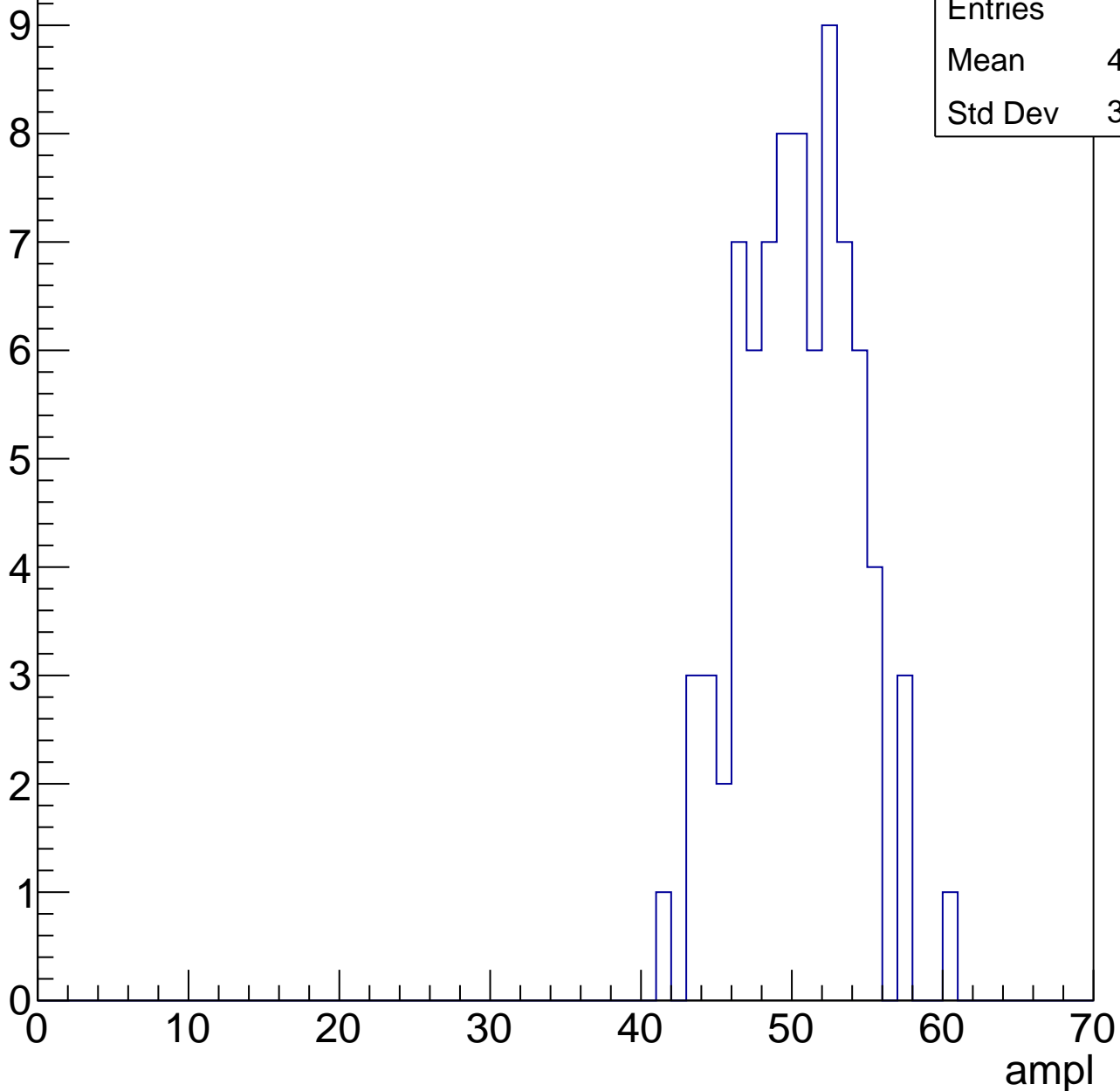


B1L100S, U19-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

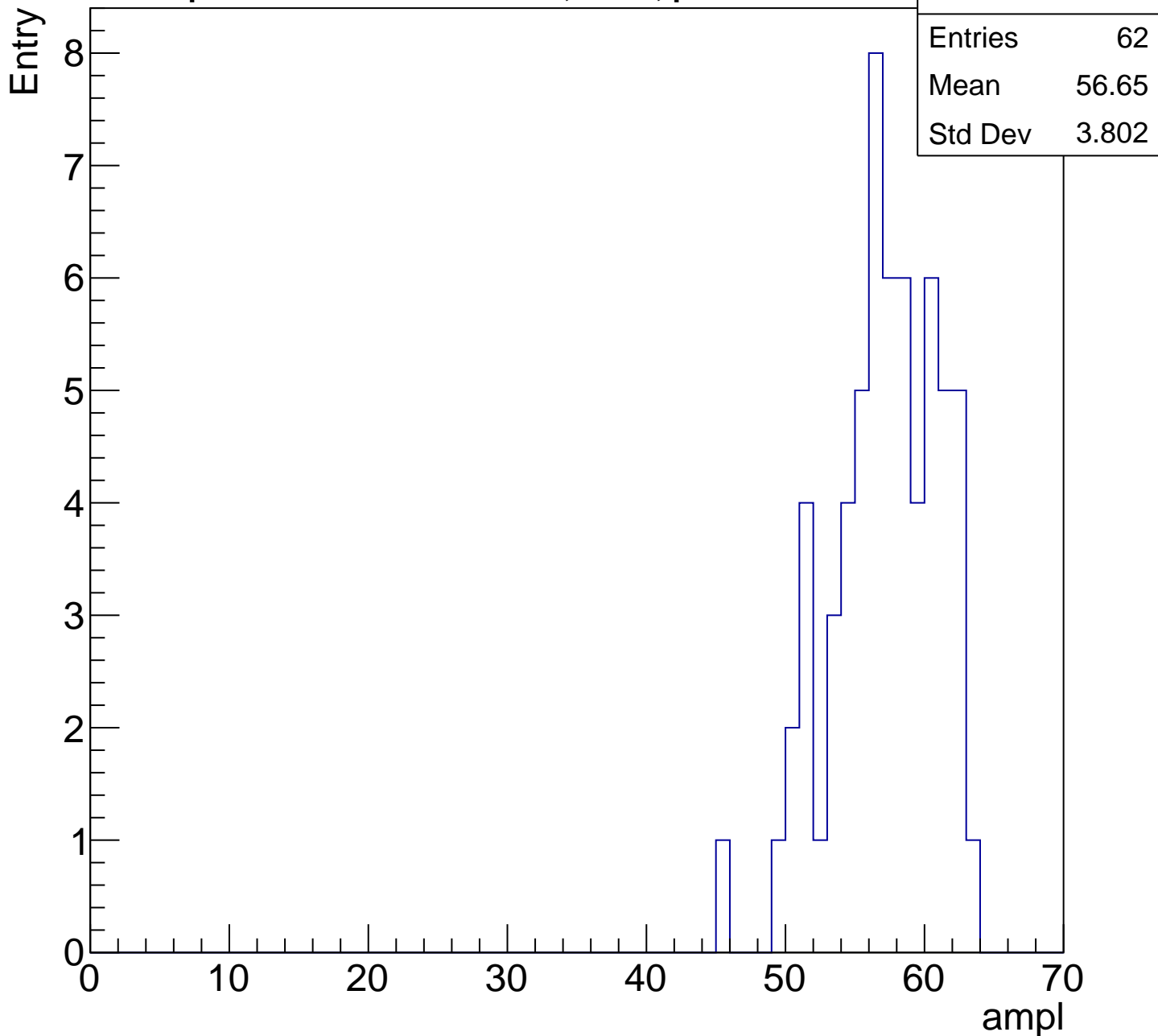
Entry

Entries	81
Mean	49.93
Std Dev	3.744



B1L100S, U19-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

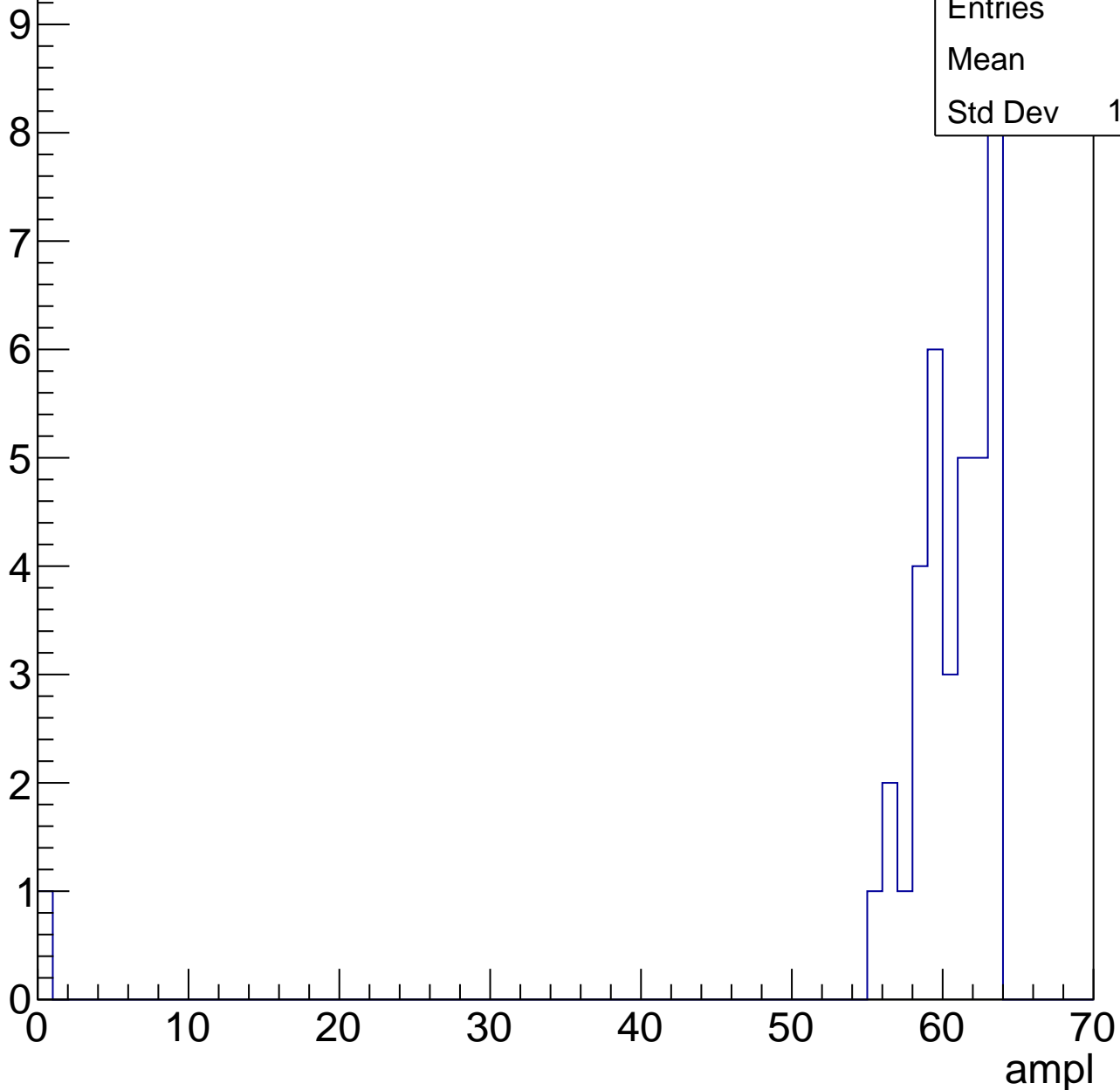


B1L100S, U19-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.7
Std Dev	10.04



B1L100S, U19-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch29, adc0

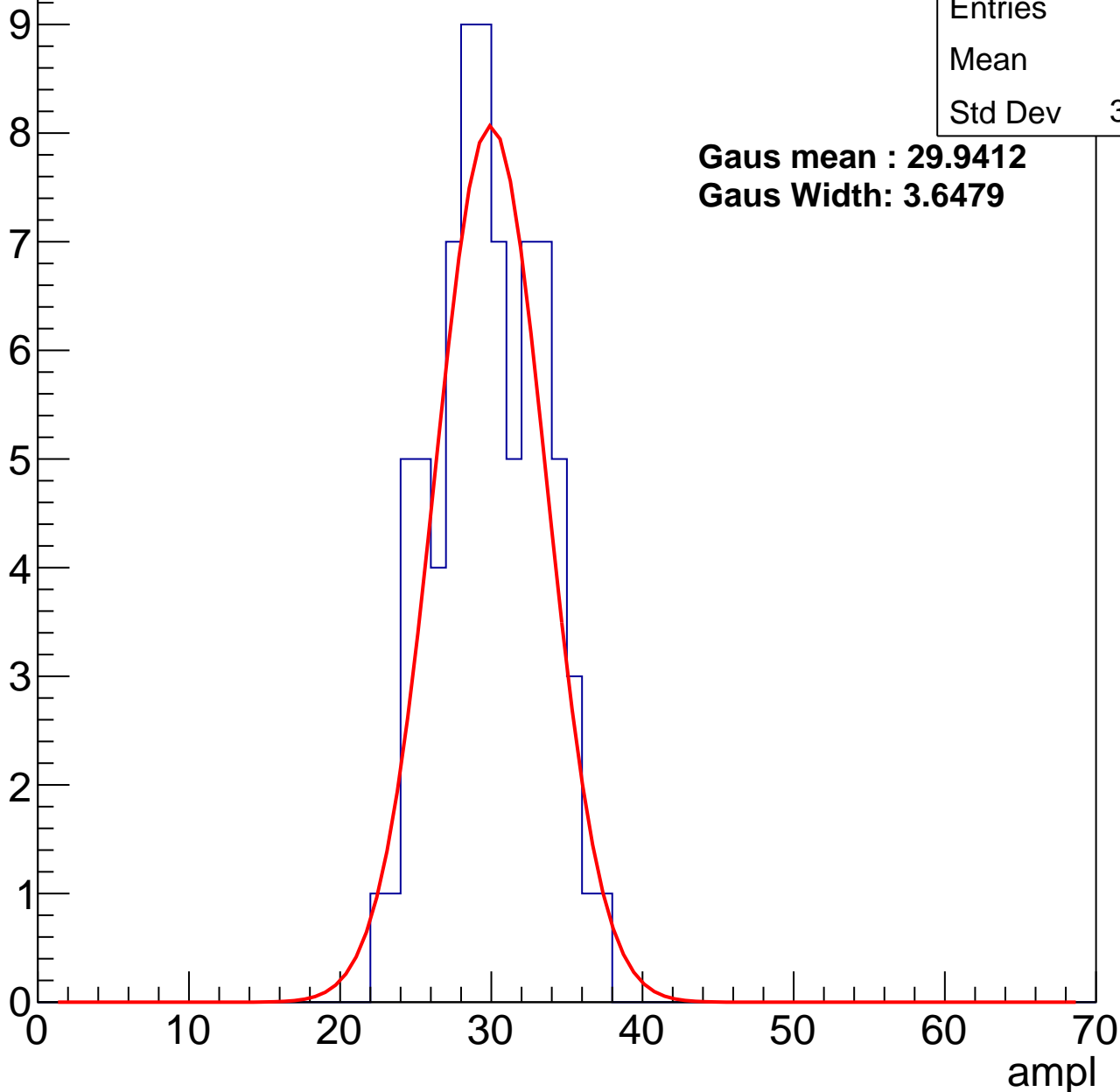
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.4
Std Dev	3.416

Gaus mean : 29.9412

Gaus Width: 3.6479



B1L100S, U19-ch29, adc1

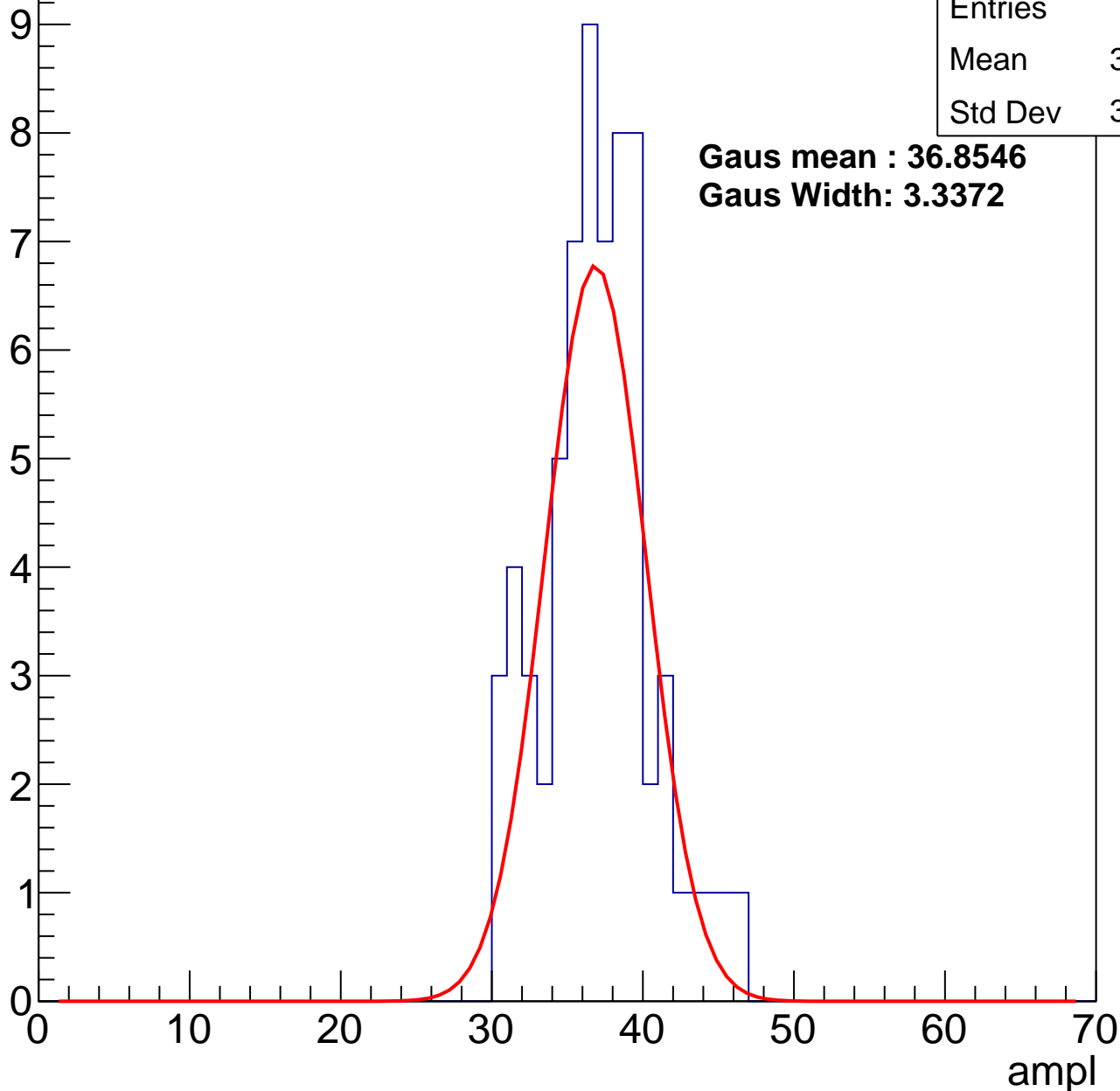
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.56
Std Dev	3.543

Gaus mean : 36.8546

Gaus Width: 3.3372



B1L100S, U19-ch29, adc2

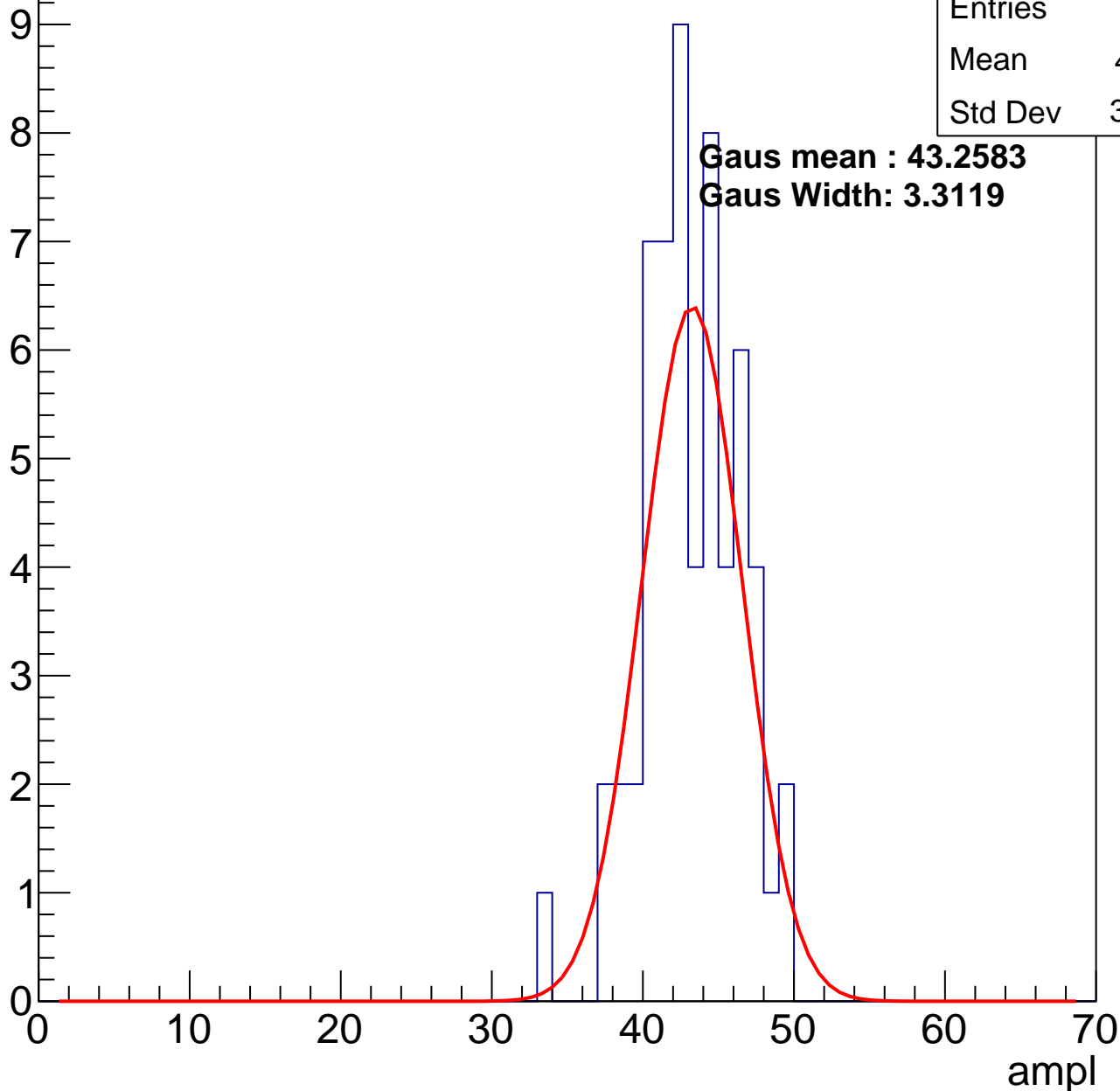
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.71
Std Dev	3.157

Gaus mean : 43.2583

Gaus Width: 3.3119

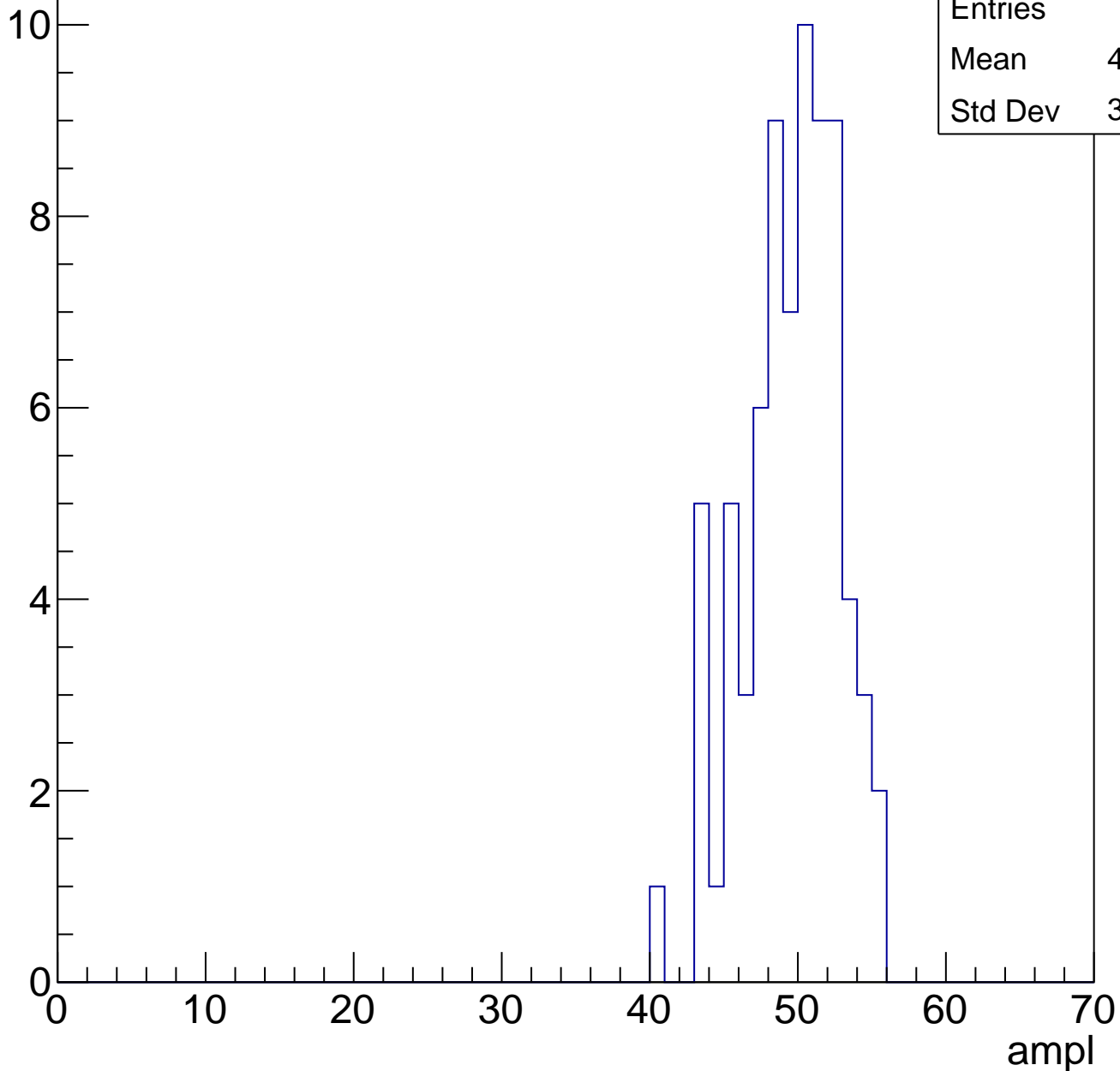


B1L100S, U19-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	49.05
Std Dev	3.213

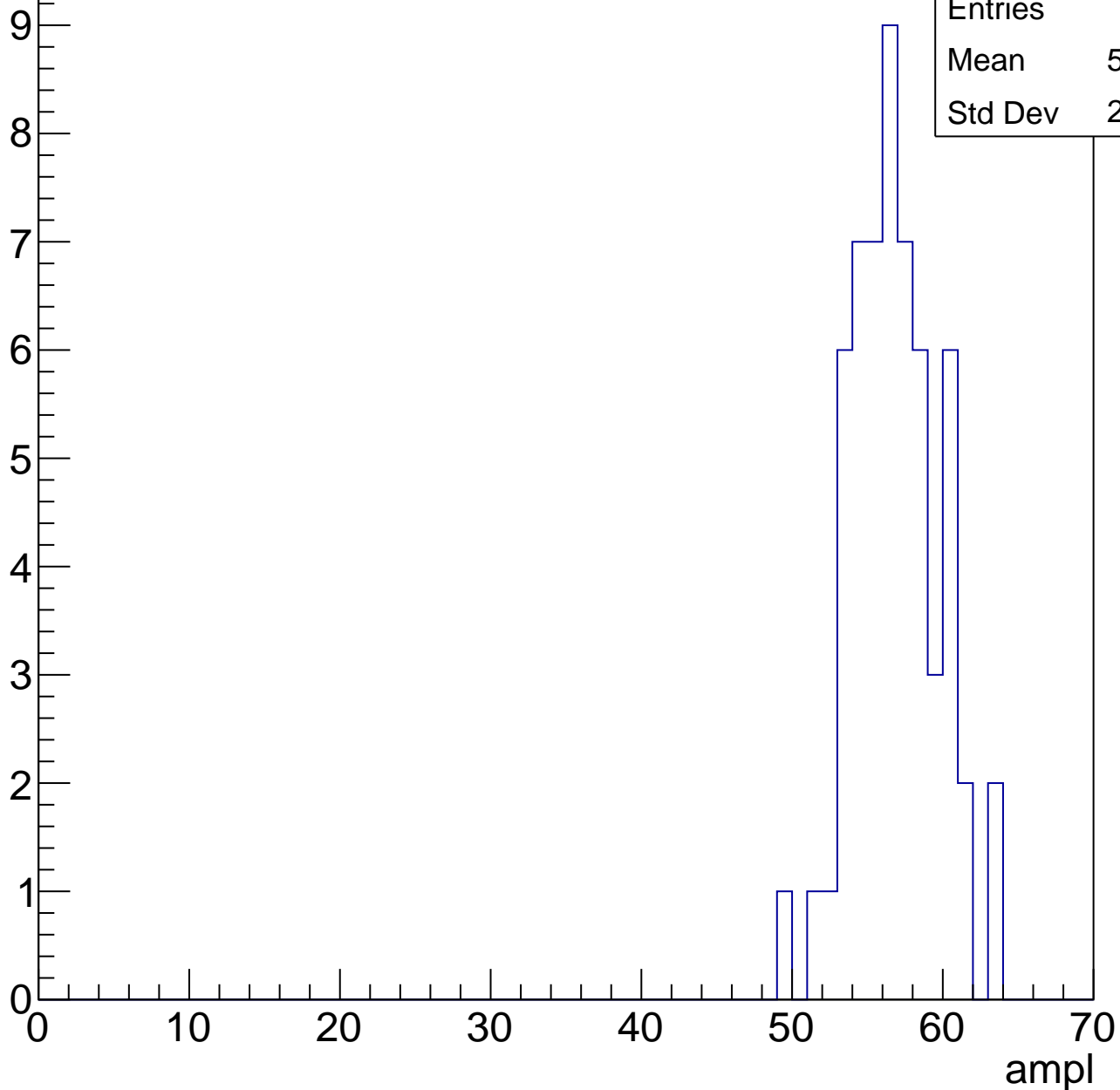
Entry



B1L100S, U19-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



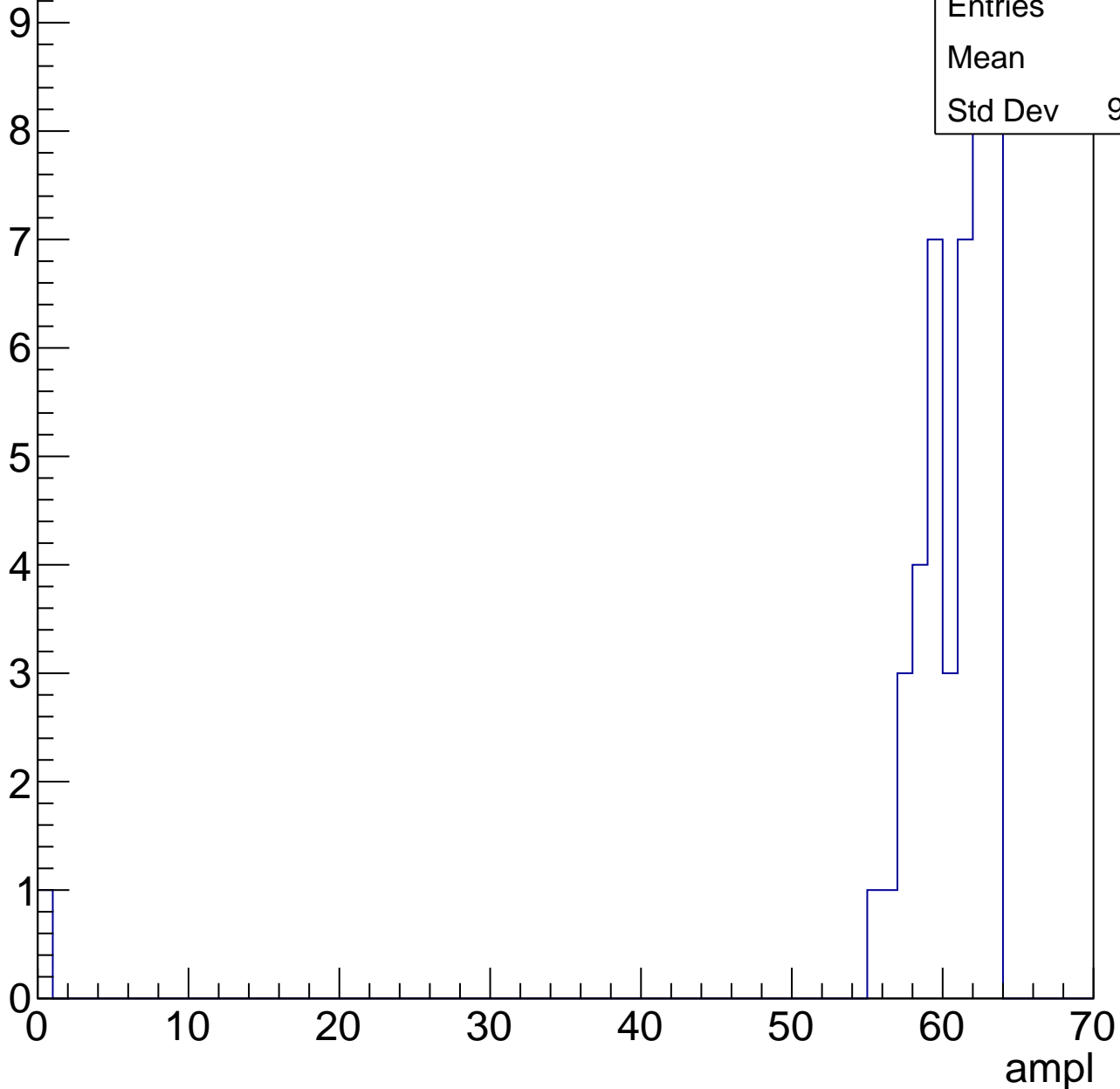
Entries	58
Mean	56.36
Std Dev	2.857

B1L100S, U19-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	59
Std Dev	9.247



B1L100S, U19-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch30, adc0

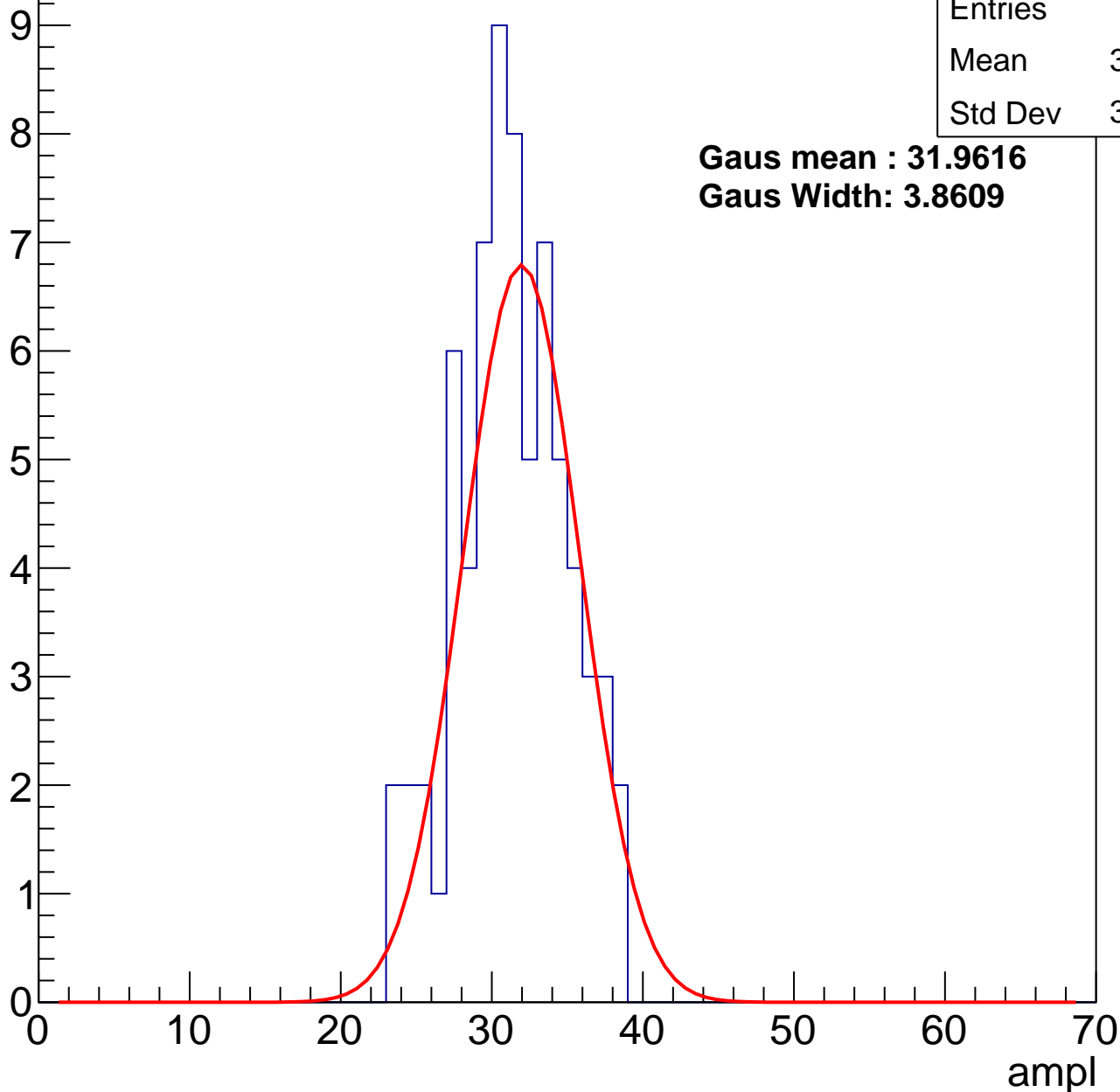
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.87
Std Dev	3.613

Gaus mean : 31.9616

Gaus Width: 3.8609



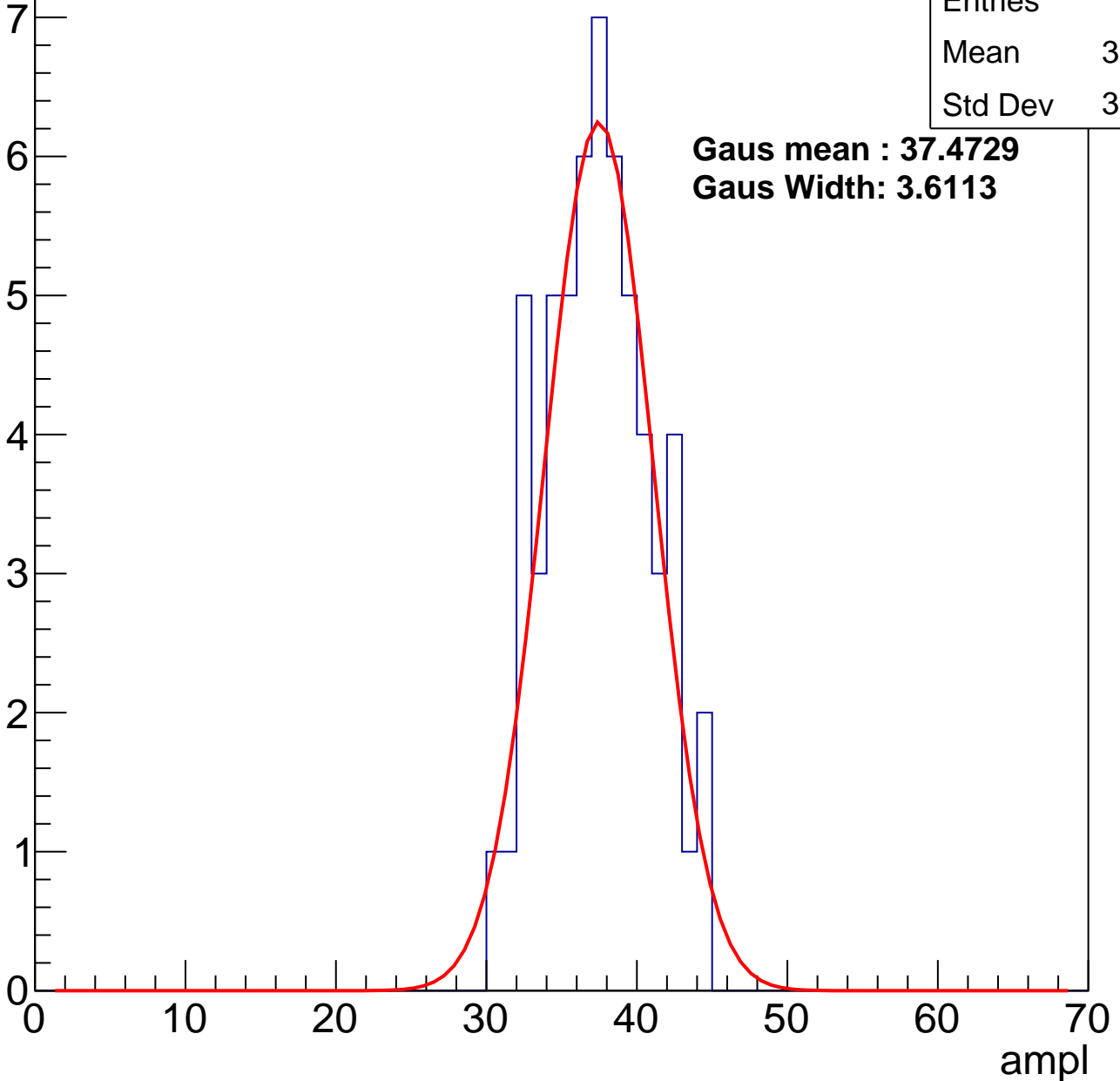
B1L100S, U19-ch30, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	36.98
Std Dev	3.406

Gaus mean : 37.4729
Gaus Width: 3.6113



B1L100S, U19-ch30, adc2

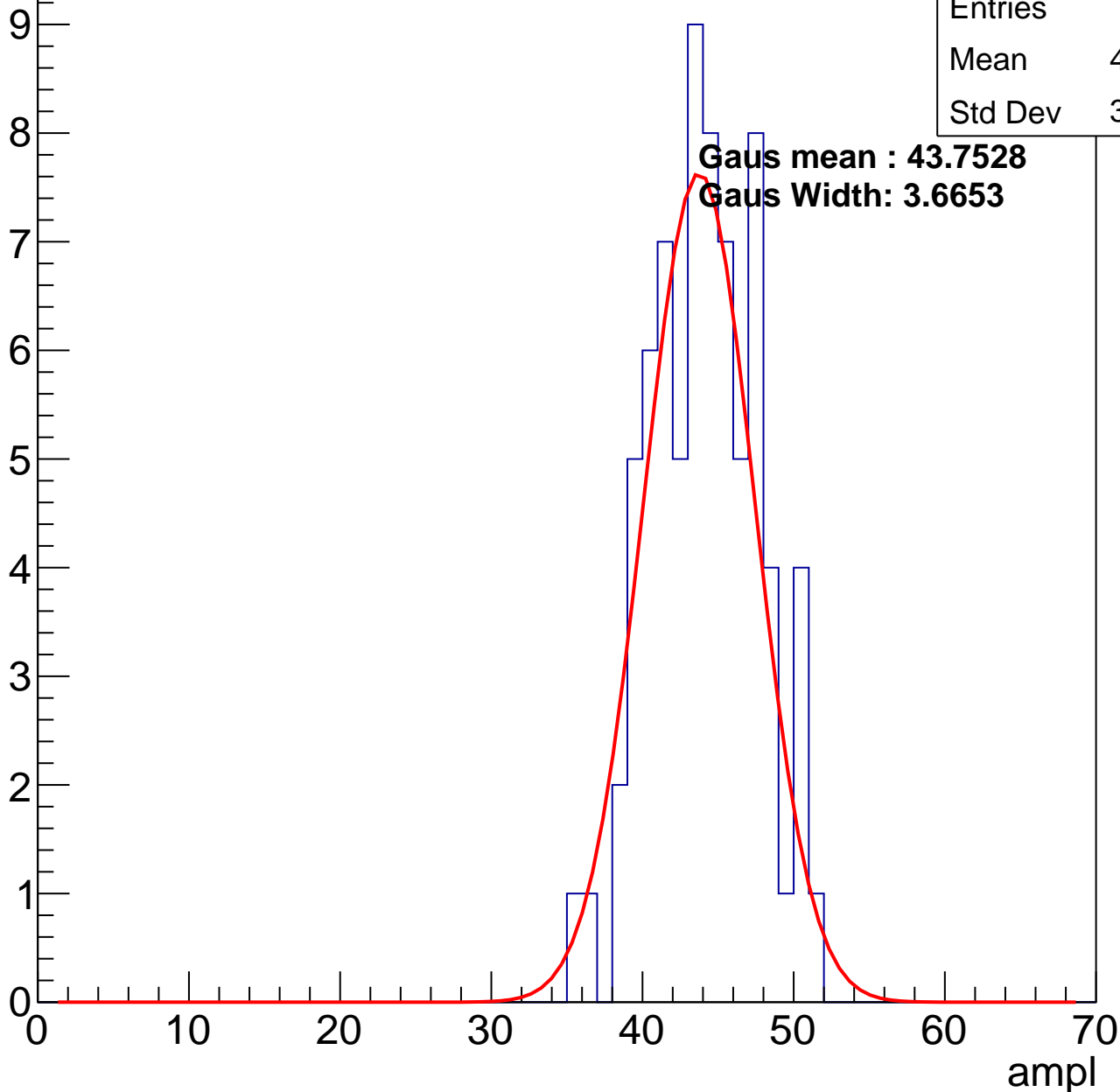
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.66
Std Dev	3.492

Gaus mean : 43.7528

Gaus Width: 3.6653

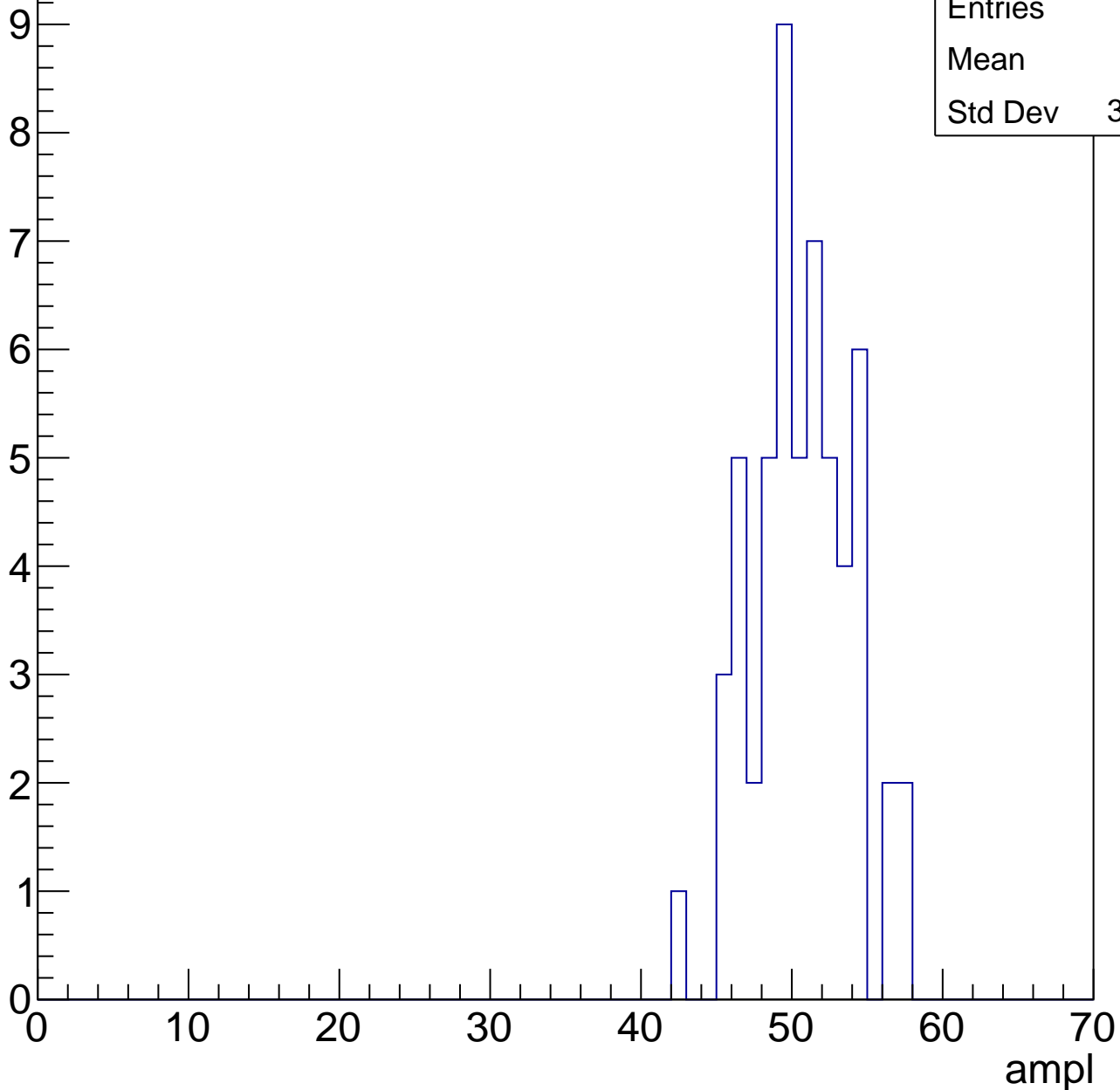


B1L100S, U19-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	50.2
Std Dev	3.254



B1L100S, U19-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	55.94
Std Dev	3.03

Entry

10

8

6

4

2

0

0

10

20

30

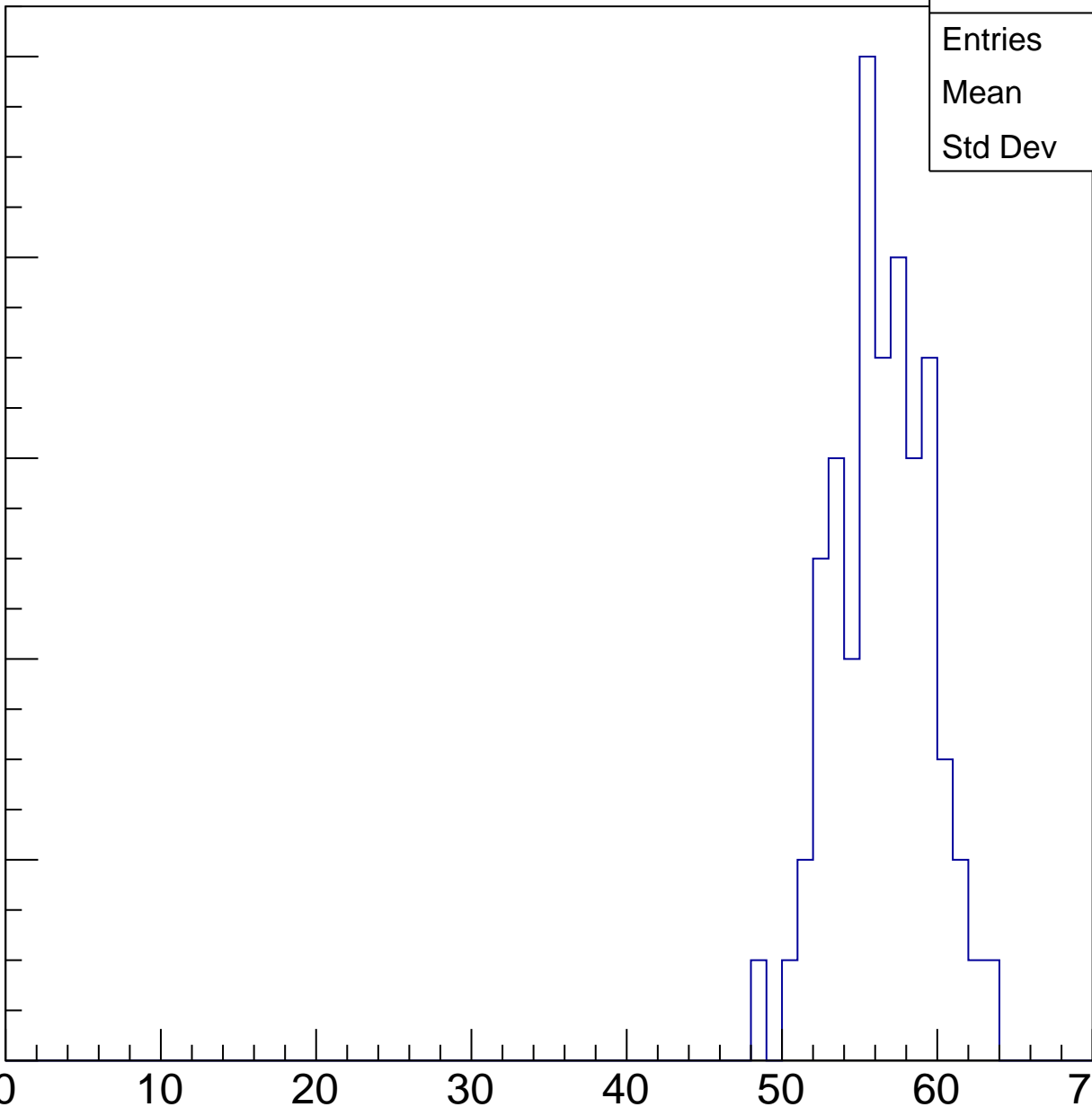
40

50

ampl

60

70

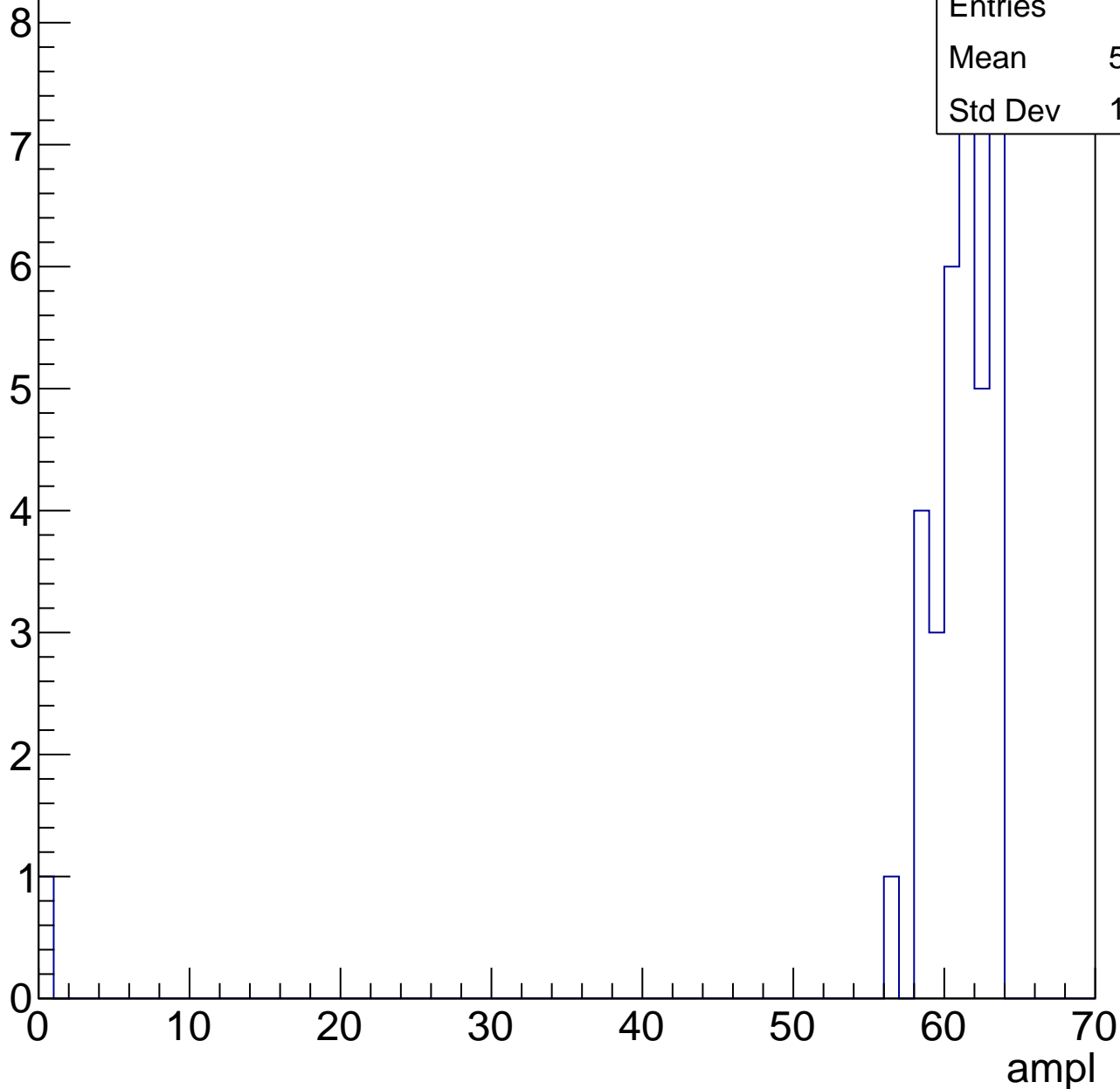


B1L100S, U19-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

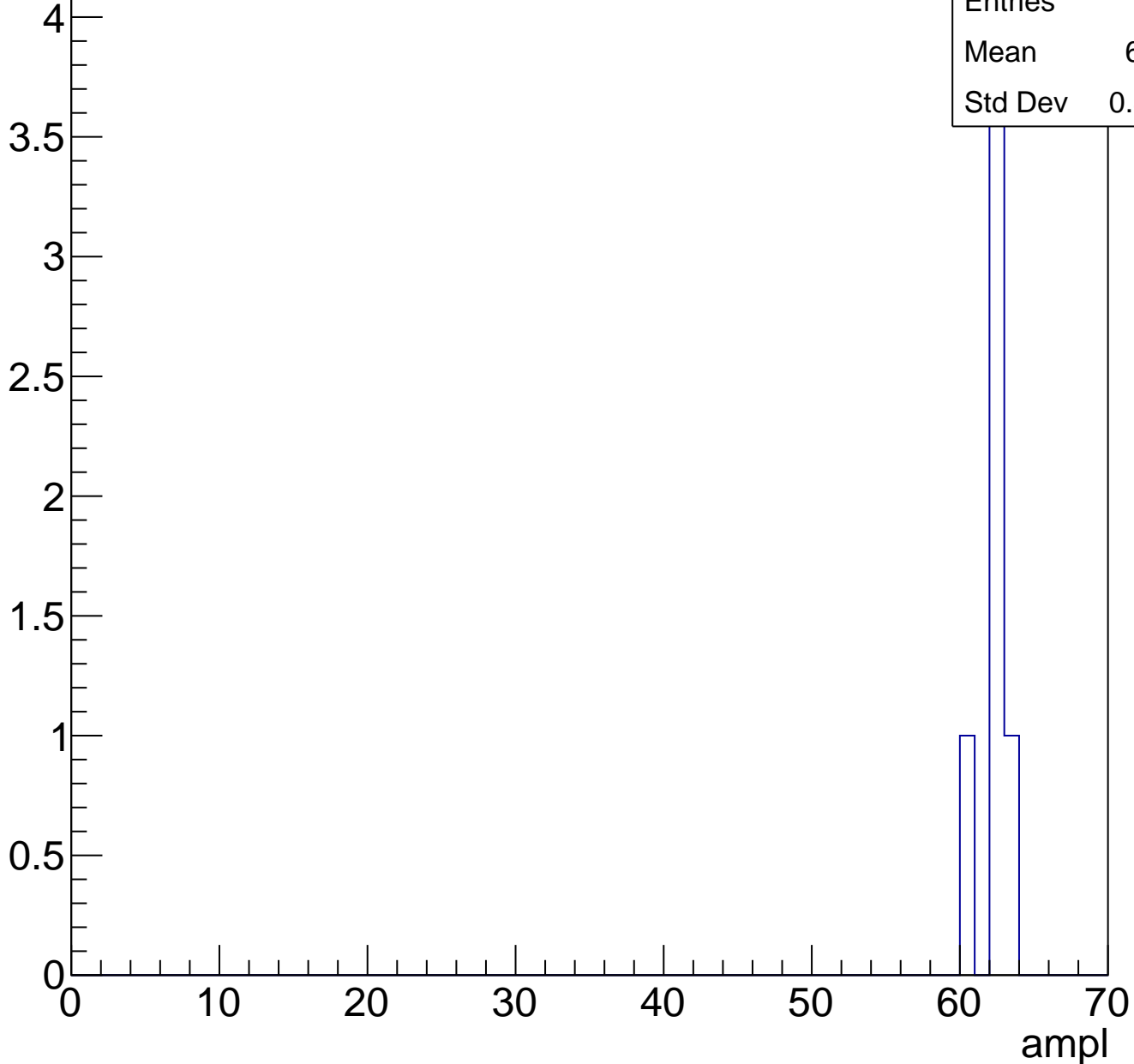
Entries	36
Mean	59.08
Std Dev	10.14



B1L100S, U19-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch31, adc0

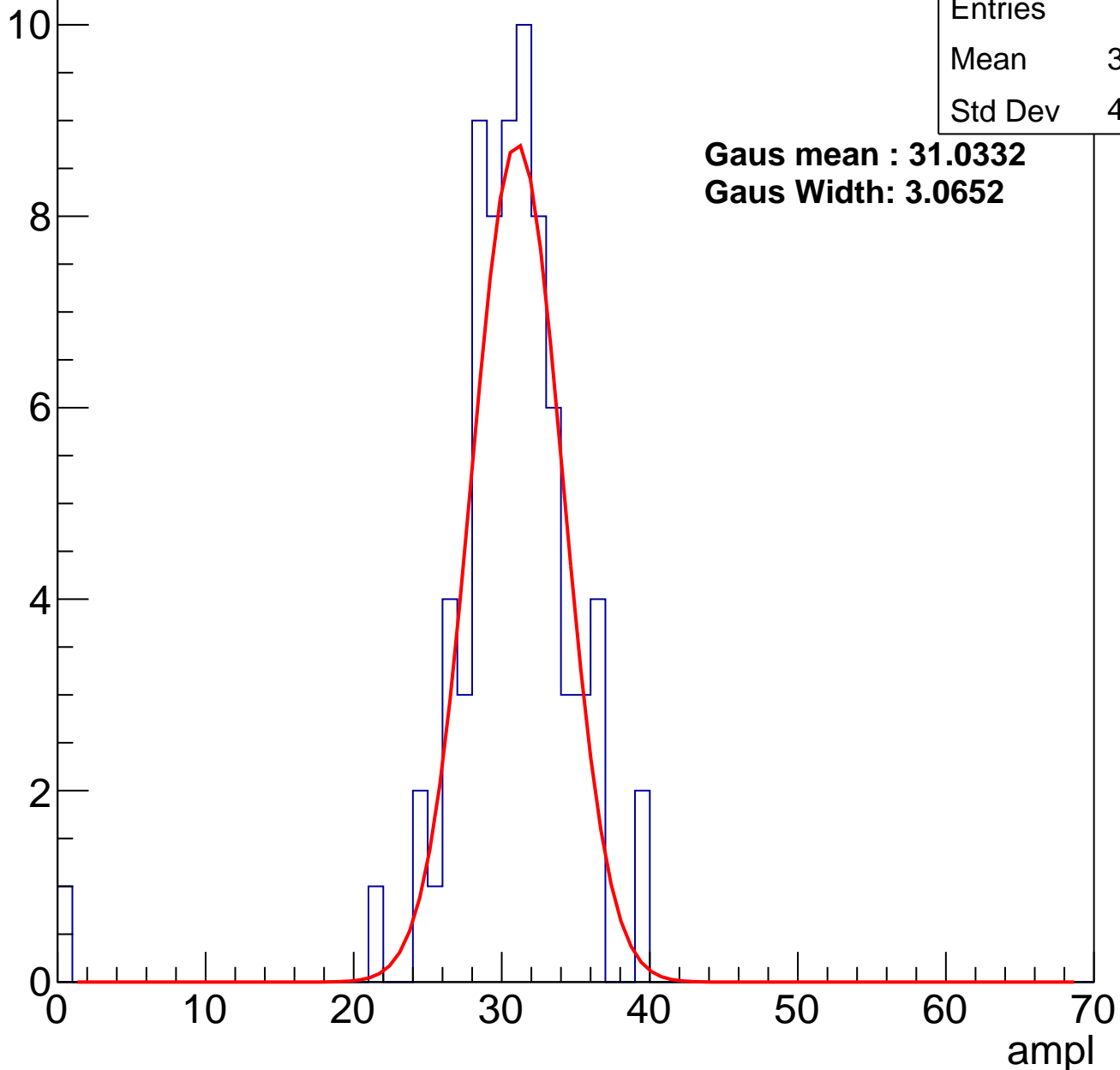
calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	30.08
Std Dev	4.846

Gaus mean : 31.0332

Gaus Width: 3.0652

Entry



B1L100S, U19-ch31, adc1

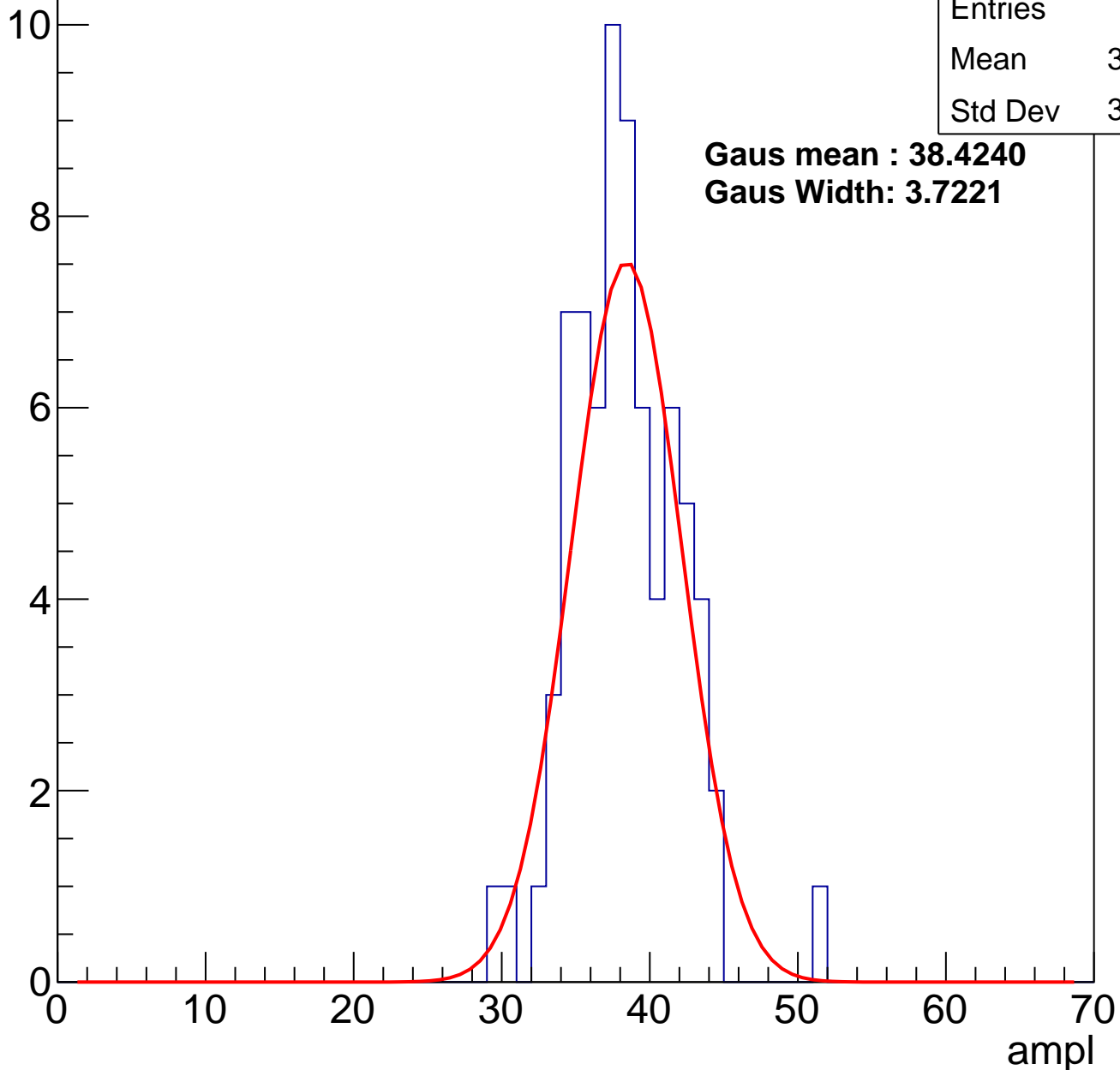
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	37.84
Std Dev	3.626

Gaus mean : 38.4240

Gaus Width: 3.7221

Entry



B1L100S, U19-ch31, adc2

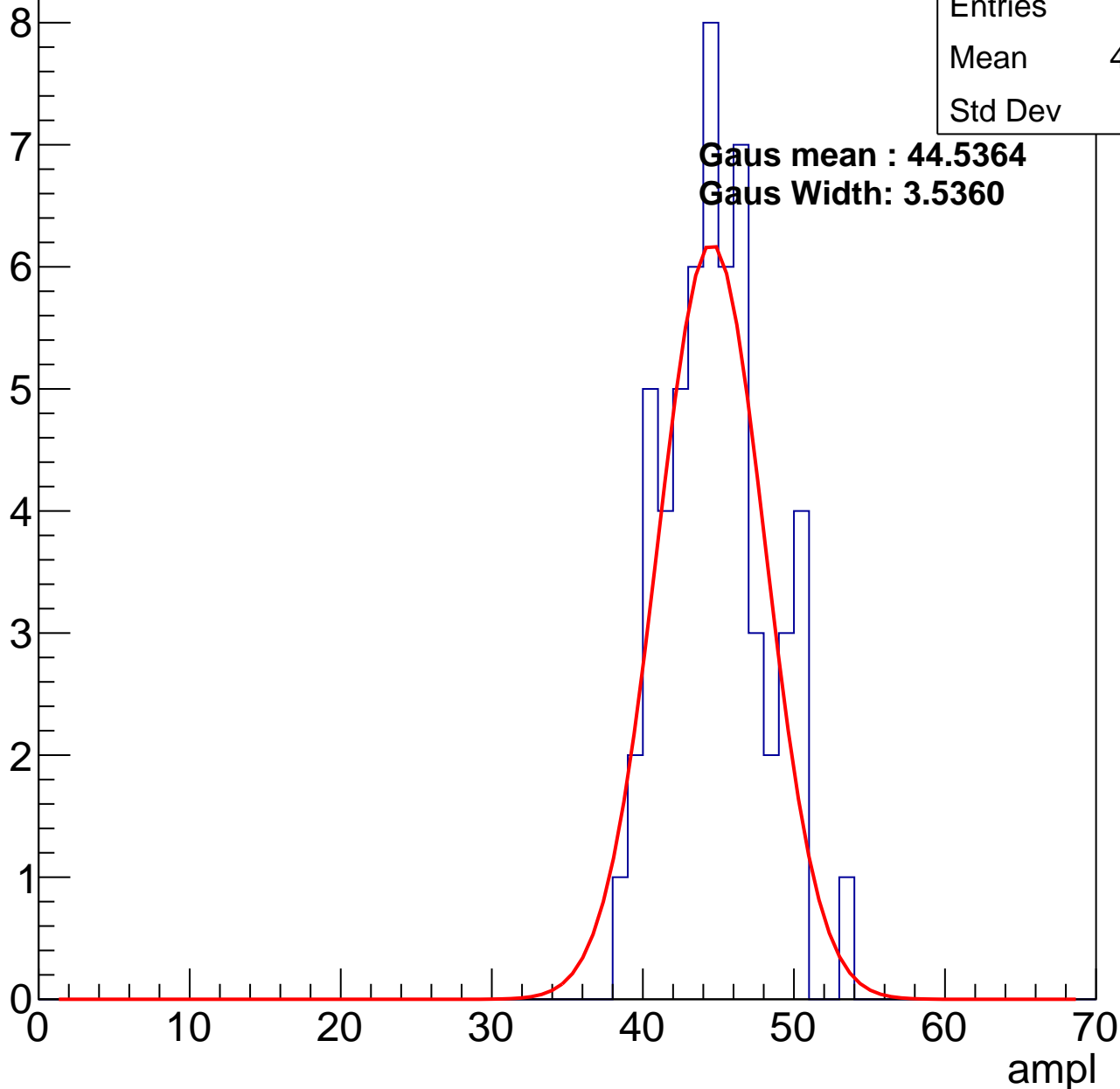
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.37
Std Dev	3.28

Gaus mean : 44.5364

Gaus Width: 3.5360



B1L100S, U19-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	50.18
Std Dev	3.339

Entry

10

8

6

4

2

0

0

10

20

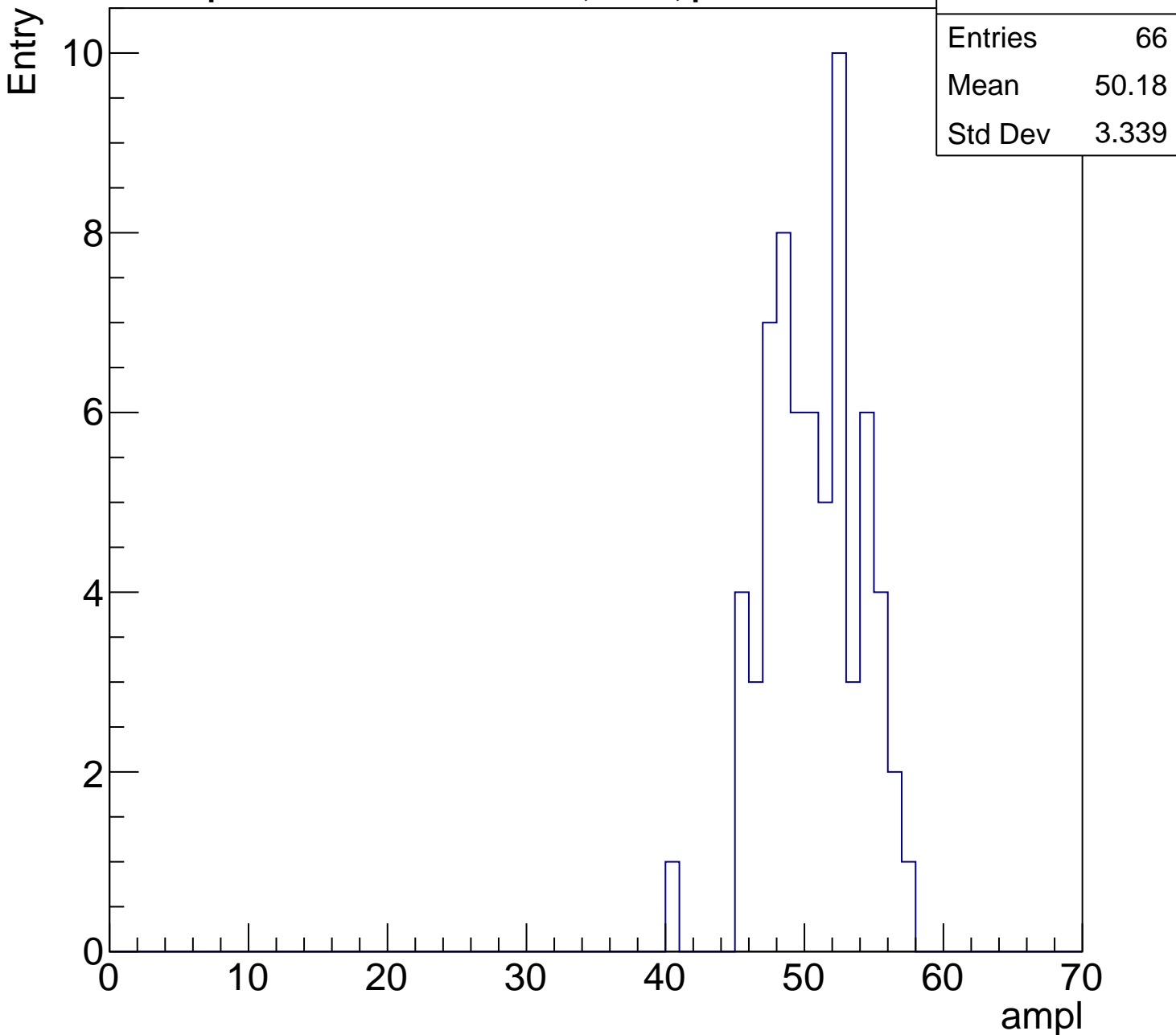
30

40

50

60

ampl

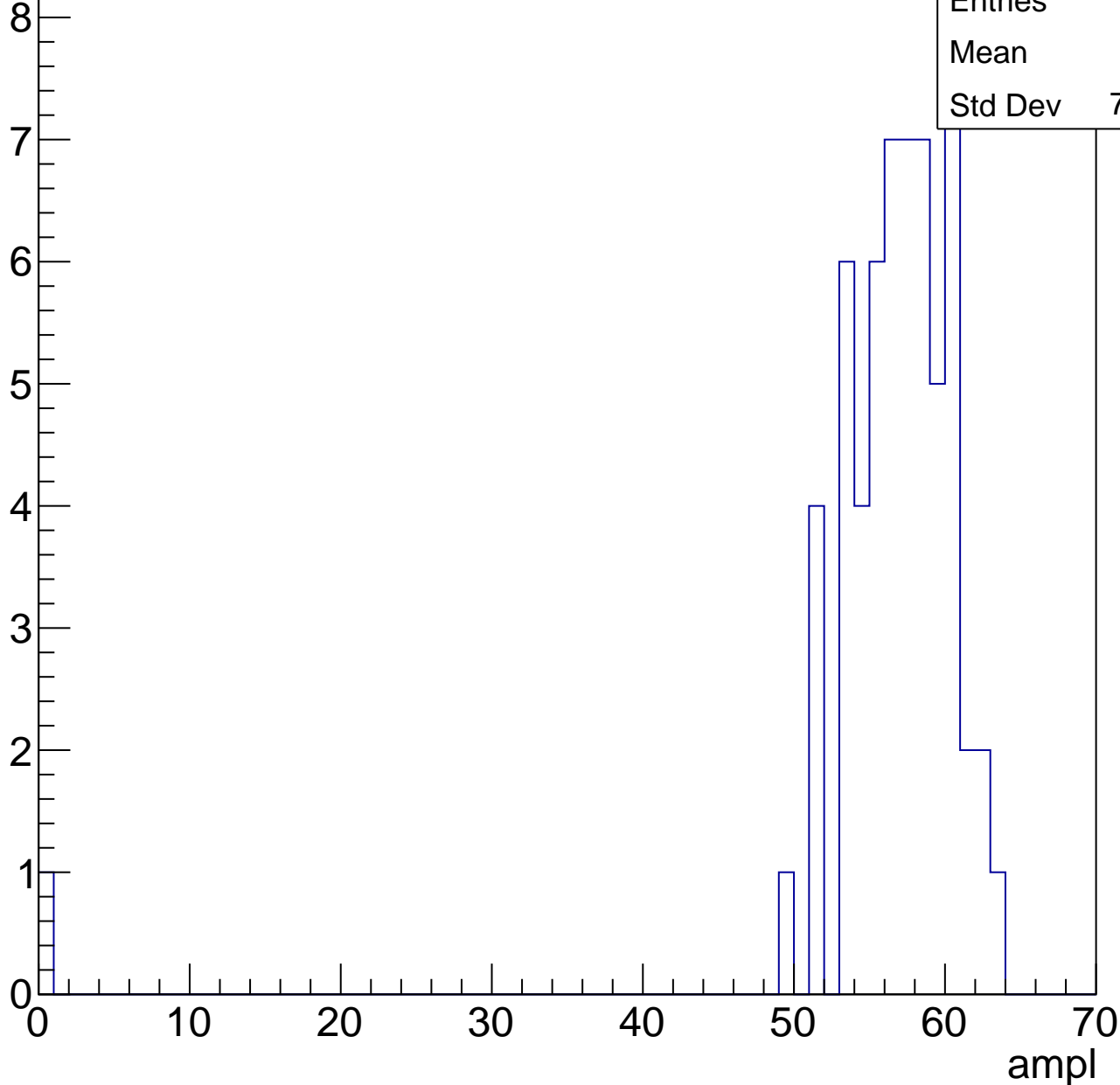


B1L100S, U19-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

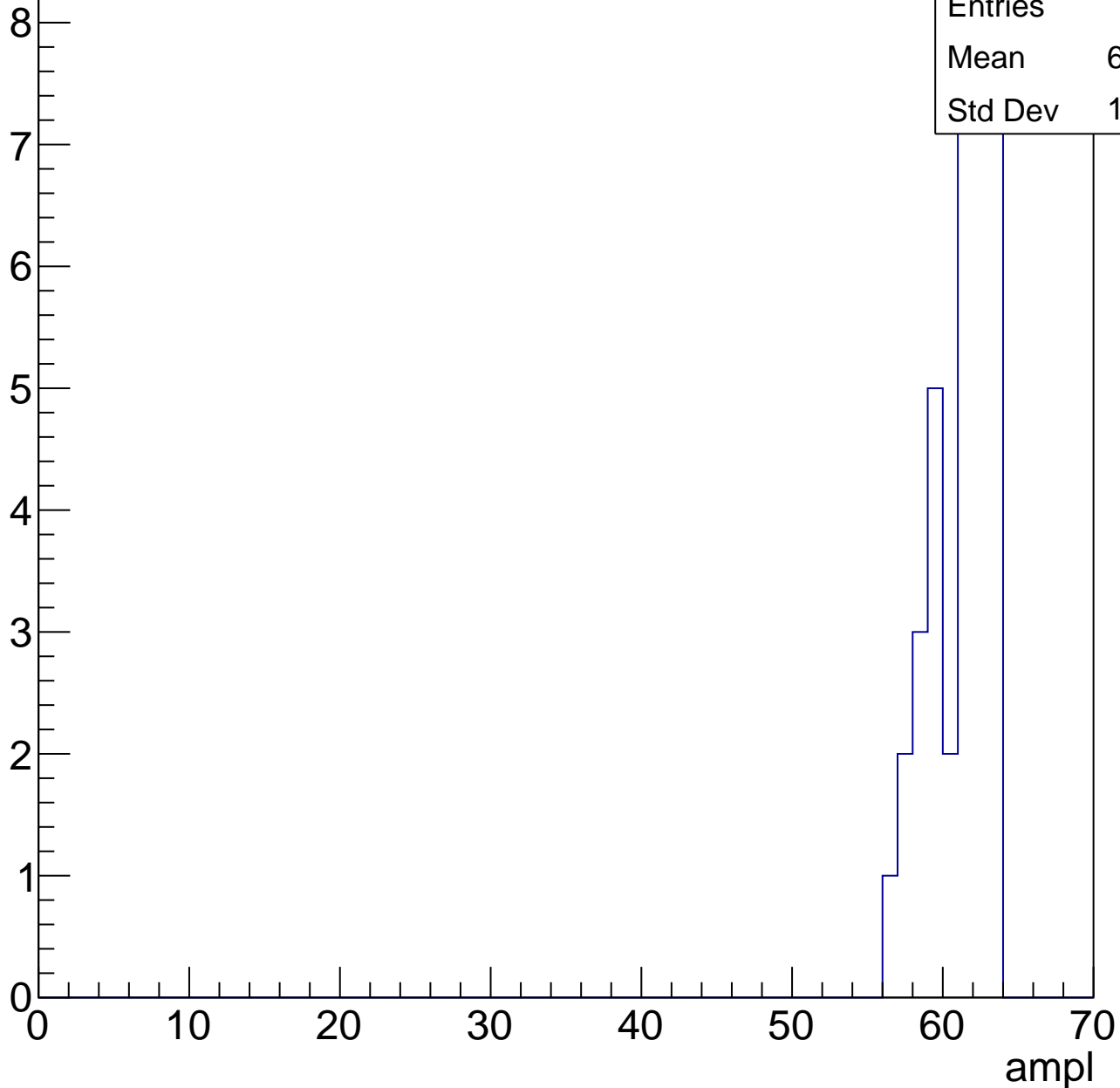
Entries	61
Mean	55.7
Std Dev	7.825



B1L100S, U19-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U19-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch32, adc0

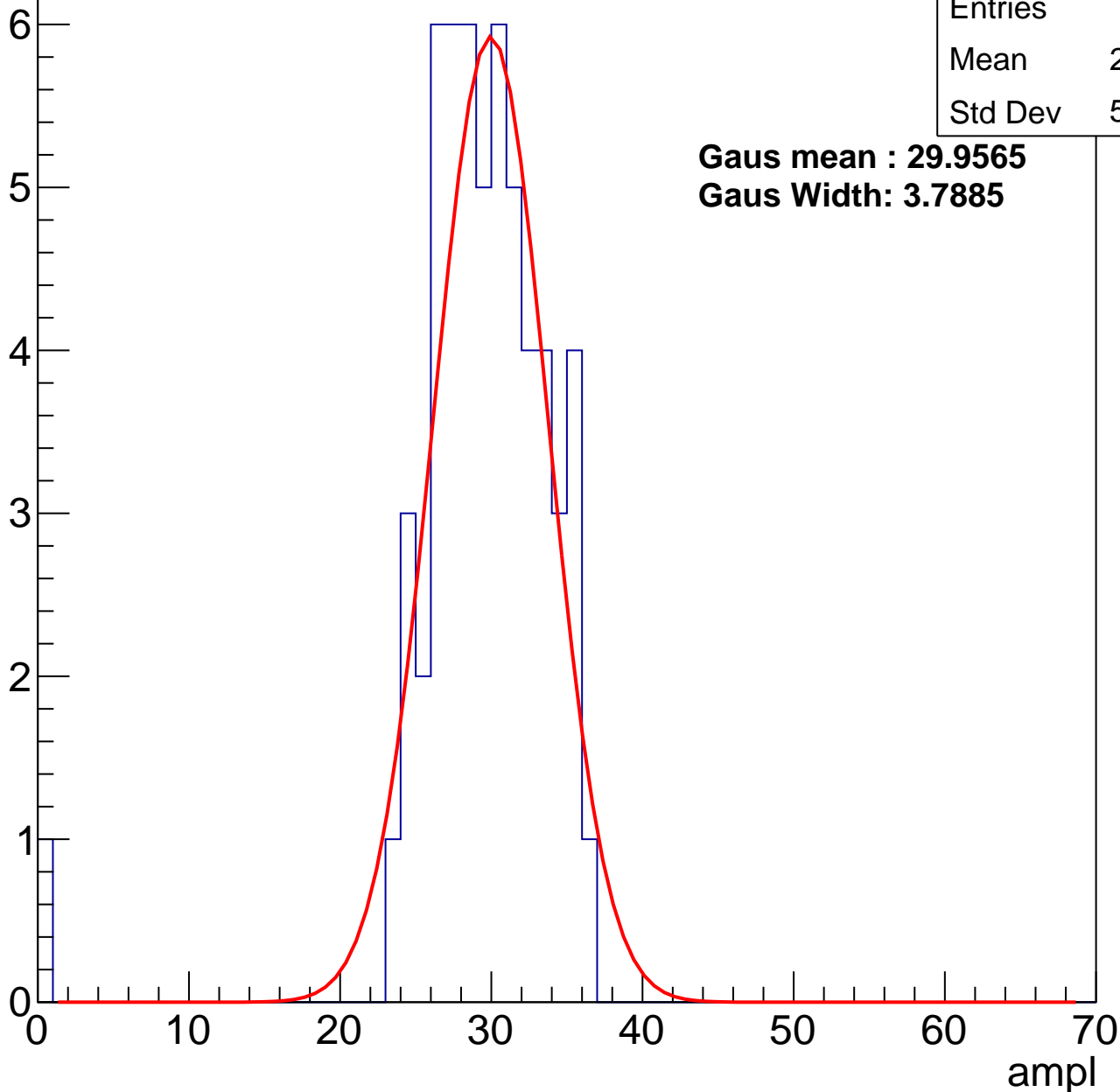
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	28.93
Std Dev	5.064

Gaus mean : 29.9565

Gaus Width: 3.7885



B1L100S, U19-ch32, adc1

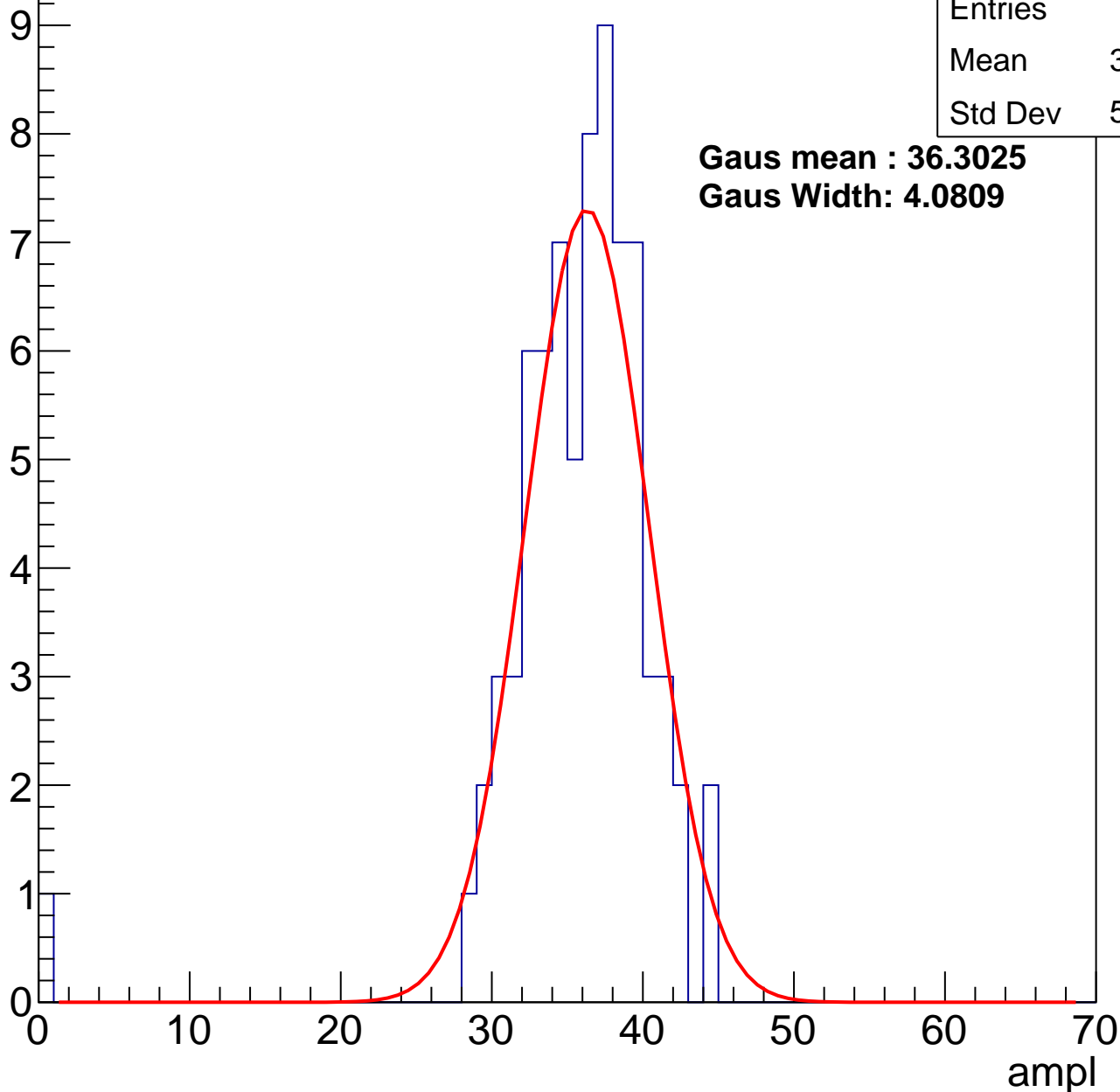
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	35.29
Std Dev	5.428

Gaus mean : 36.3025

Gaus Width: 4.0809



B1L100S, U19-ch32, adc2

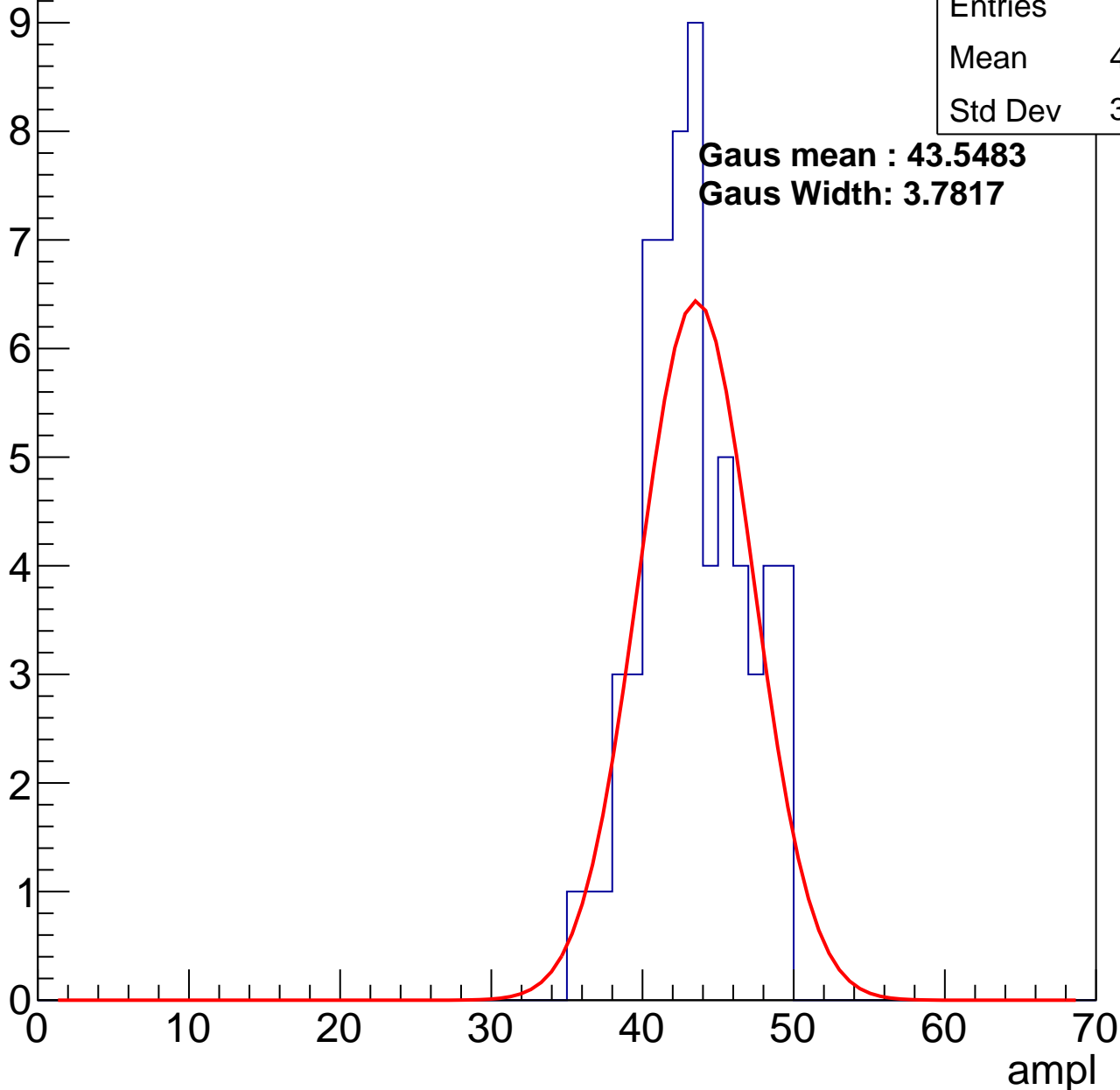
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.86
Std Dev	3.358

Gaus mean : 43.5483

Gaus Width: 3.7817

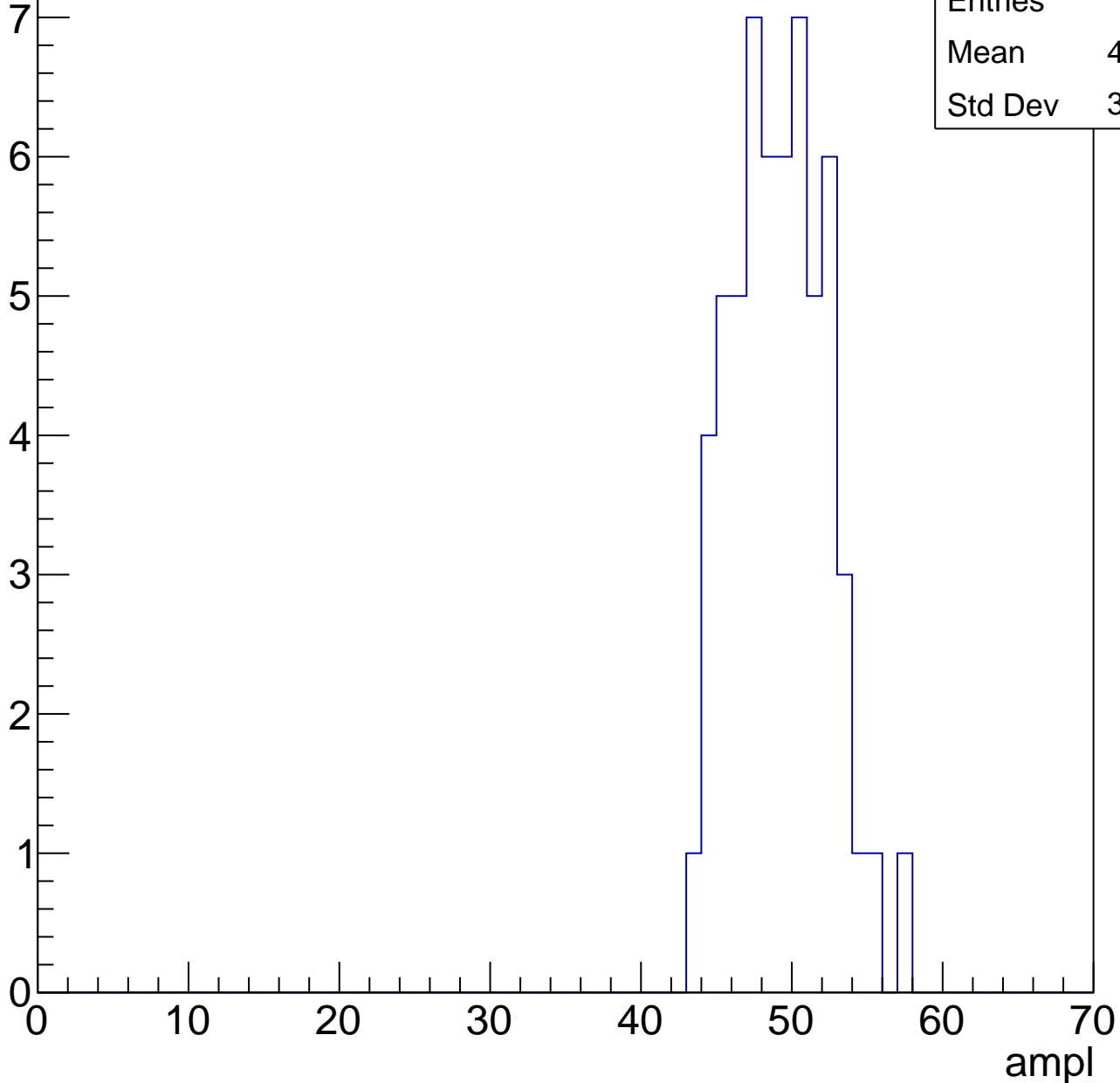


B1L100S, U19-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

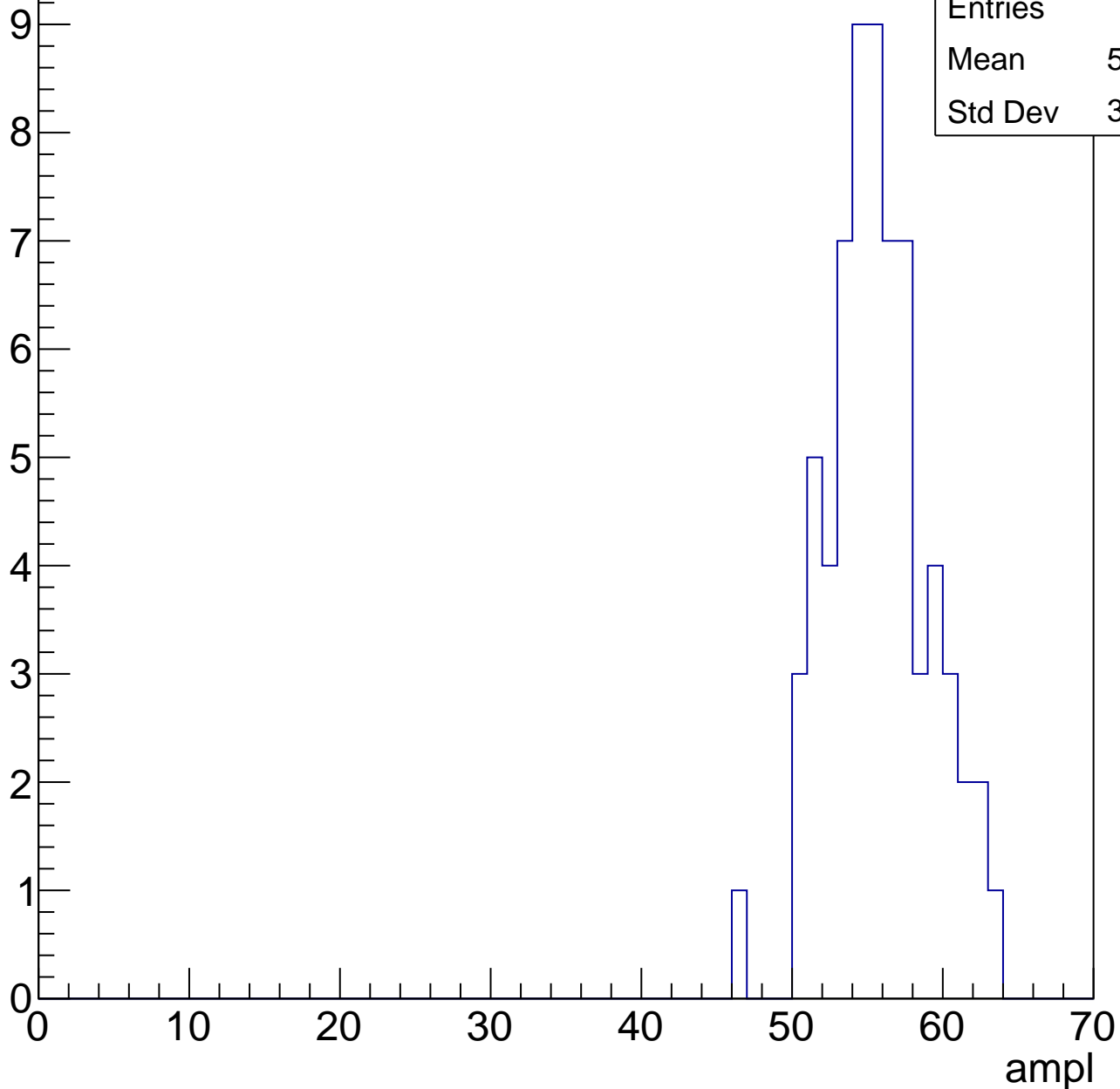
Entries	58
Mean	48.74
Std Dev	3.066



B1L100S, U19-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

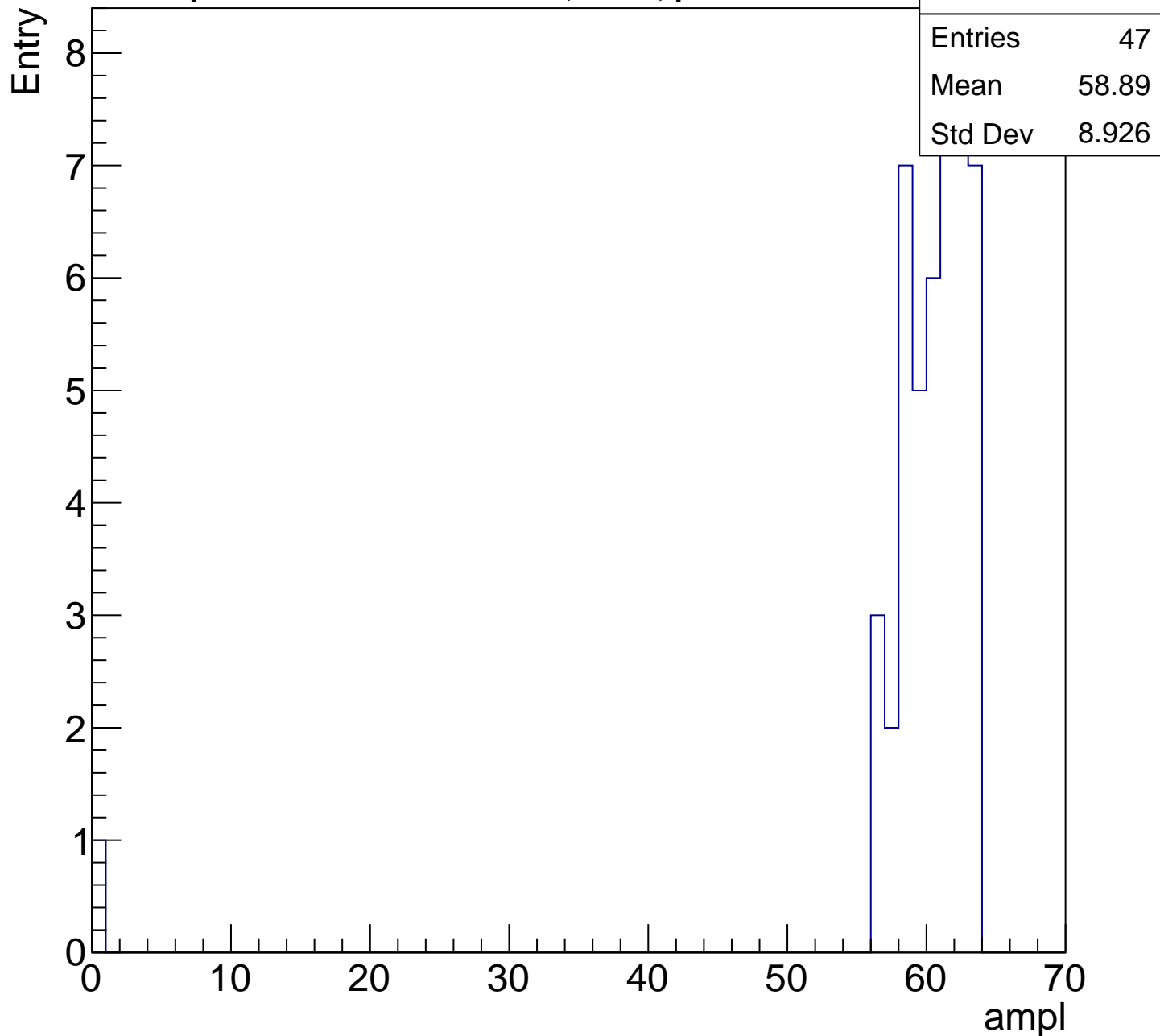
Entry



Entries	67
Mean	55.24
Std Dev	3.324

B1L100S, U19-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch33, adc0

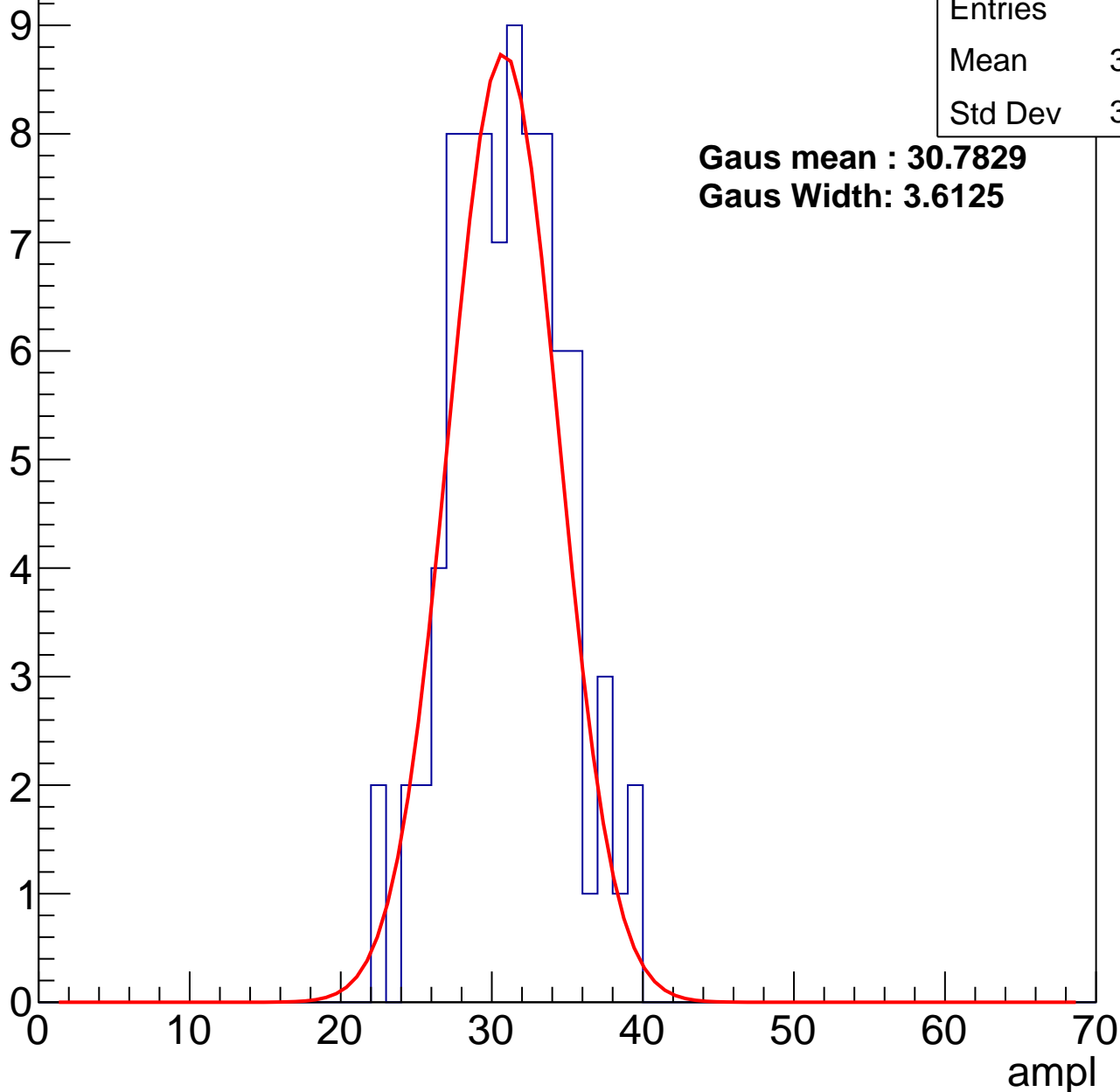
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.64
Std Dev	3.694

Gaus mean : 30.7829

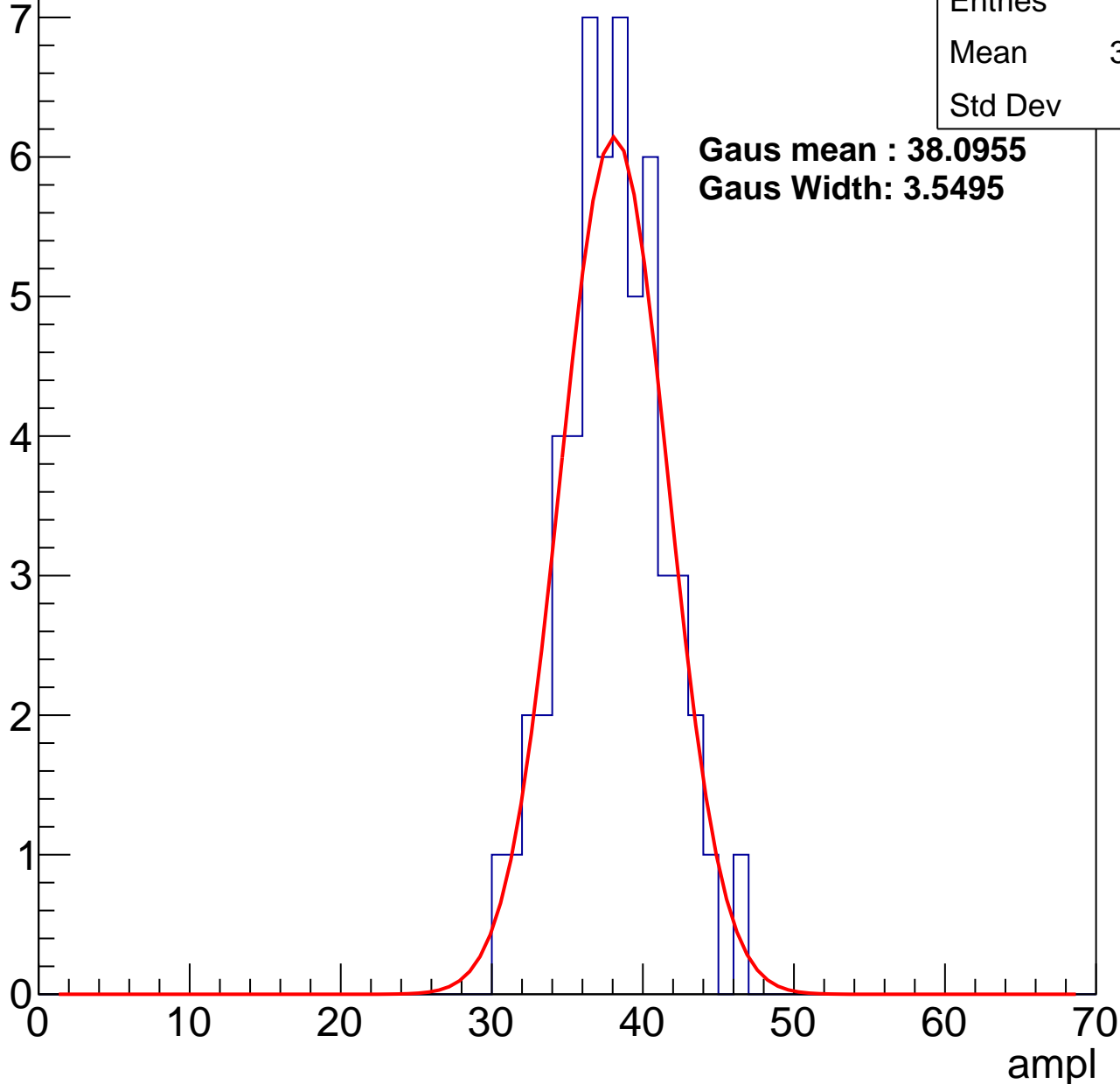
Gaus Width: 3.6125



B1L100S, U19-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch33, adc2

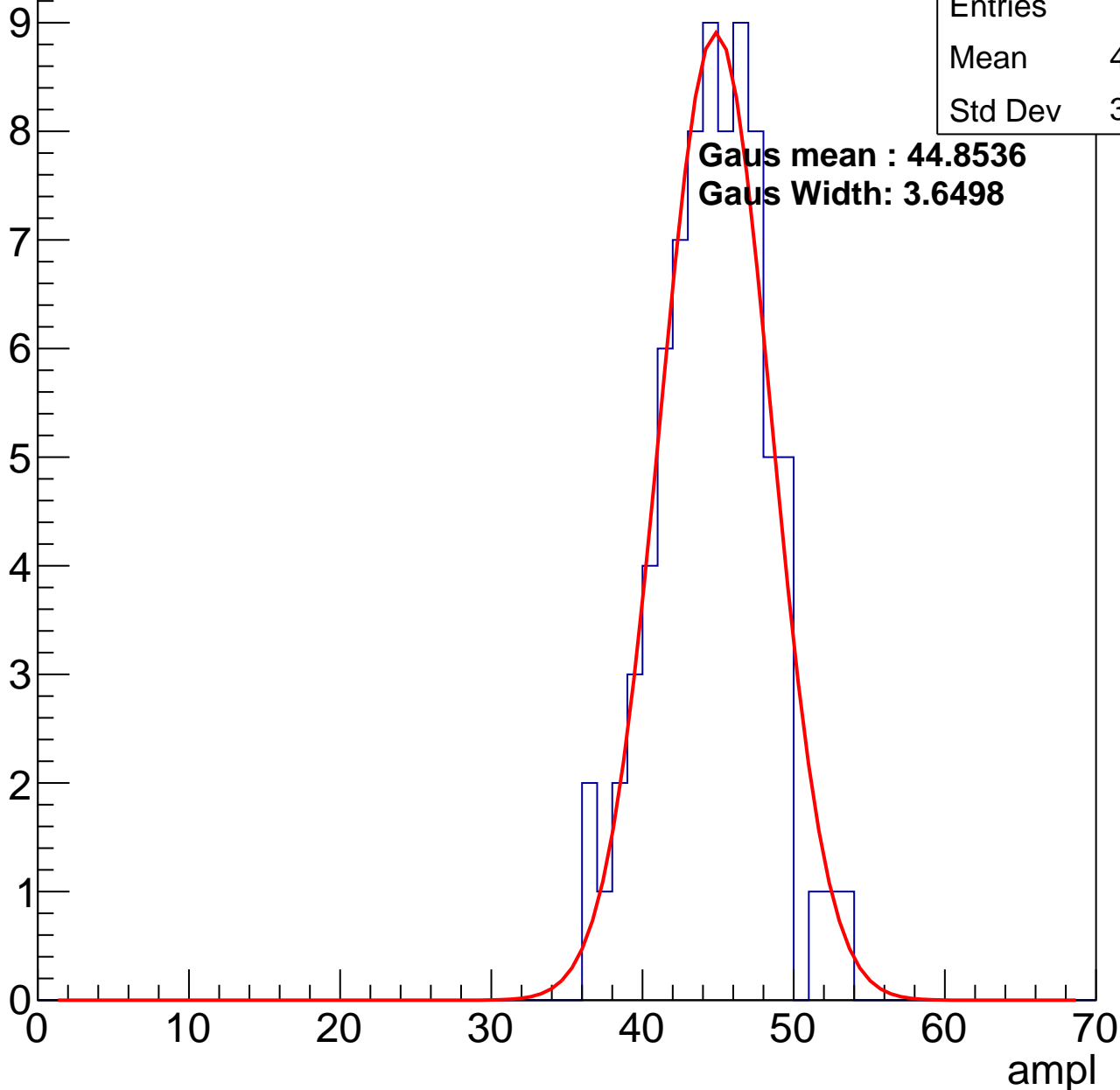
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	44.16
Std Dev	3.519

Gaus mean : 44.8536

Gaus Width: 3.6498

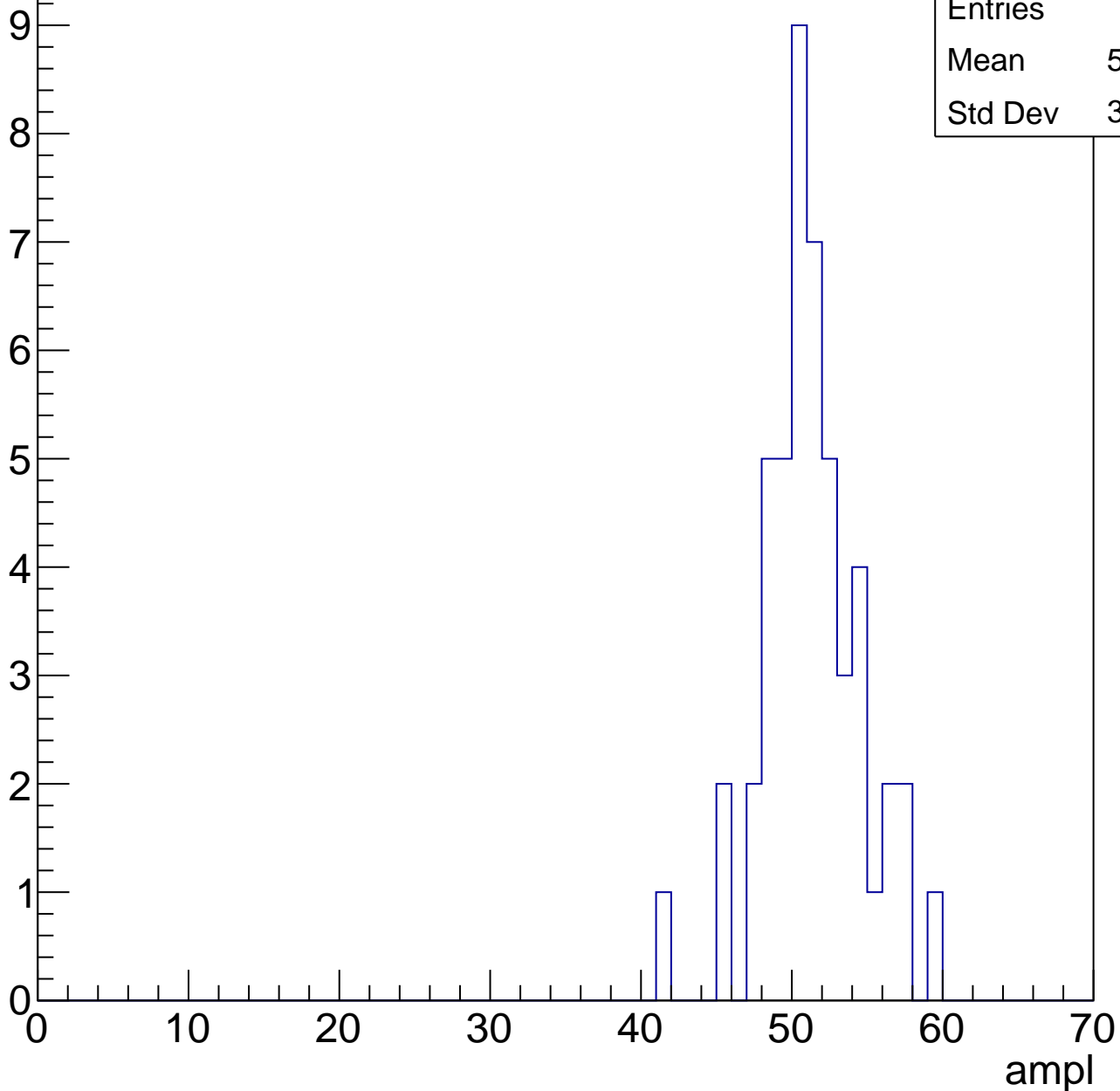


B1L100S, U19-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

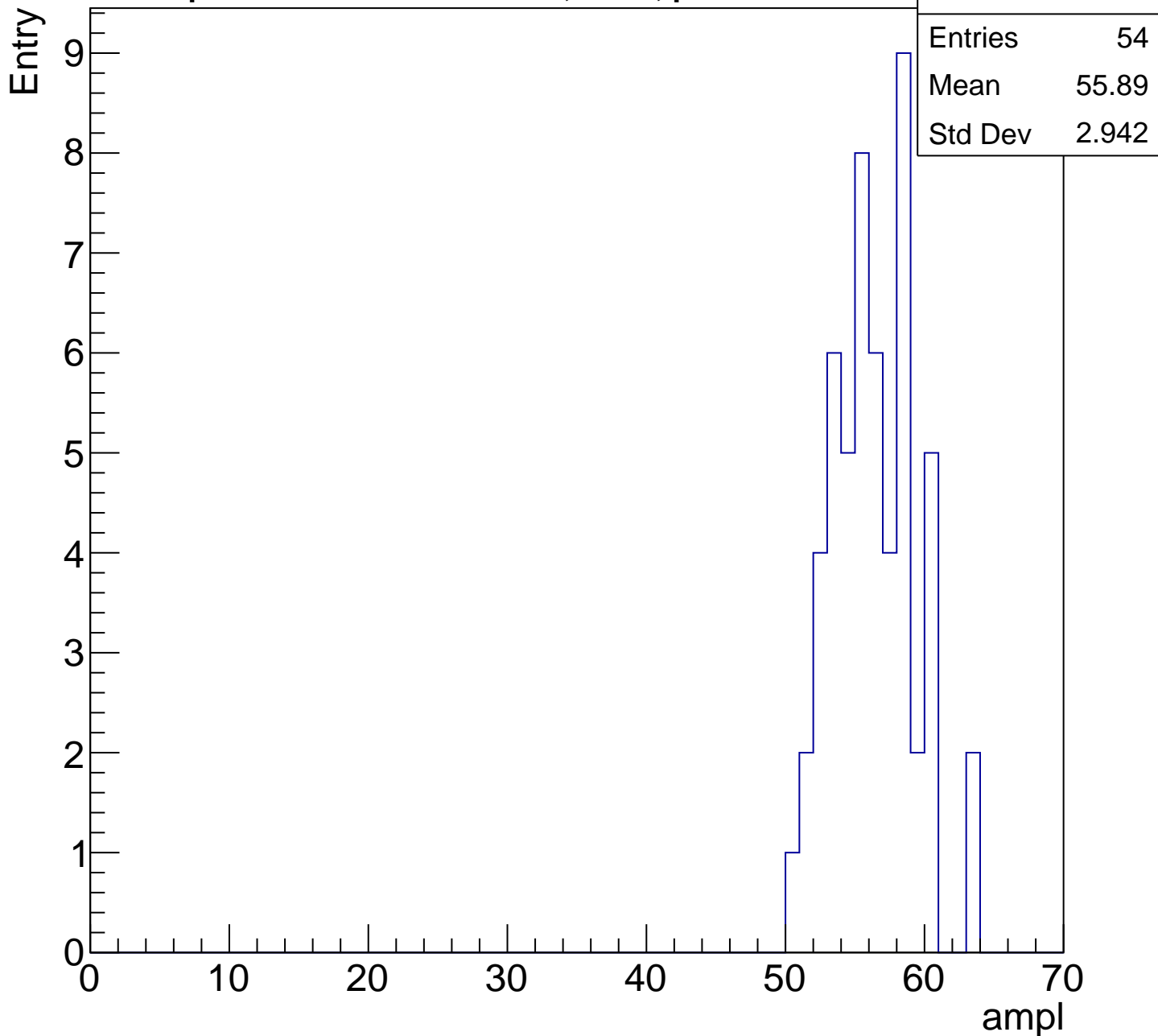
Entry

Entries	49
Mean	50.86
Std Dev	3.295



B1L100S, U19-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.98
Std Dev	8.906

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L100S, U19-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch34, adc0

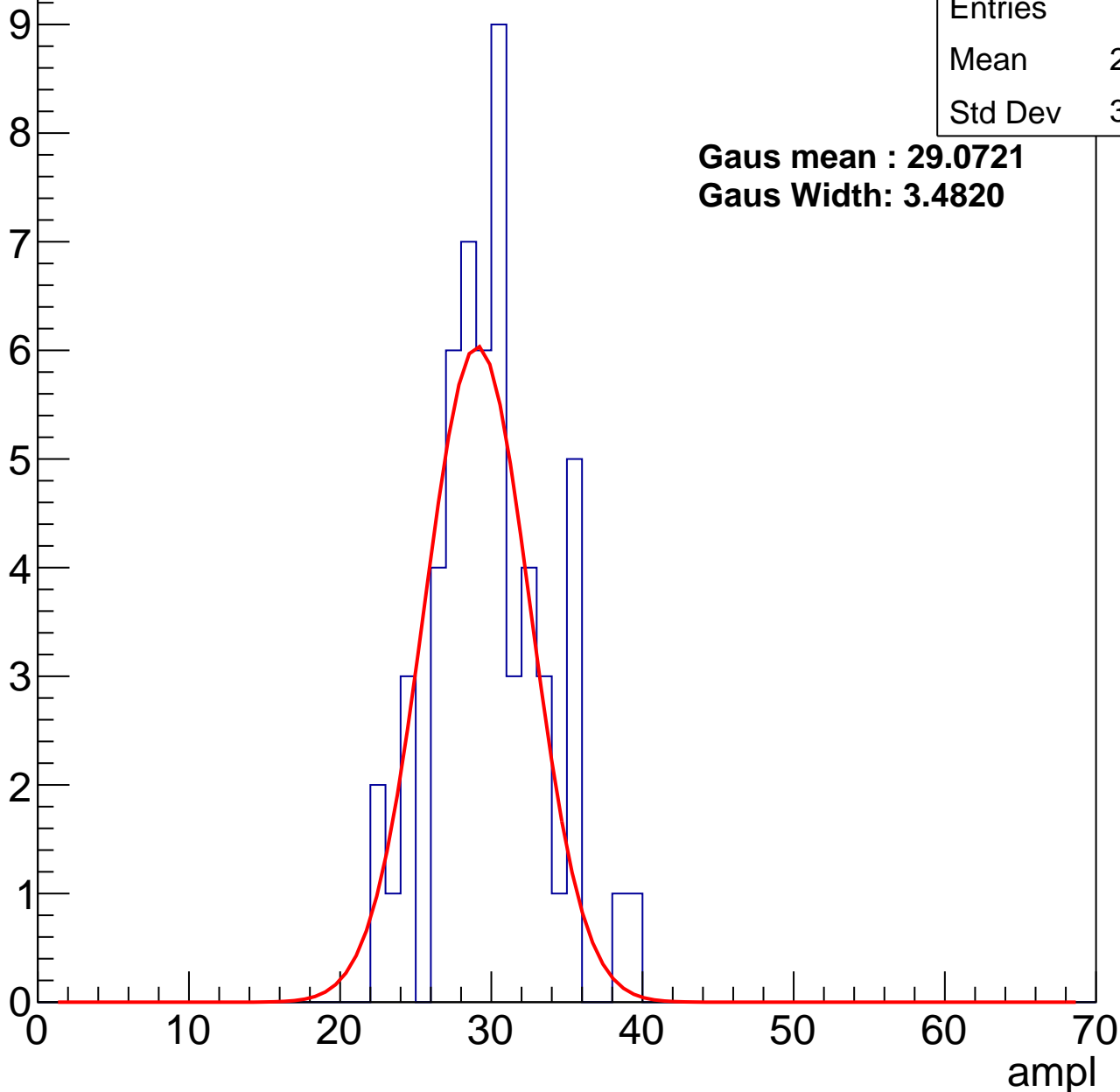
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.48
Std Dev	3.669

Gaus mean : 29.0721

Gaus Width: 3.4820



B1L100S, U19-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	102
Mean	36.85
Std Dev	4.525

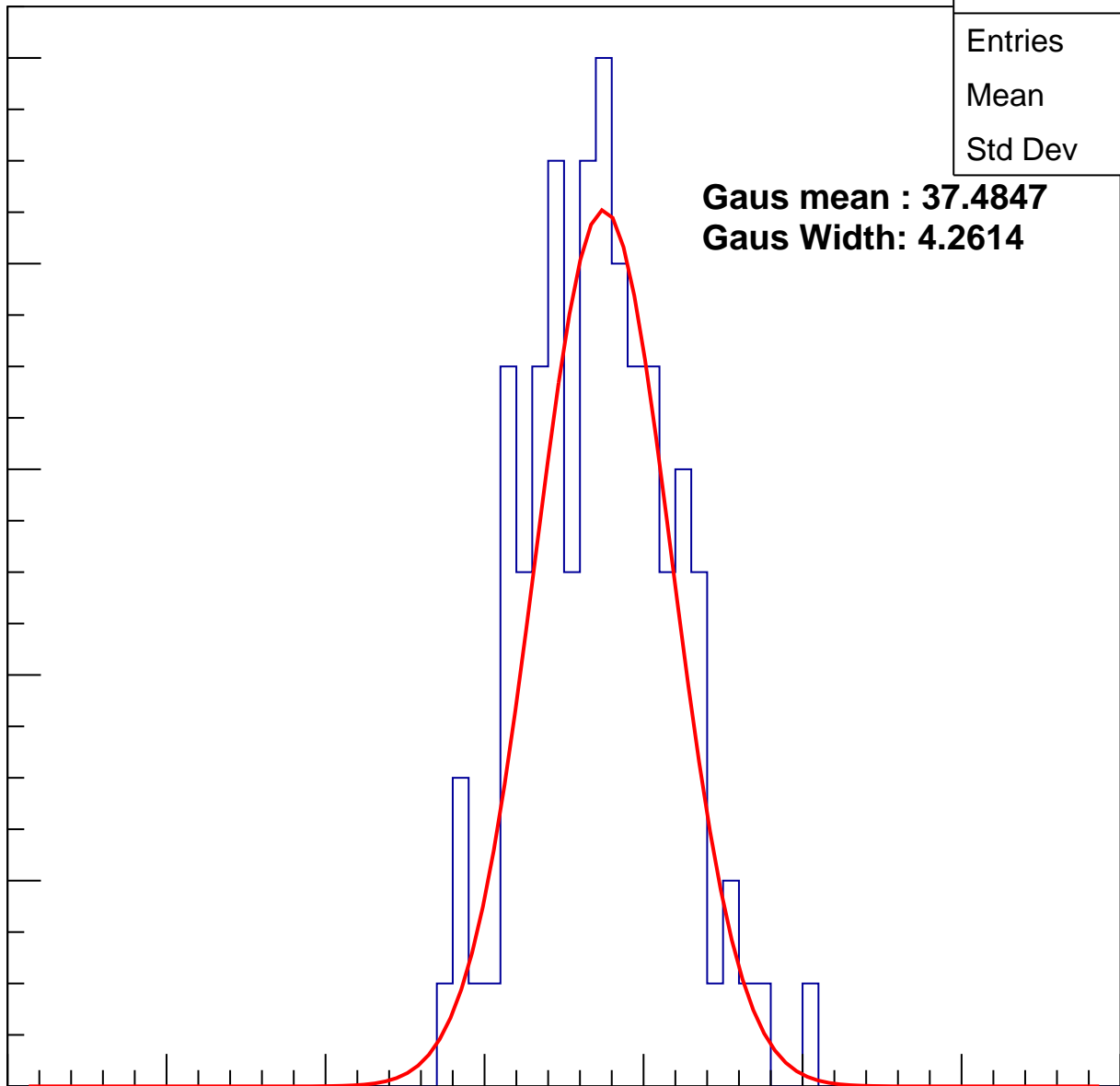
Gaus mean : 37.4847
Gaus Width: 4.2614

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch34, adc2

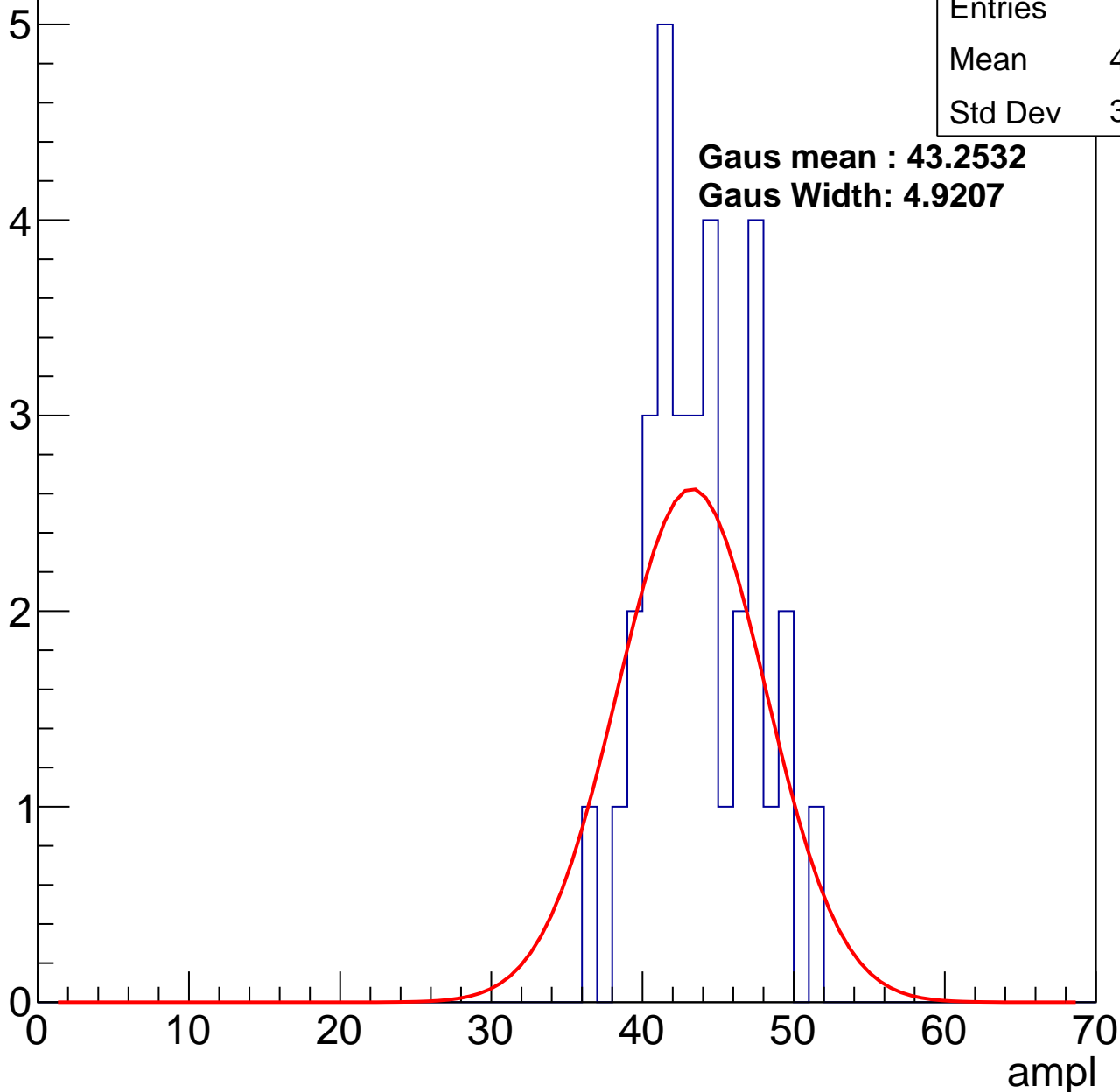
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	43.33
Std Dev	3.496

Gaus mean : 43.2532

Gaus Width: 4.9207

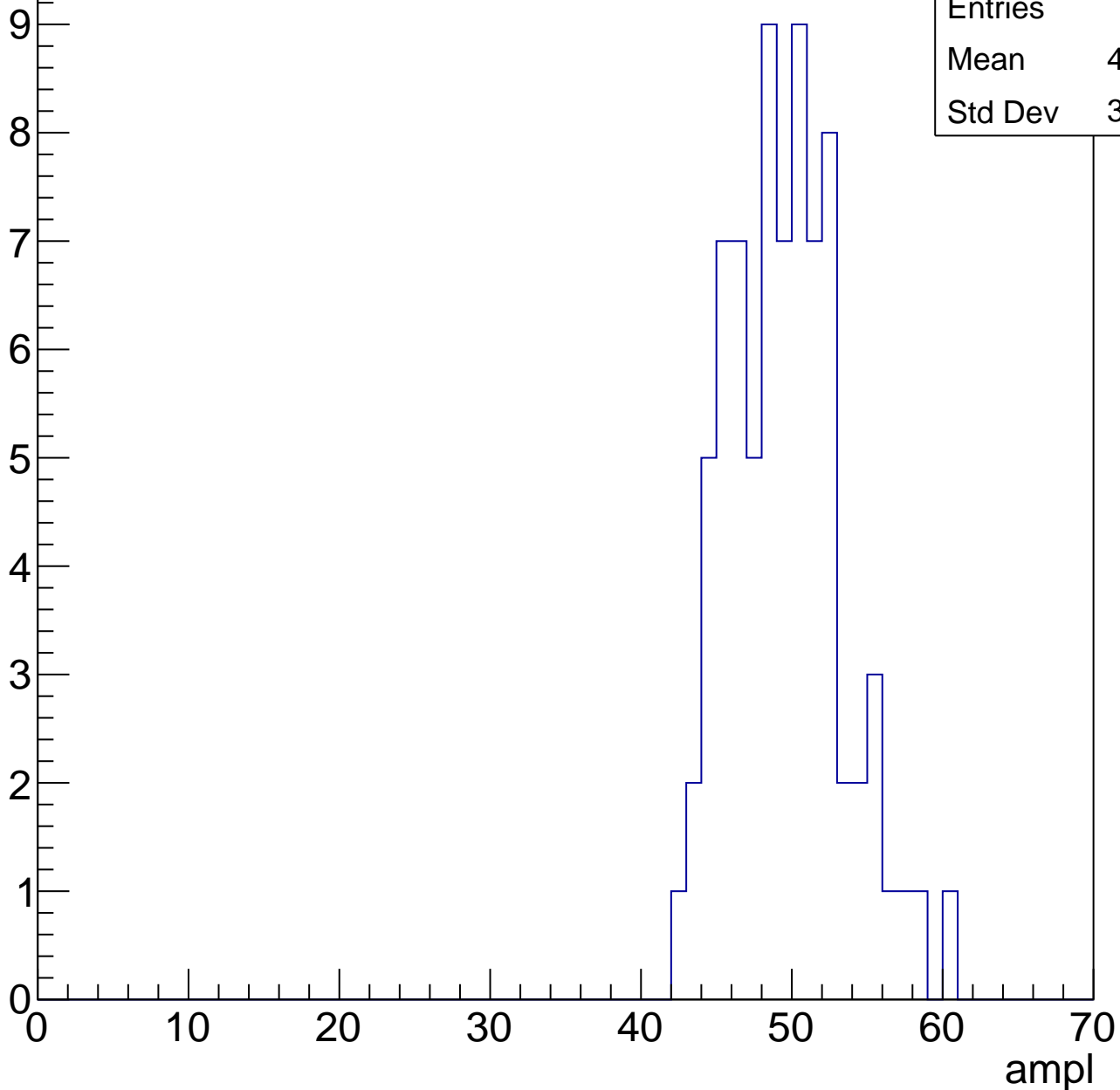


B1L100S, U19-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

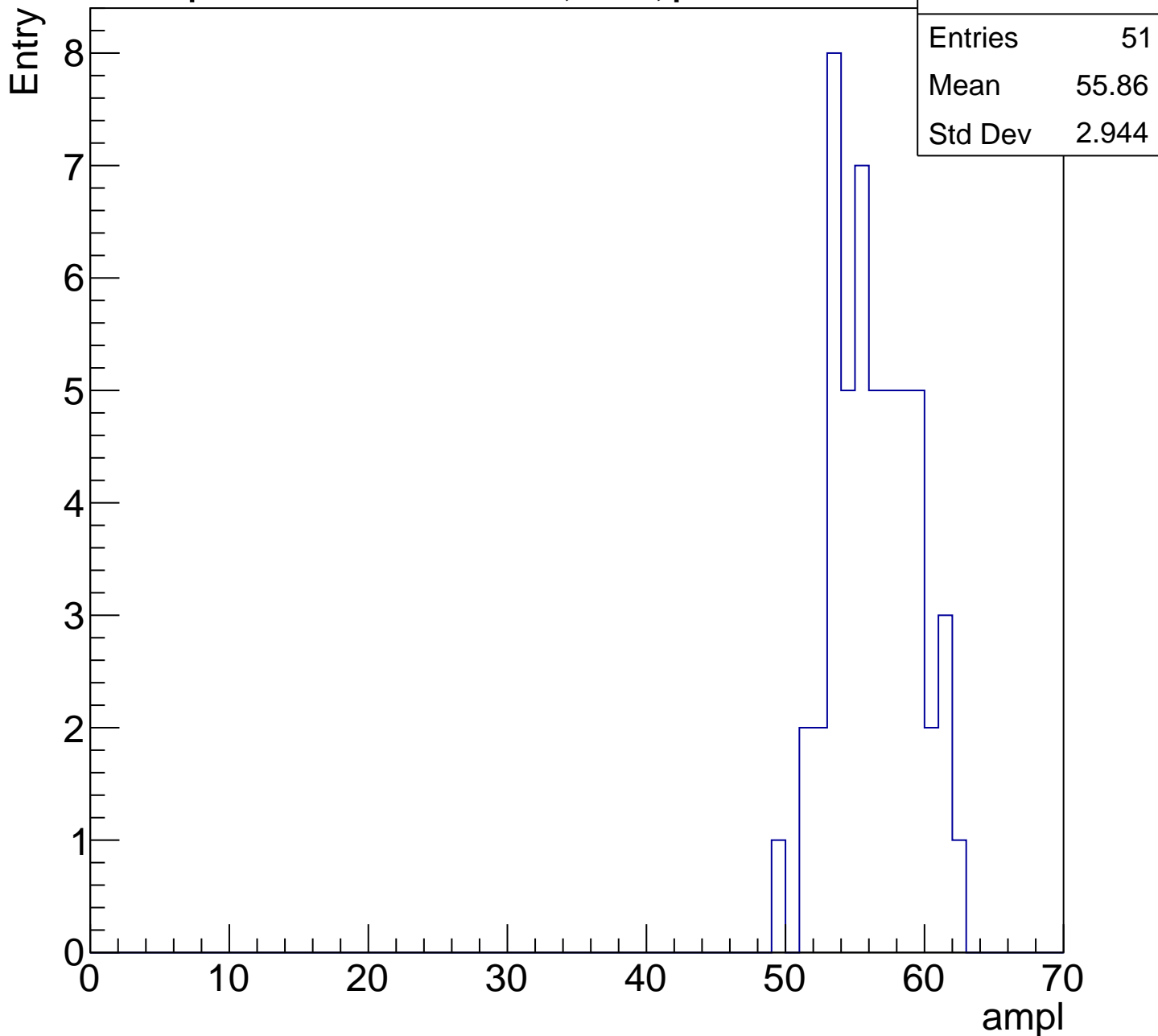
Entry

Entries	78
Mean	49.08
Std Dev	3.693



B1L100S, U19-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

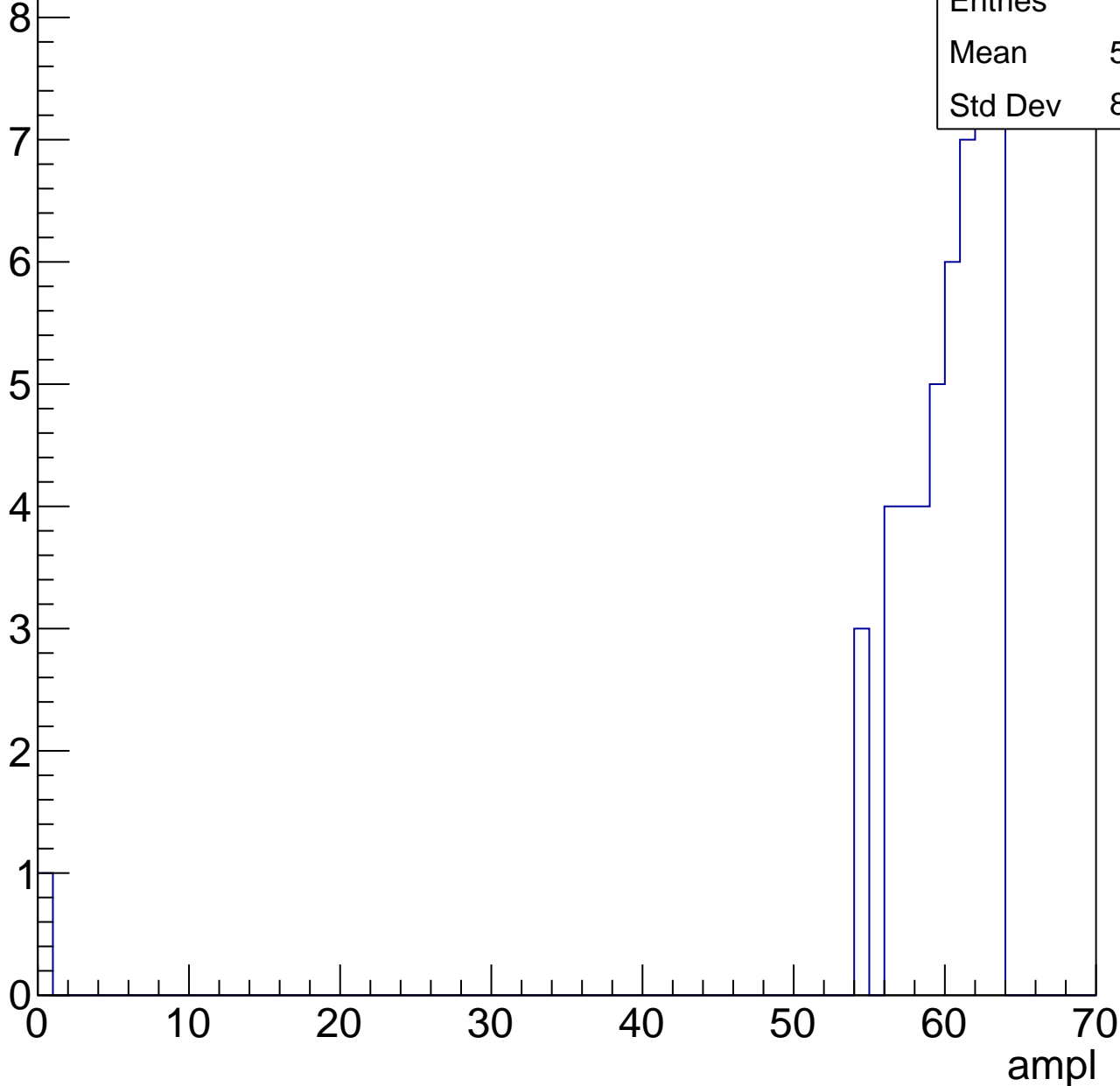


B1L100S, U19-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.56
Std Dev	8.759



B1L100S, U19-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

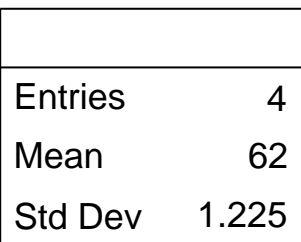
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch35, adc0

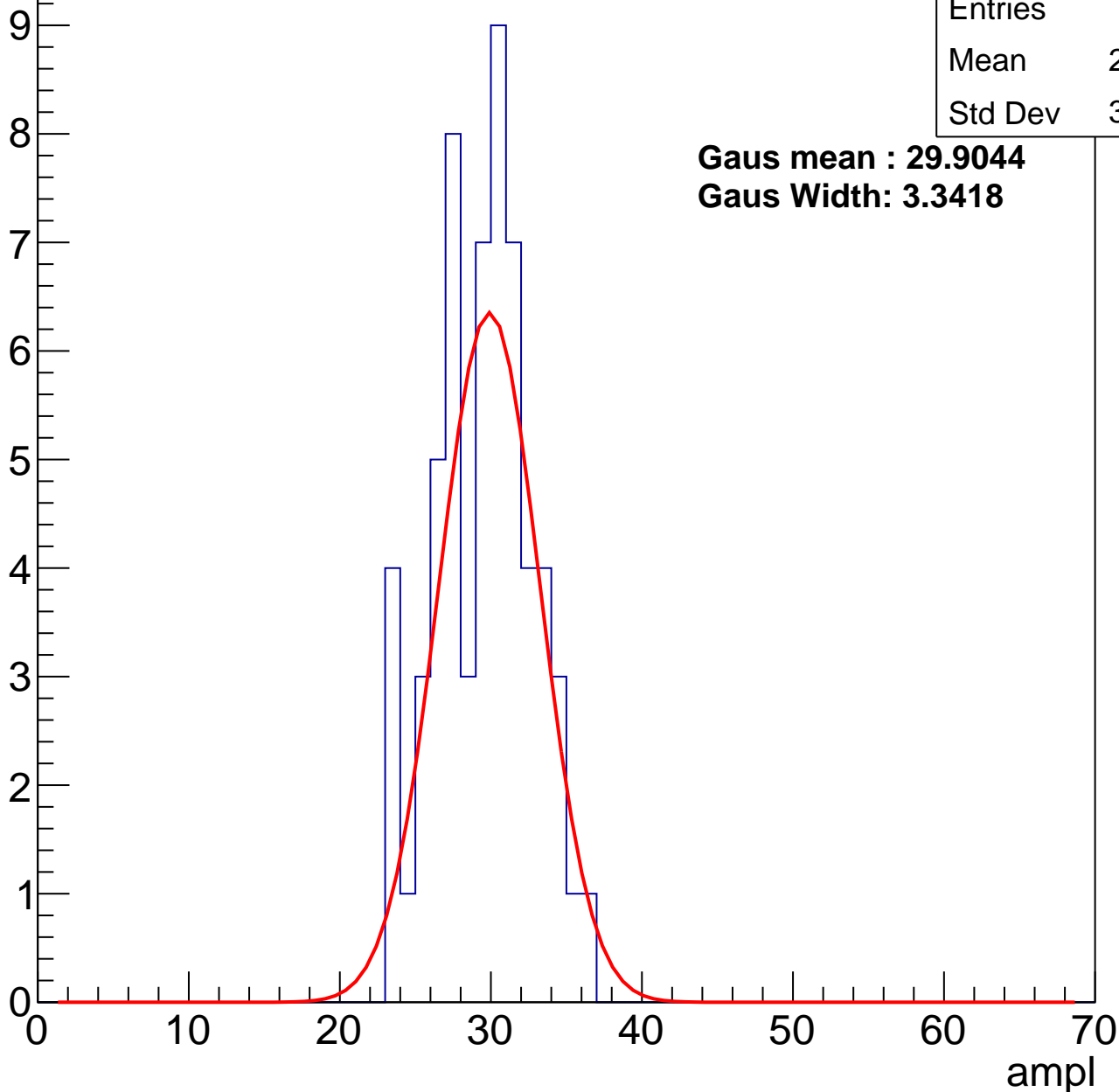
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.07
Std Dev	3.146

Gaus mean : 29.9044

Gaus Width: 3.3418



B1L100S, U19-ch35, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	36.37
Std Dev	3.59

Gaus mean : 36.8166

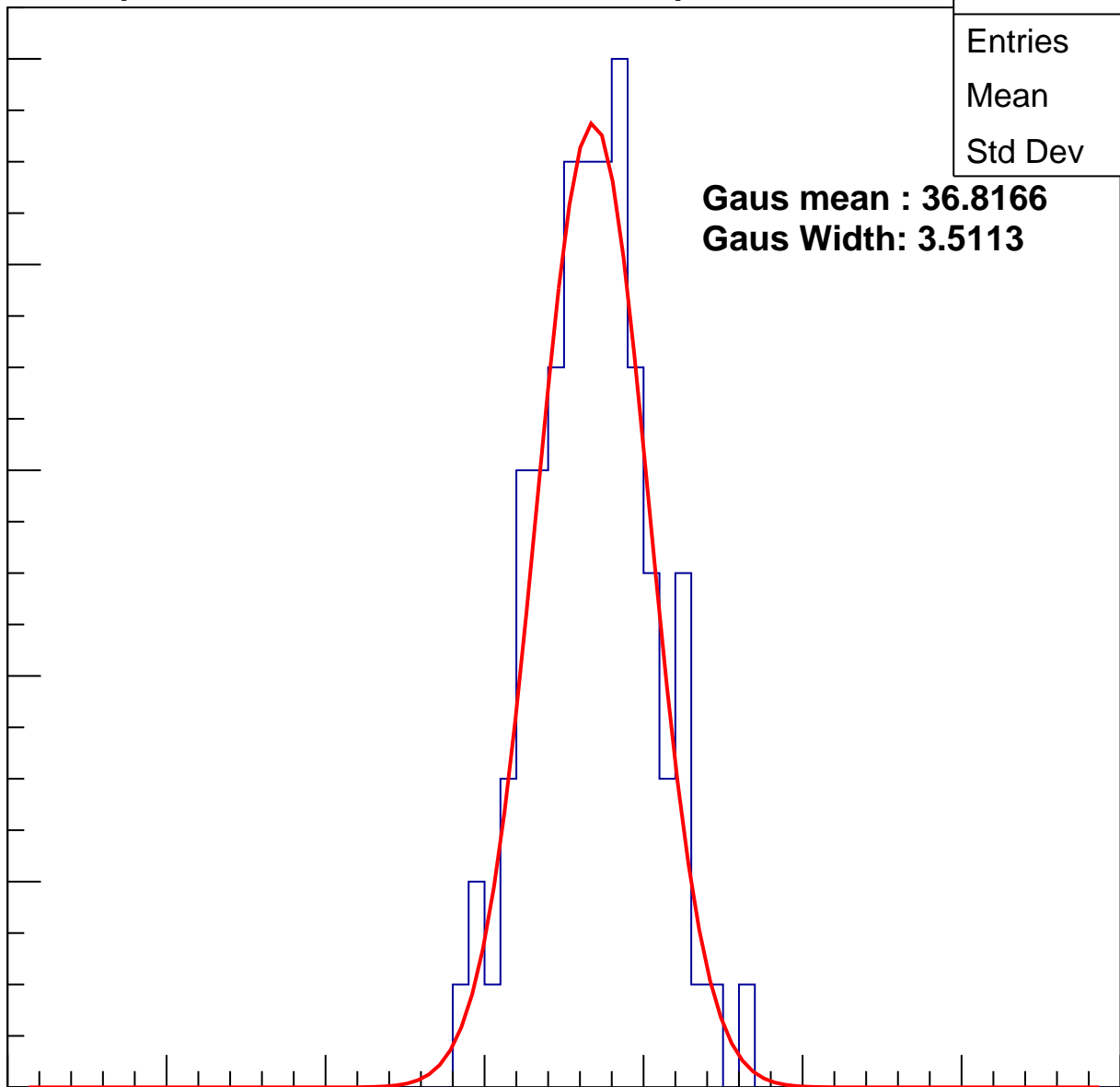
Gaus Width: 3.5113

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch35, adc2

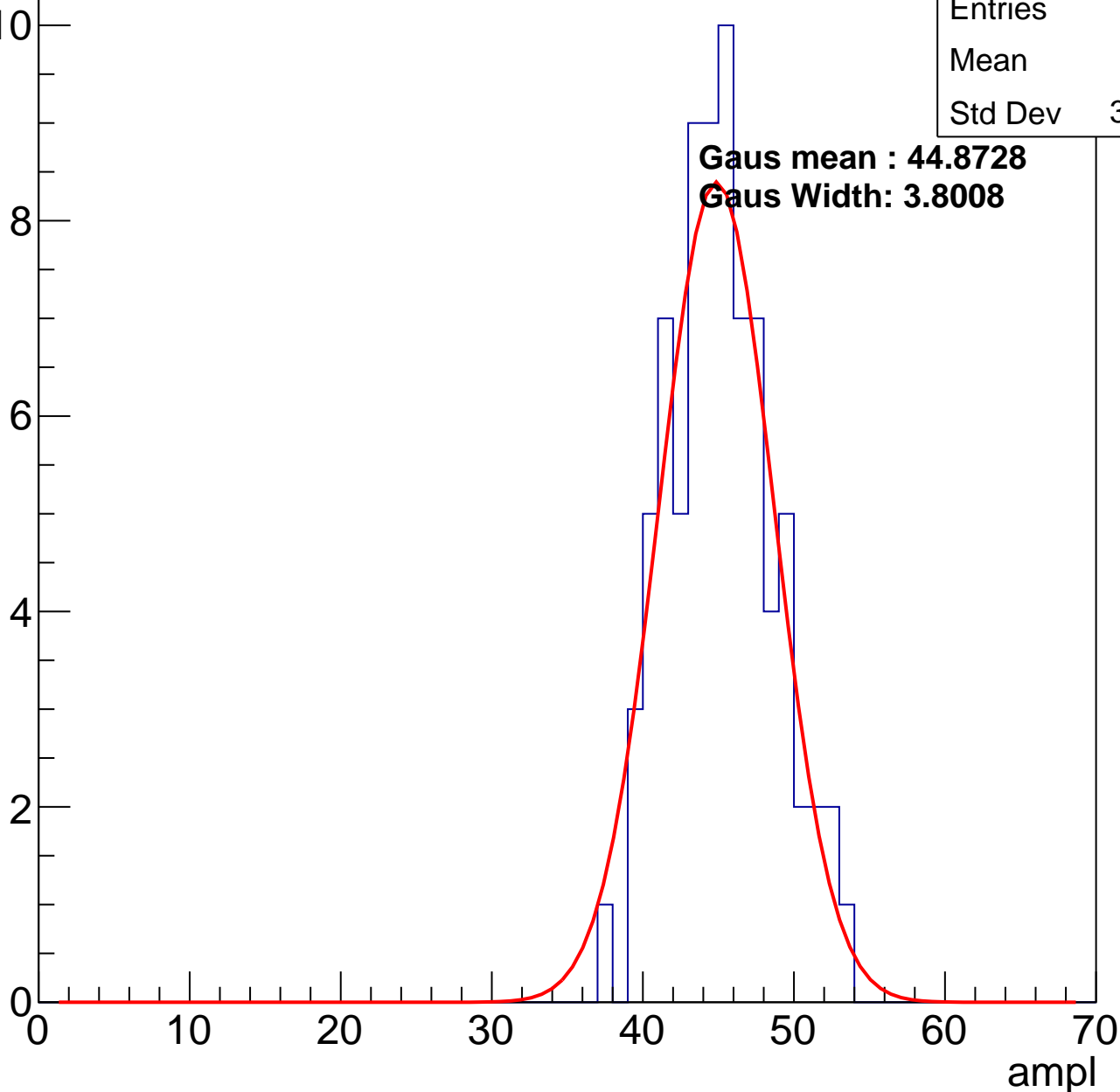
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	44.7
Std Dev	3.425

Gaus mean : 44.8728

Gaus Width: 3.8008

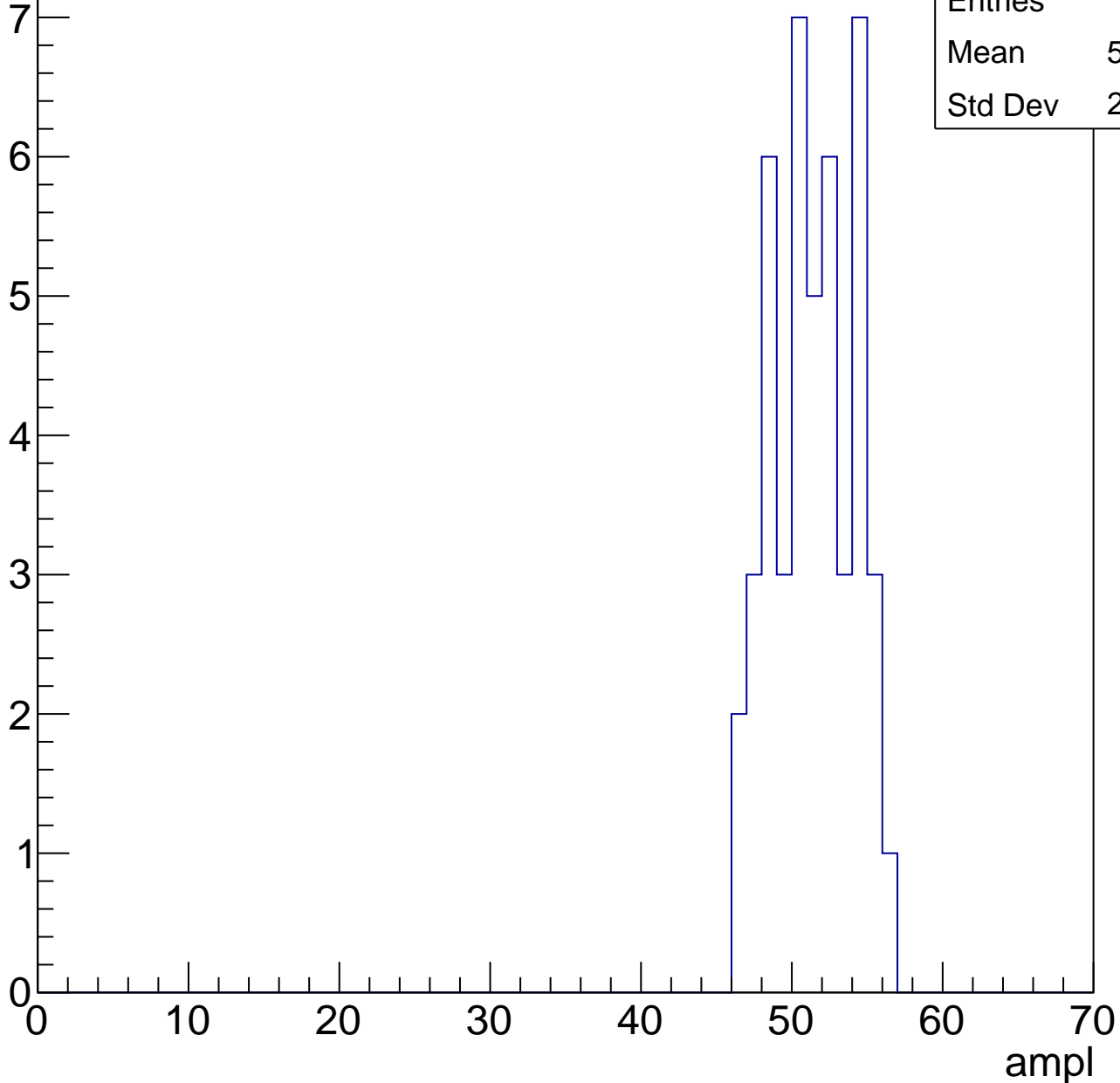


B1L100S, U19-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

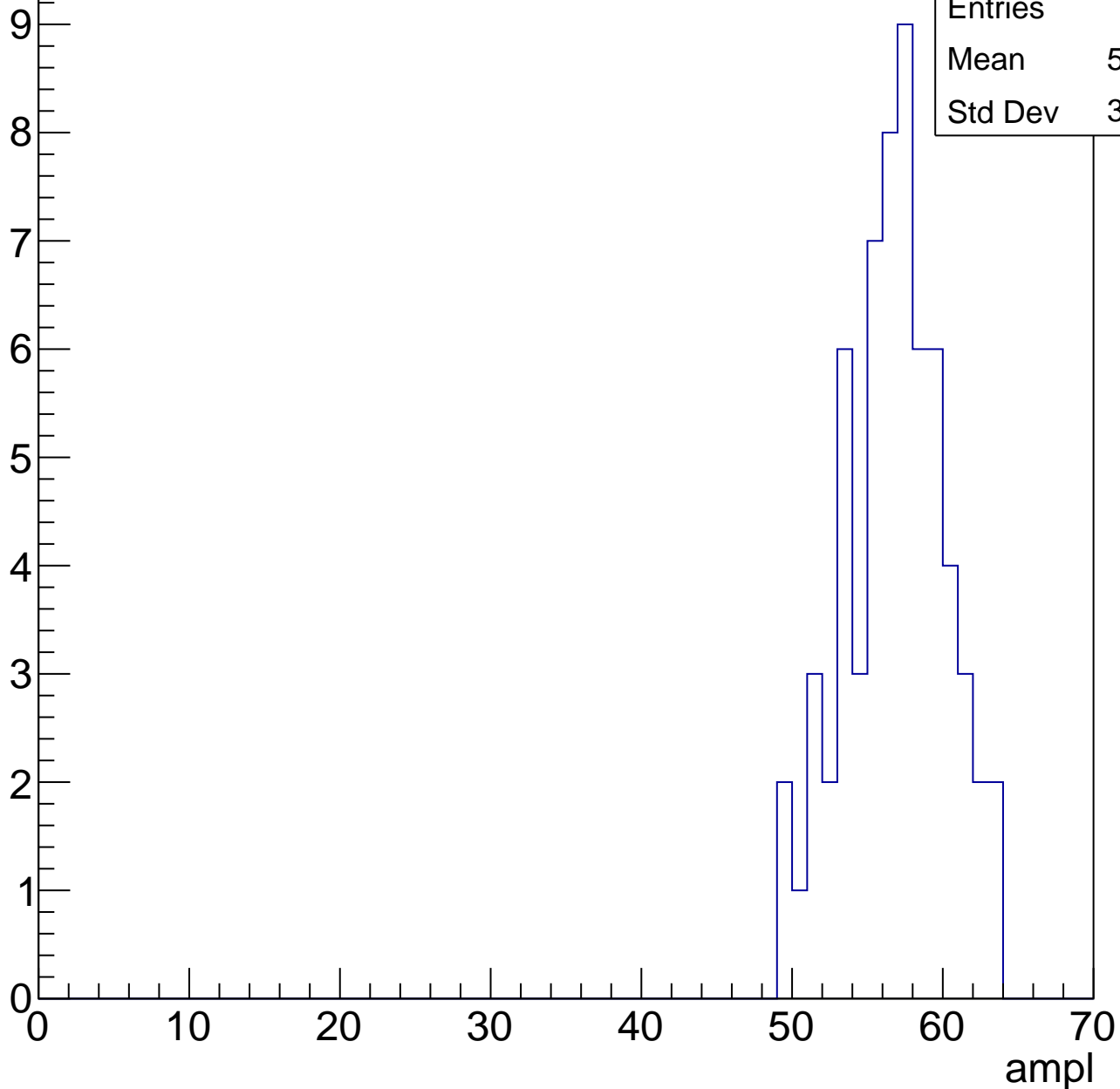
Entries	46
Mean	50.93
Std Dev	2.657



B1L100S, U19-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

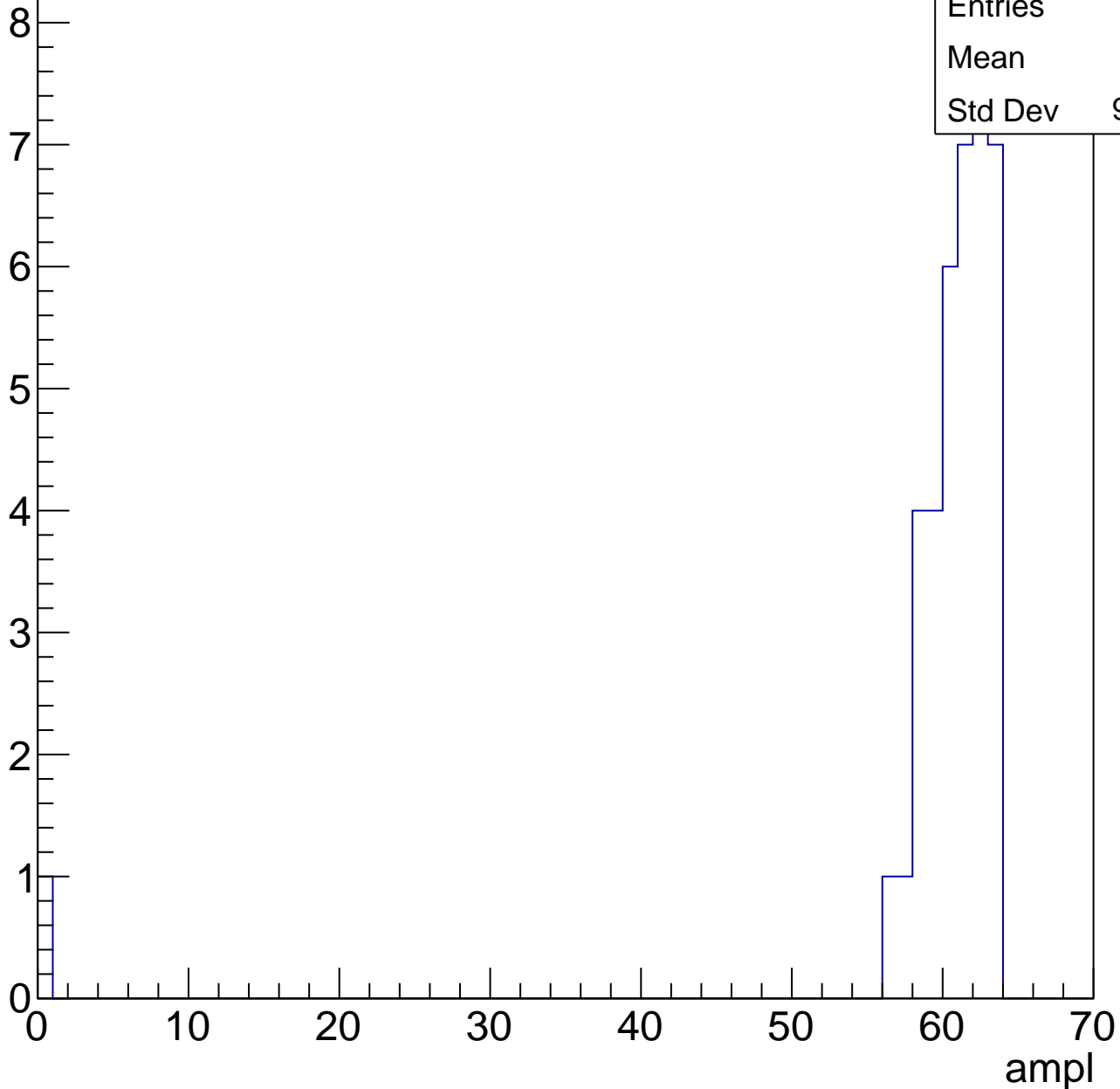


B1L100S, U19-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.1
Std Dev	9.761



B1L100S, U19-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U19-ch36, adc0

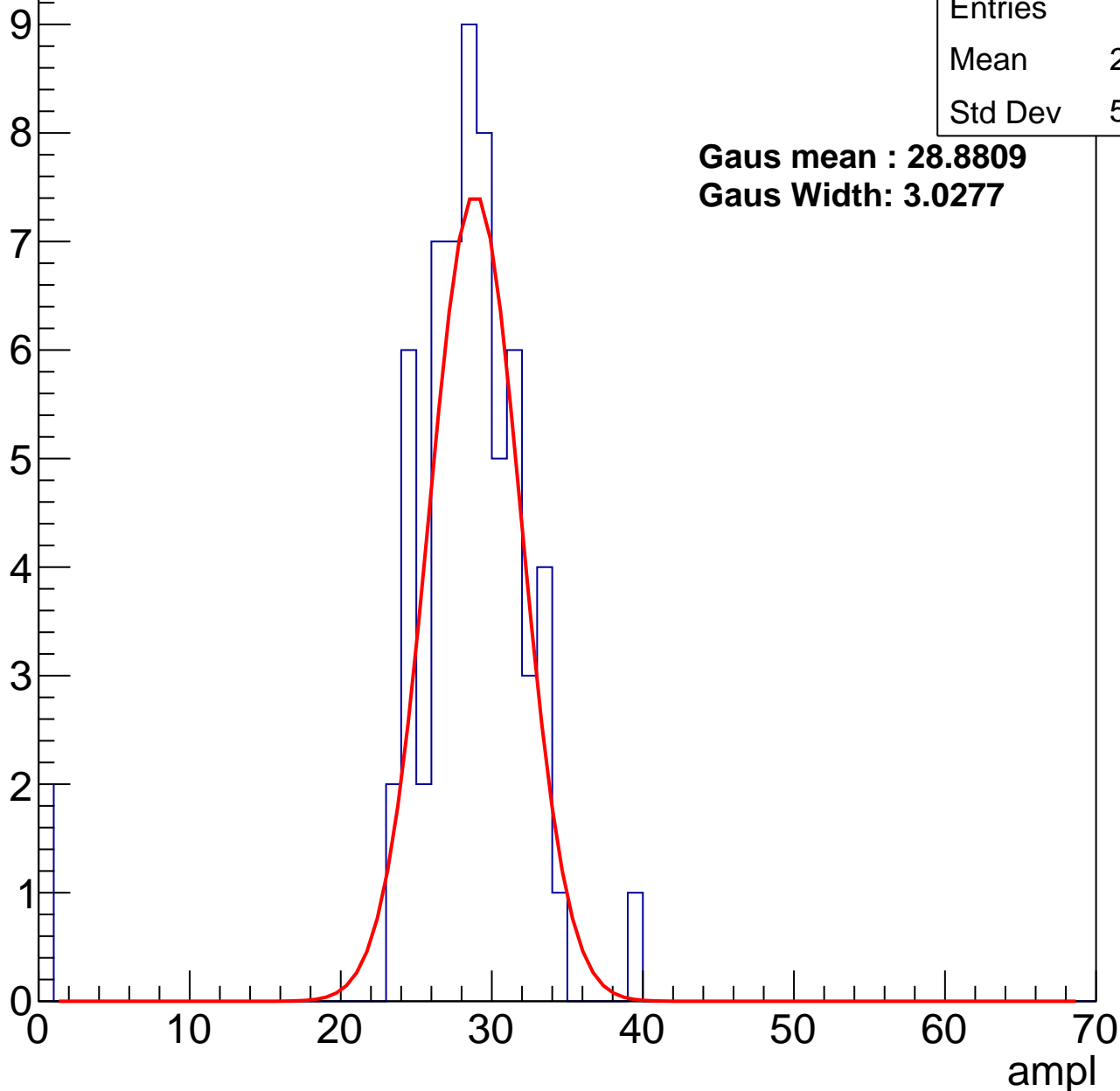
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.49
Std Dev	5.825

Gaus mean : 28.8809

Gaus Width: 3.0277



B1L100S, U19-ch36, adc1

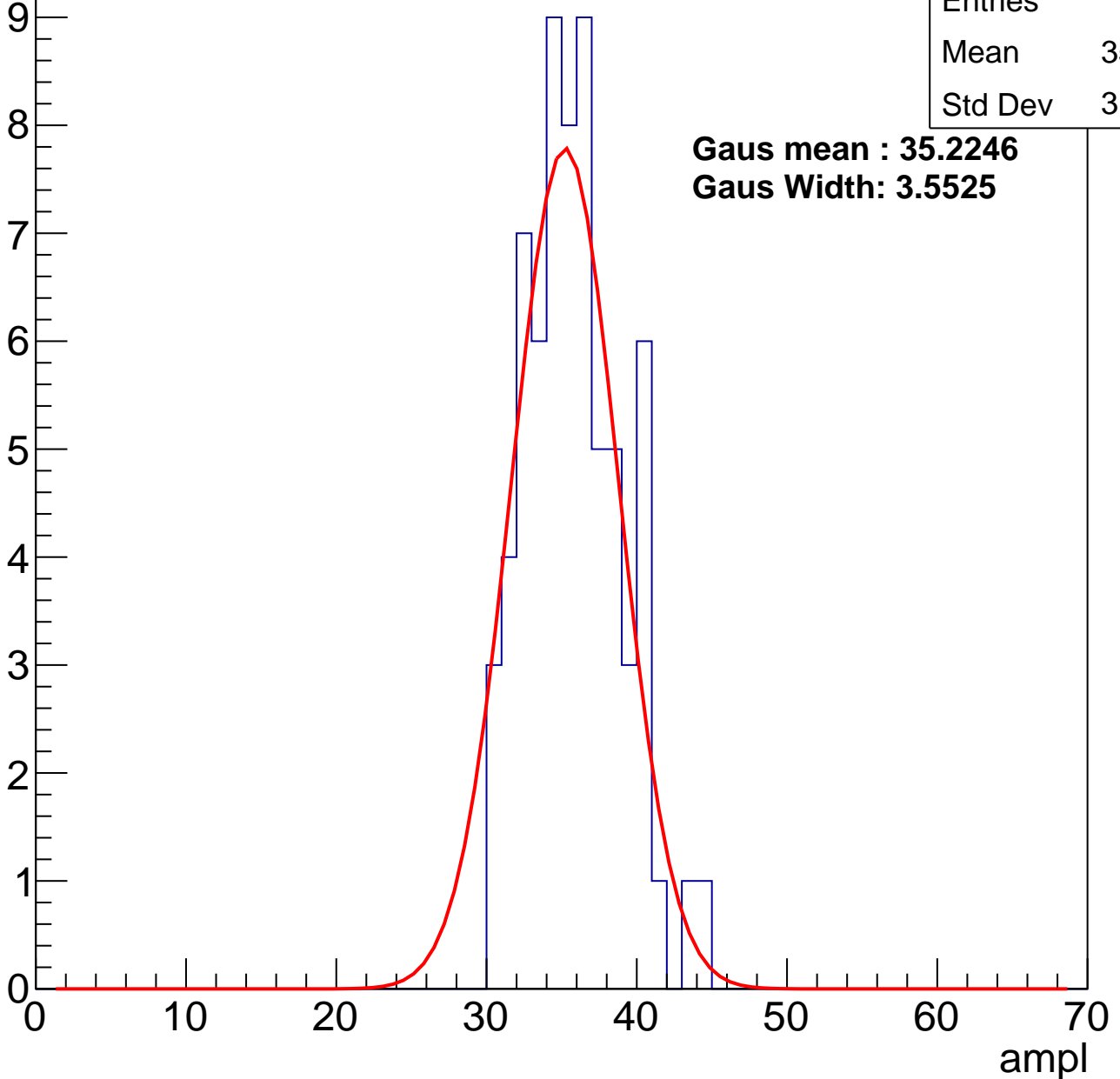
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.38
Std Dev	3.158

Gaus mean : 35.2246

Gaus Width: 3.5525



B1L100S, U19-ch36, adc2

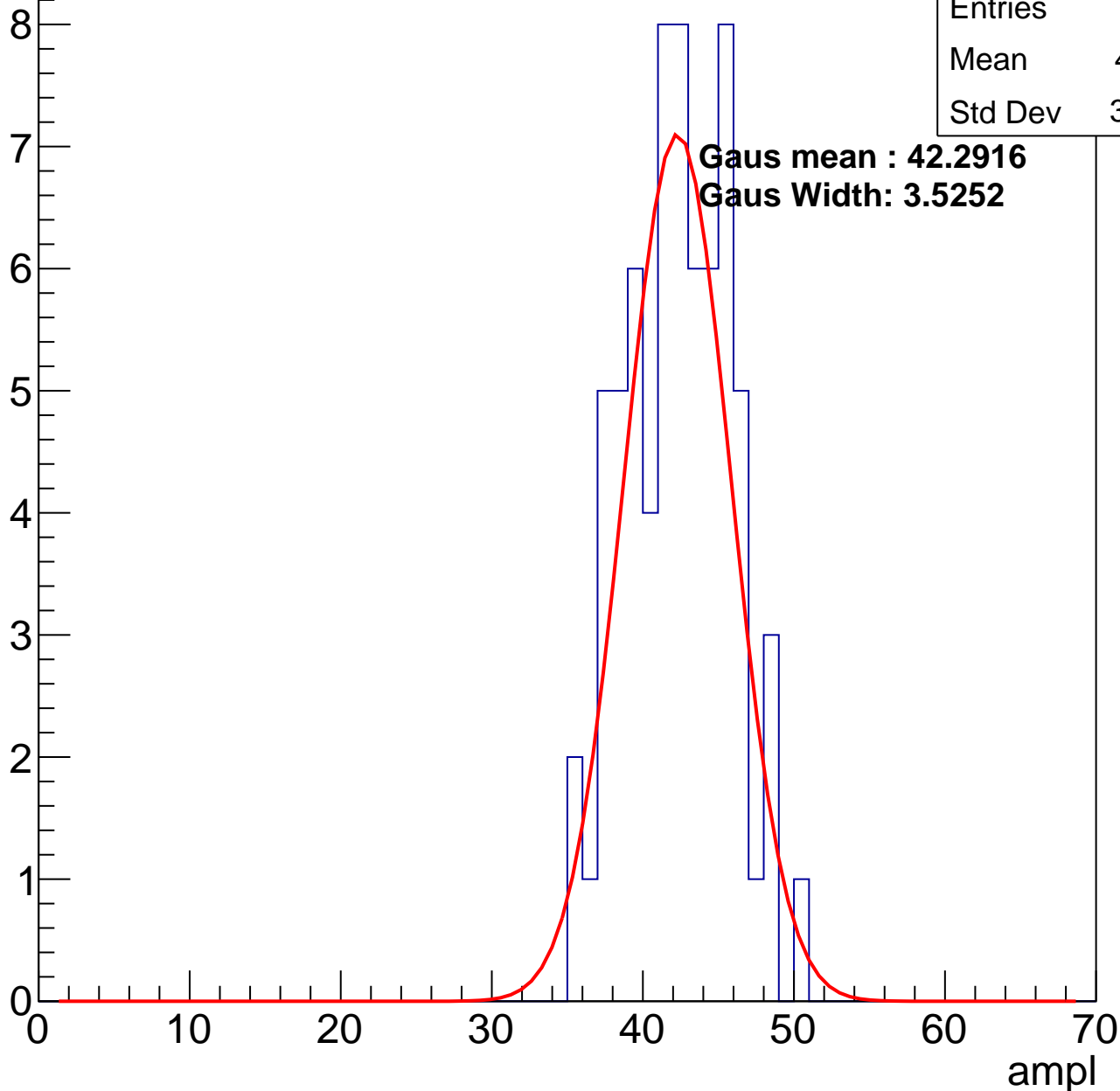
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.91
Std Dev	3.395

Gaus mean : 42.2916

Gaus Width: 3.5252

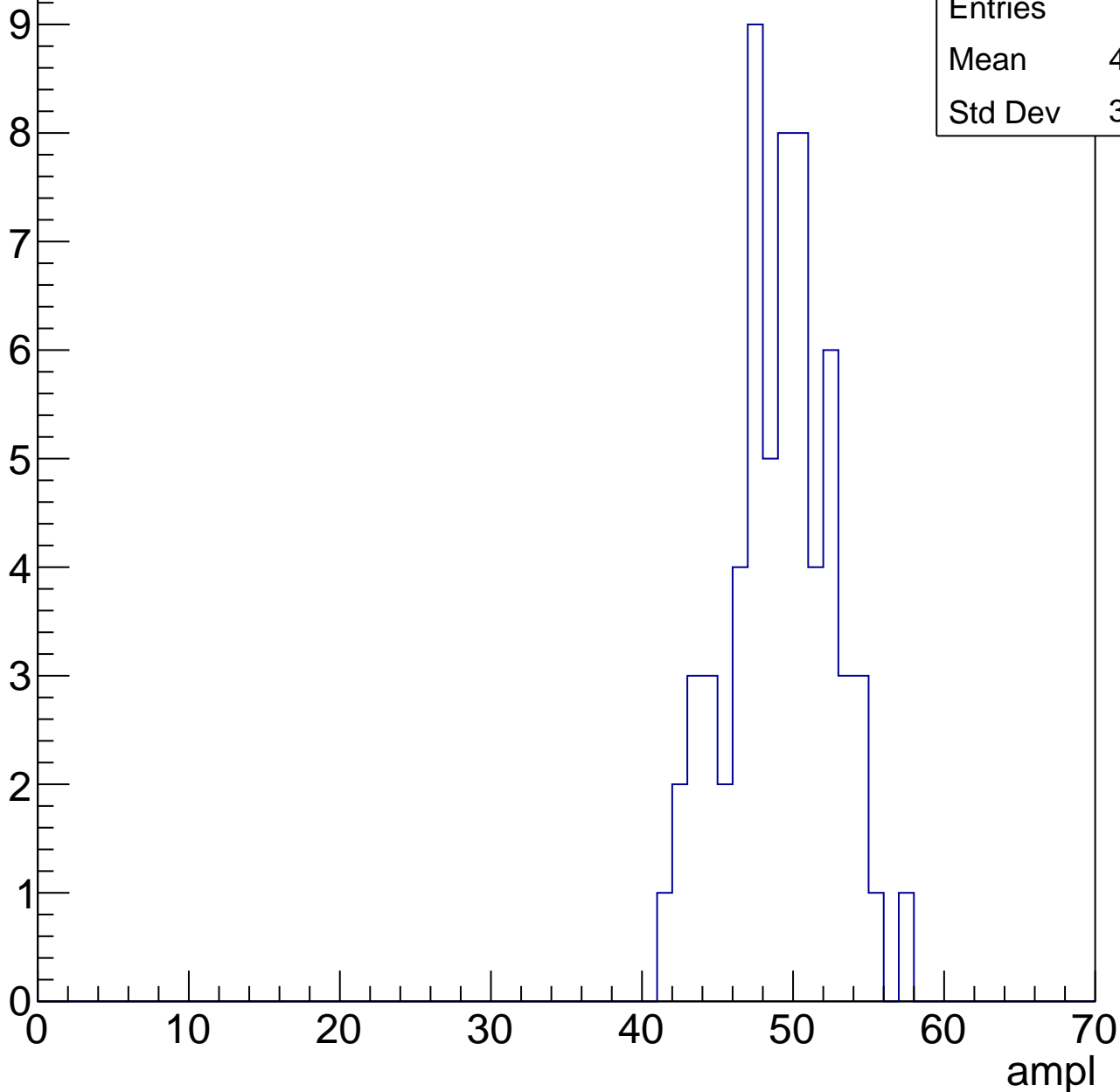


B1L100S, U19-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

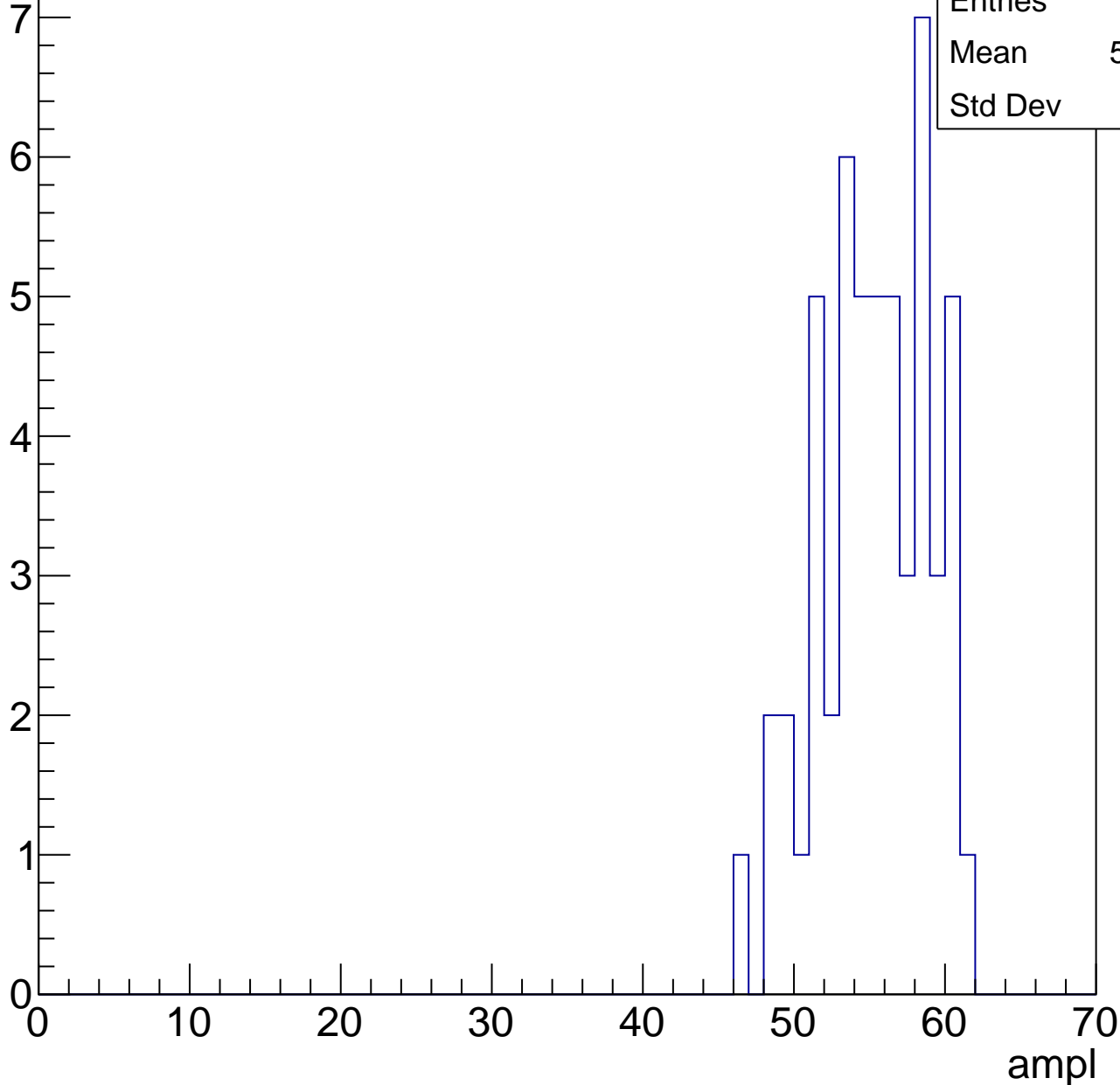
Entries	63
Mean	48.63
Std Dev	3.443



B1L100S, U19-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

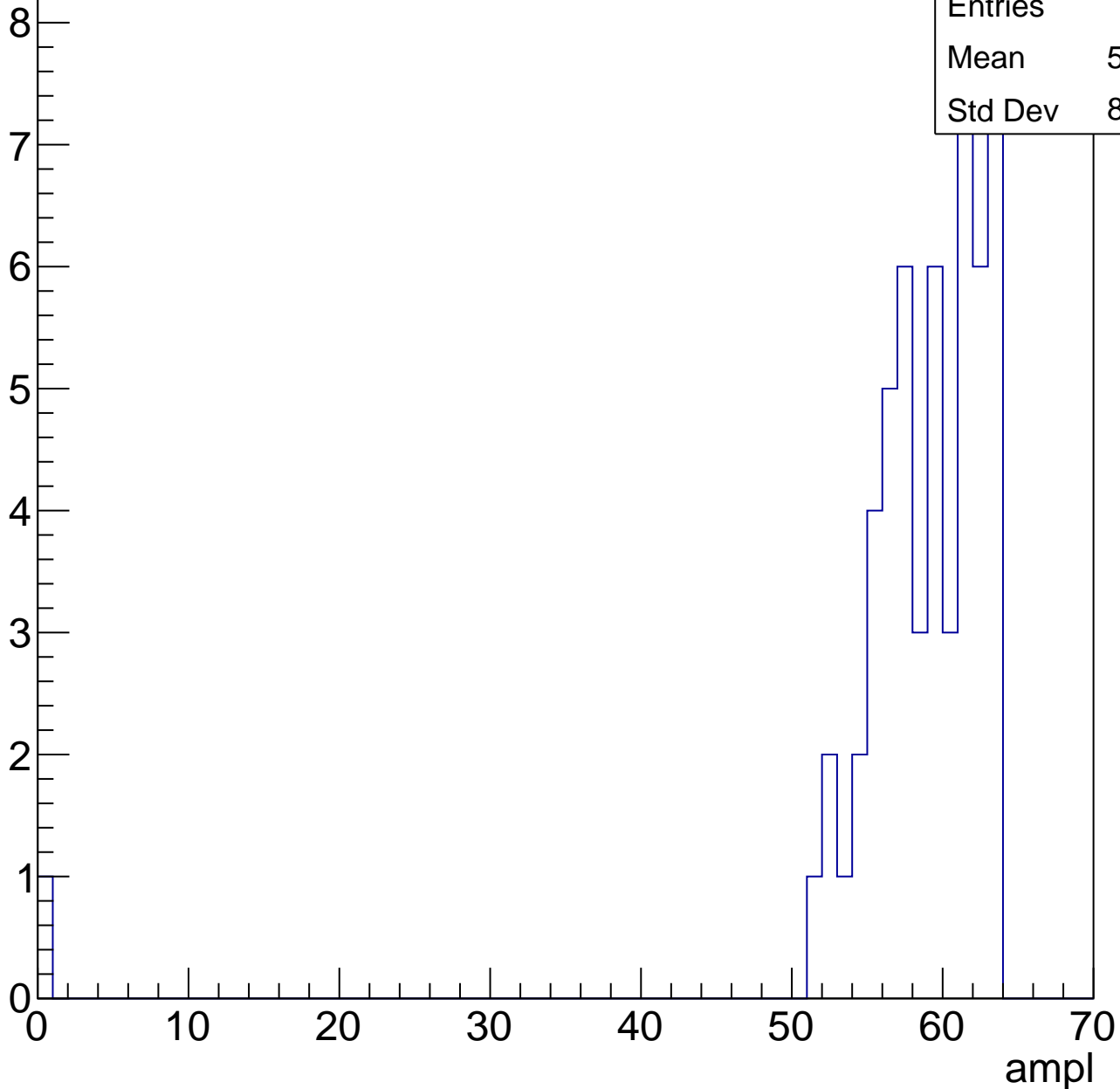


B1L100S, U19-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

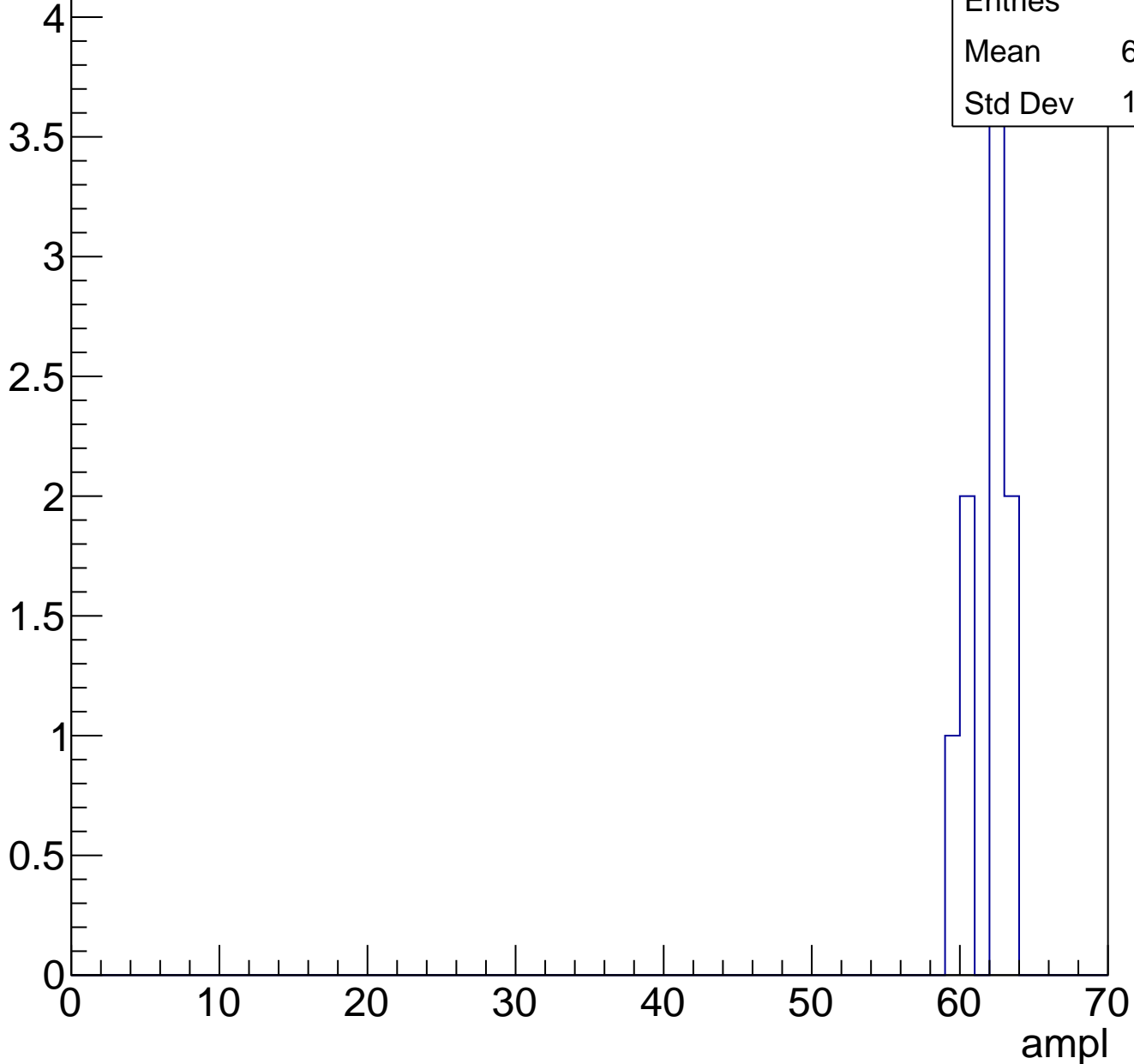
Entries	56
Mean	57.68
Std Dev	8.435



B1L100S, U19-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L100S, U19-ch37, adc0

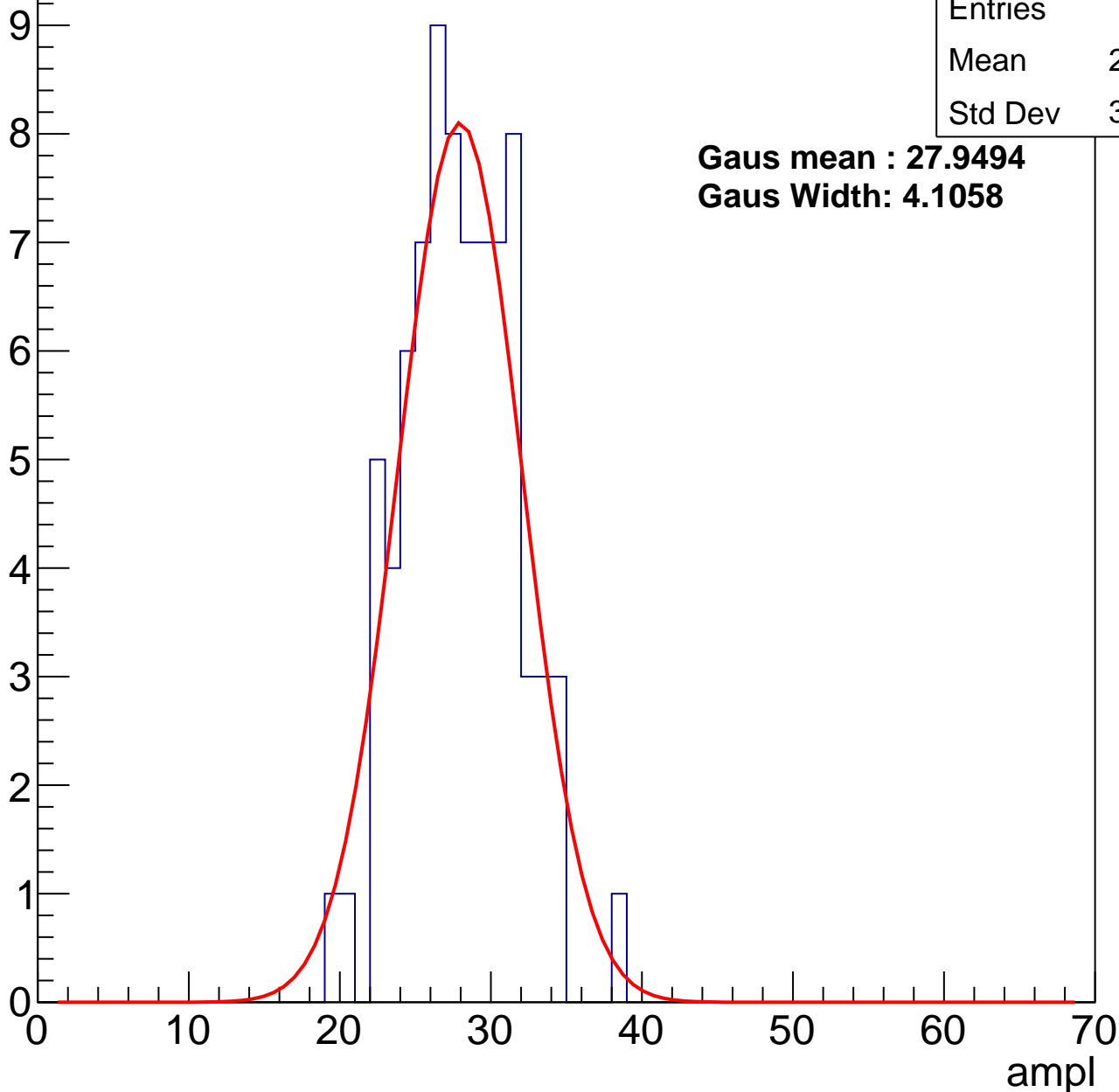
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	27.52
Std Dev	3.619

Gaus mean : 27.9494

Gaus Width: 4.1058



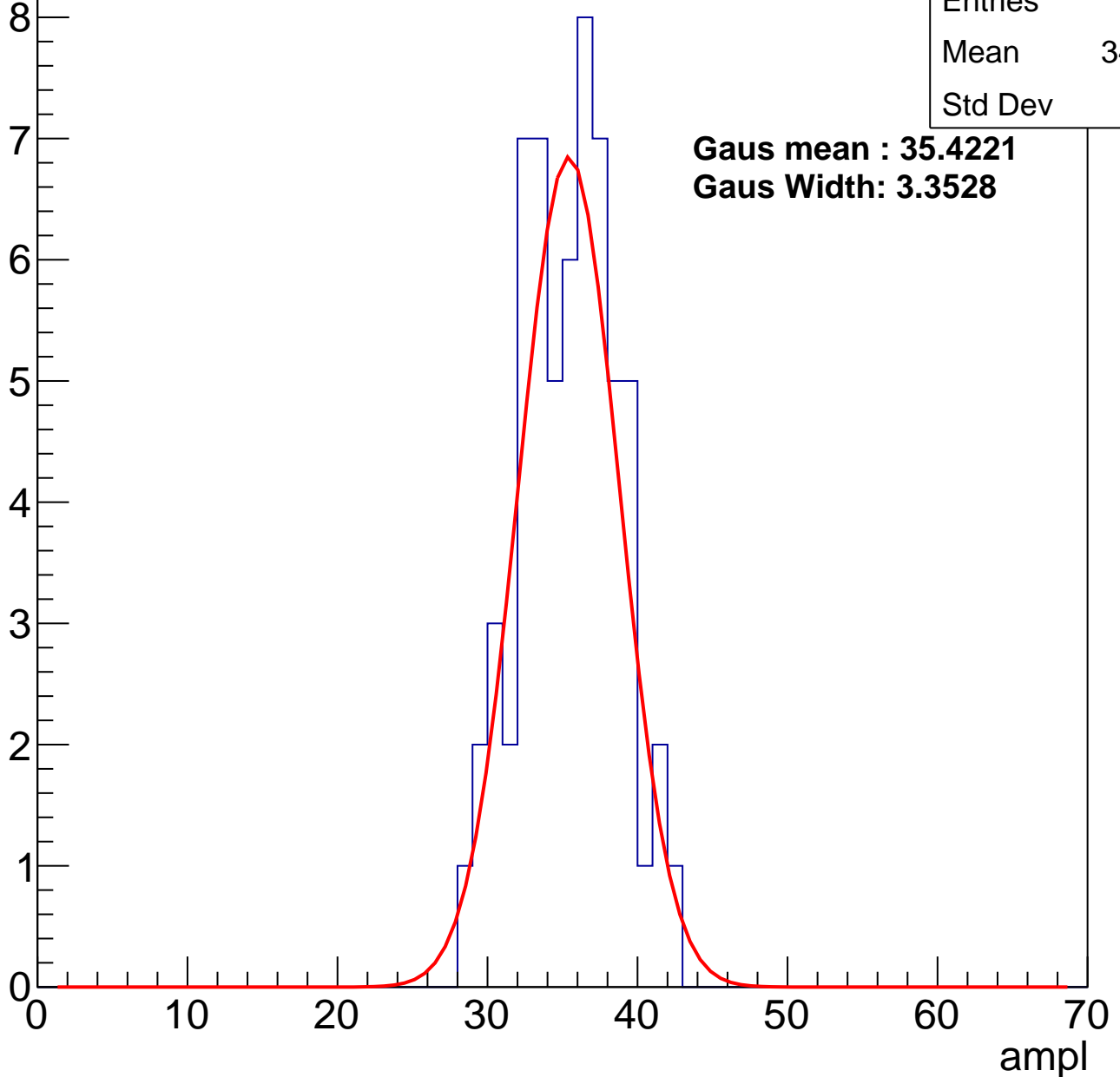
B1L100S, U19-ch37, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	34.98
Std Dev	3.19

Gaus mean : 35.4221
Gaus Width: 3.3528



B1L100S, U19-ch37, adc2

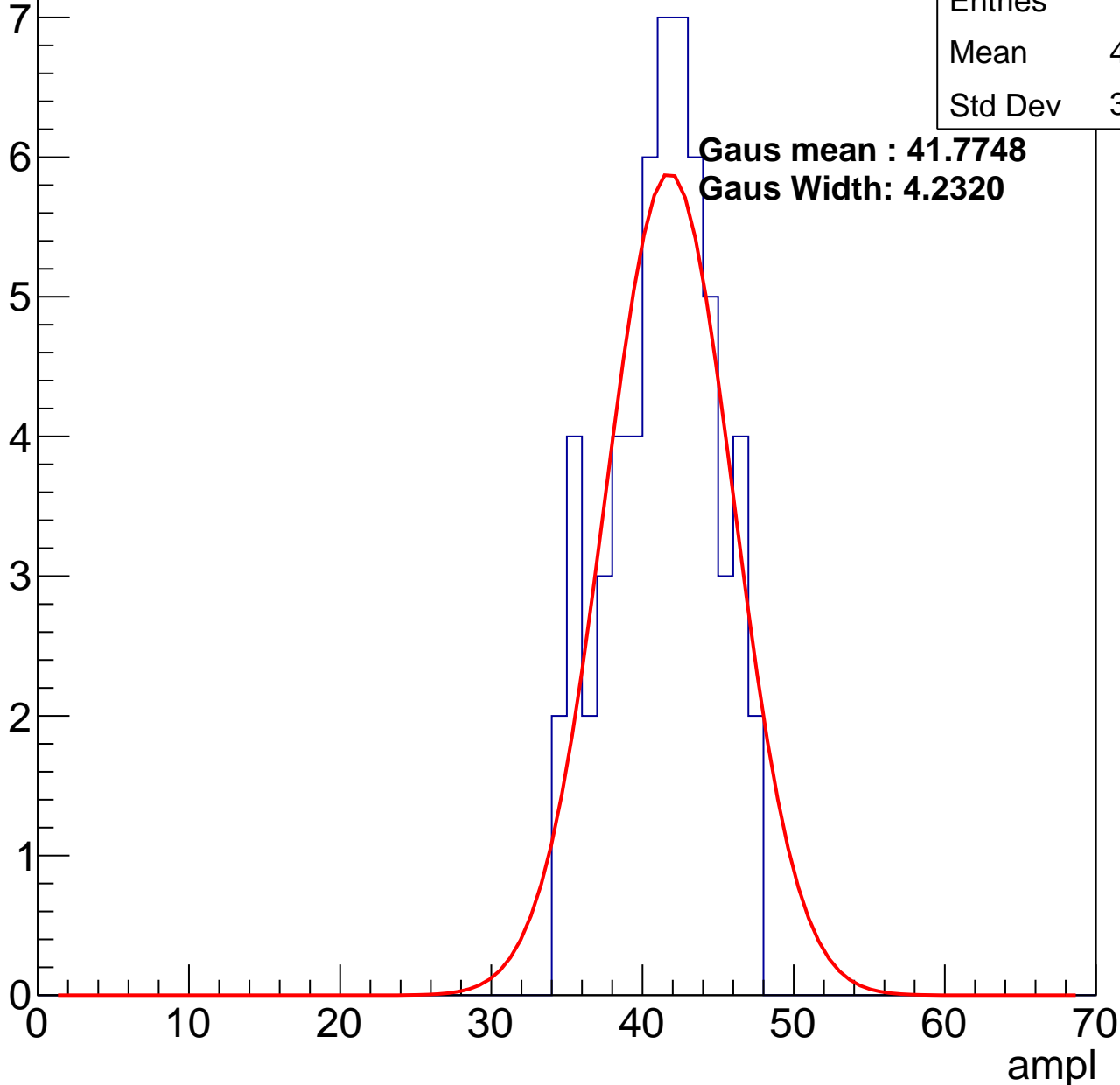
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	40.86
Std Dev	3.427

Gaus mean : 41.7748

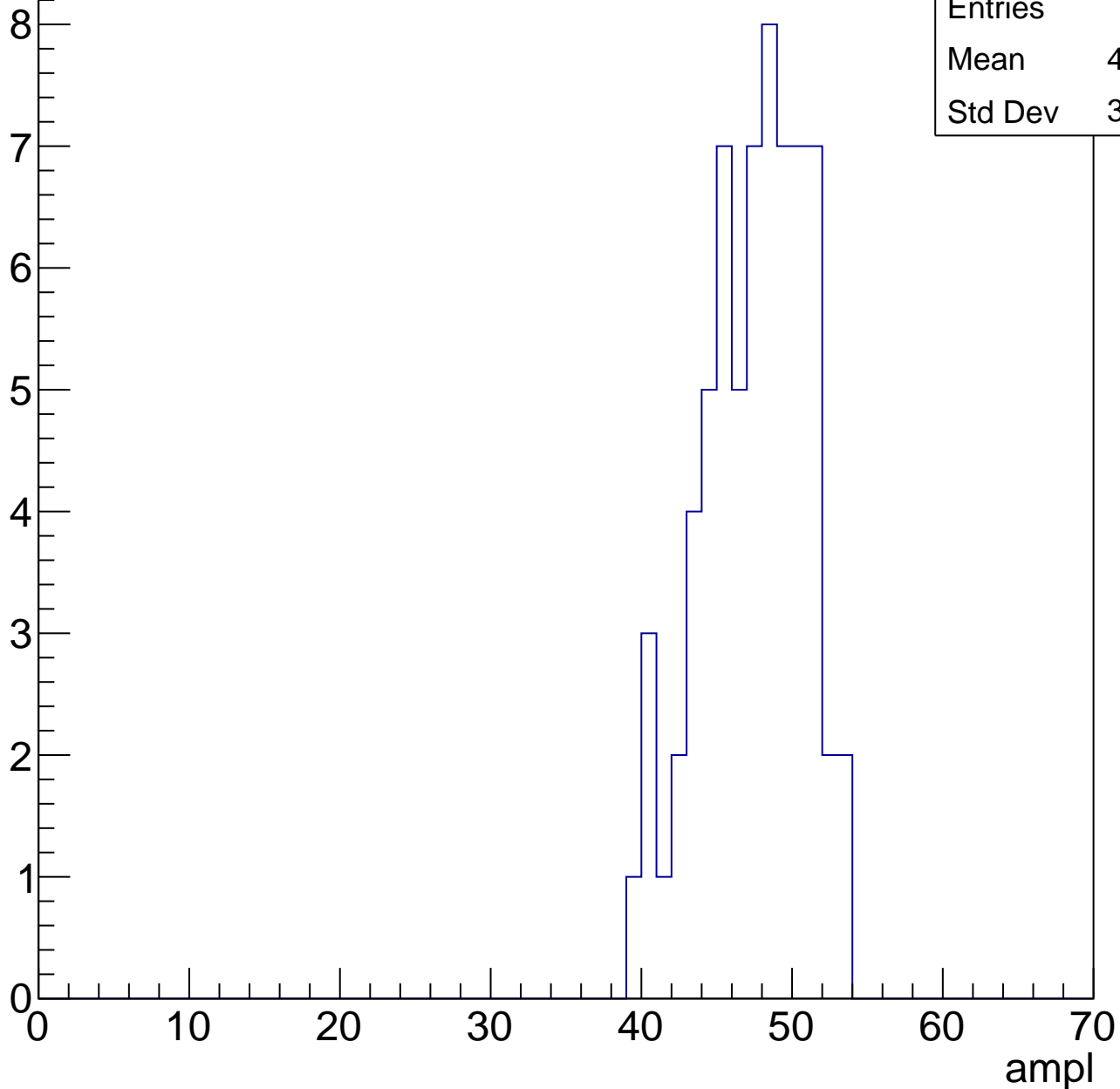
Gaus Width: 4.2320



B1L100S, U19-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



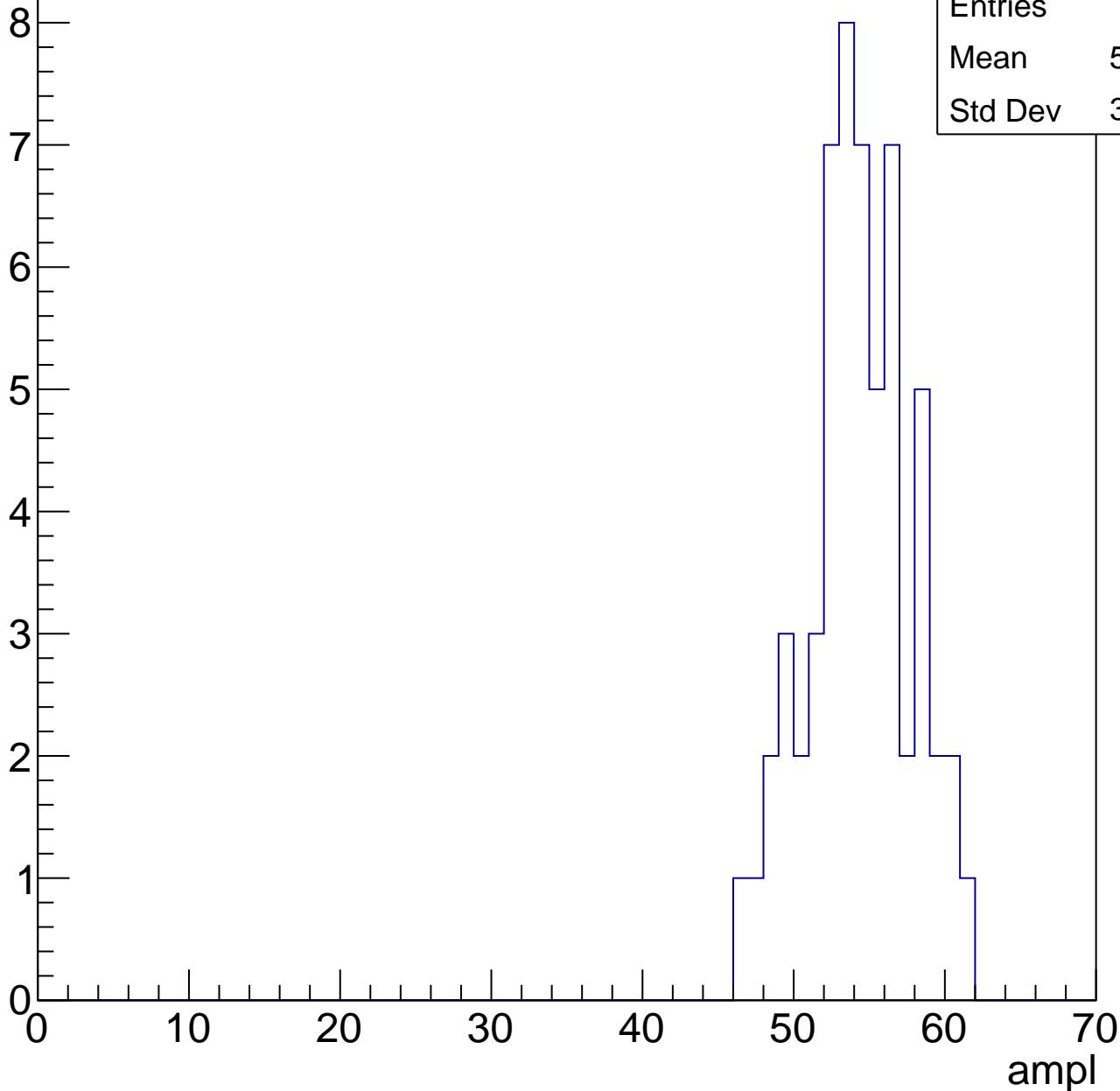
Entries	68
Mean	46.97
Std Dev	3.369

B1L100S, U19-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

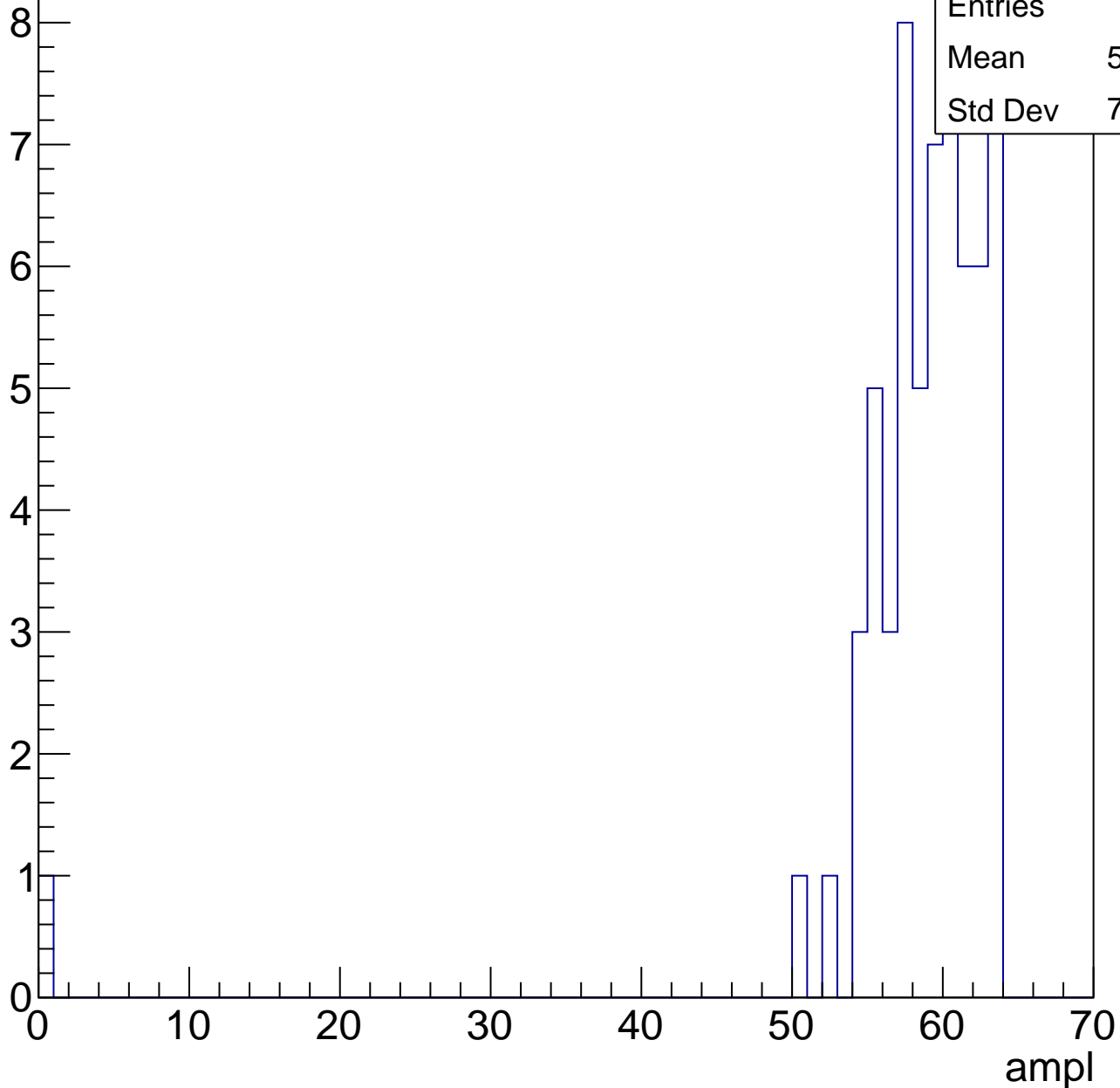
Entries	58
Mean	53.88
Std Dev	3.363



B1L100S, U19-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

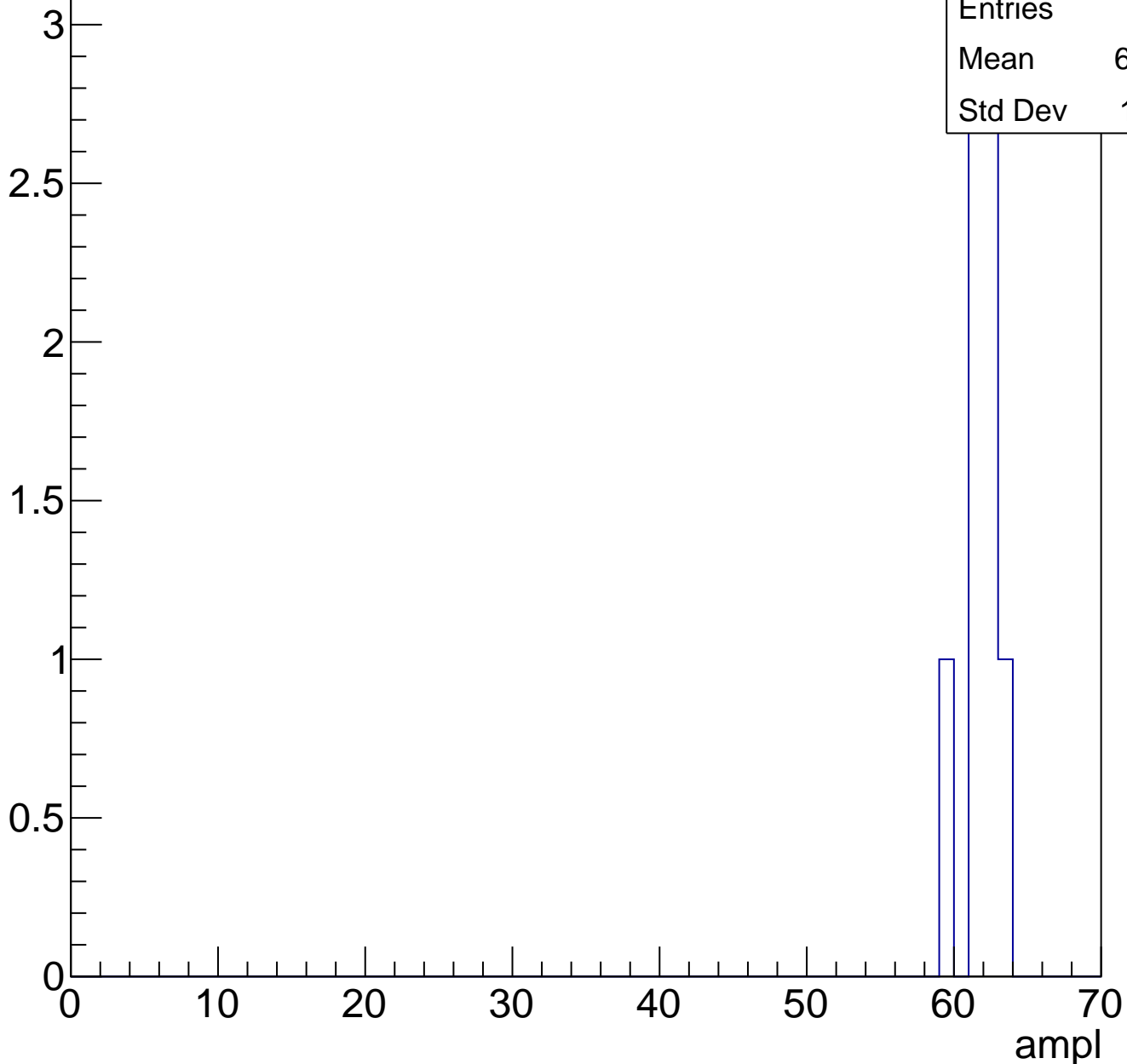
Entry



B1L100S, U19-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

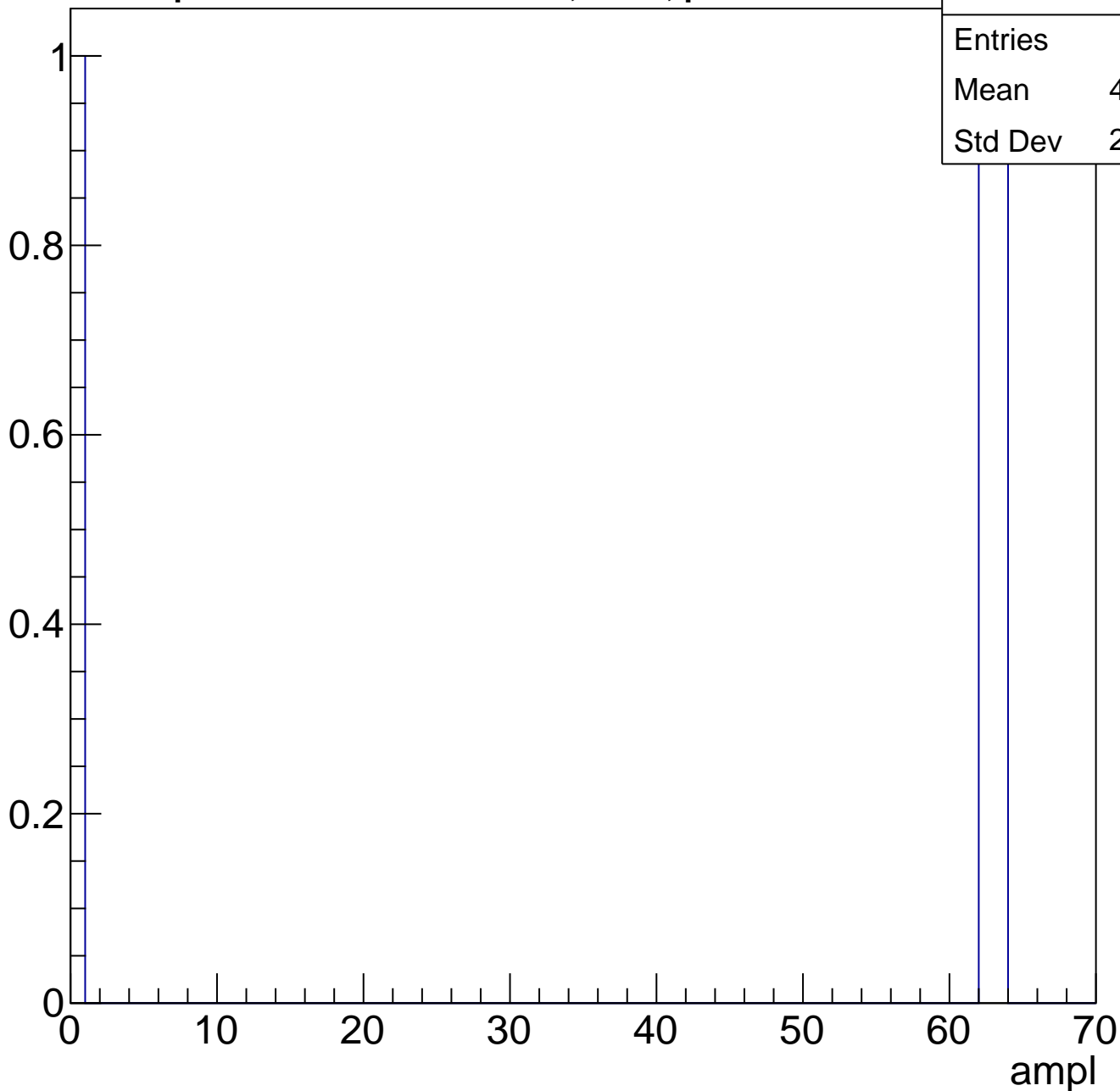
Entry



B1L100S, U19-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch38, adc0

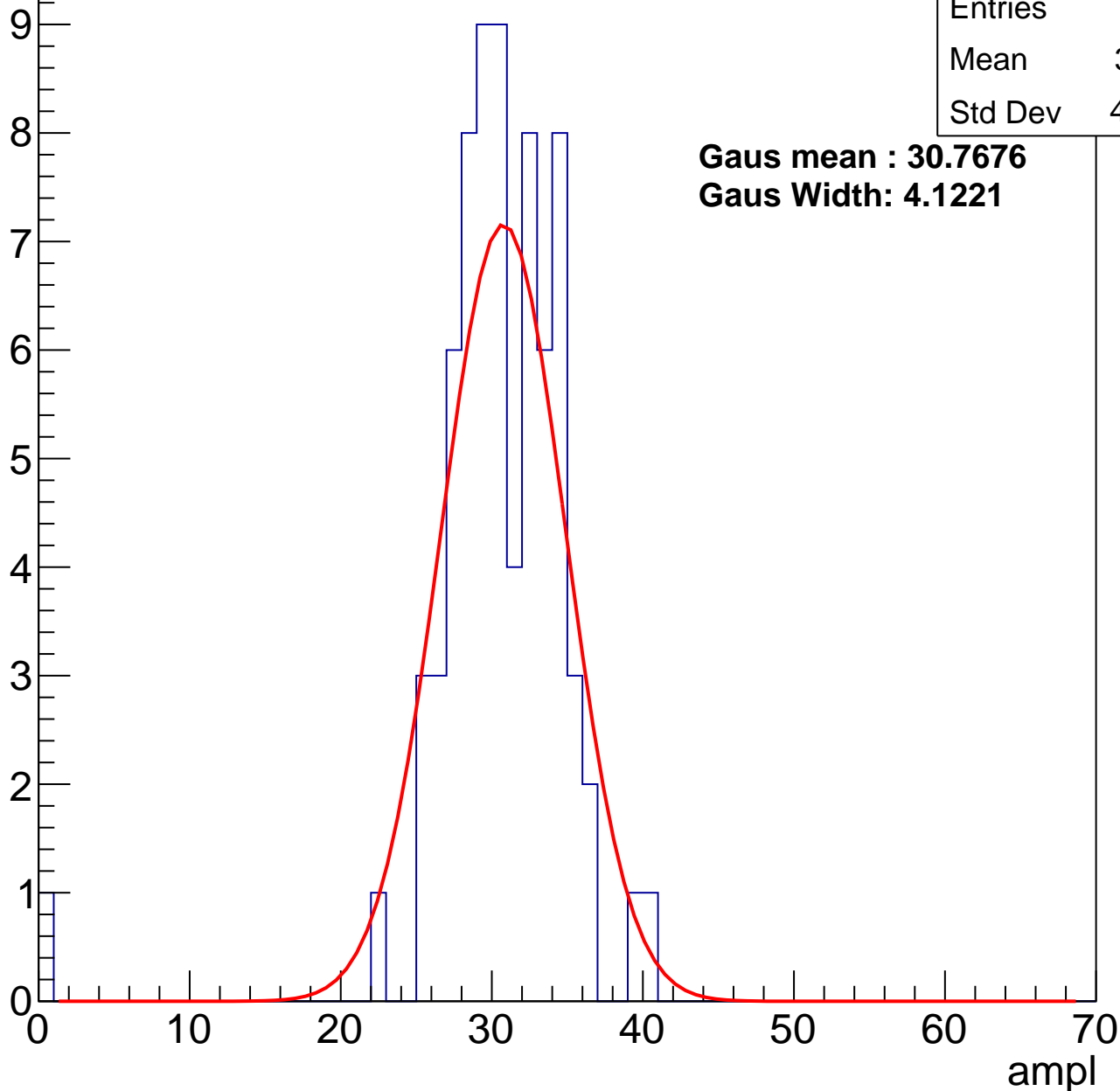
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.11
Std Dev	4.864

Gaus mean : 30.7676

Gaus Width: 4.1221



B1L100S, U19-ch38, adc1

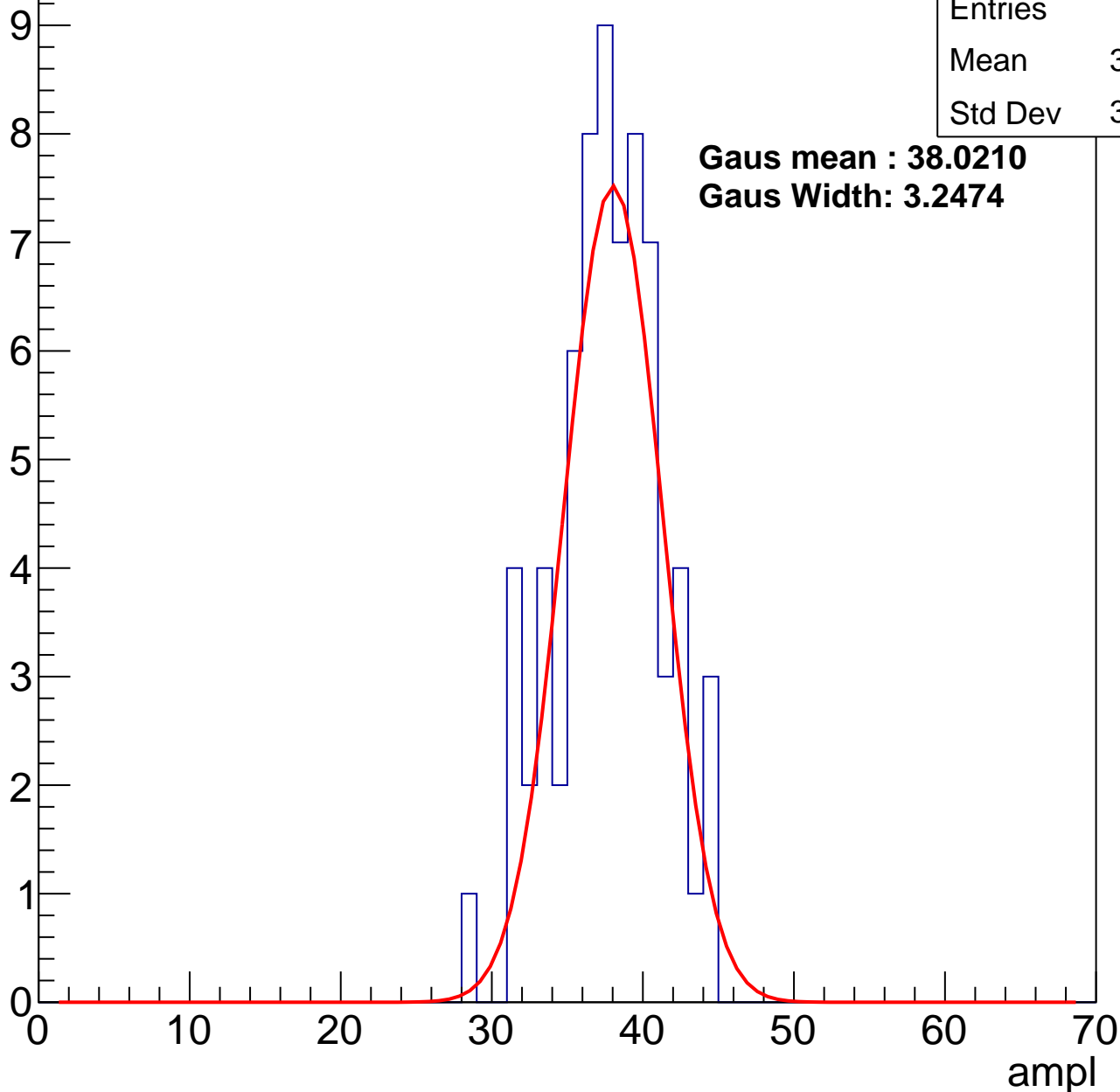
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.26
Std Dev	3.442

Gaus mean : 38.0210

Gaus Width: 3.2474



B1L100S, U19-ch38, adc2

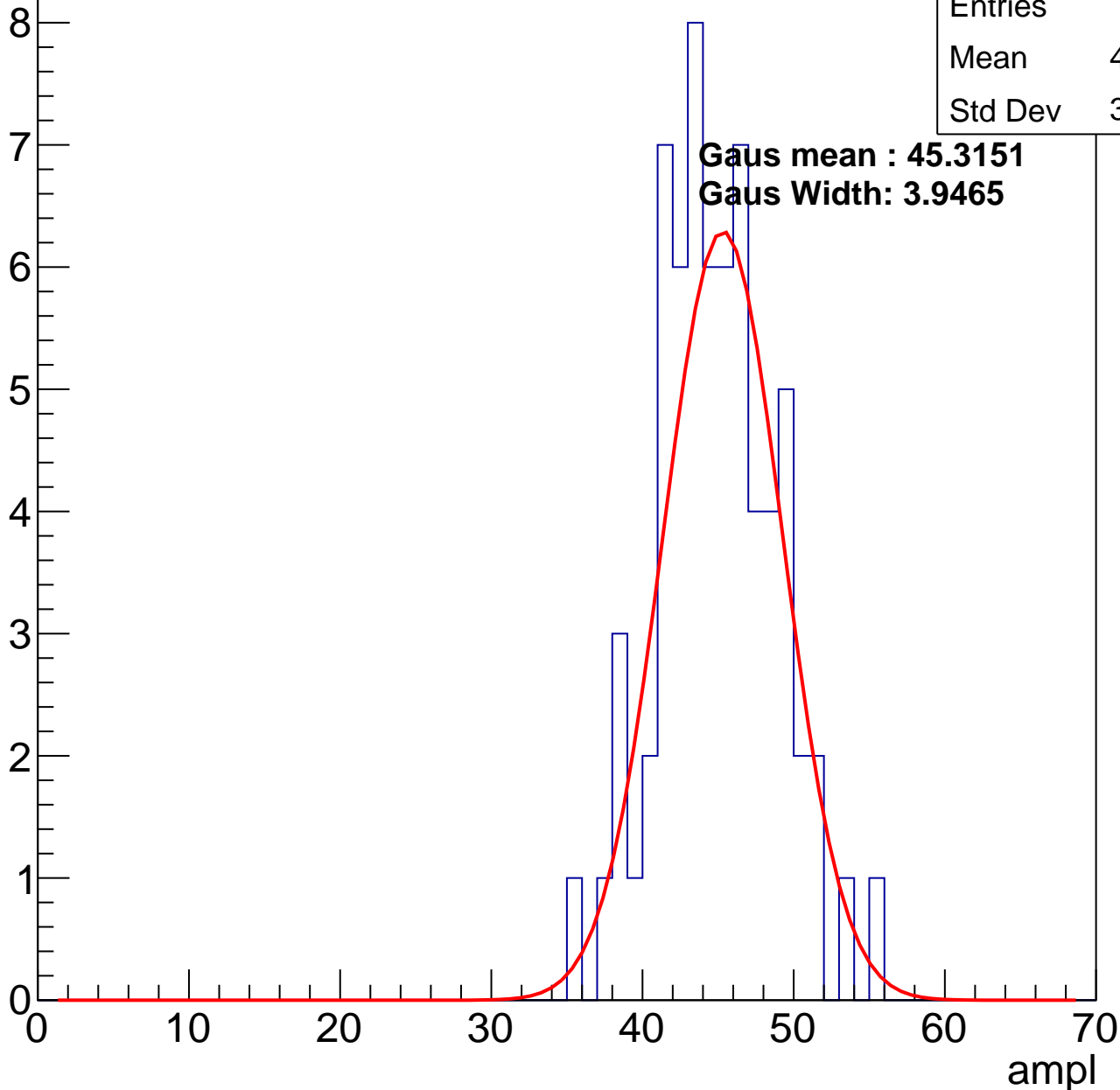
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.46
Std Dev	3.876

Gaus mean : 45.3151

Gaus Width: 3.9465

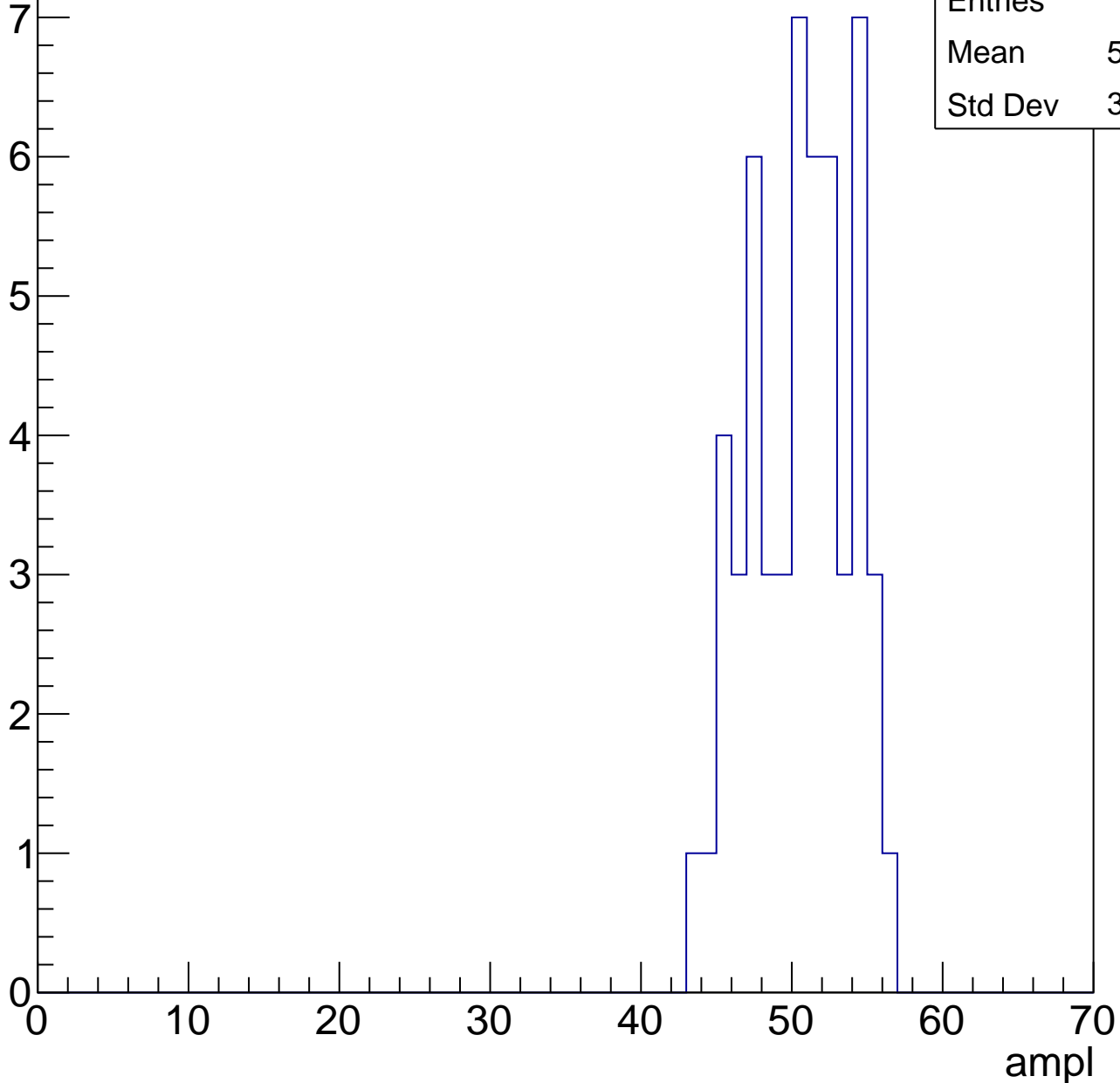


B1L100S, U19-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	50.07
Std Dev	3.282

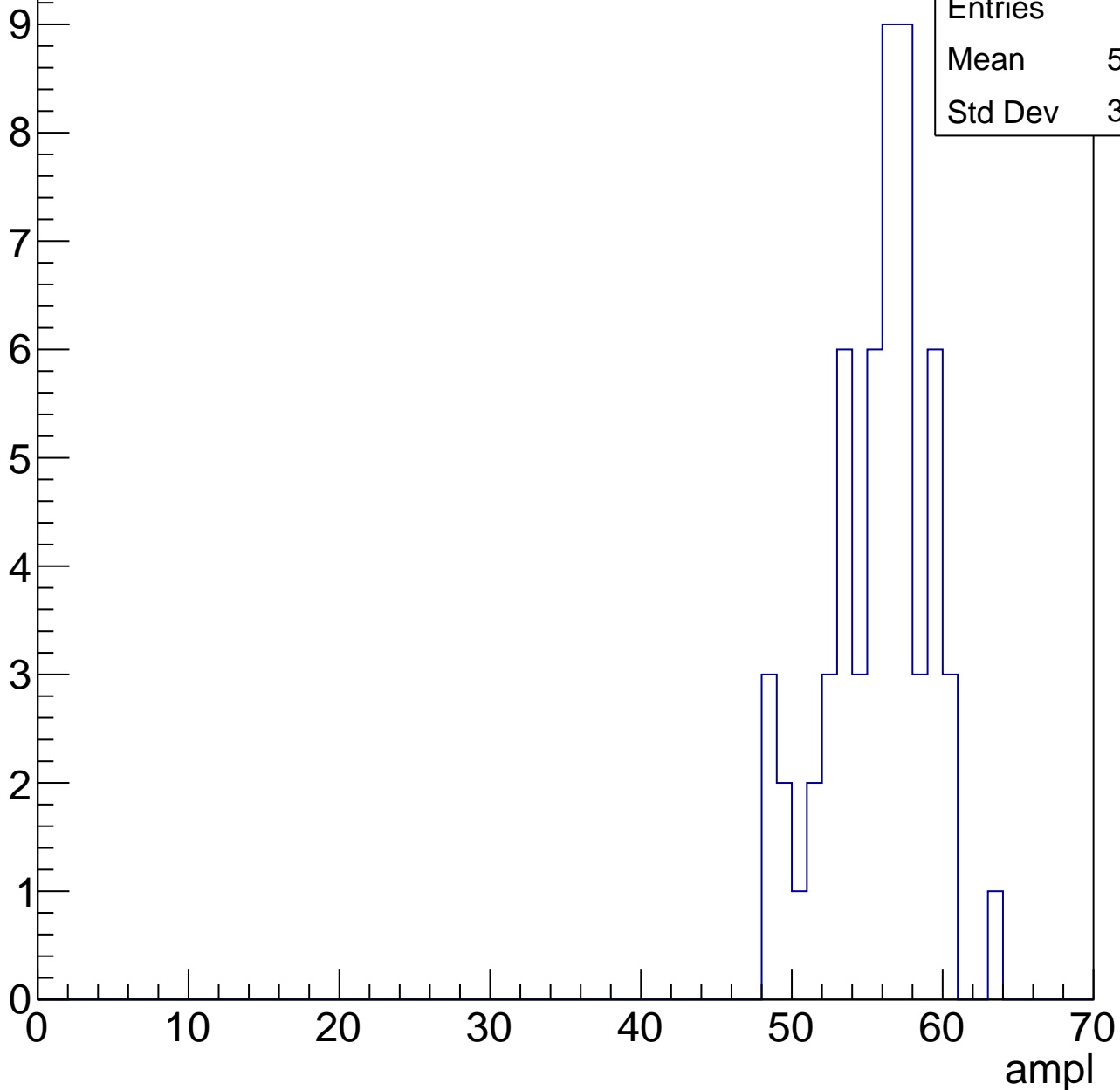


B1L100S, U19-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.23
Std Dev	3.335

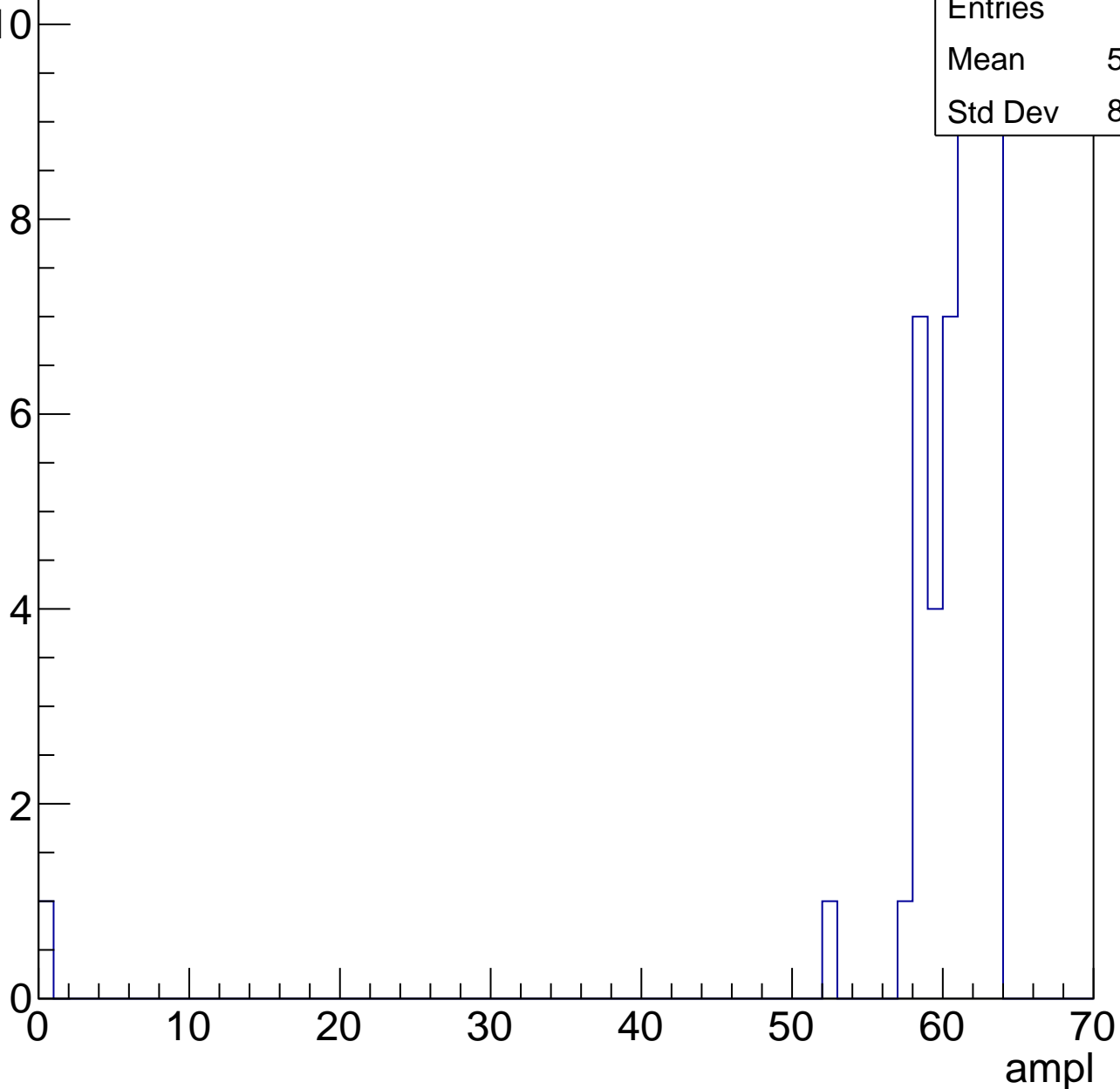


B1L100S, U19-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	59.36
Std Dev	8.736



B1L100S, U19-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

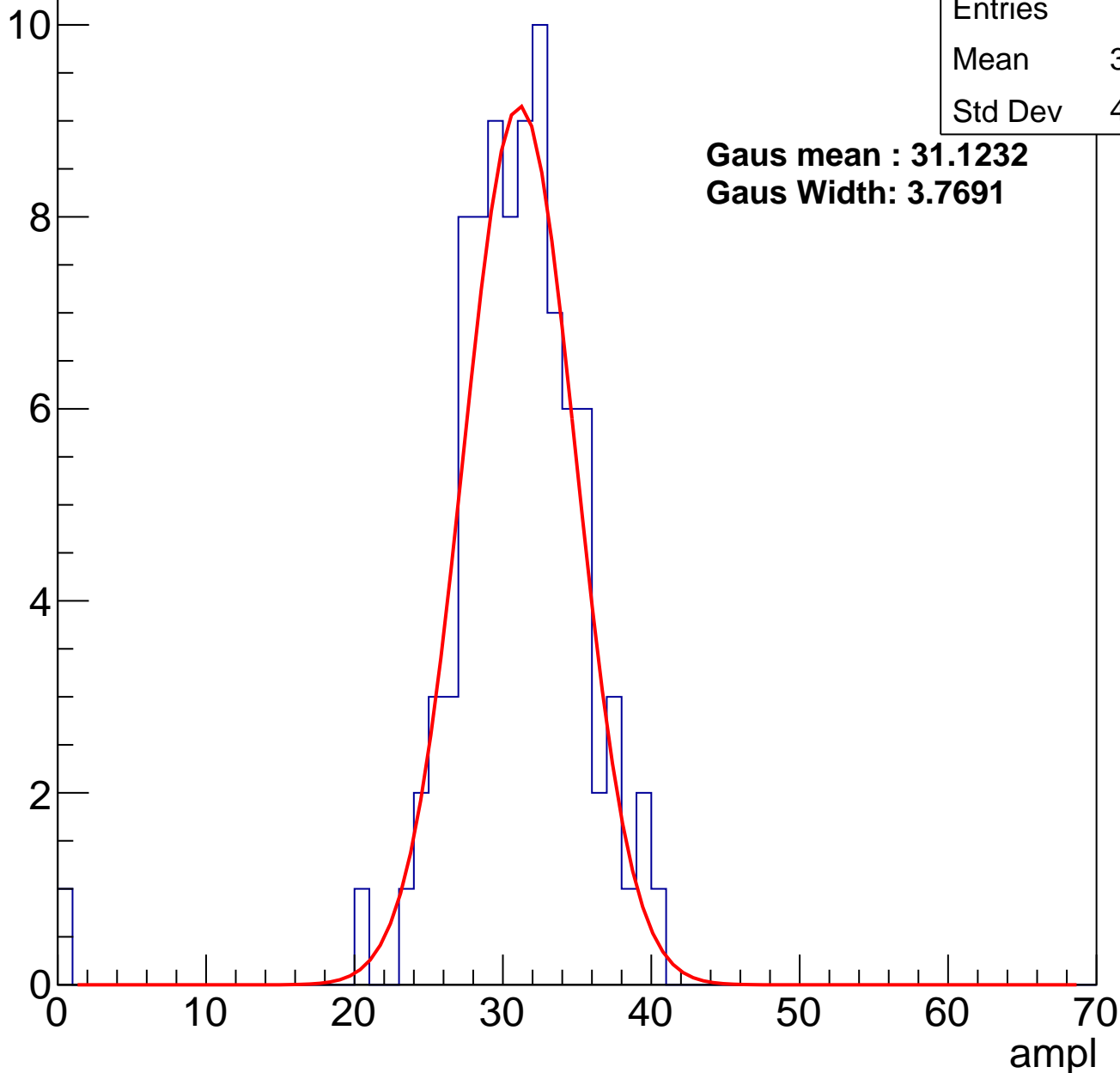
B1L100S, U19-ch39, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	30.42
Std Dev	4.966

Gaus mean : 31.1232
Gaus Width: 3.7691

Entry



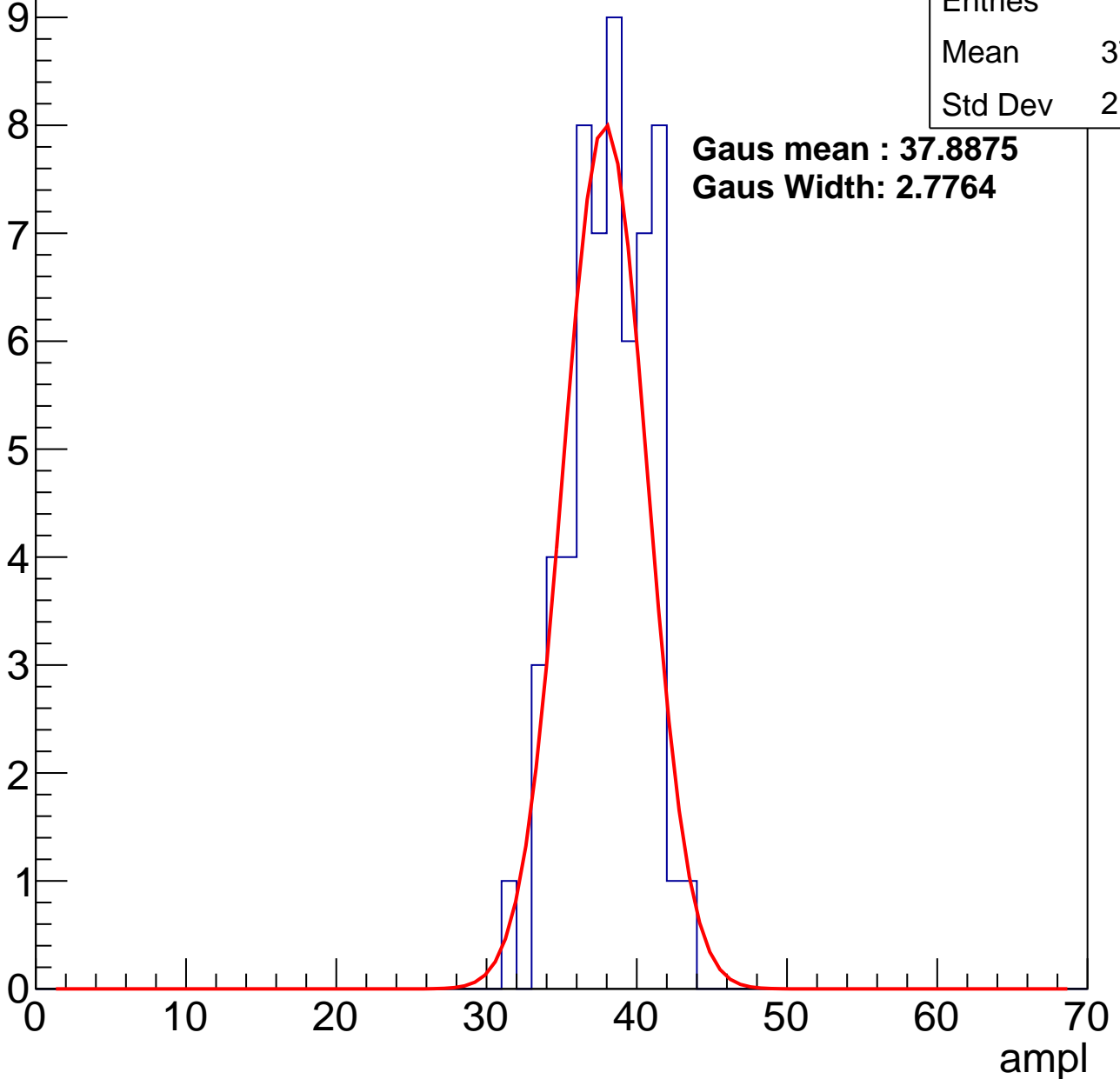
B1L100S, U19-ch39, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

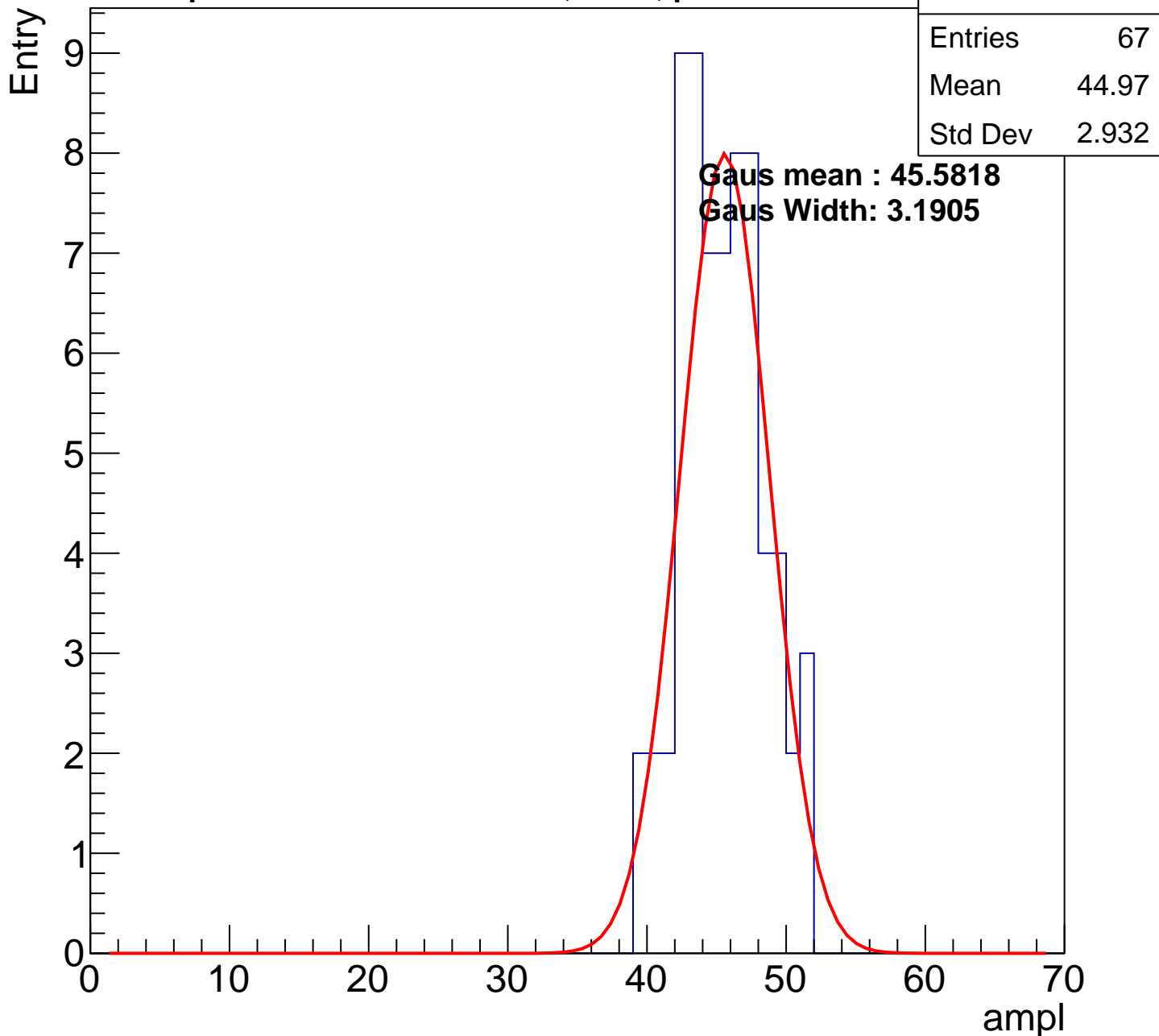
Entries	59
Mean	37.66
Std Dev	2.614

Gaus mean : 37.8875
Gaus Width: 2.7764



B1L100S, U19-ch39, adc2

calib_packv5_042523_0143.root, FC#4, port A2

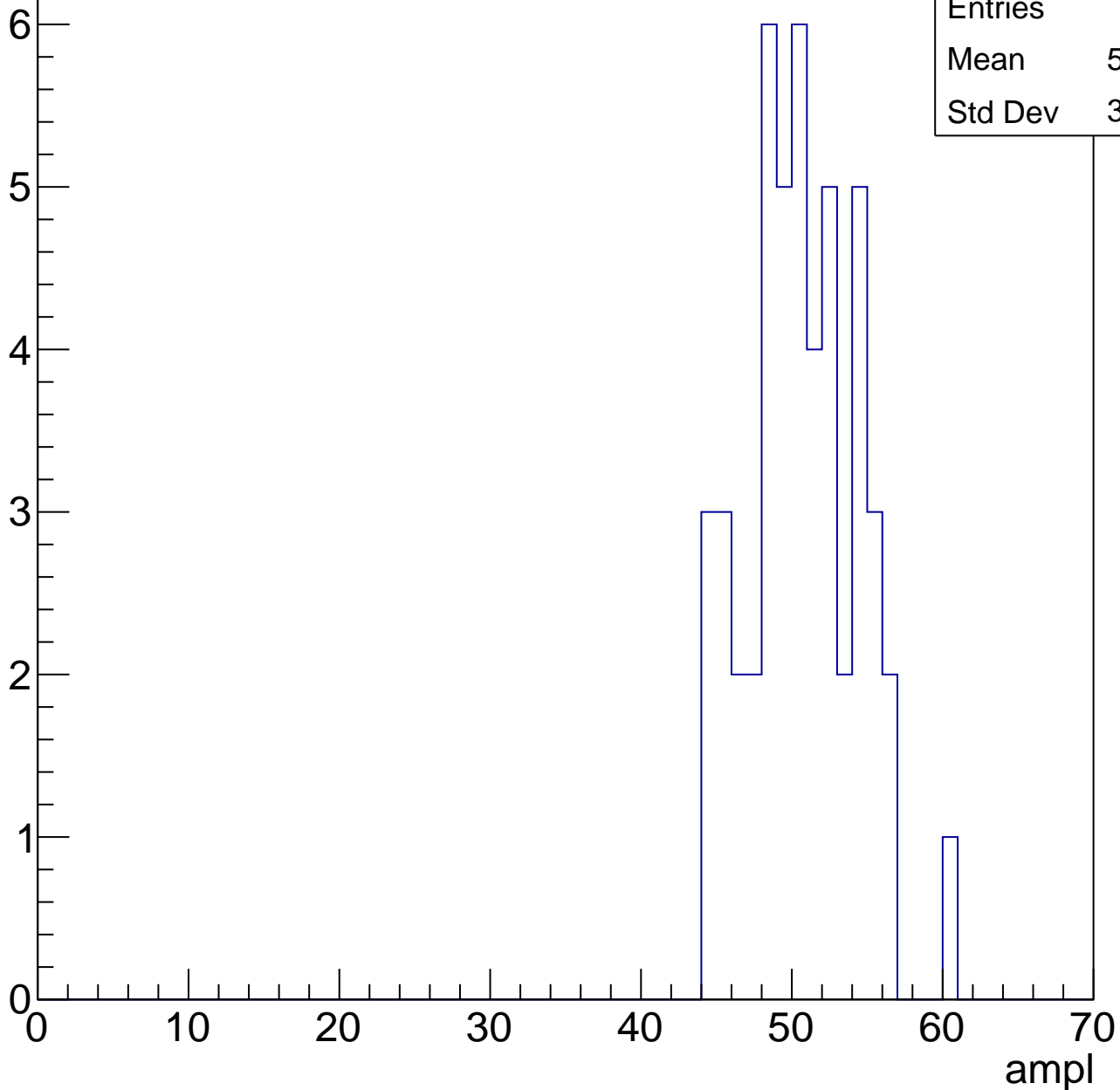


B1L100S, U19-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	50.27
Std Dev	3.579

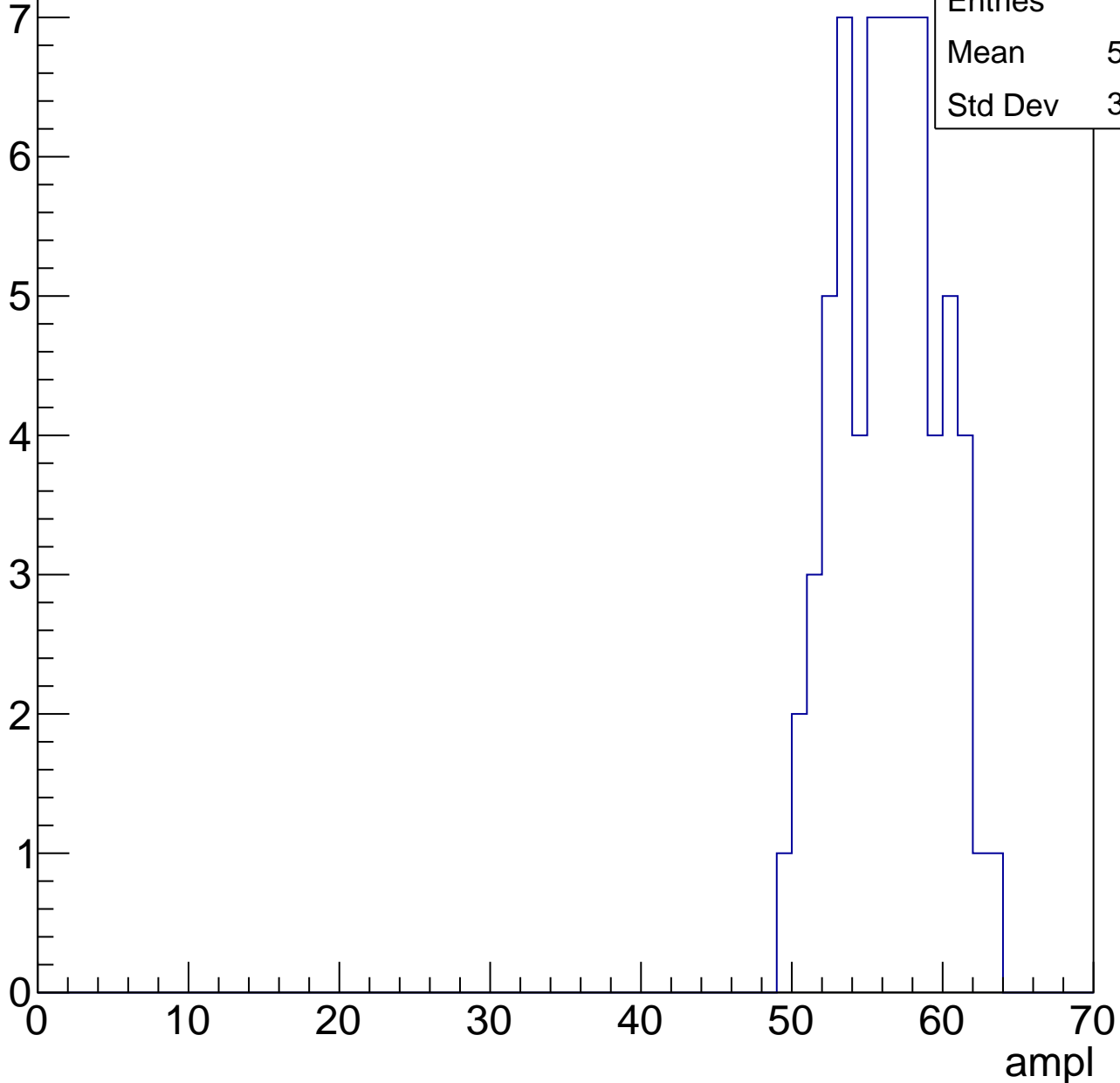


B1L100S, U19-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	55.94
Std Dev	3.276



B1L100S, U19-ch39, adc5

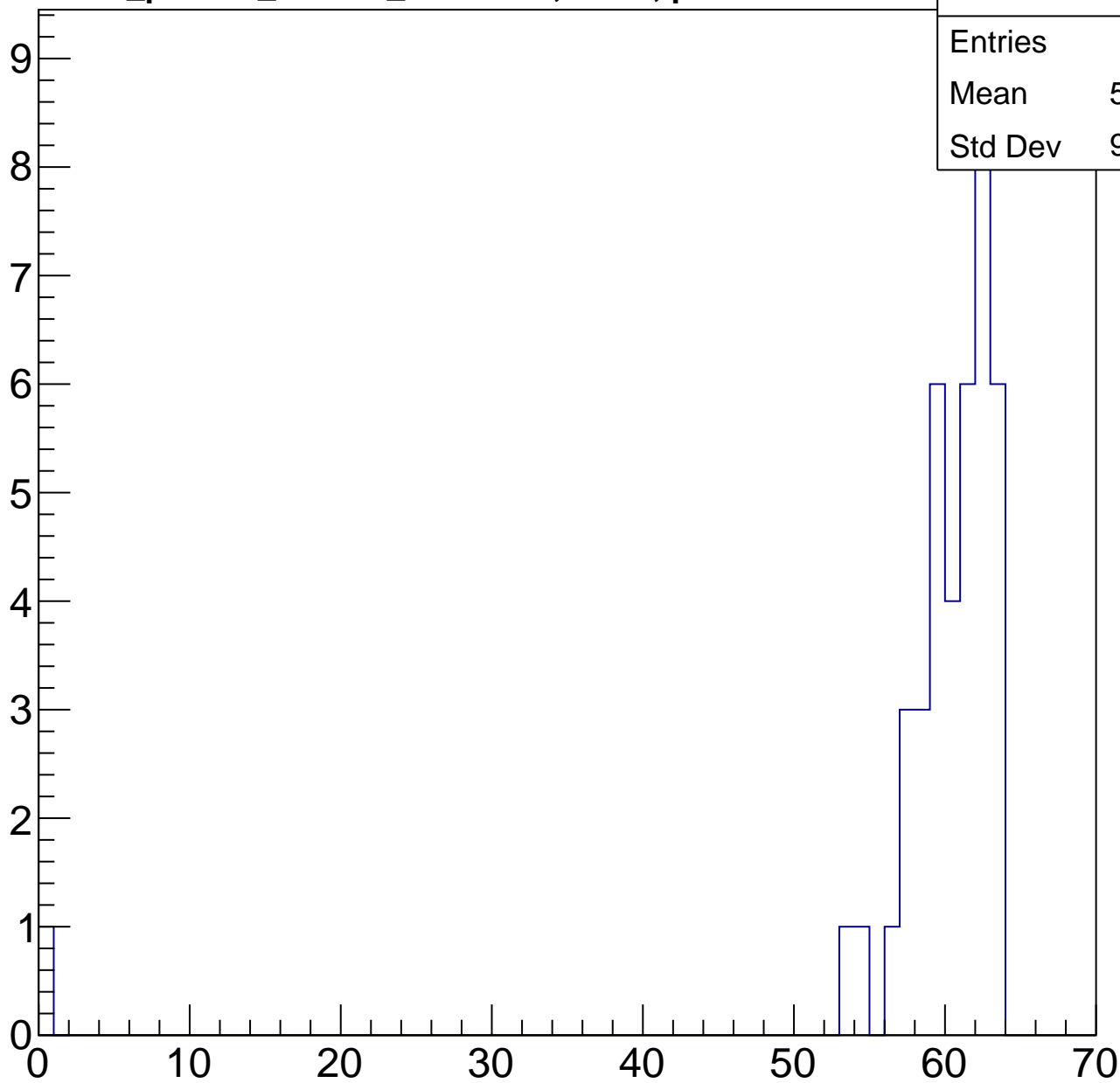
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.63
Std Dev	9.583

ampl



B1L100S, U19-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch40, adc0

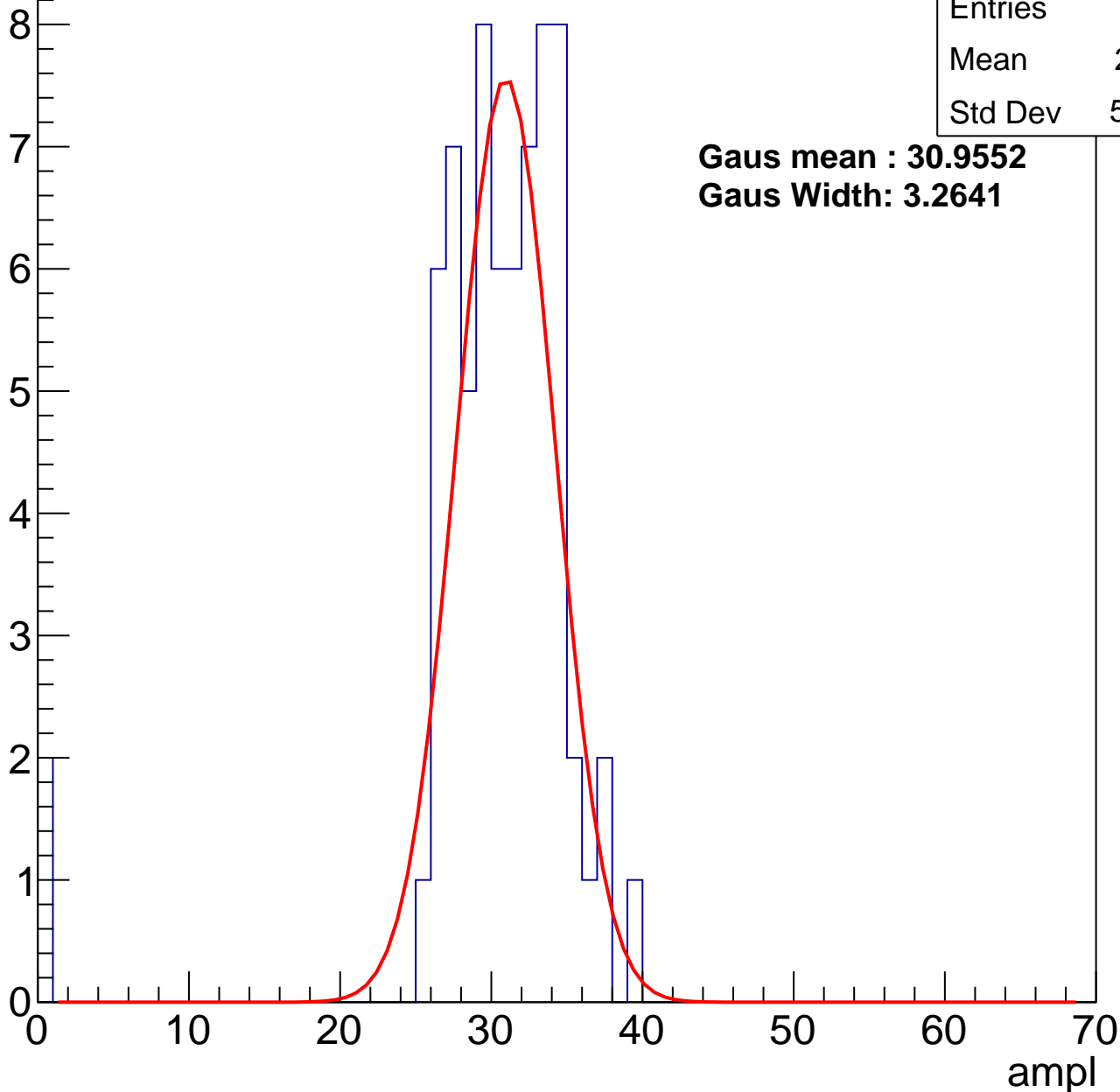
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.81
Std Dev	5.986

Gaus mean : 30.9552

Gaus Width: 3.2641



B1L100S, U19-ch40, adc1

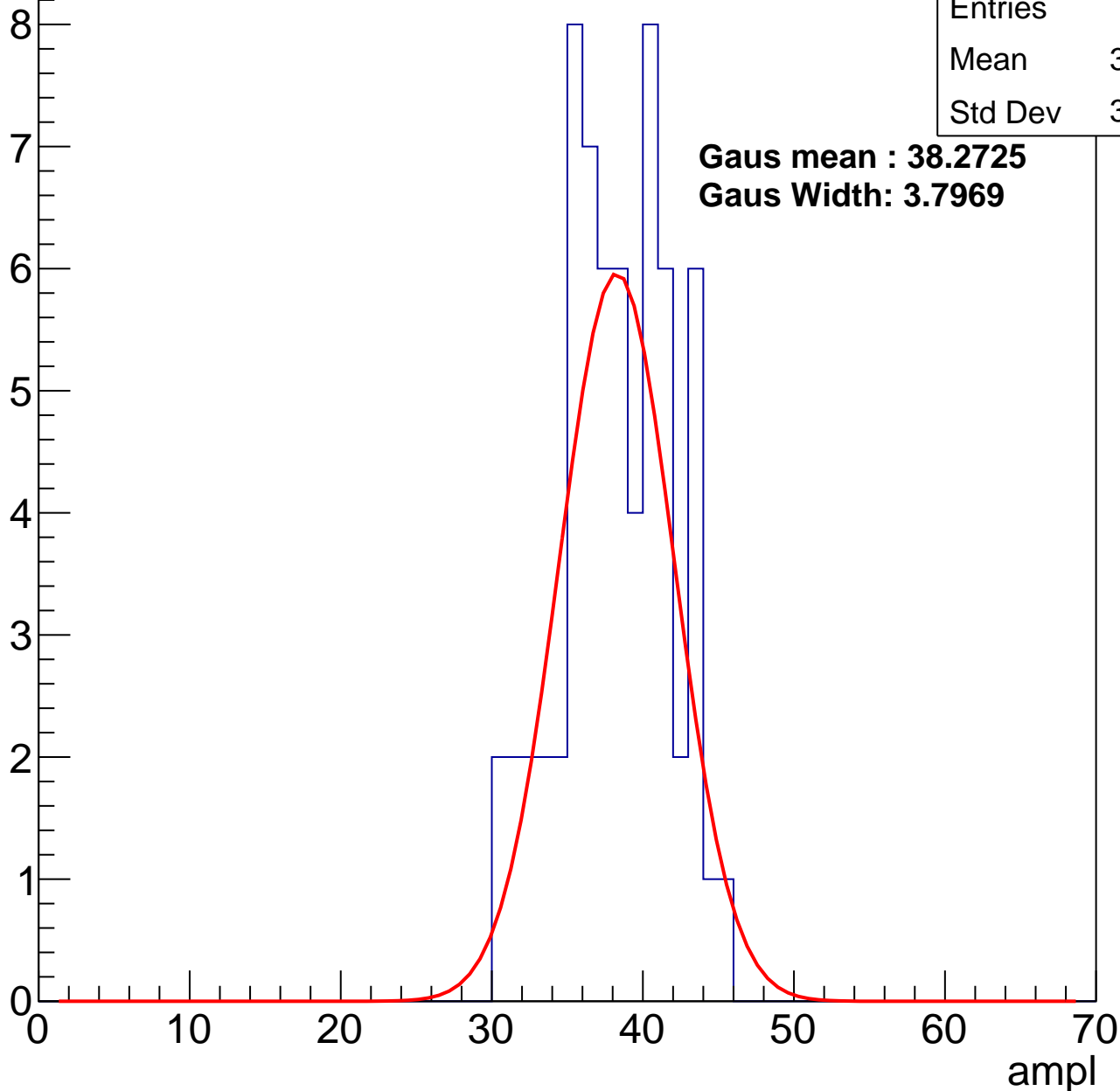
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.77
Std Dev	3.594

Gaus mean : 38.2725

Gaus Width: 3.7969



B1L100S, U19-ch40, adc2

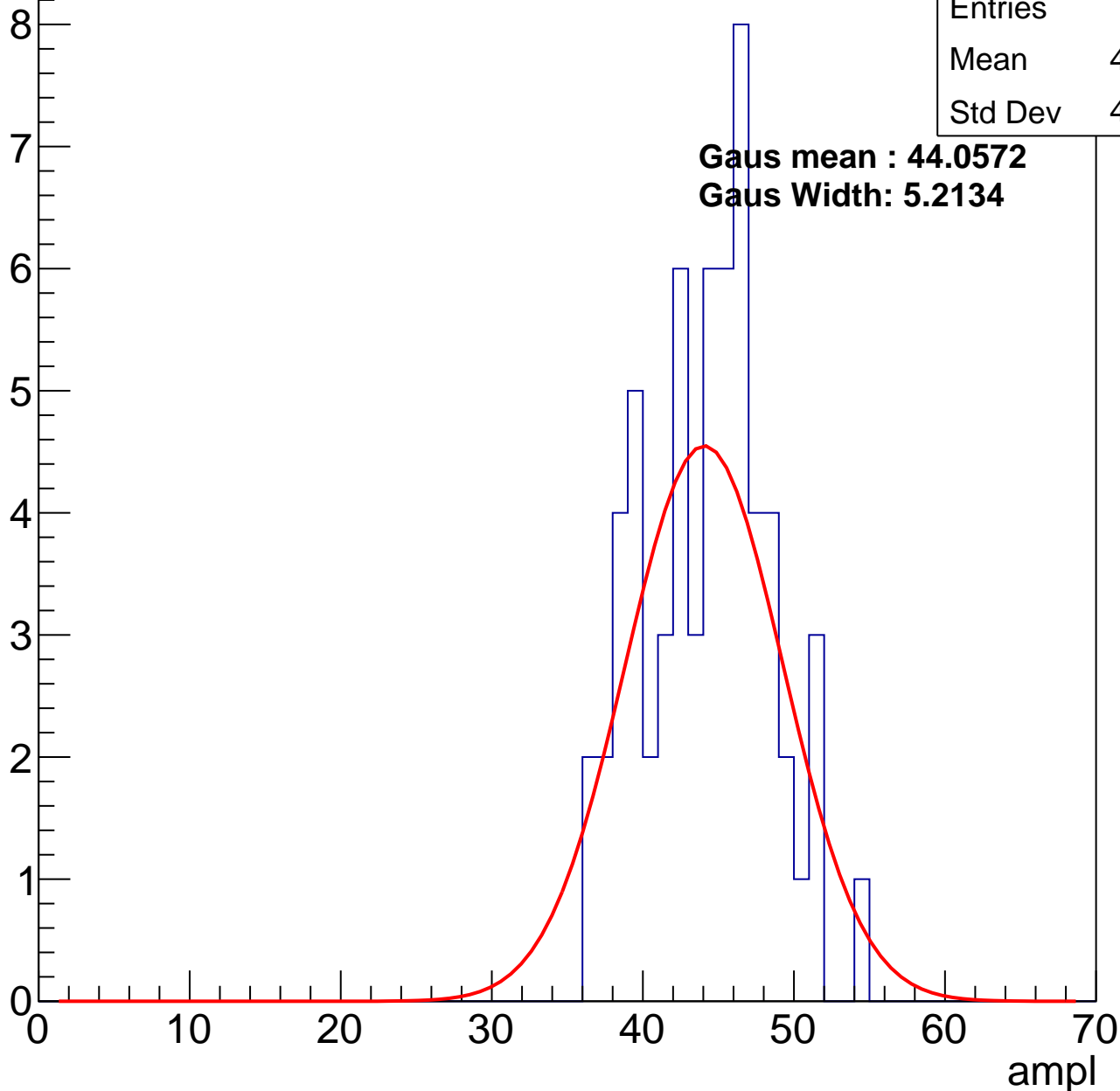
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.77
Std Dev	4.113

Gaus mean : 44.0572

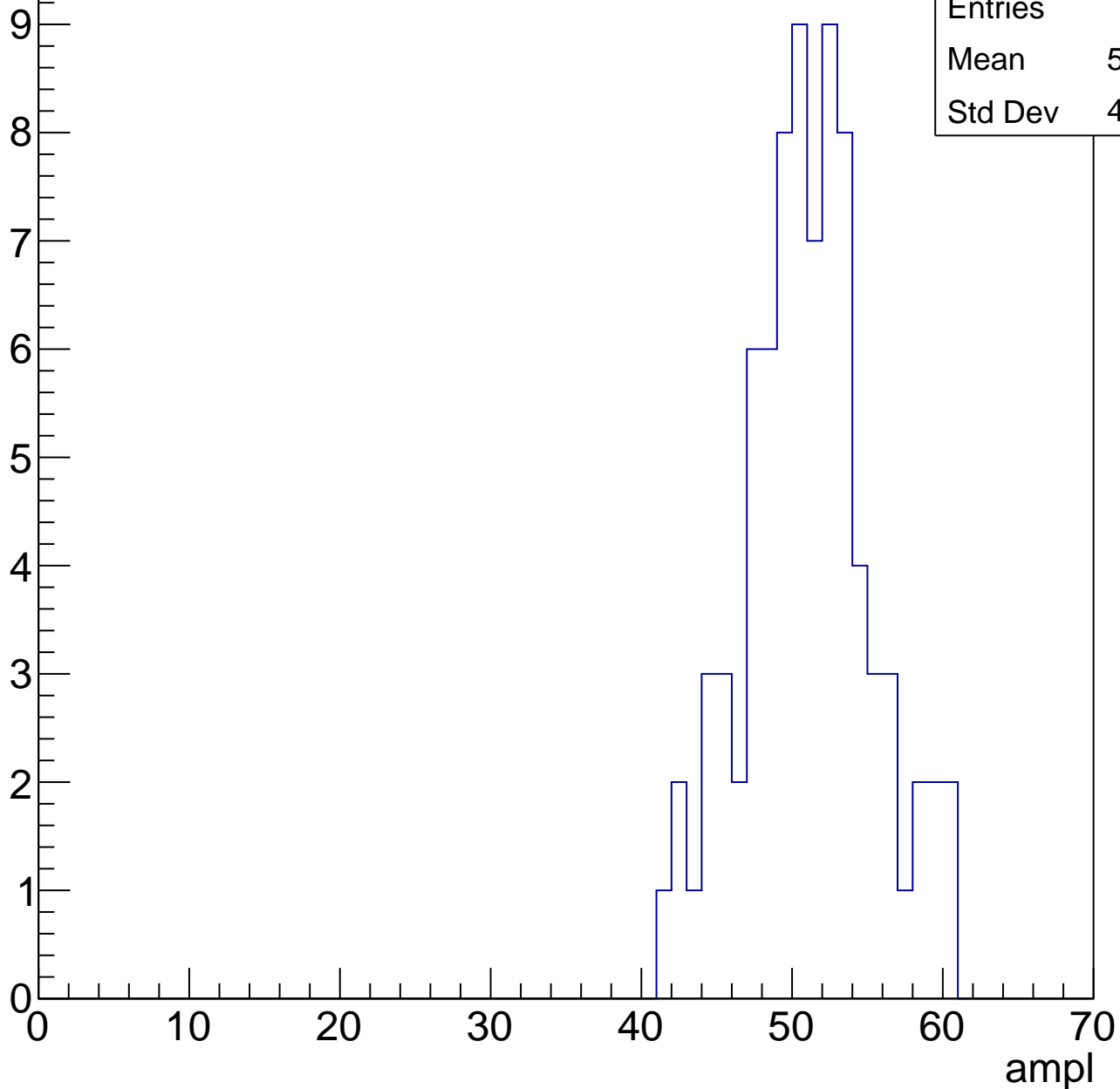
Gaus Width: 5.2134



B1L100S, U19-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



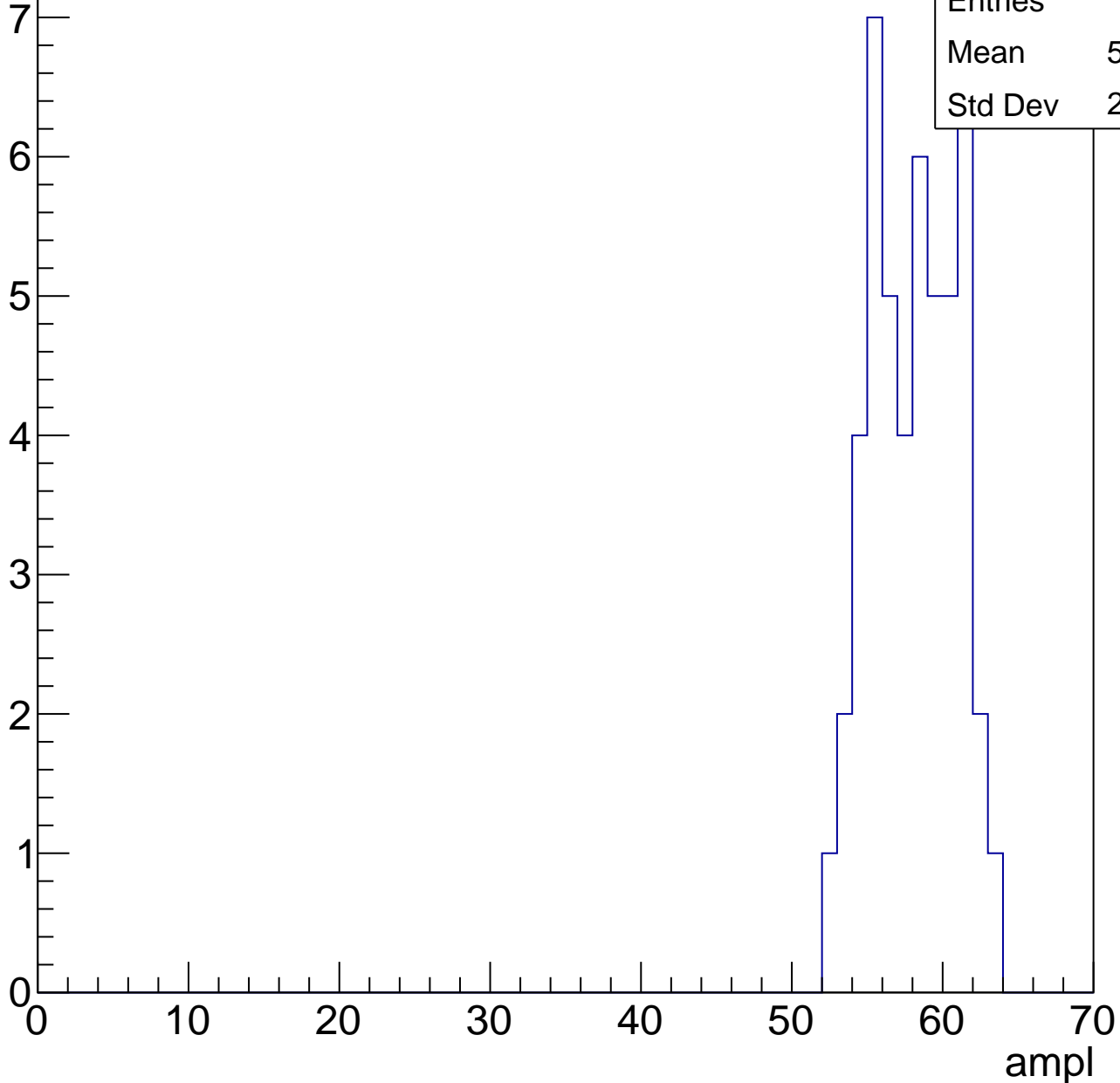
Entries	82
Mean	50.59
Std Dev	4.179

B1L100S, U19-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	57.63
Std Dev	2.768

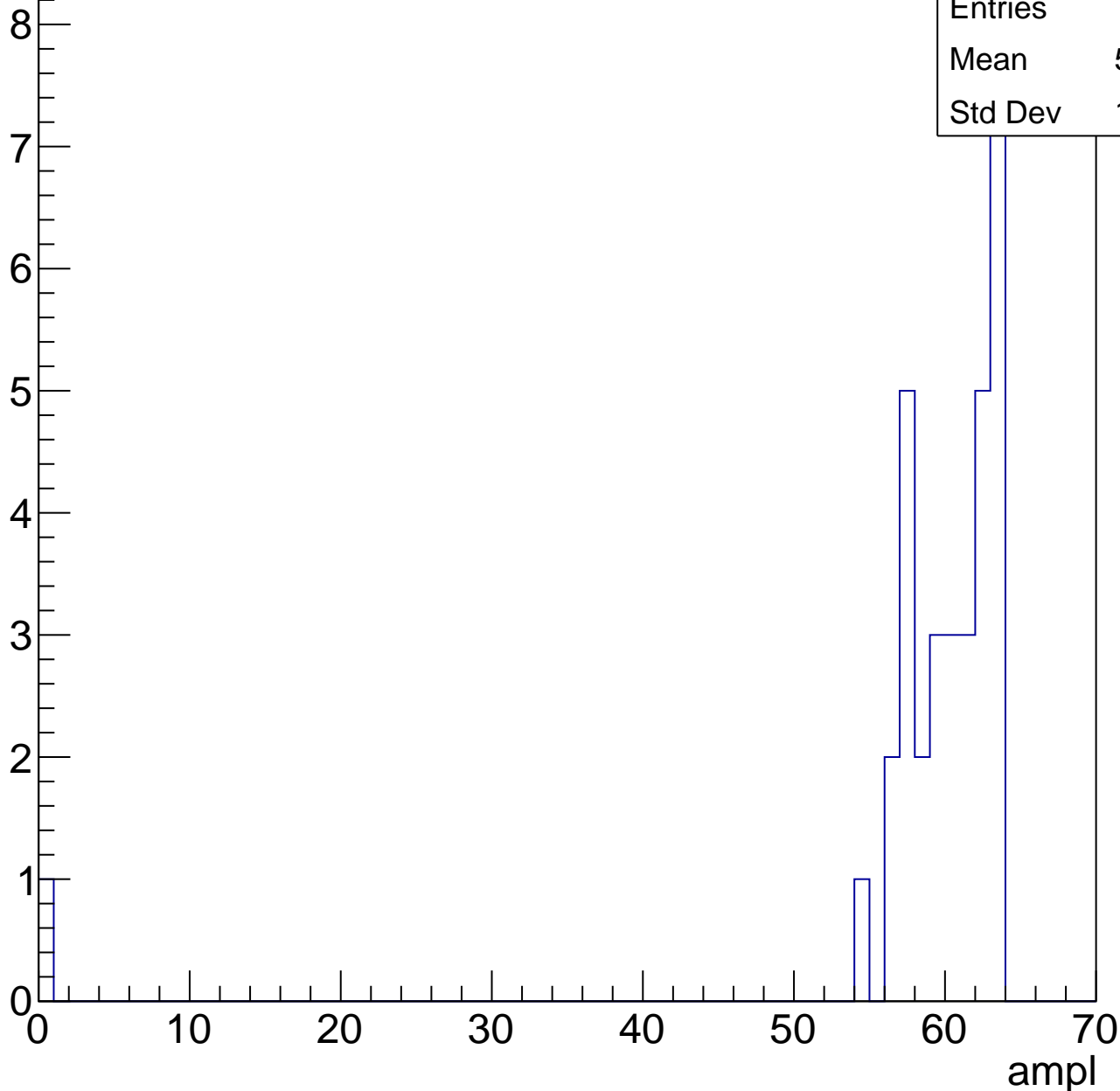


B1L100S, U19-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.21
Std Dev	10.61



B1L100S, U19-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch41, adc0

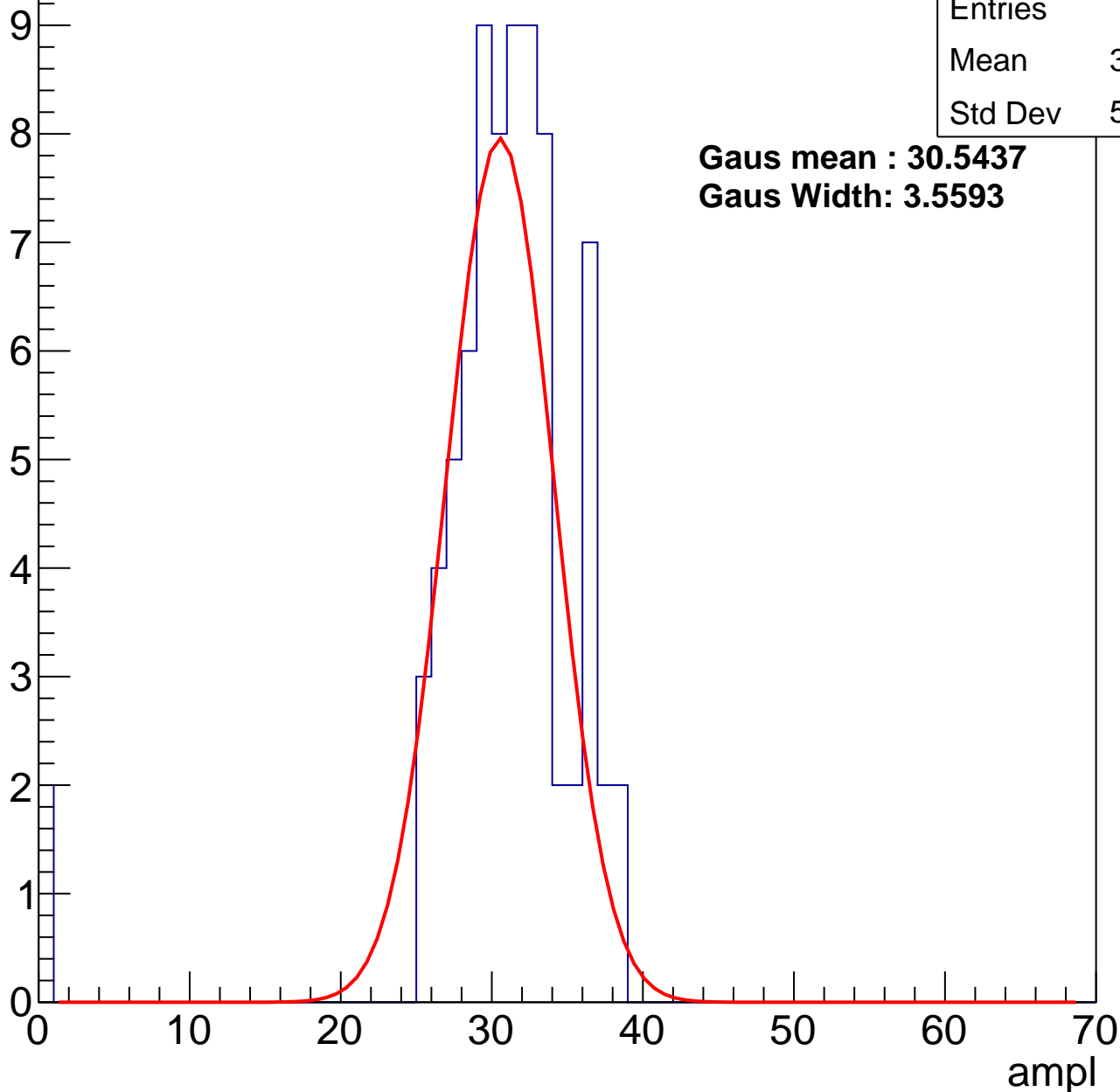
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.18
Std Dev	5.874

Gaus mean : 30.5437

Gaus Width: 3.5593



B1L100S, U19-ch41, adc1

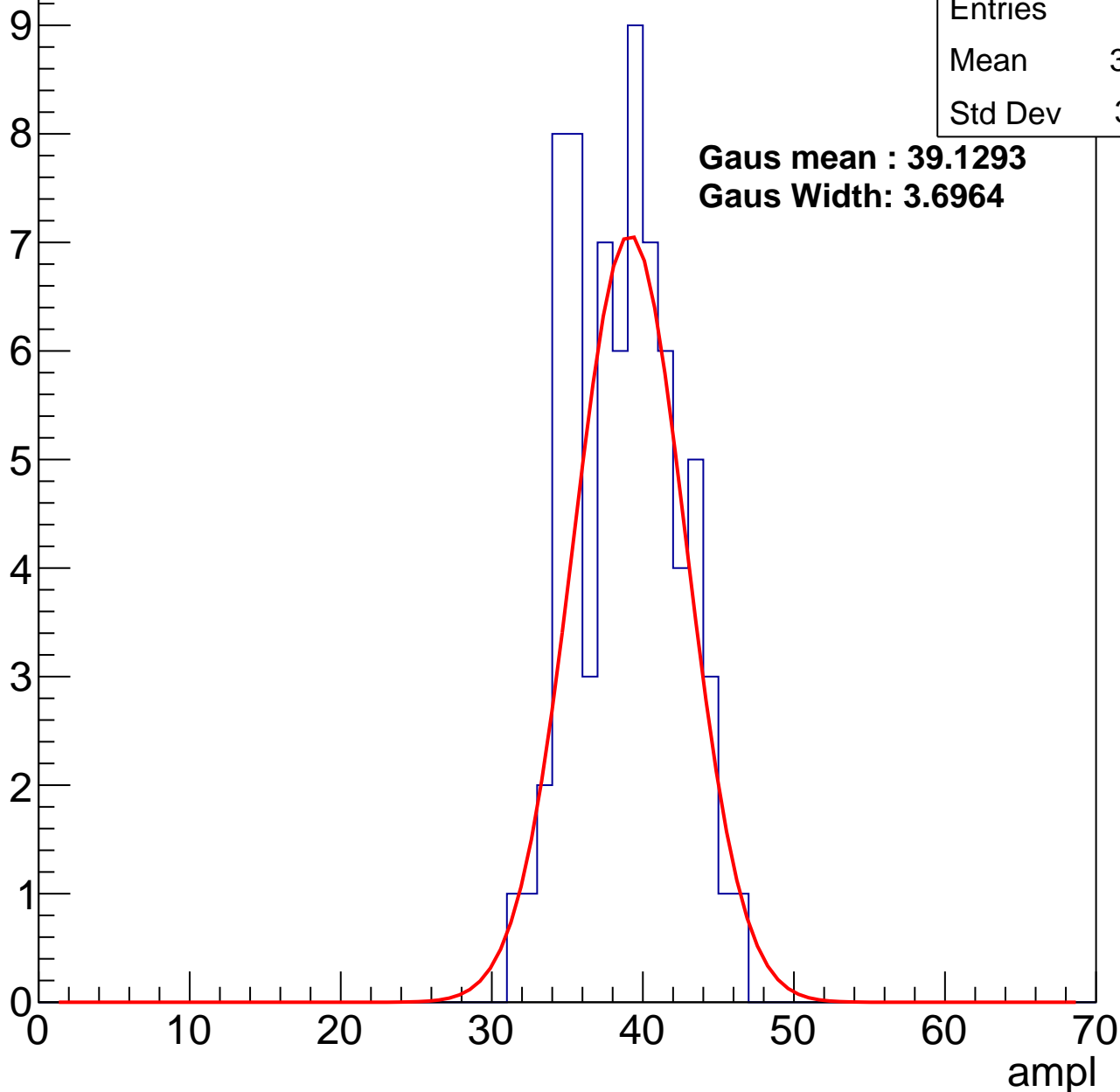
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.32
Std Dev	3.431

Gaus mean : 39.1293

Gaus Width: 3.6964



B1L100S, U19-ch41, adc2

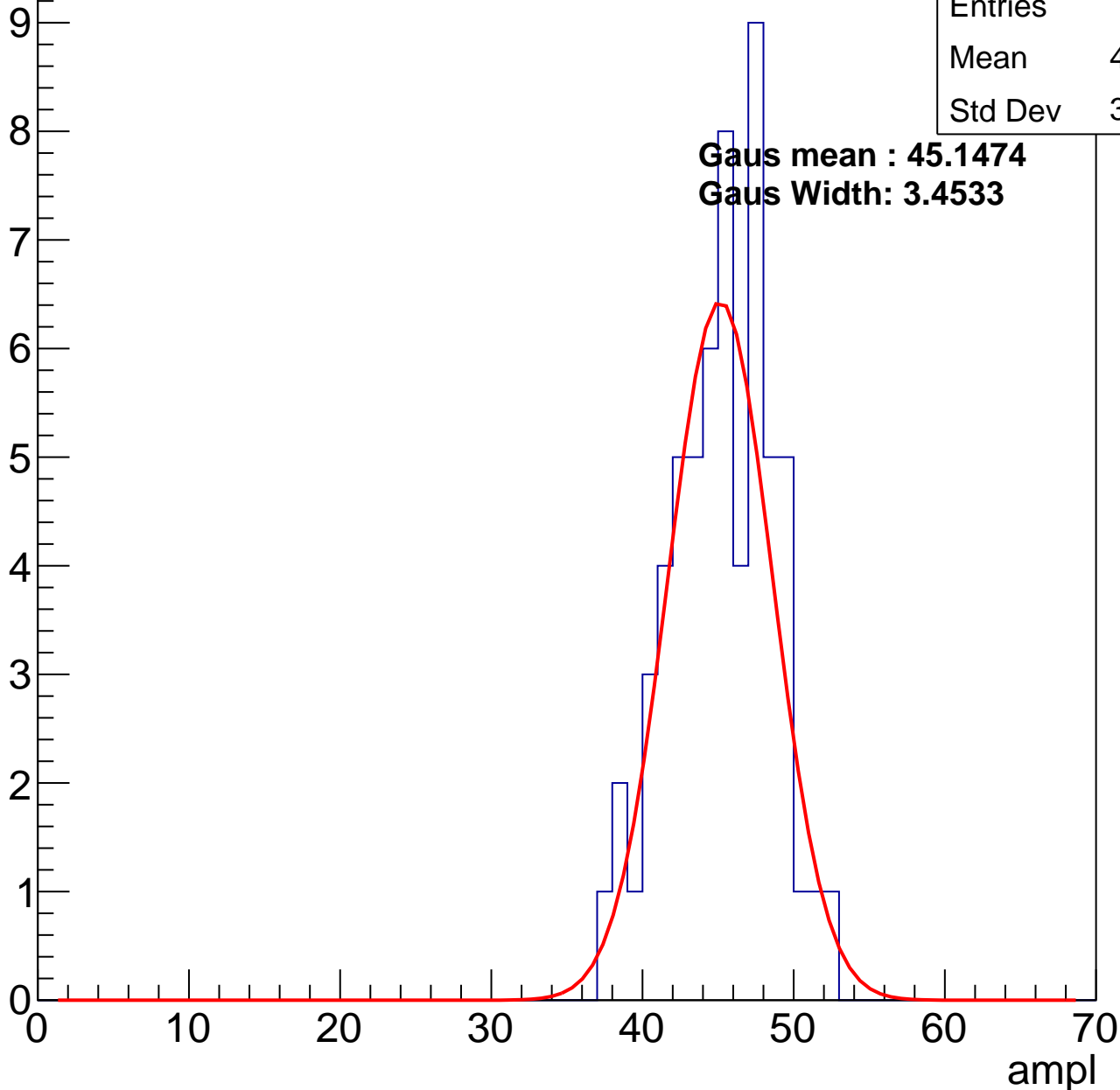
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.75
Std Dev	3.337

Gaus mean : 45.1474

Gaus Width: 3.4533

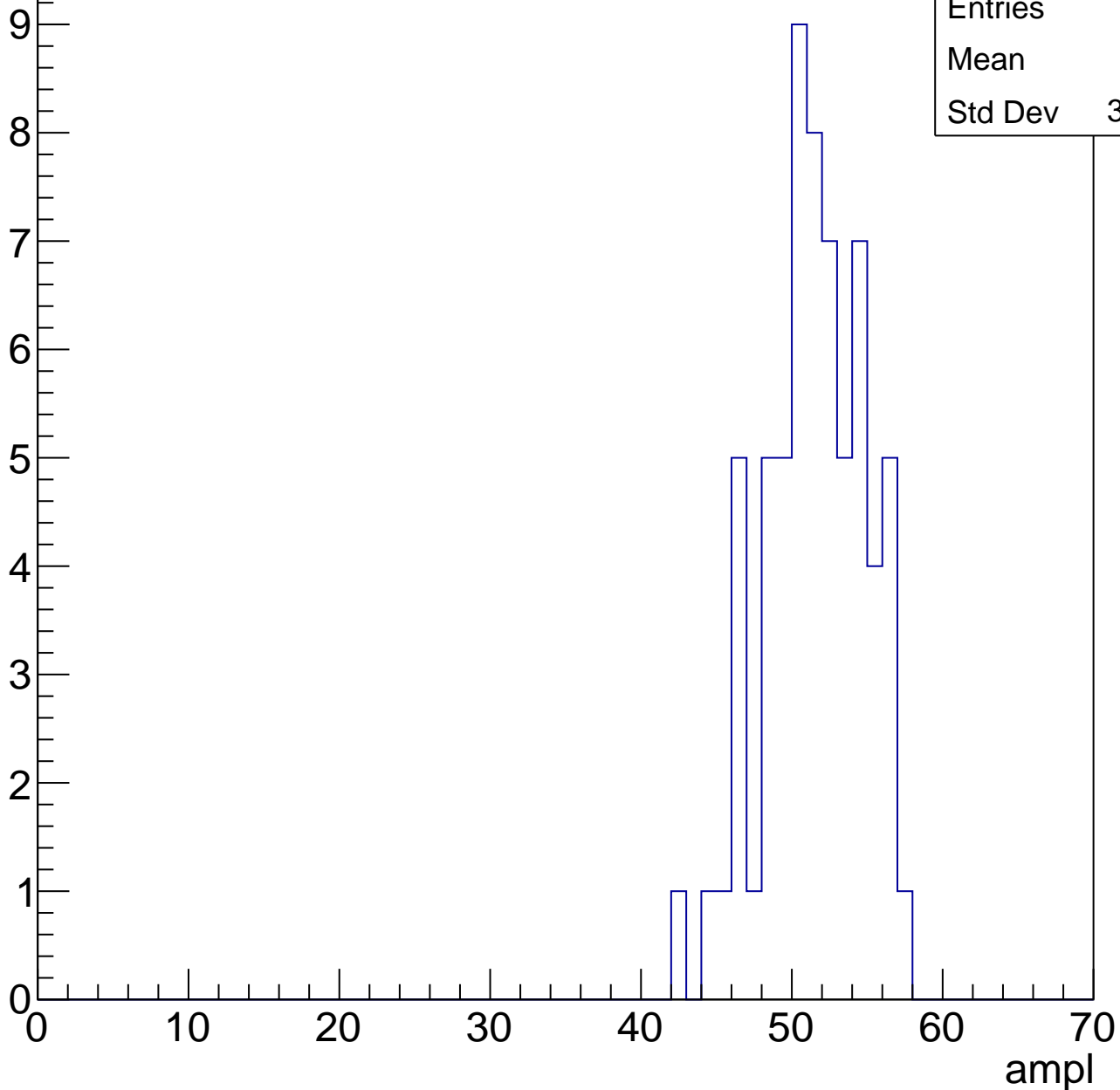


B1L100S, U19-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

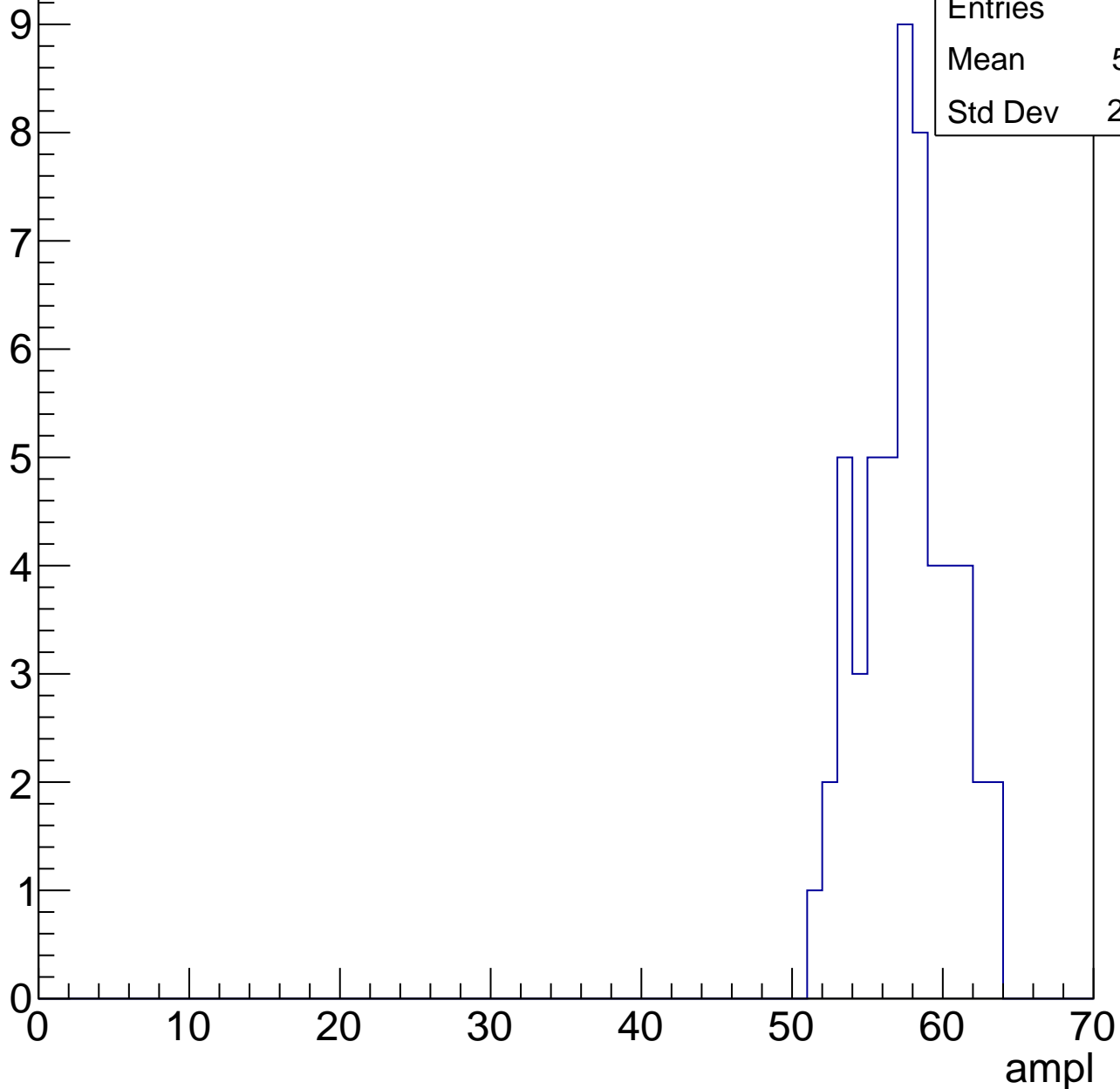
Entries	65
Mean	51
Std Dev	3.272



B1L100S, U19-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



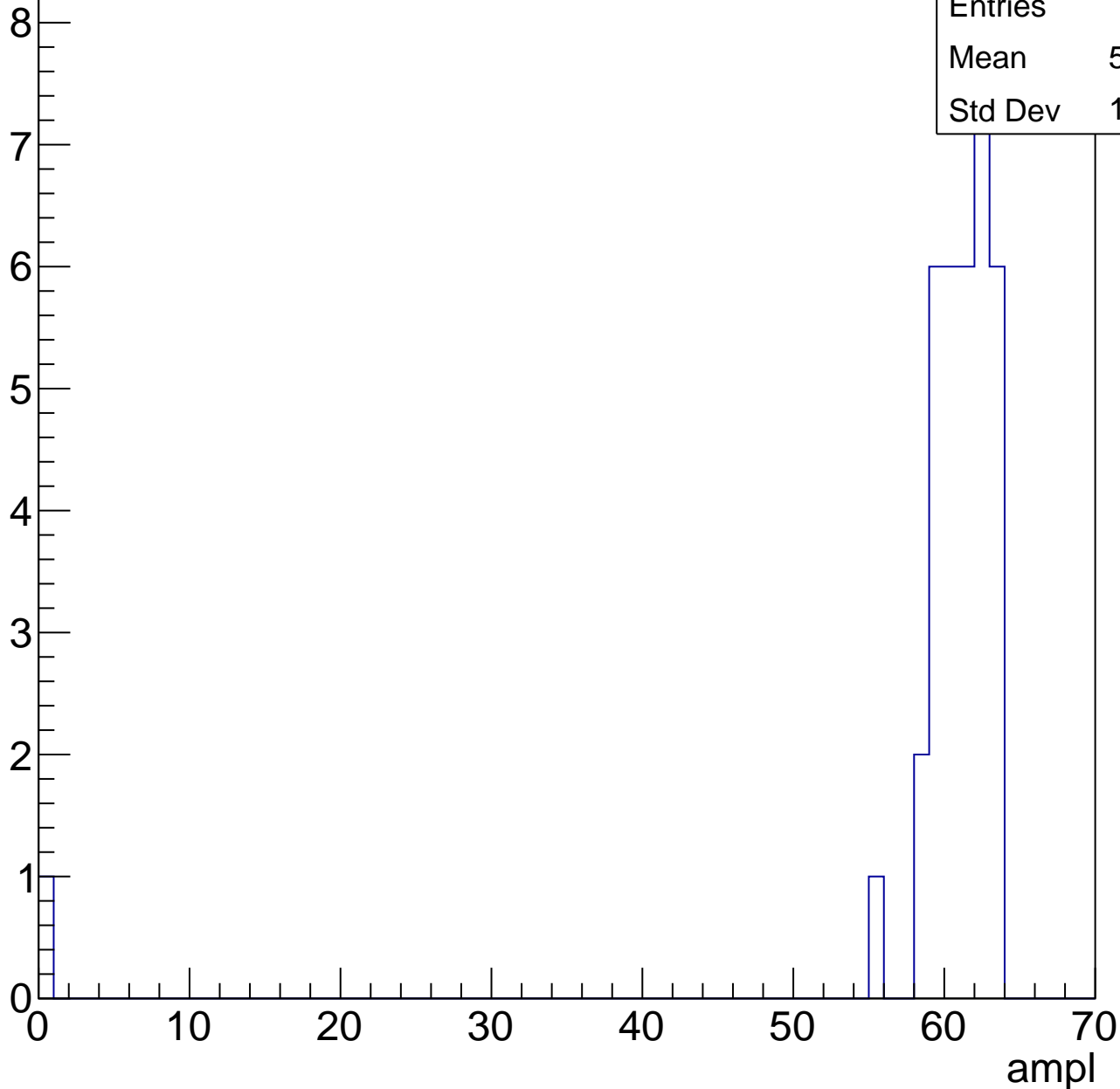
Entries	54
Mean	57.11
Std Dev	2.929

B1L100S, U19-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	59.03
Std Dev	10.13



B1L100S, U19-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U19-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch42, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	97
Mean	28.73
Std Dev	4.737

Gaus mean : 29.7012

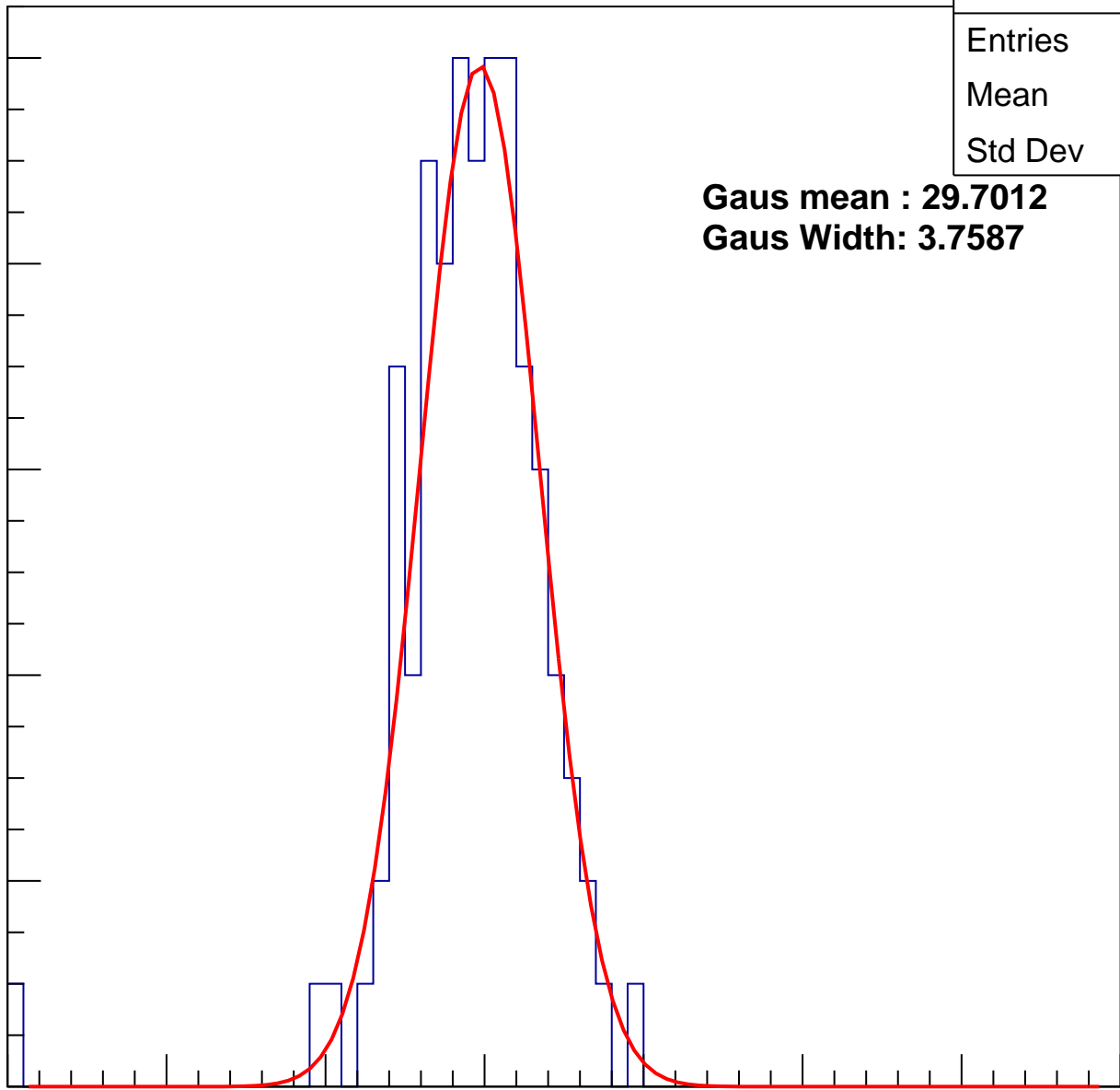
Gaus Width: 3.7587

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



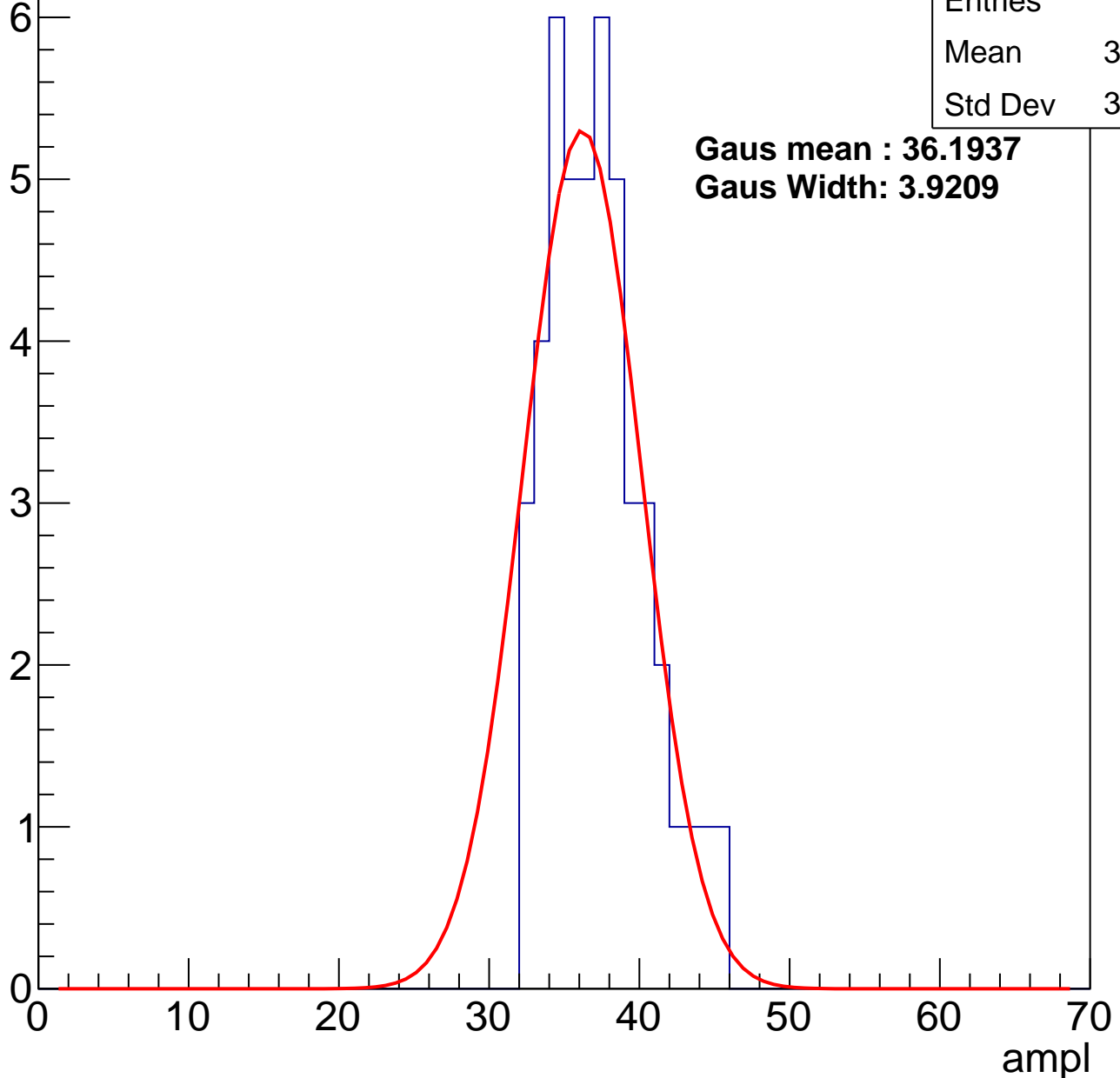
B1L100S, U19-ch42, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	36.78
Std Dev	3.182

Gaus mean : 36.1937
Gaus Width: 3.9209



B1L100S, U19-ch42, adc2

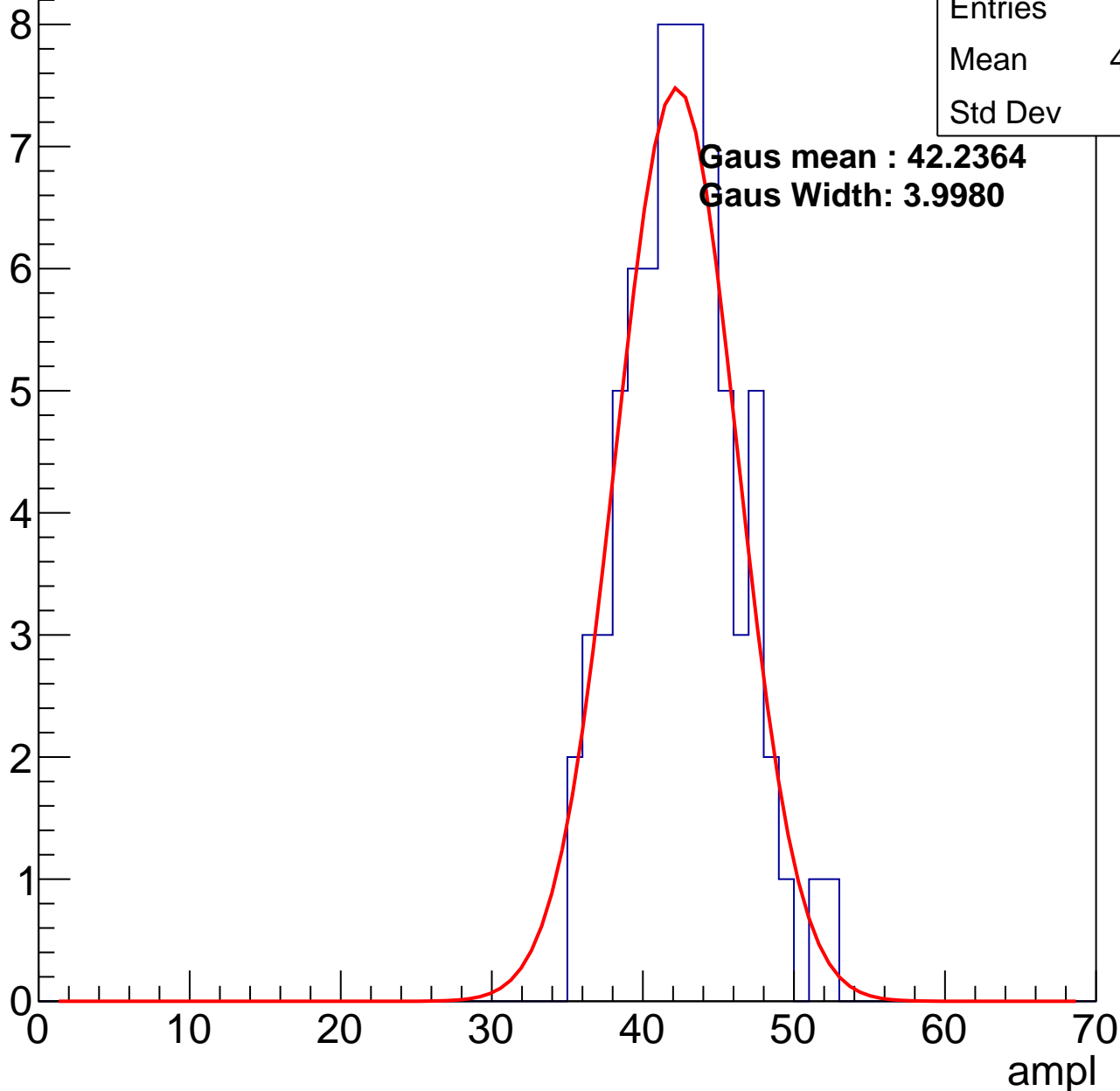
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	42.09
Std Dev	3.68

Gaus mean : 42.2364

Gaus Width: 3.9980

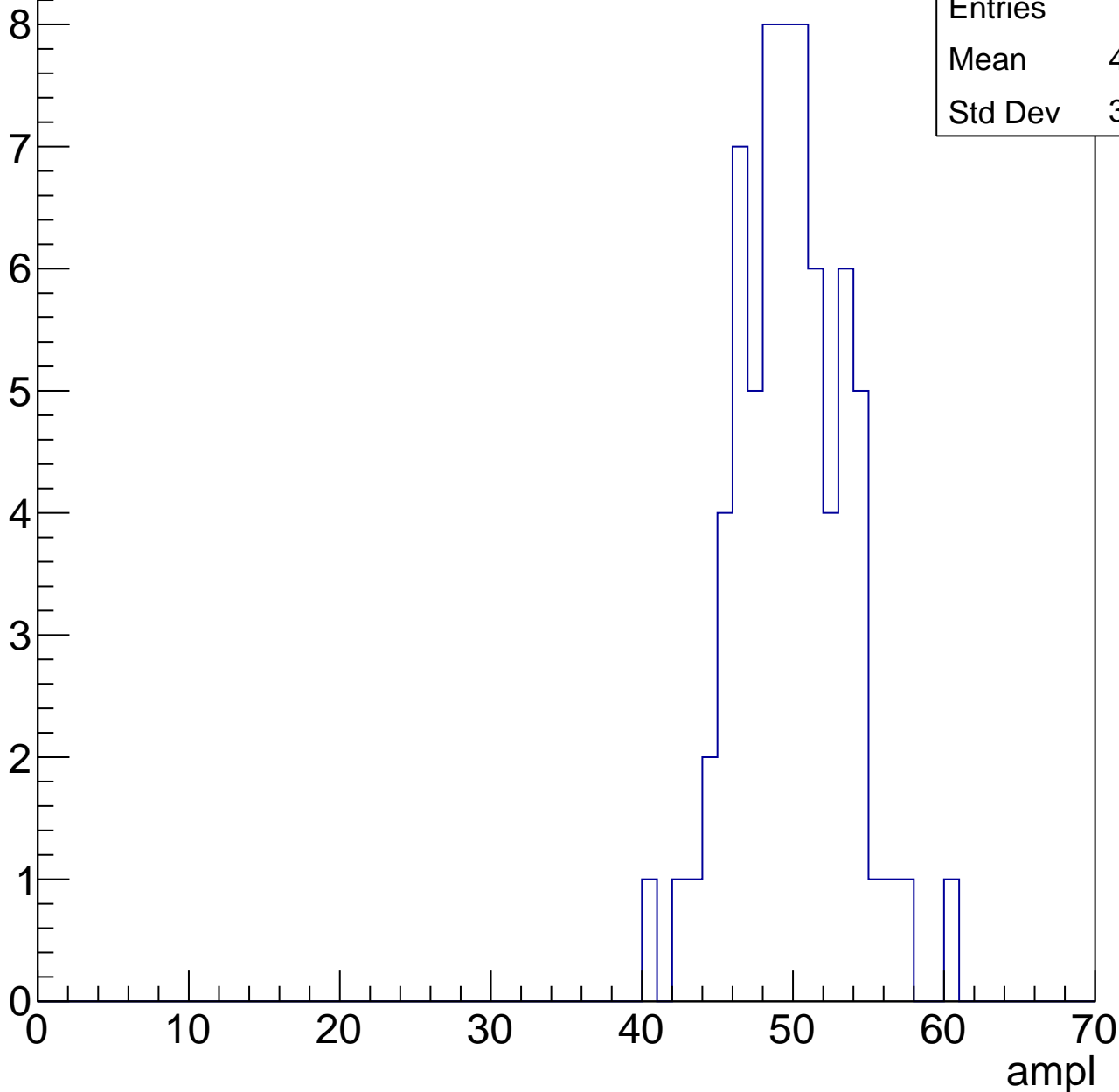


B1L100S, U19-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	49.37
Std Dev	3.626

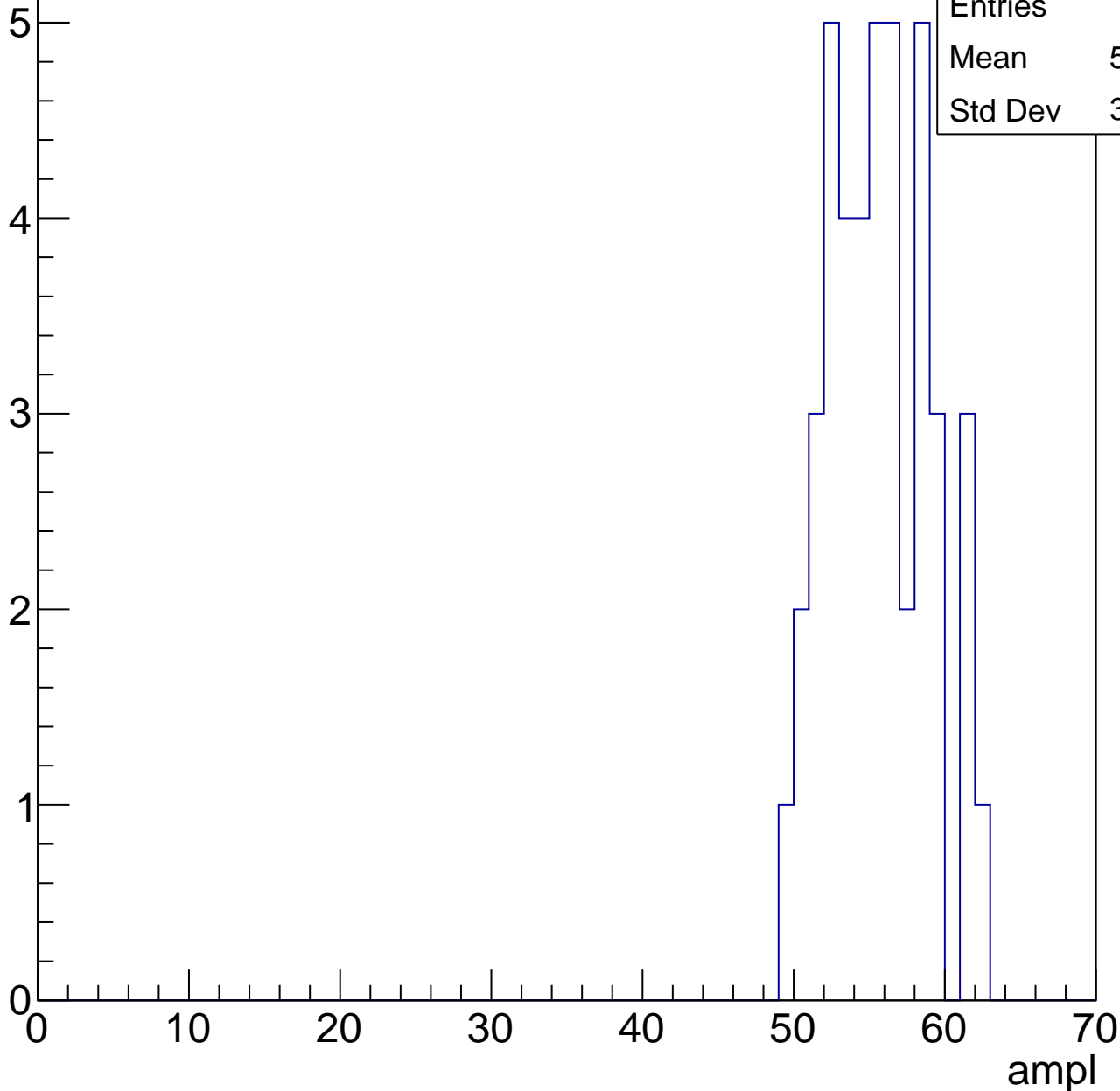


B1L100S, U19-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

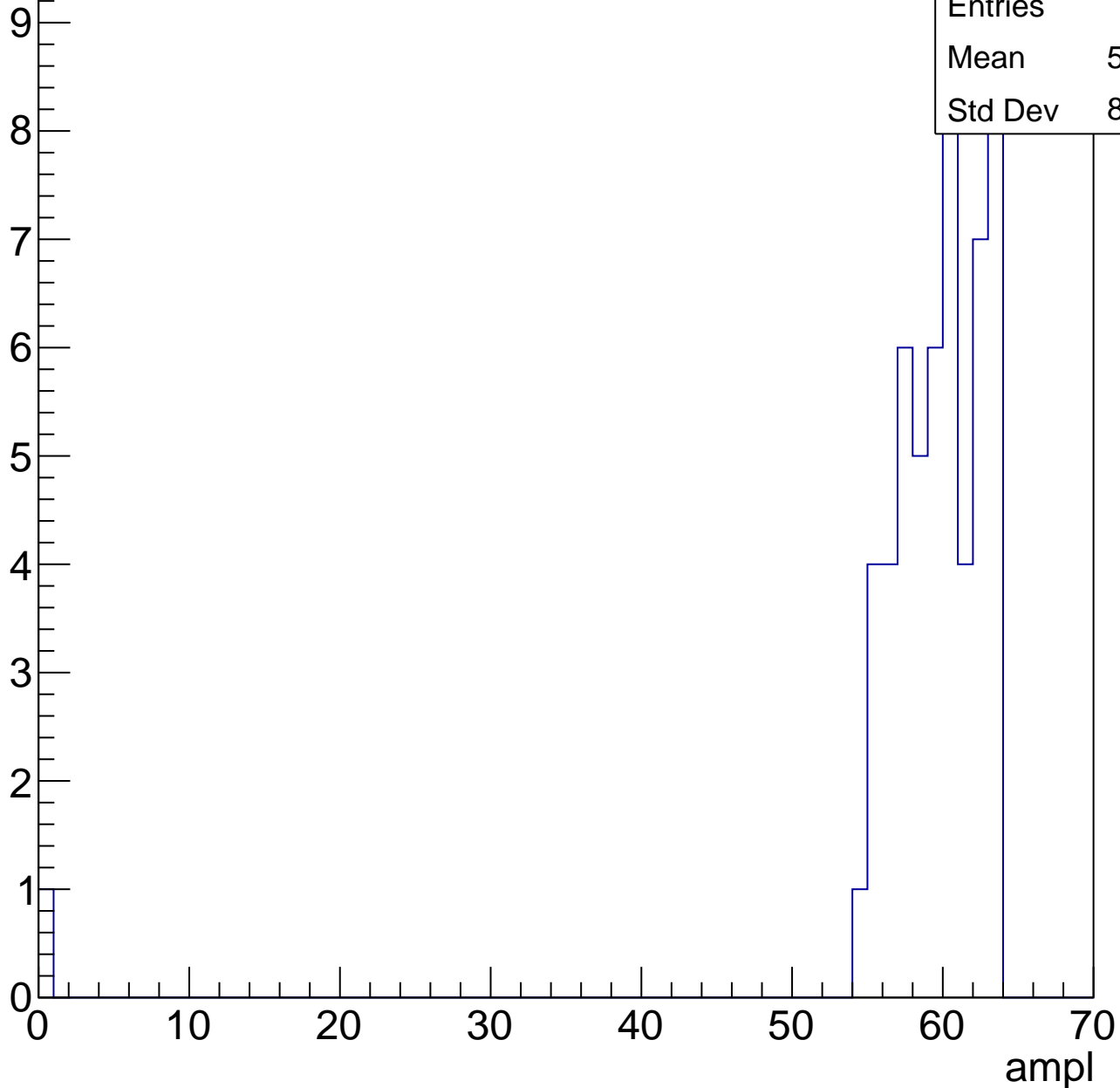
Entries	43
Mean	55.14
Std Dev	3.275



B1L100S, U19-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

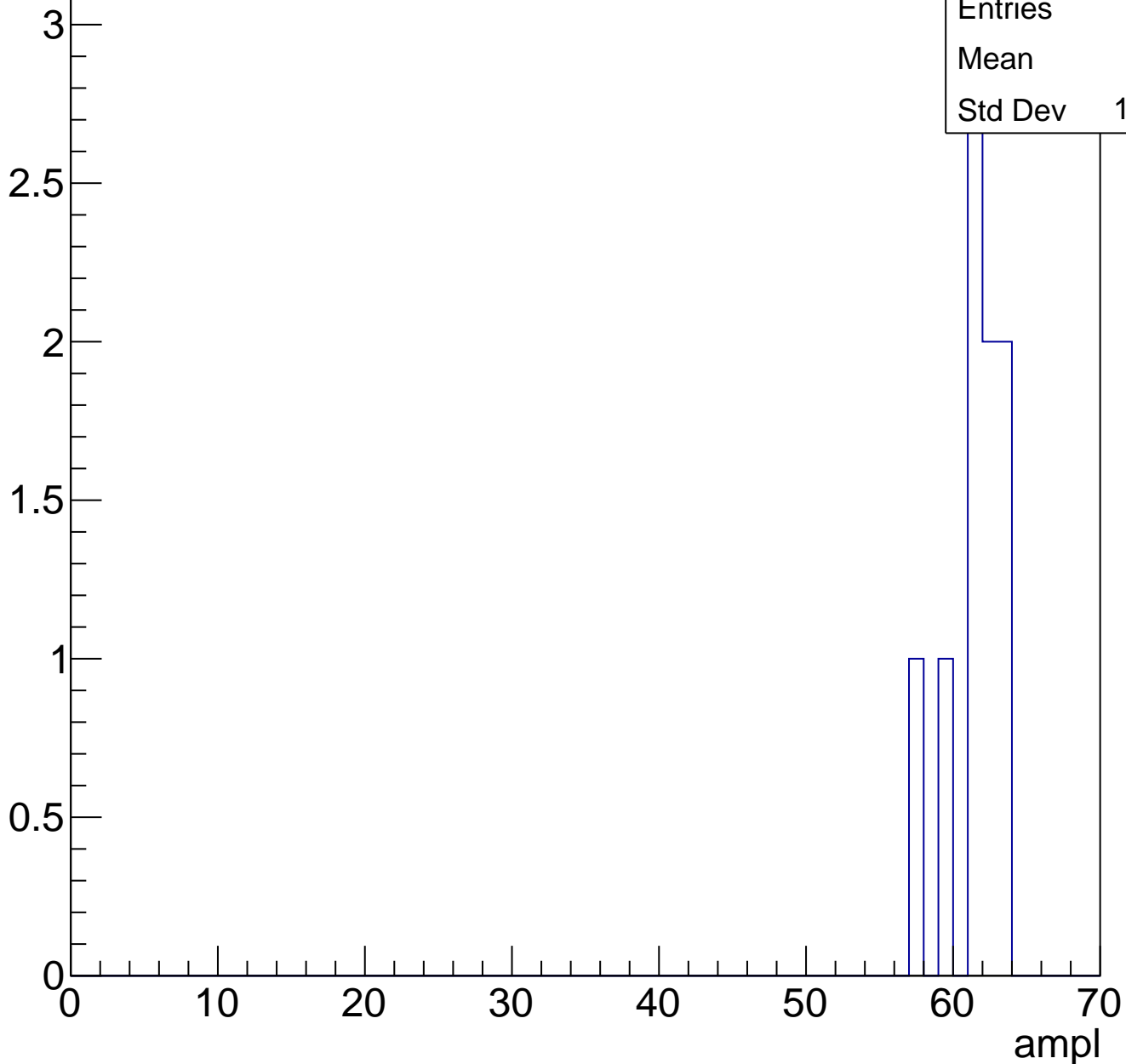
Entry



B1L100S, U19-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch43, adc0

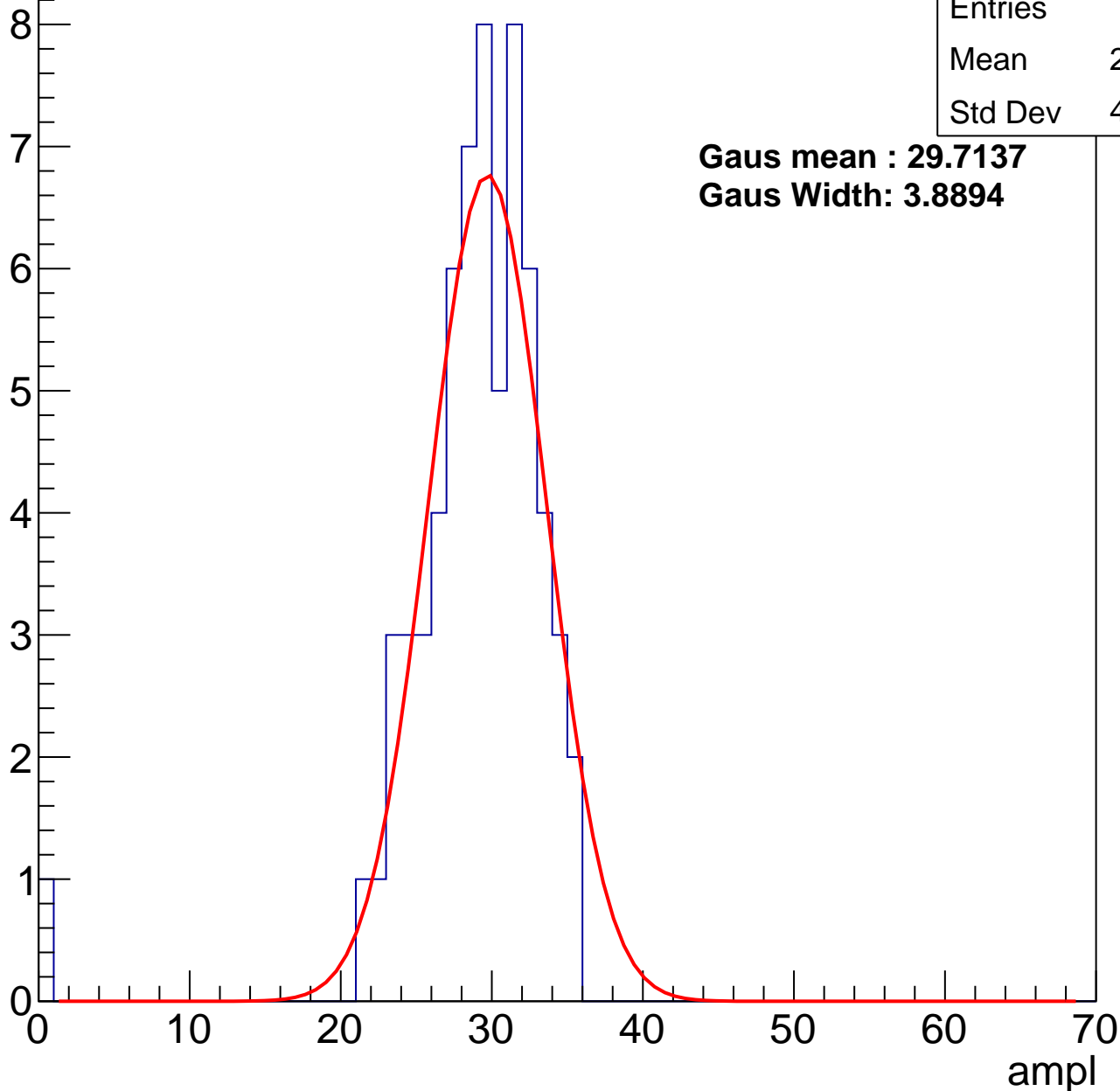
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.42
Std Dev	4.854

Gaus mean : 29.7137

Gaus Width: 3.8894



B1L100S, U19-ch43, adc1

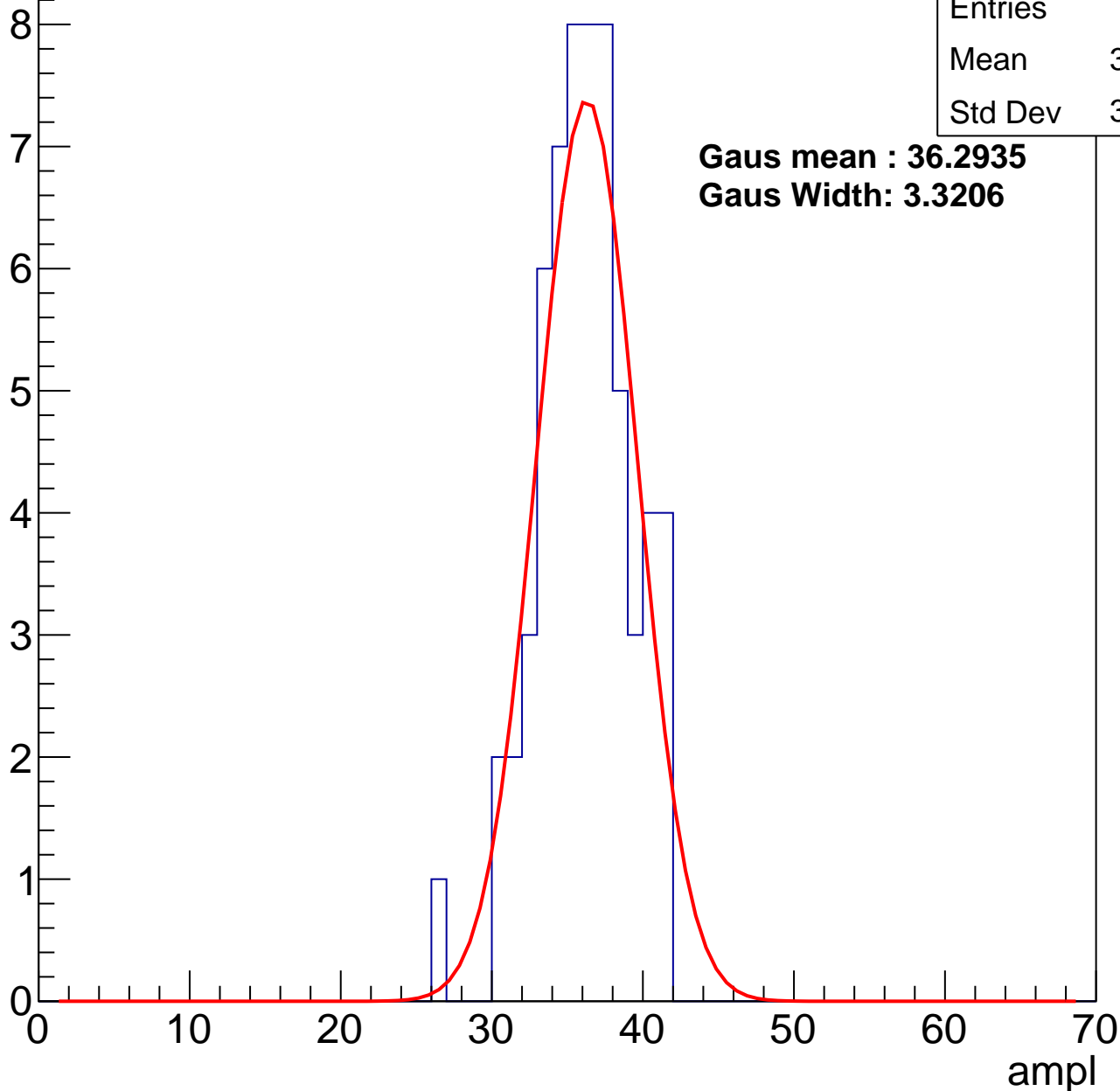
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.66
Std Dev	3.062

Gaus mean : 36.2935

Gaus Width: 3.3206



B1L100S, U19-ch43, adc2

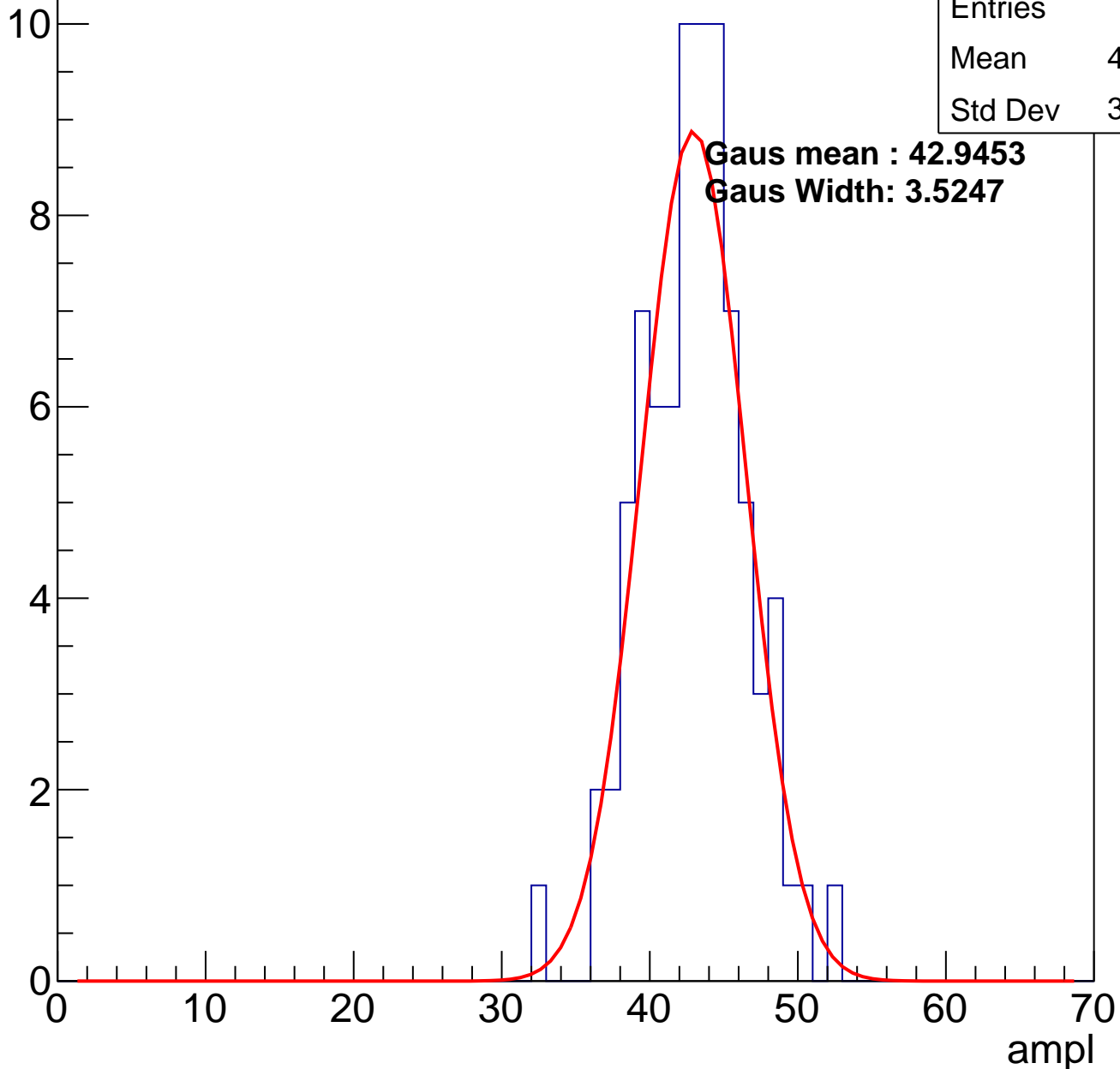
calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	42.54
Std Dev	3.507

Gaus mean : 42.9453

Gaus Width: 3.5247

Entry



B1L100S, U19-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

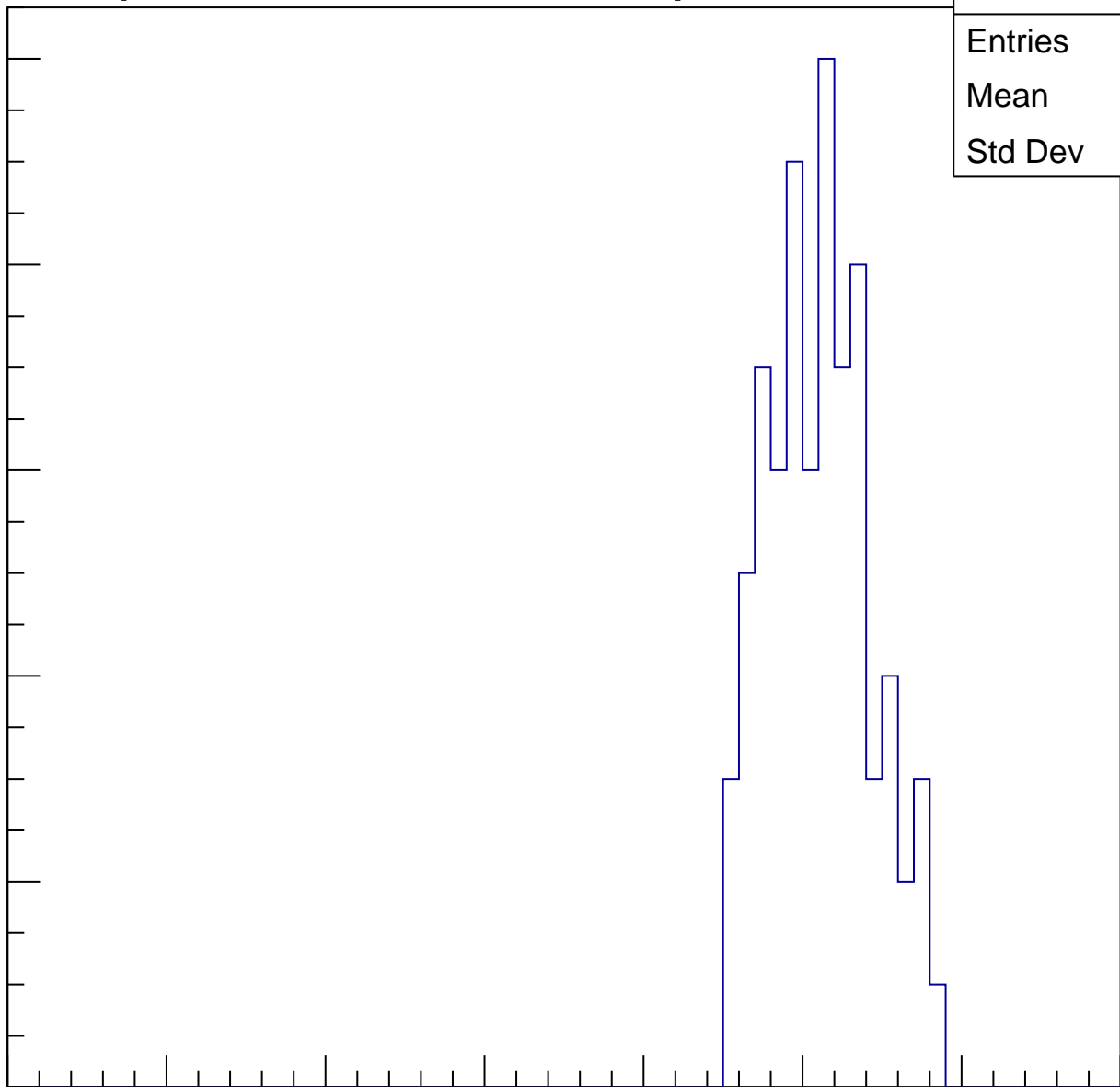
Entries	74
Mean	50.59
Std Dev	3.217

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

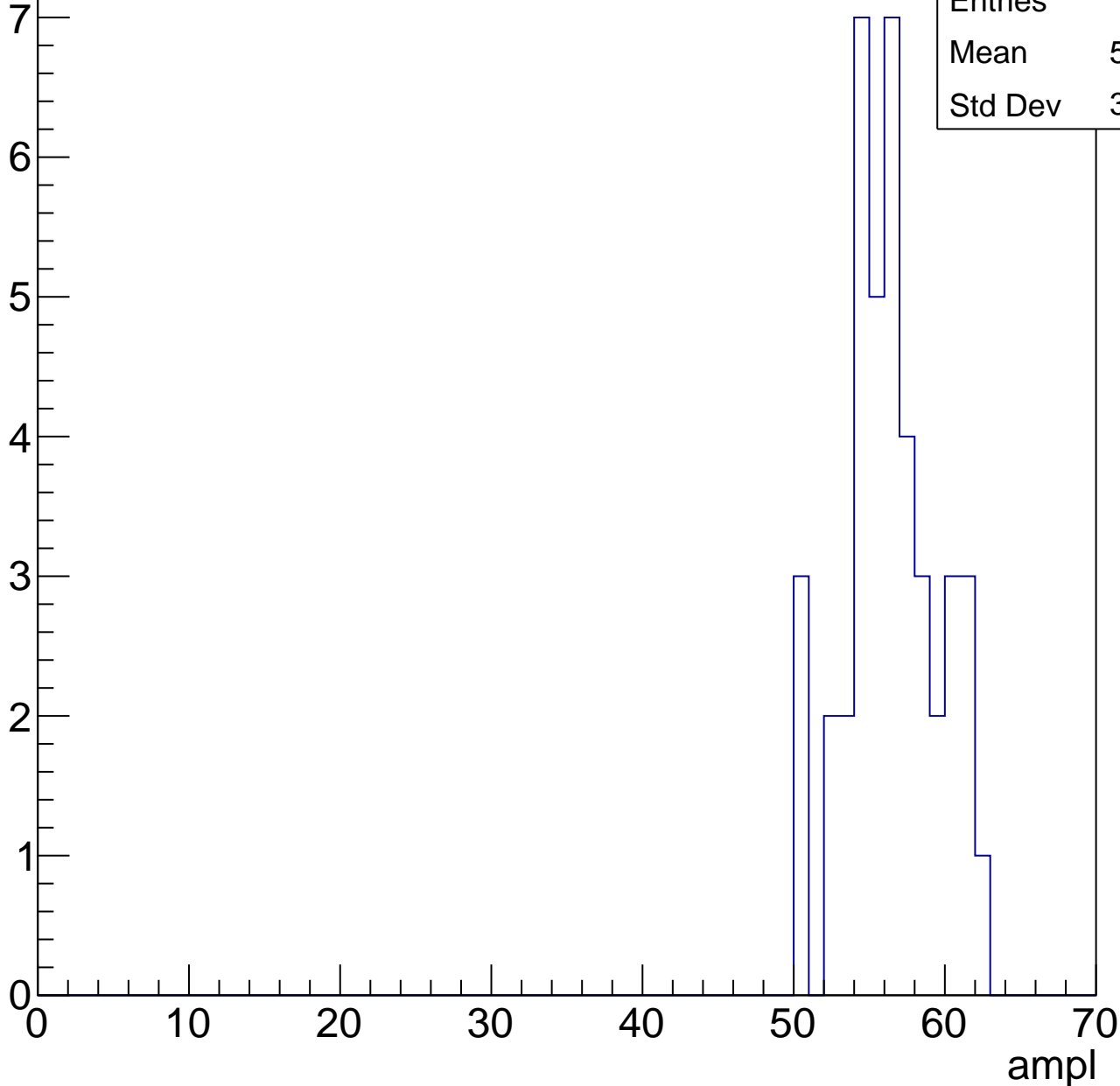


B1L100S, U19-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	55.95
Std Dev	3.023



B1L100S, U19-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.98
Std Dev	8.671

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L100S, U19-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch44, adc0

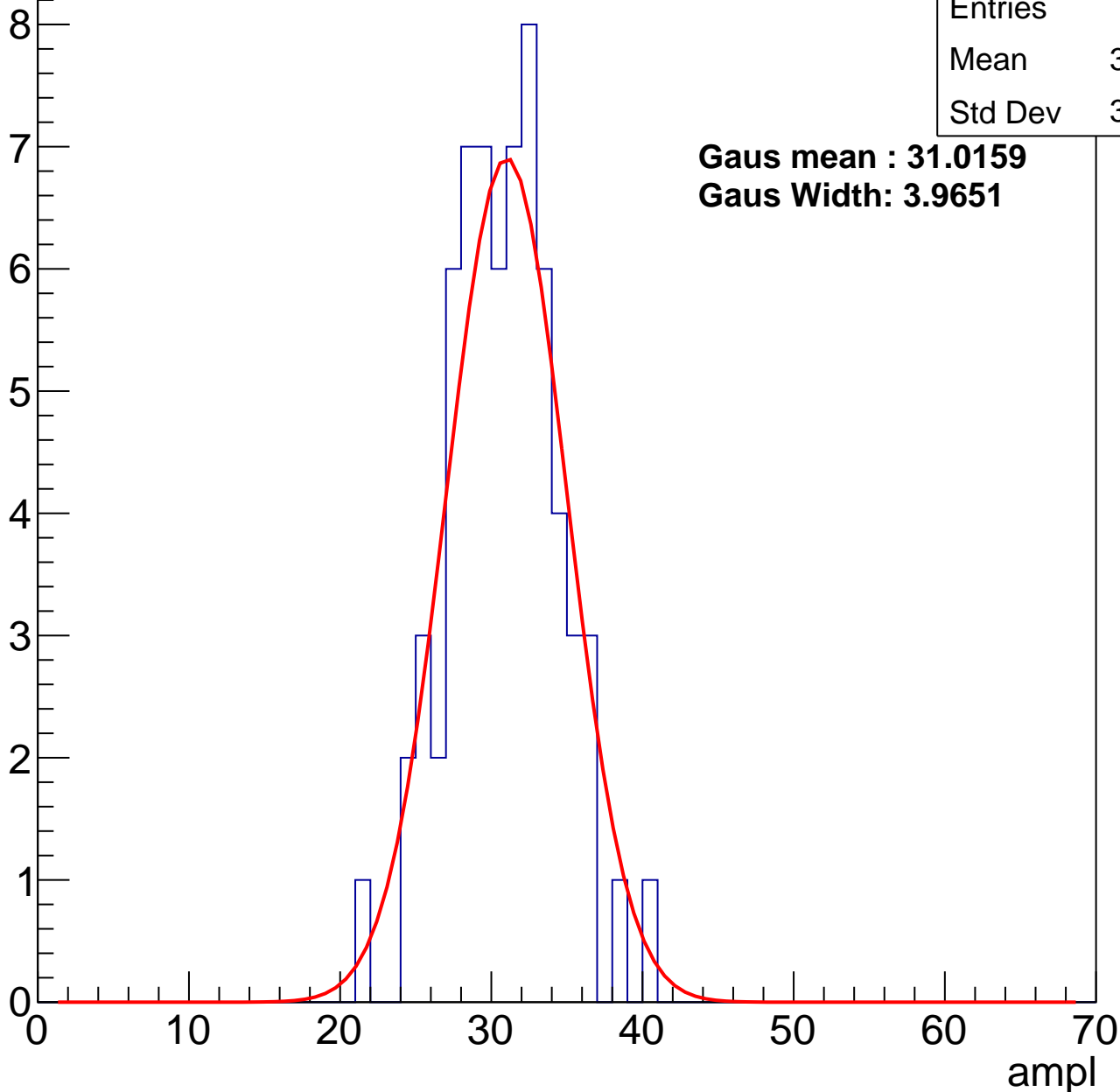
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.37
Std Dev	3.549

Gaus mean : 31.0159

Gaus Width: 3.9651



B1L100S, U19-ch44, adc1

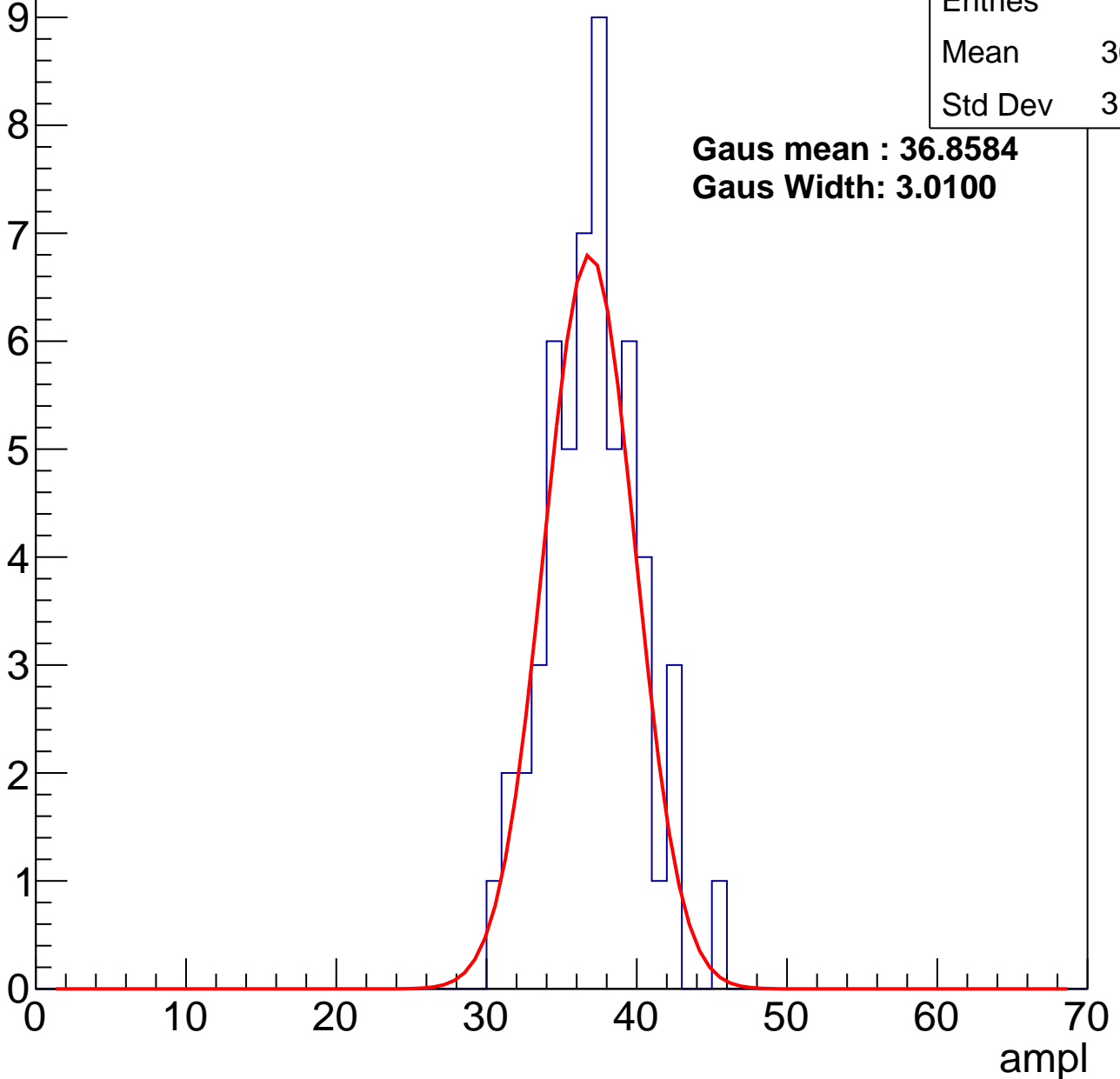
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	36.64
Std Dev	3.053

Gaus mean : 36.8584

Gaus Width: 3.0100



B1L100S, U19-ch44, adc2

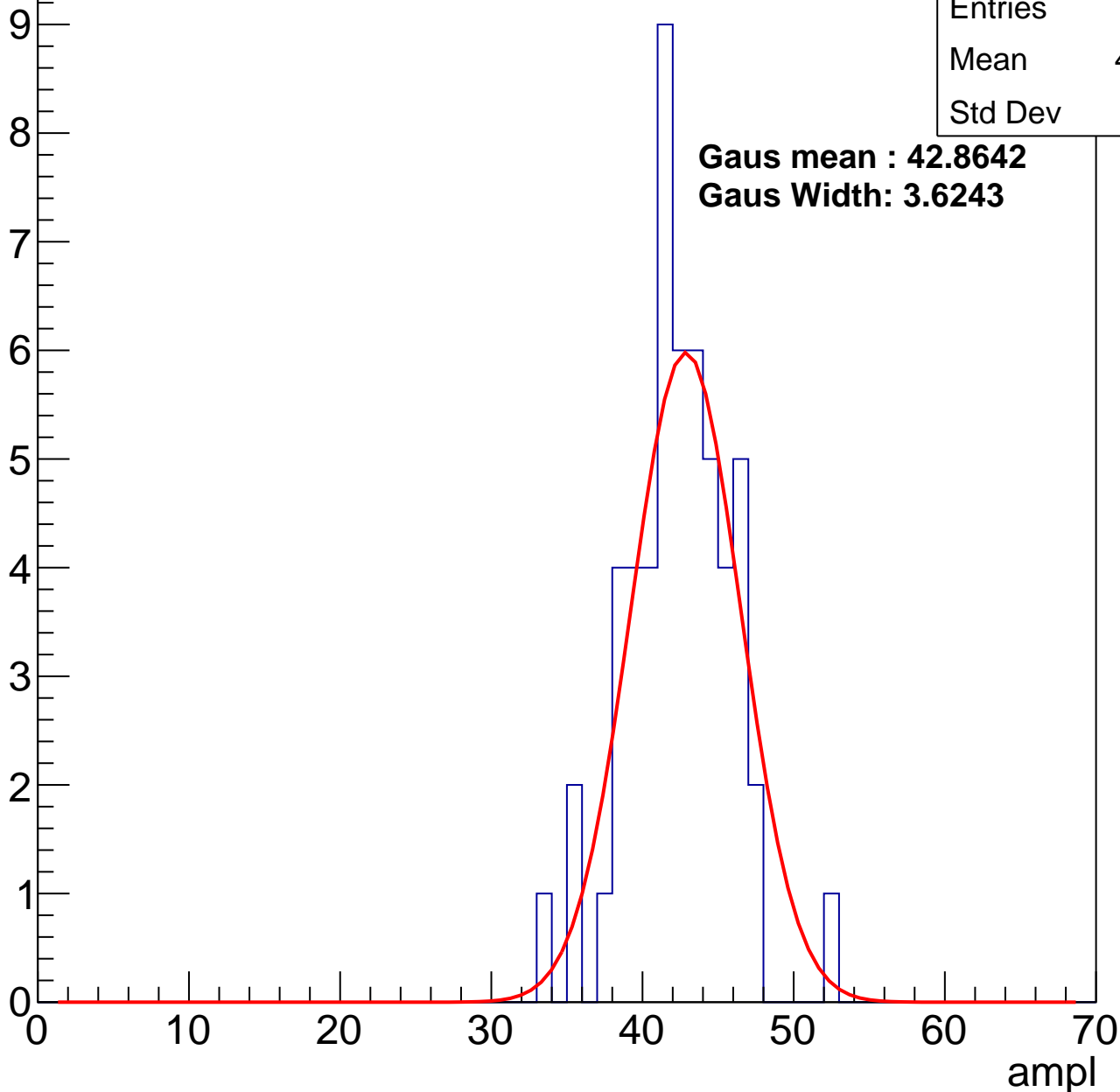
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	41.91
Std Dev	3.39

Gaus mean : 42.8642

Gaus Width: 3.6243

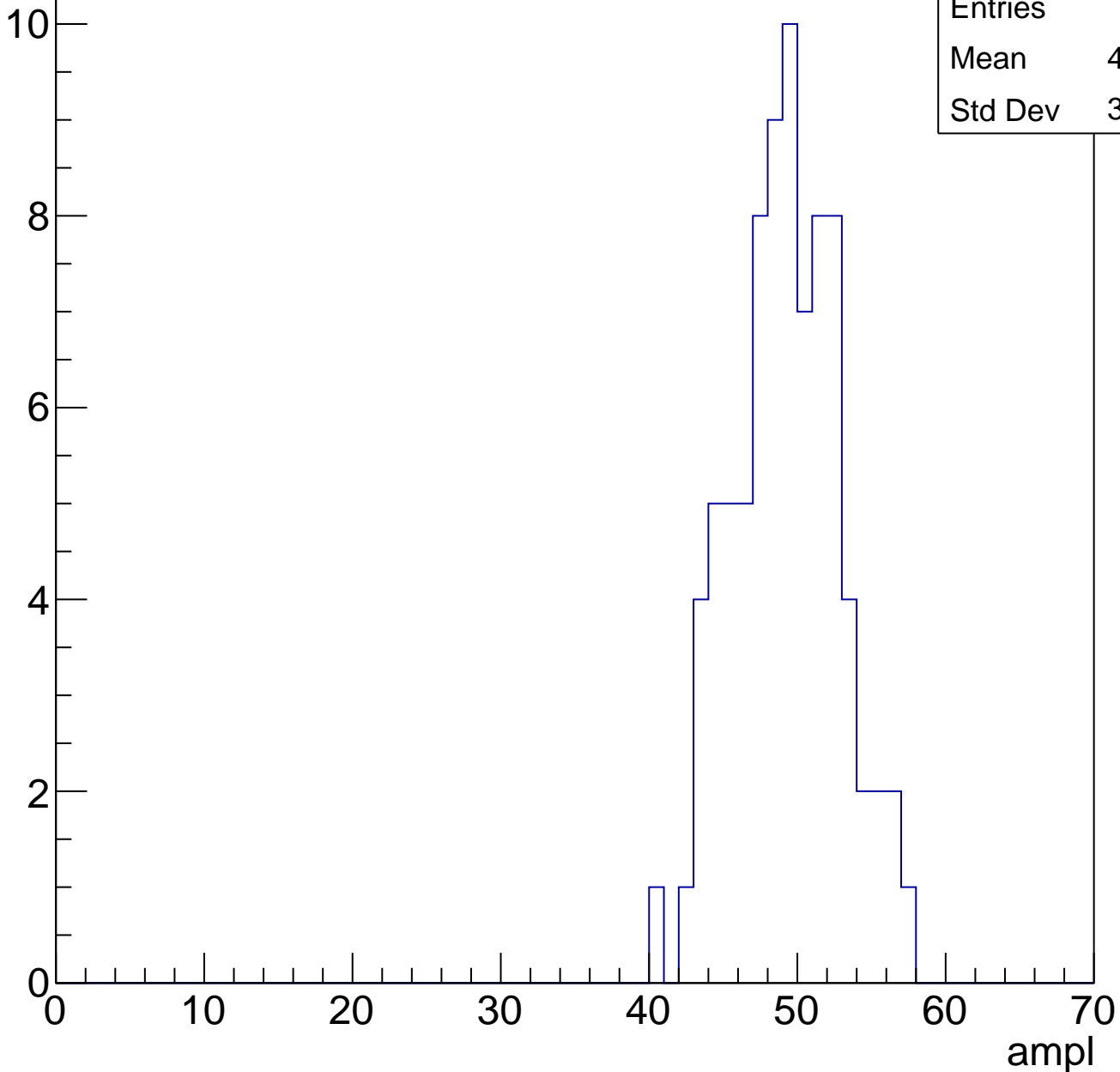


B1L100S, U19-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	48.78
Std Dev	3.527

Entry

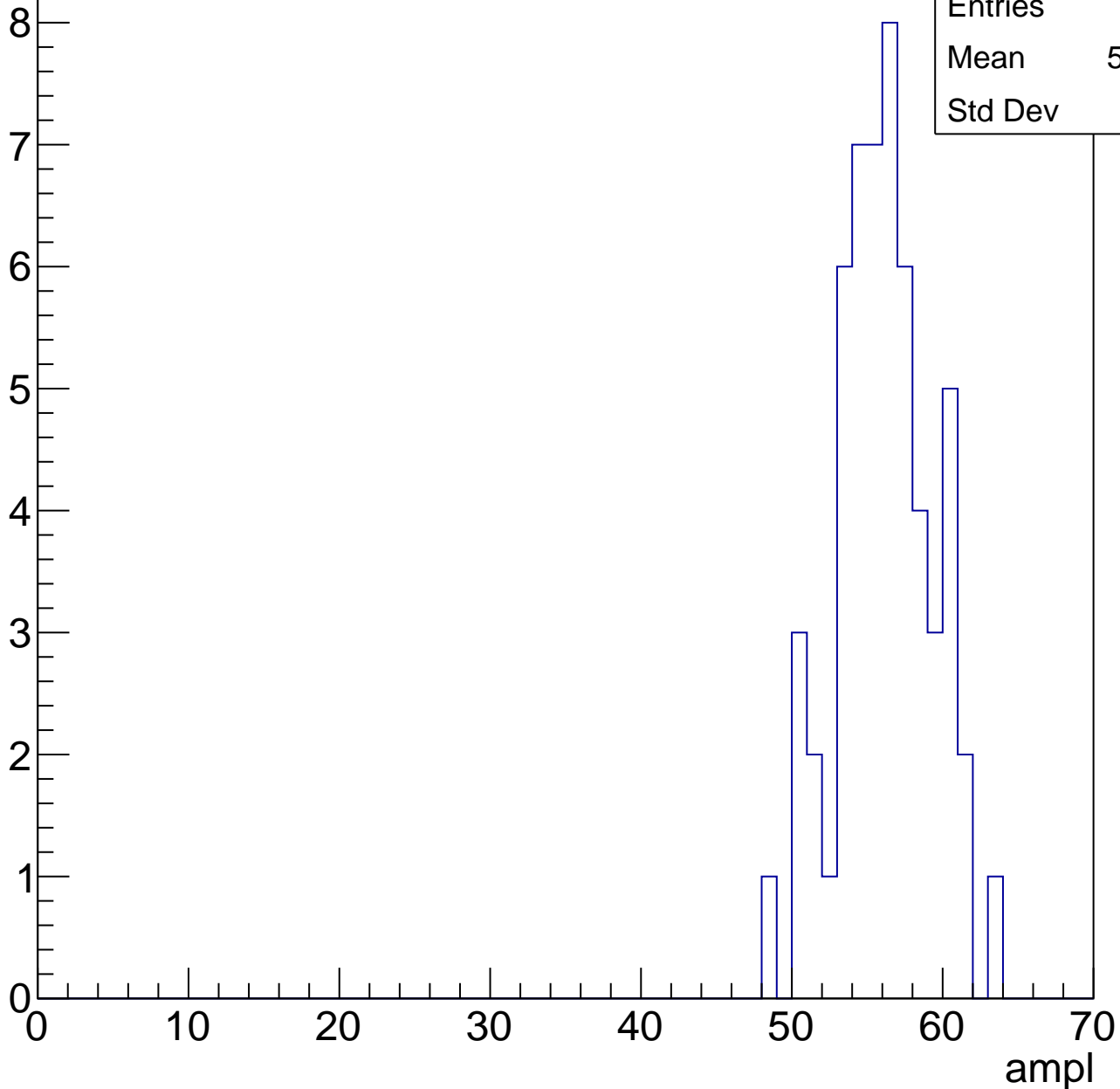


B1L100S, U19-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

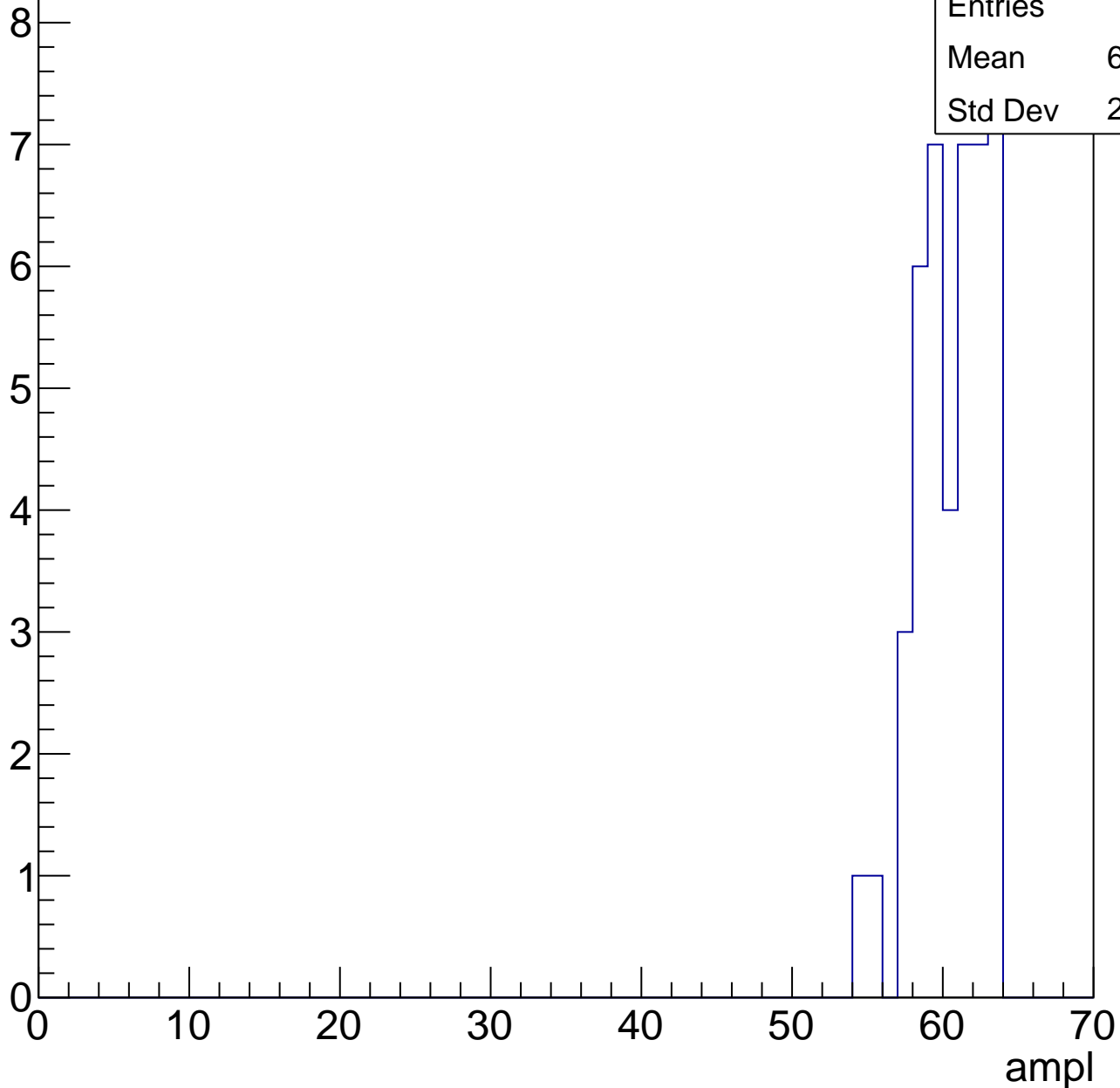
Entries	56
Mean	55.66
Std Dev	3.13



B1L100S, U19-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

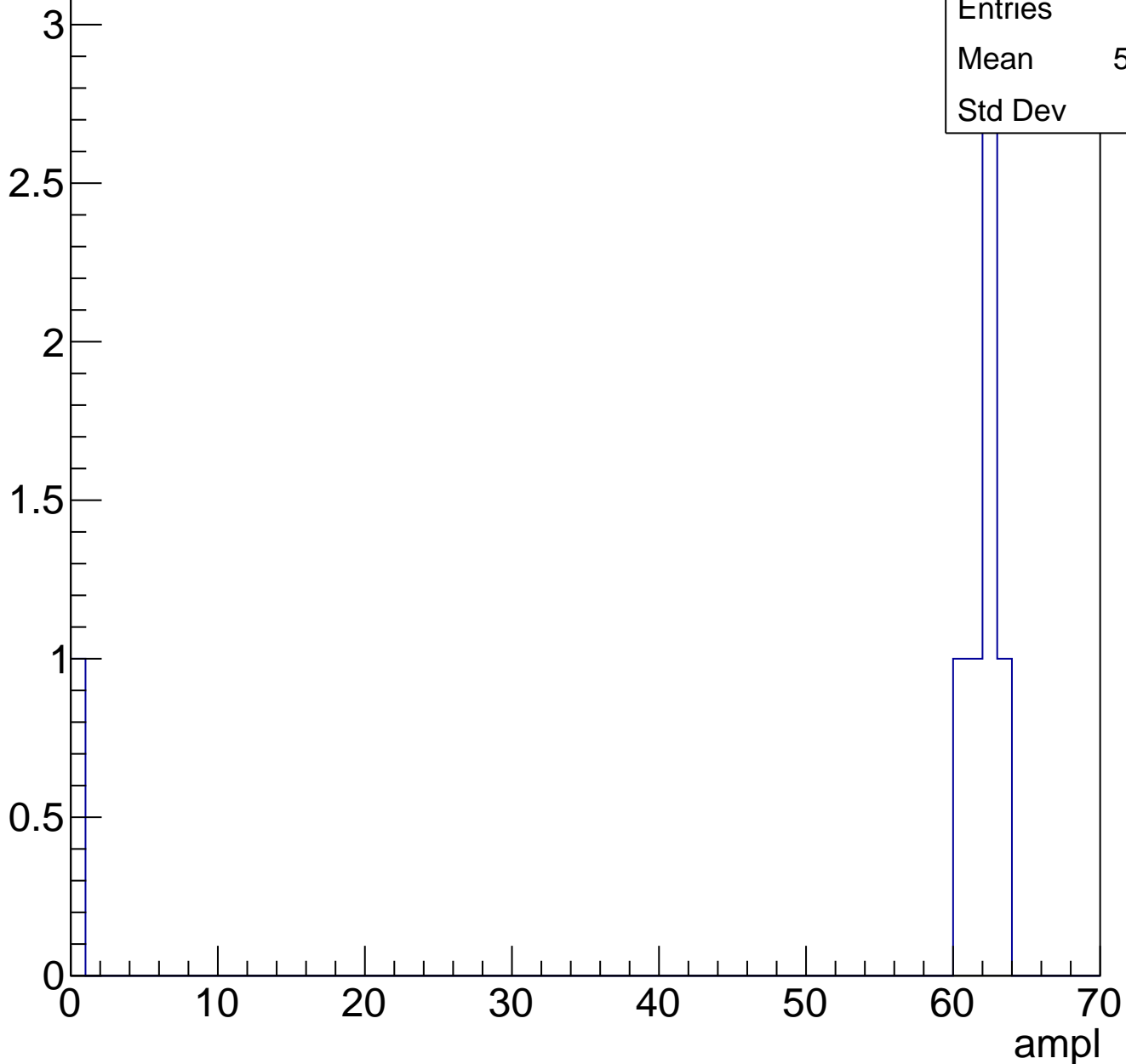
Entry



B1L100S, U19-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch45, adc0

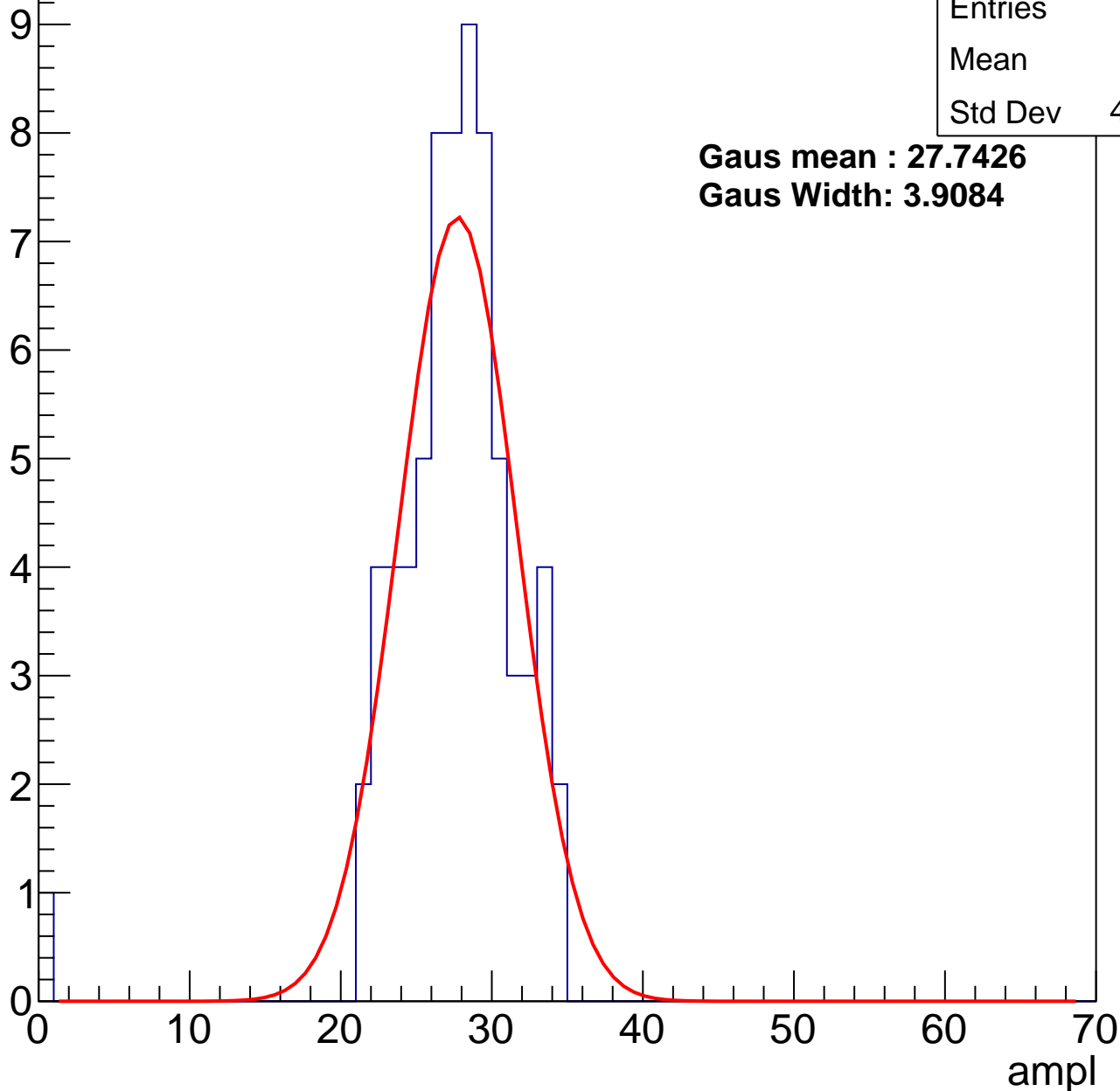
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	27
Std Dev	4.598

Gaus mean : 27.7426

Gaus Width: 3.9084



B1L100S, U19-ch45, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	35.14
Std Dev	3.449

Gaus mean : 35.6439

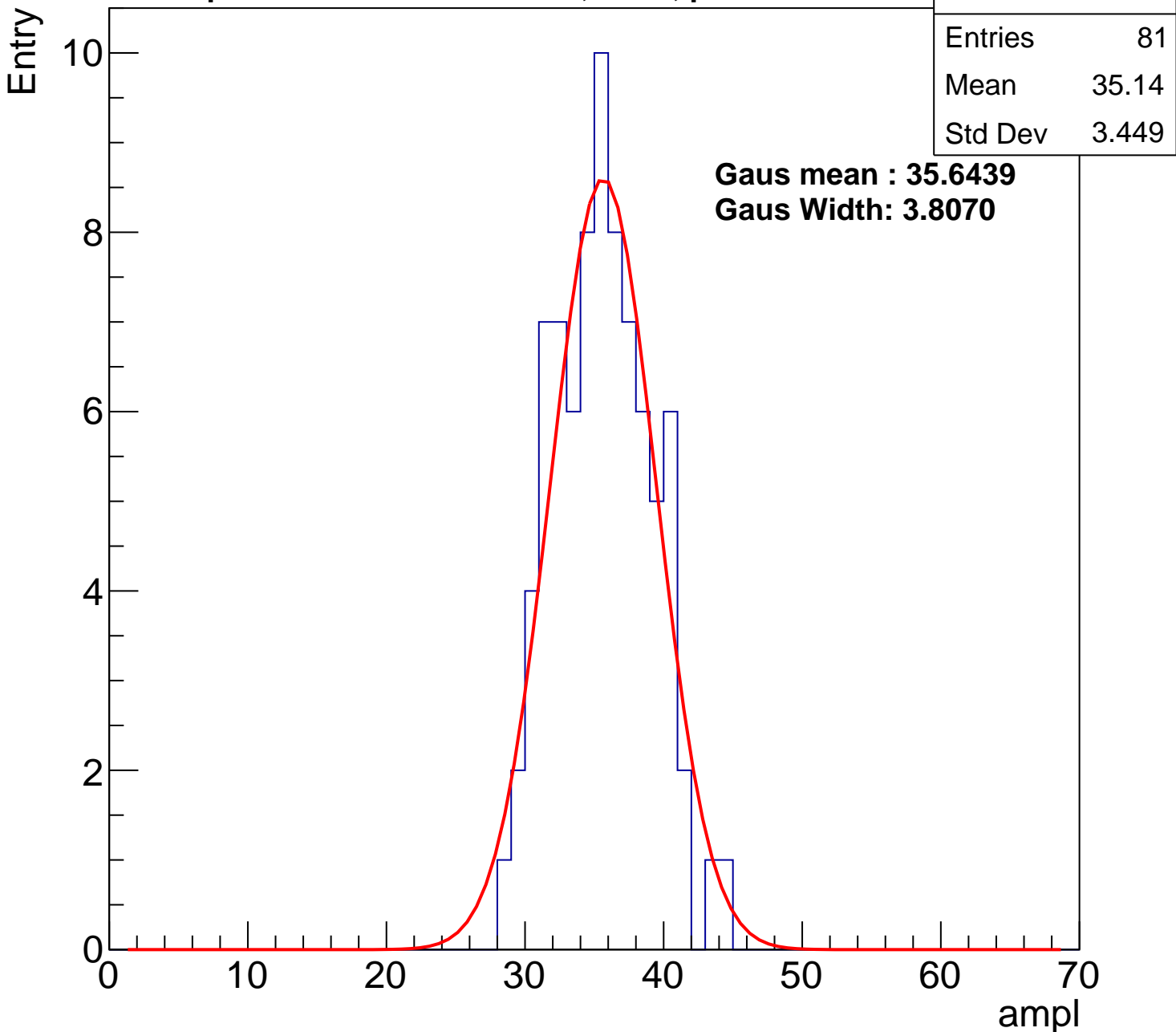
Gaus Width: 3.8070

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch45, adc2

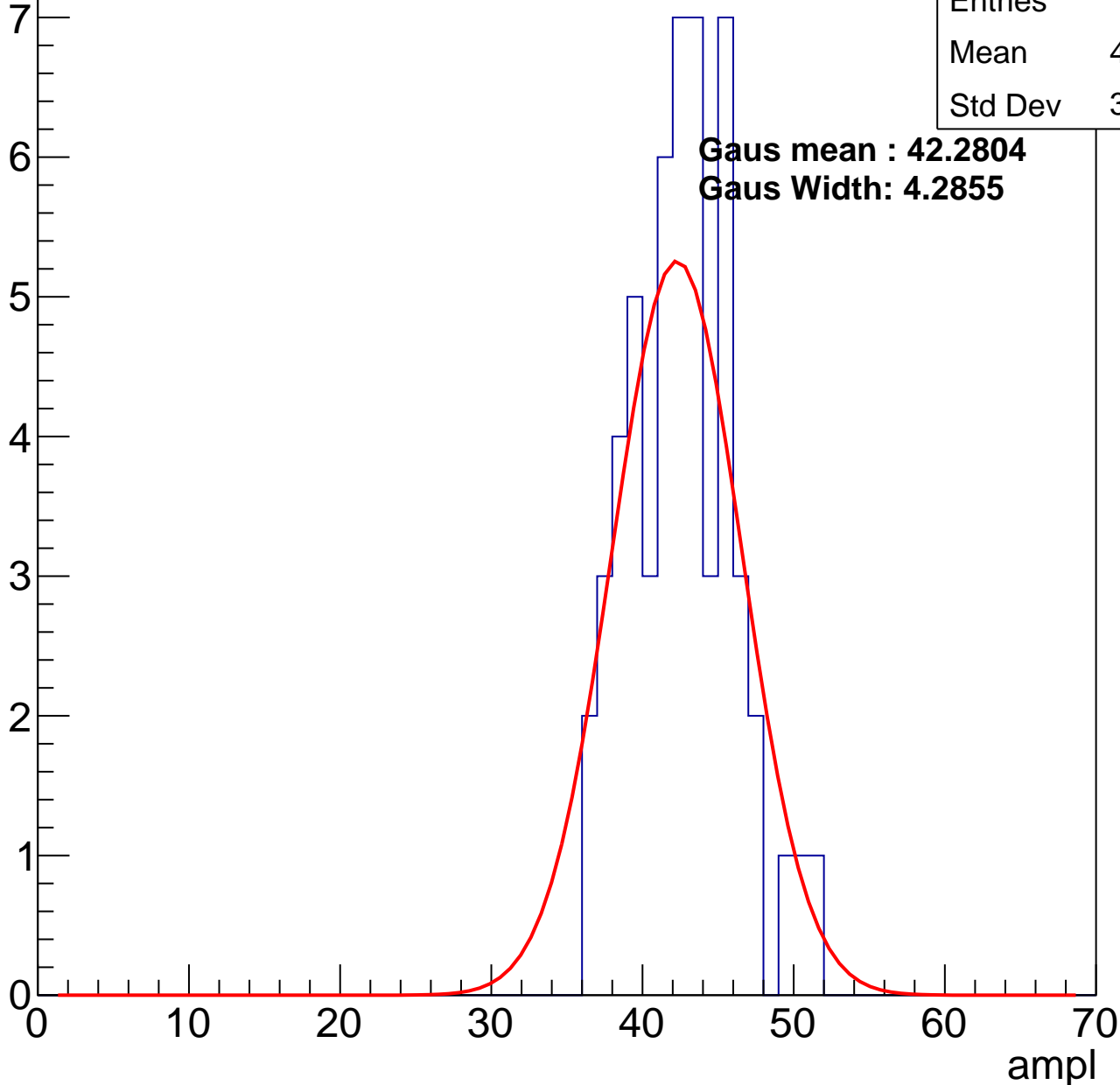
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.18
Std Dev	3.428

Gaus mean : 42.2804

Gaus Width: 4.2855



B1L100S, U19-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

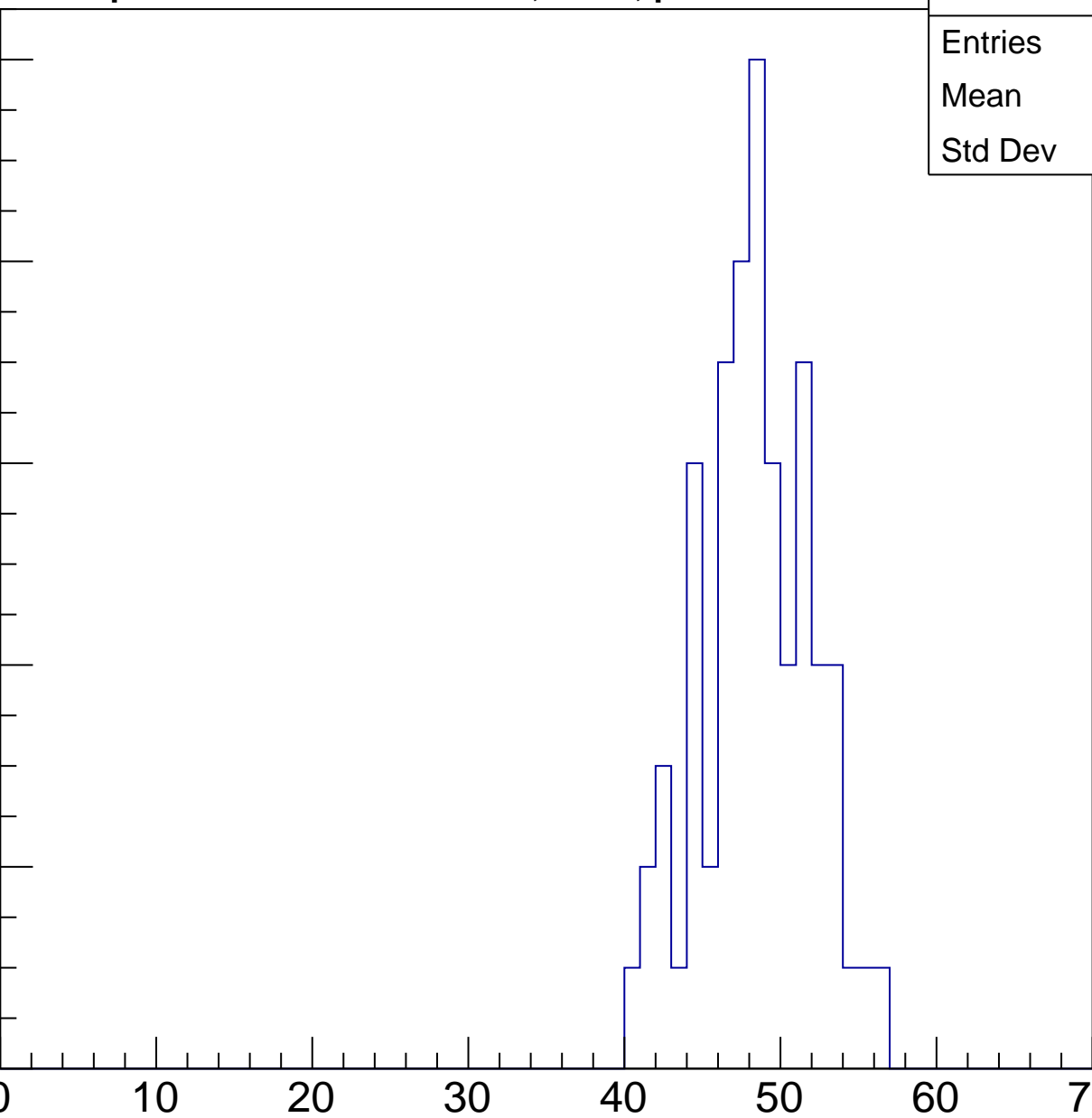
Entries	68
Mean	47.93
Std Dev	3.52

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

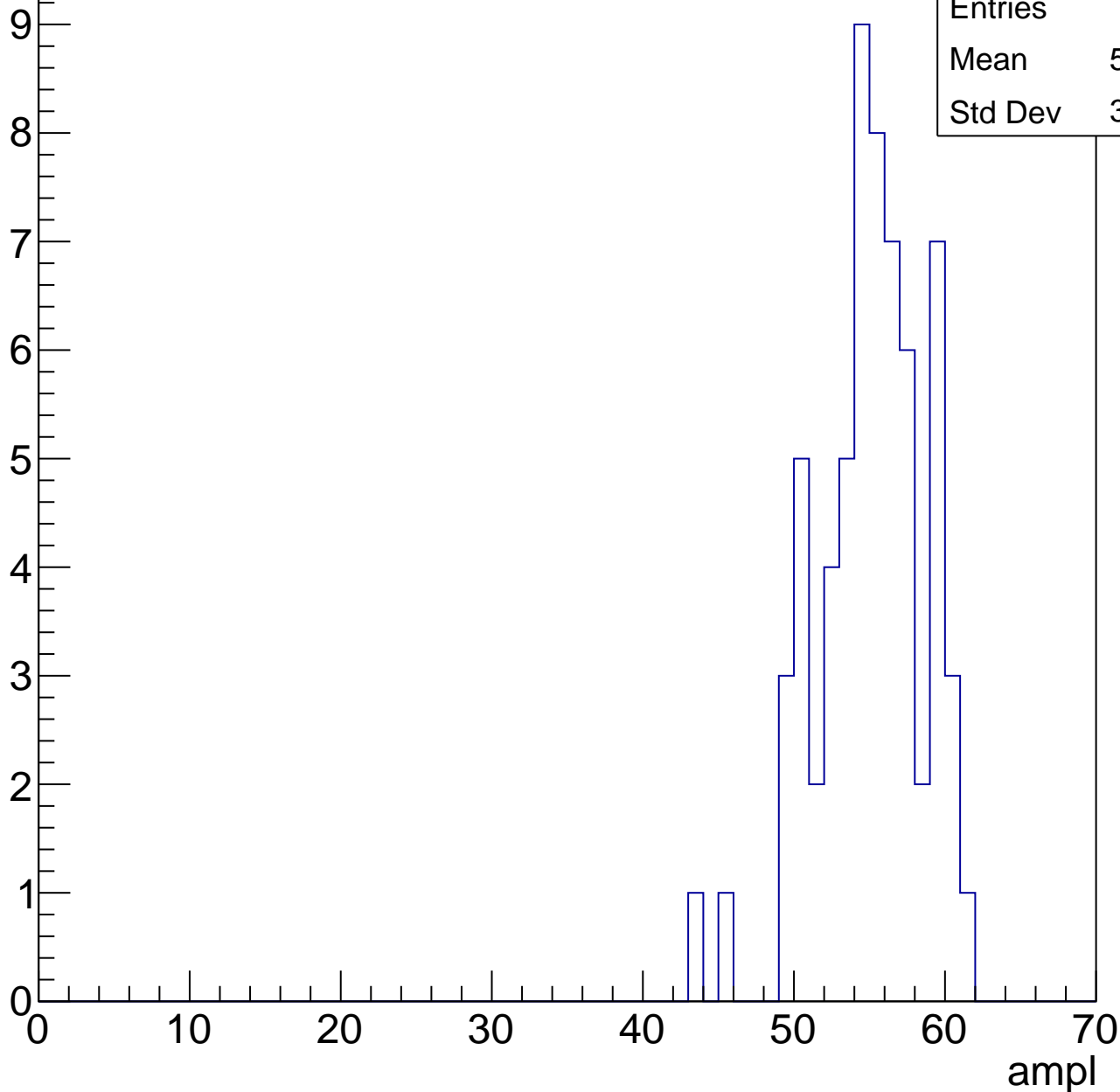


B1L100S, U19-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

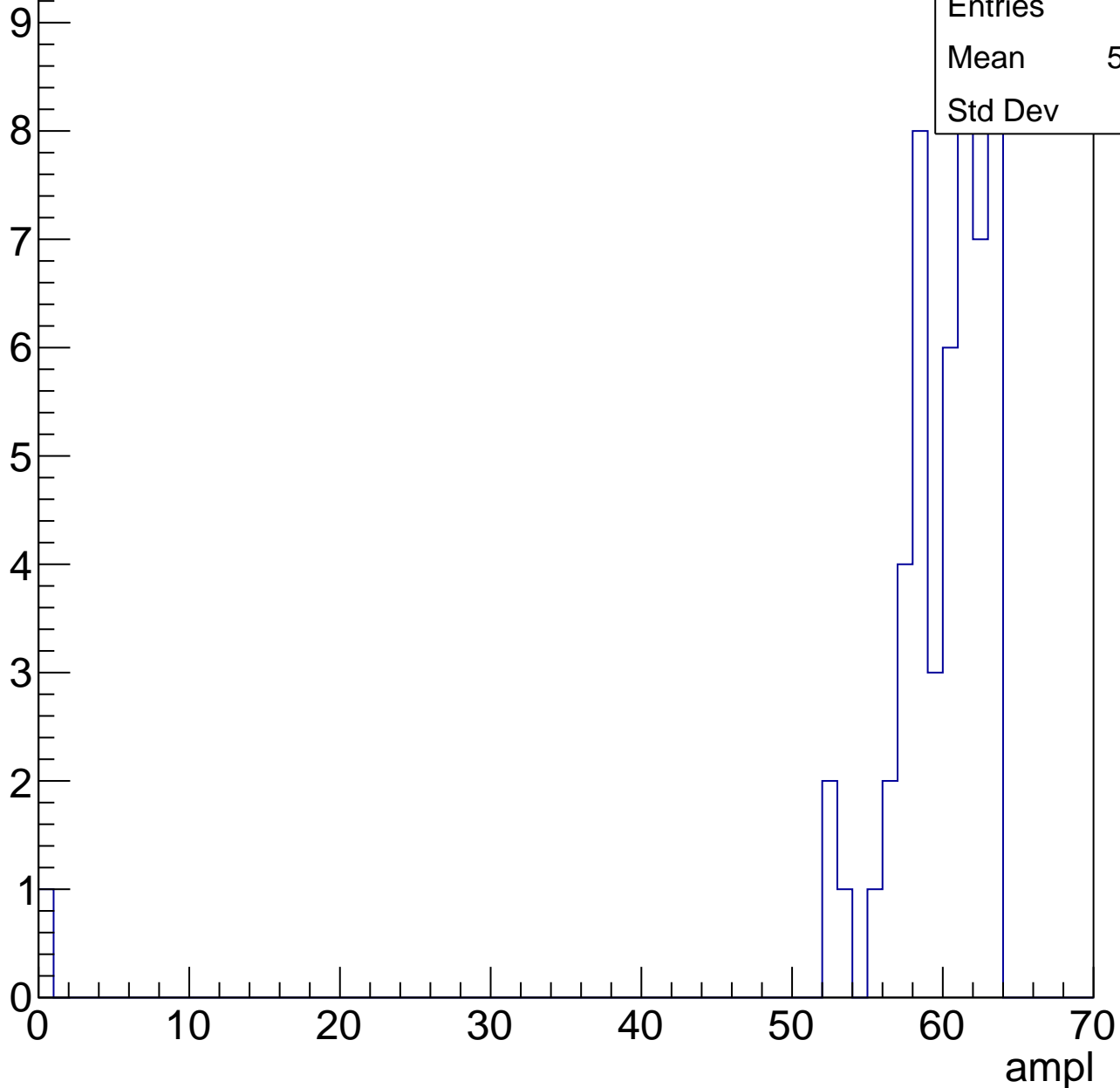
Entries	64
Mean	54.53
Std Dev	3.605



B1L100S, U19-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

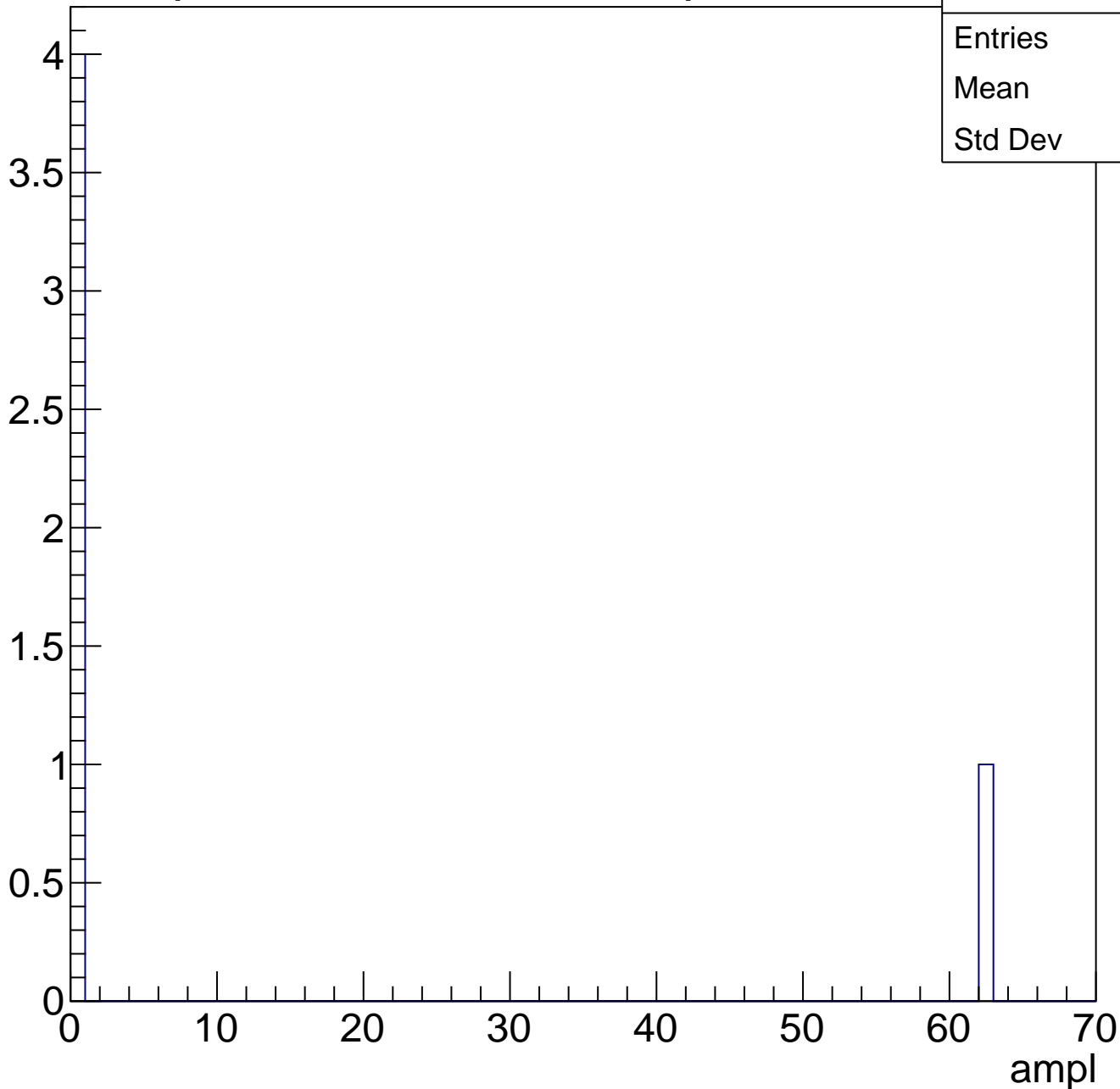
Entry



B1L100S, U19-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch46, adc0

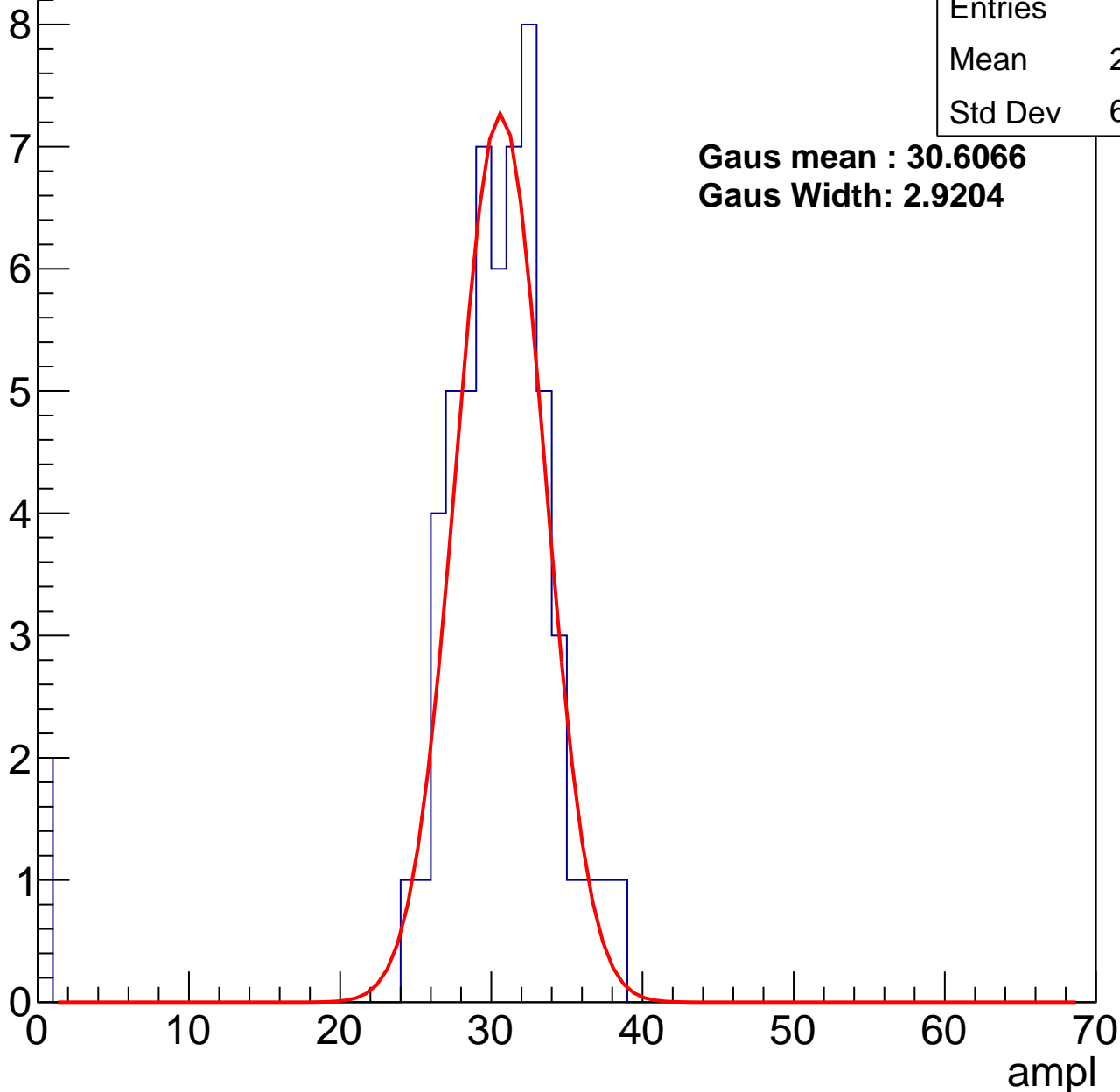
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.26
Std Dev	6.258

Gaus mean : 30.6066

Gaus Width: 2.9204



B1L100S, U19-ch46, adc1

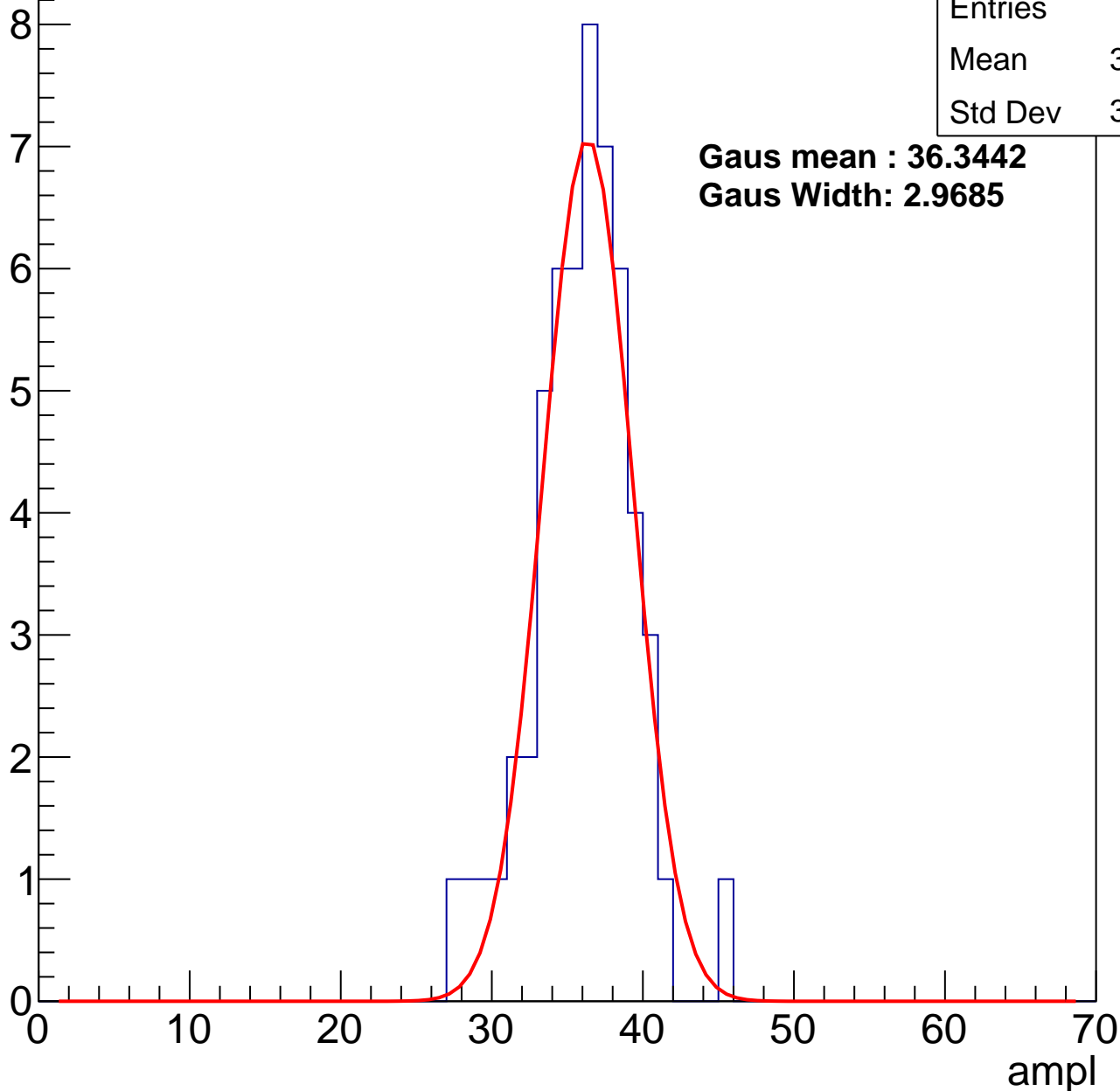
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.56
Std Dev	3.296

Gaus mean : 36.3442

Gaus Width: 2.9685



B1L100S, U19-ch46, adc2

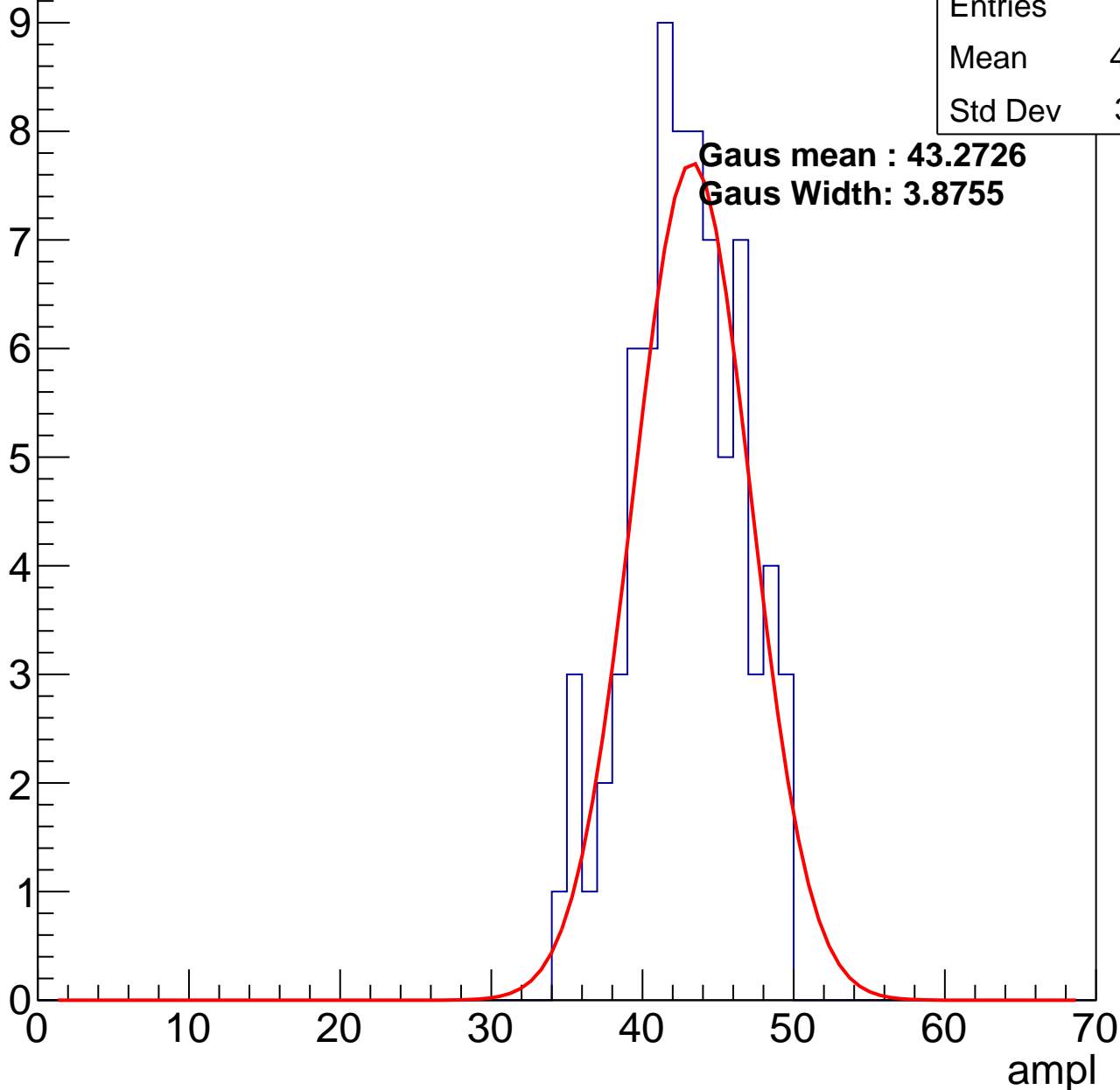
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.38
Std Dev	3.591

Gaus mean : 43.2726

Gaus Width: 3.8755



B1L100S, U19-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	49.49
Std Dev	3.678

Entry

10

8

6

4

2

0

0

10

20

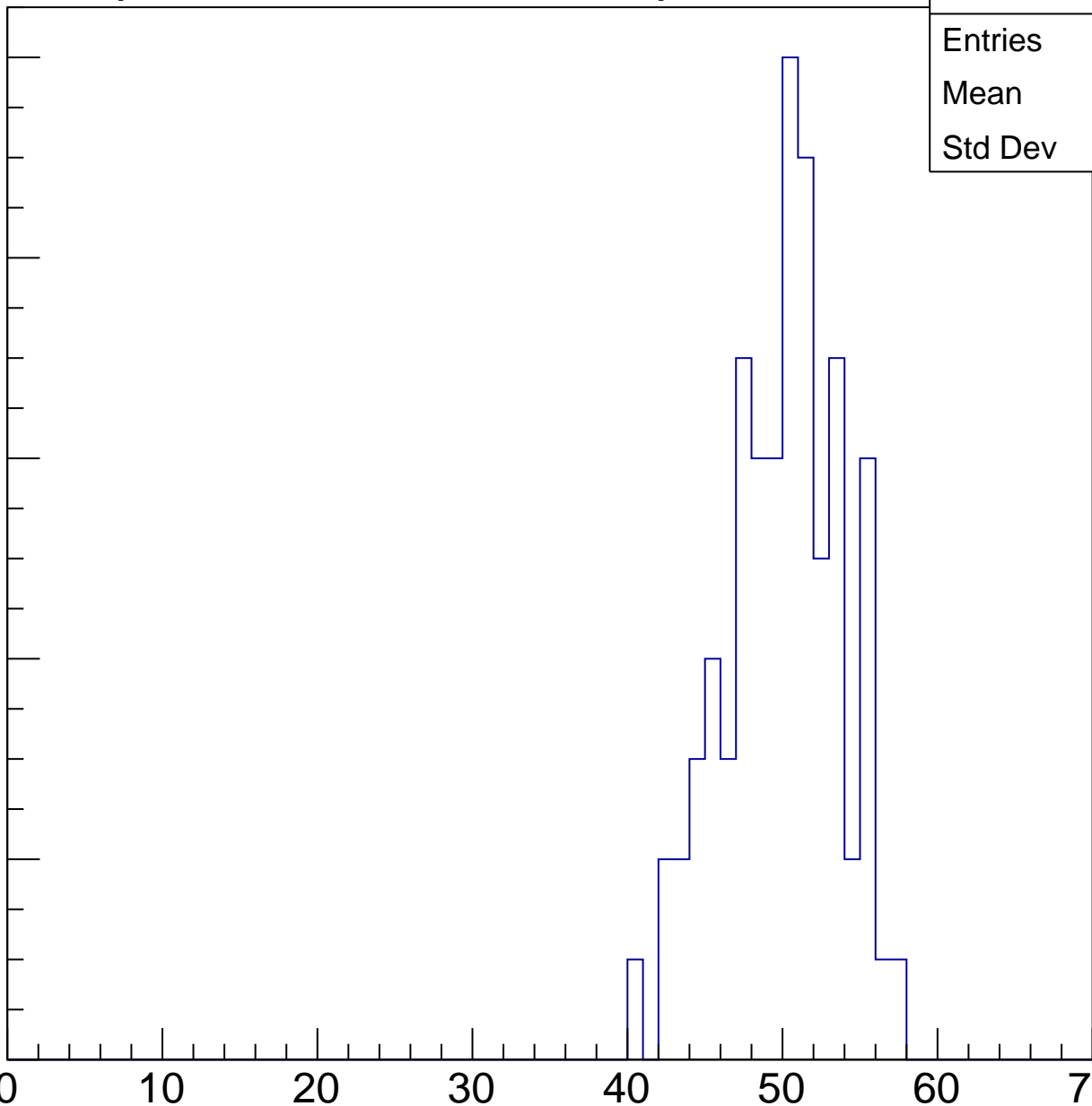
30

40

50

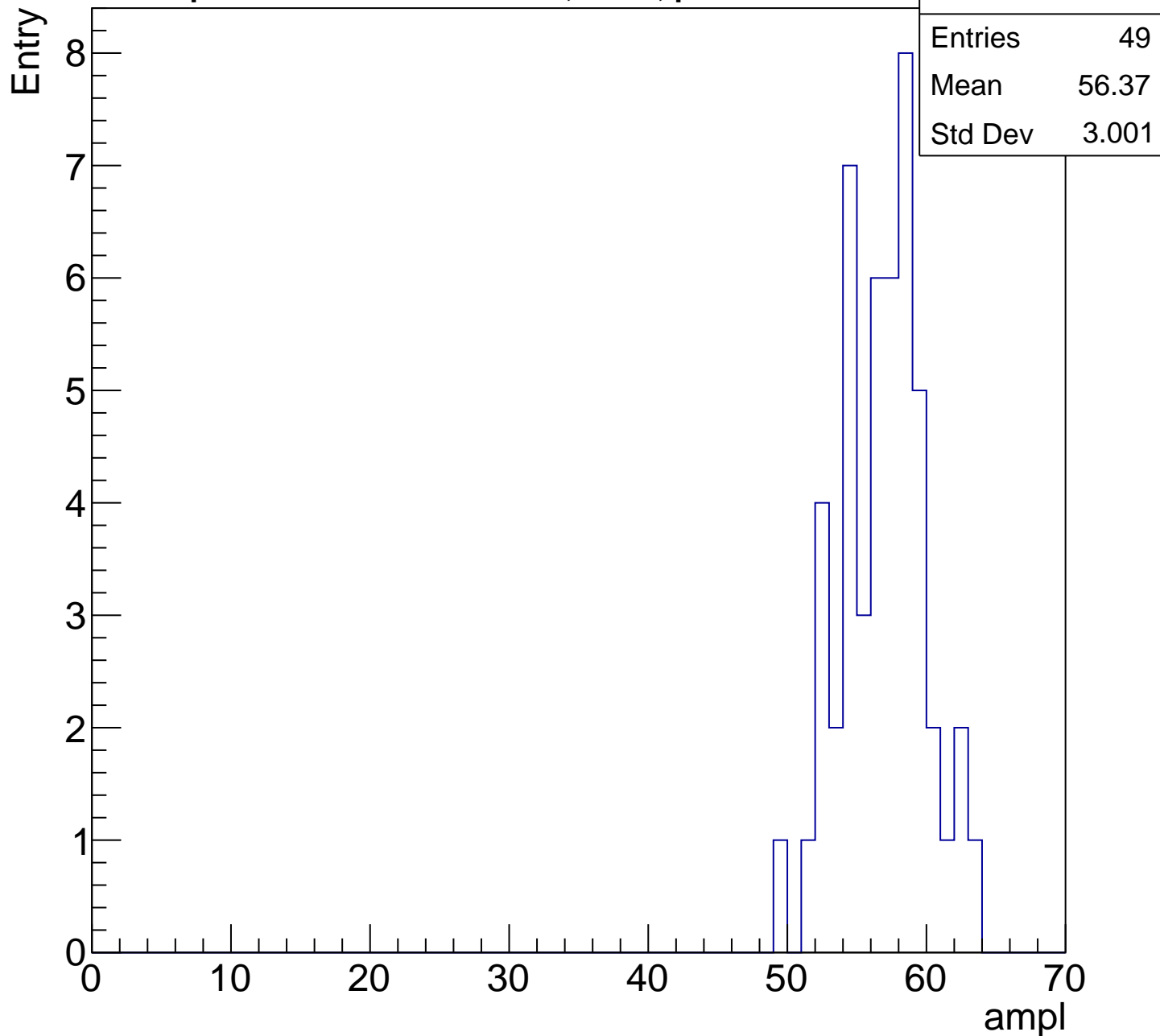
60

ampl



B1L100S, U19-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

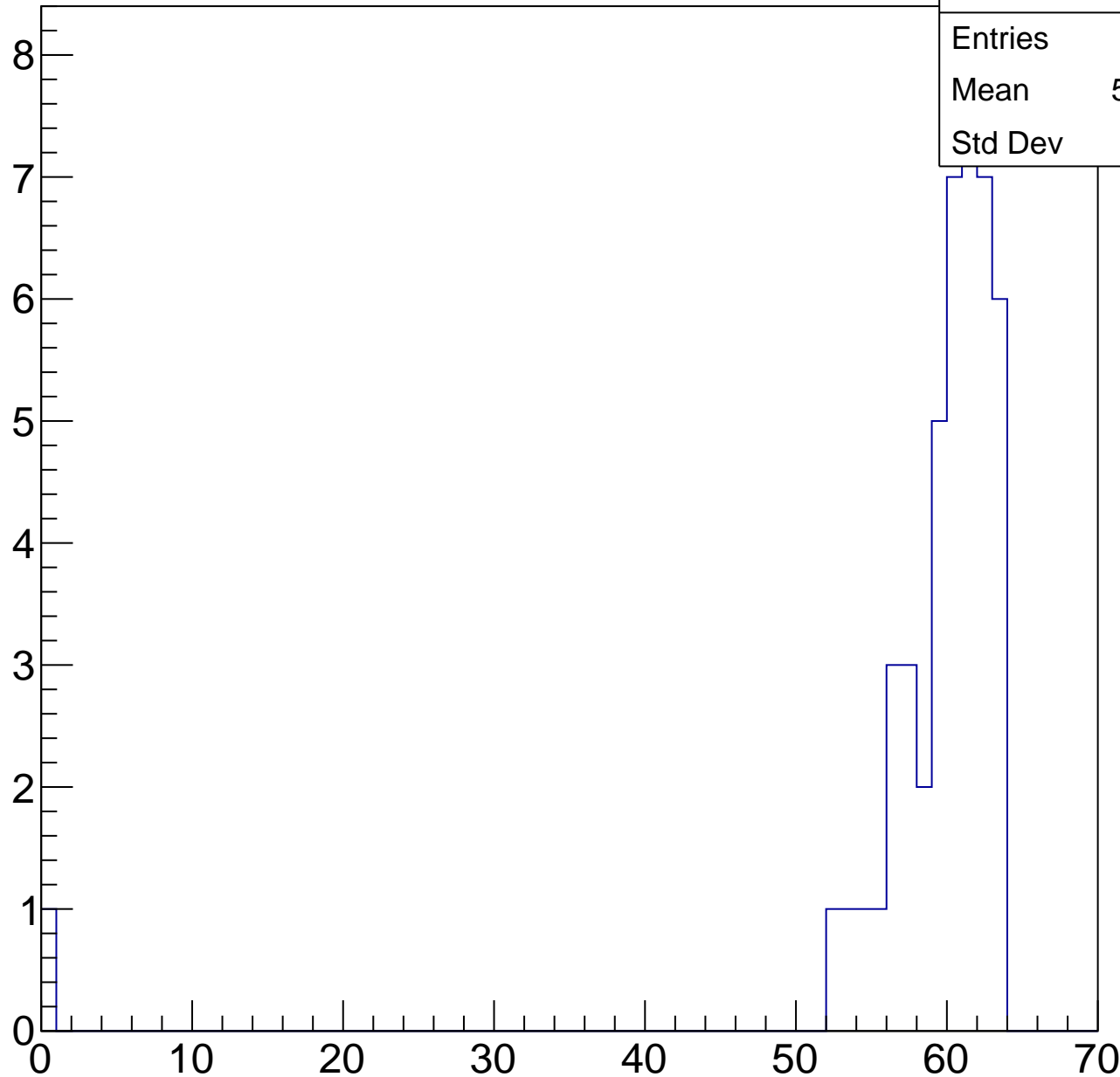
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.35
Std Dev	9.12

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch47, adc0

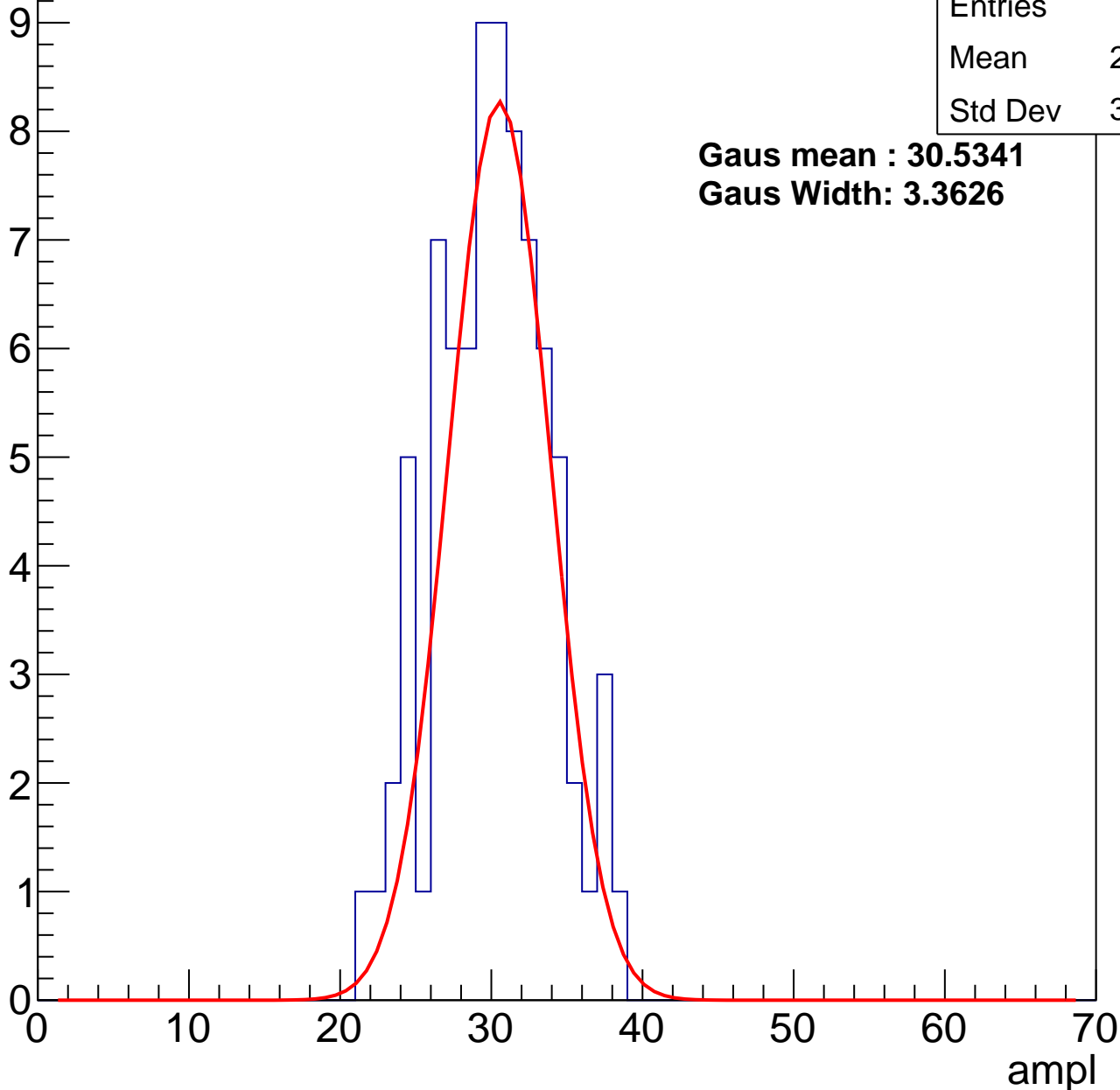
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	29.65
Std Dev	3.715

Gaus mean : 30.5341

Gaus Width: 3.3626



B1L100S, U19-ch47, adc1

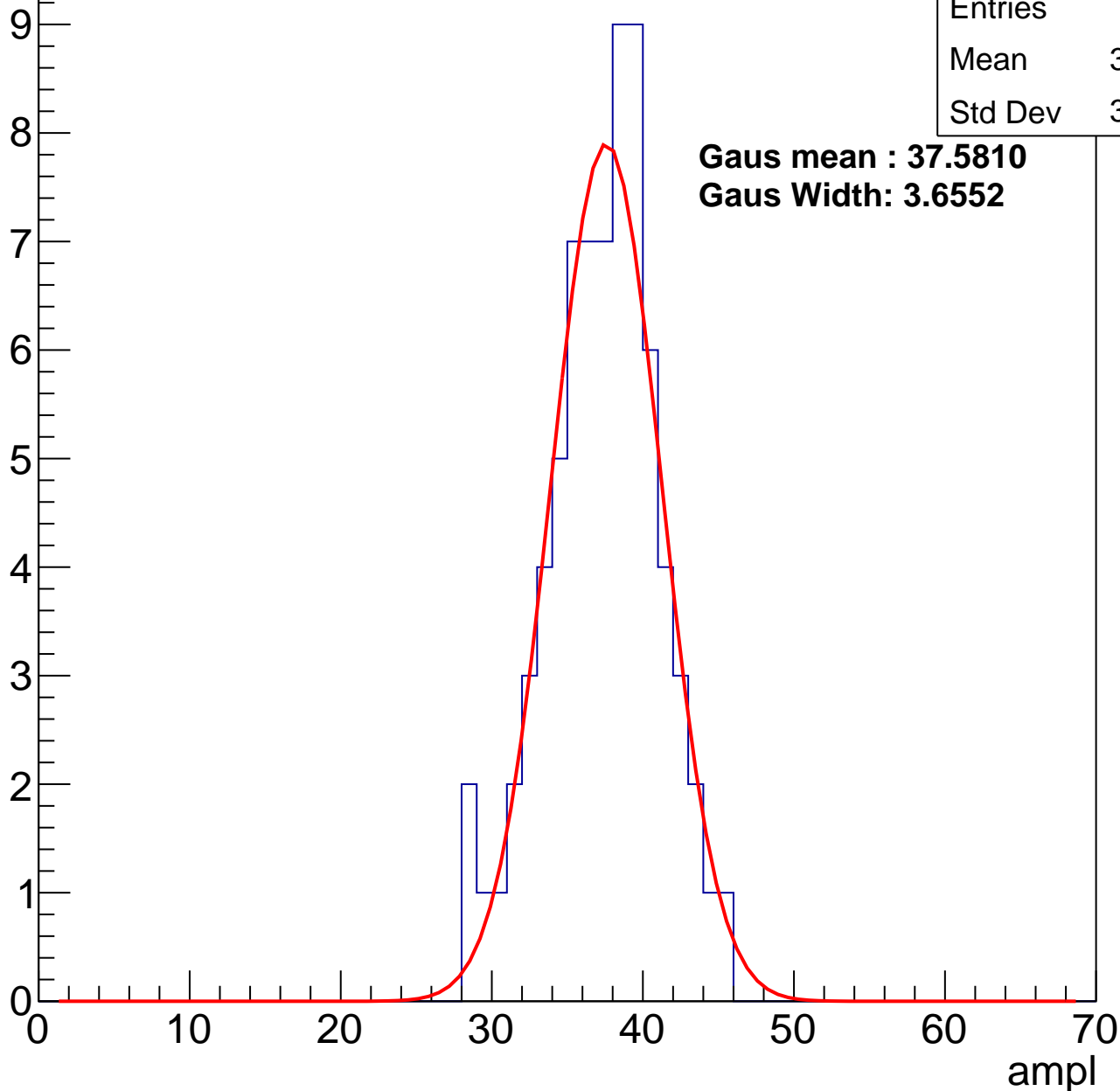
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.88
Std Dev	3.654

Gaus mean : 37.5810

Gaus Width: 3.6552



B1L100S, U19-ch47, adc2

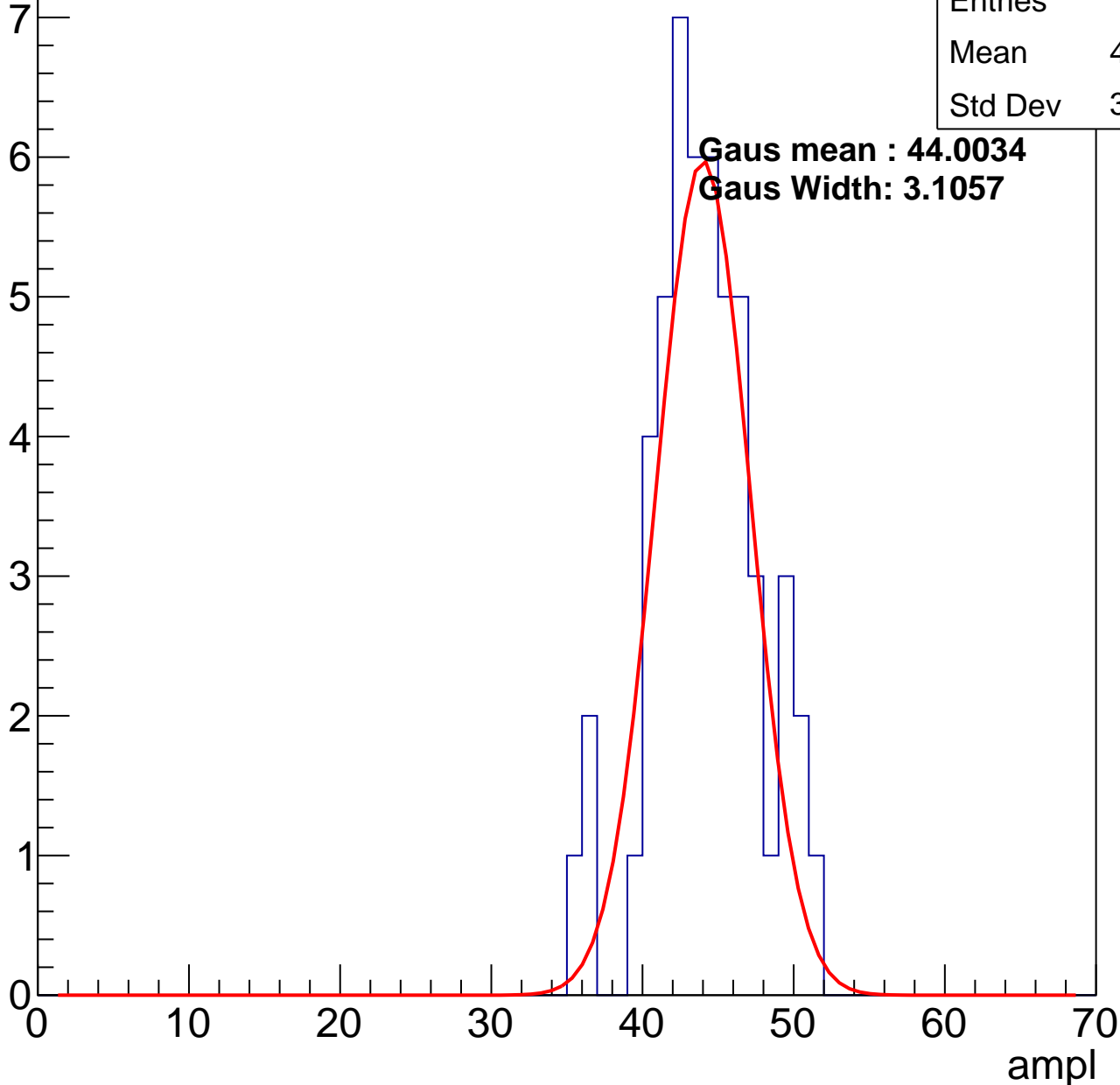
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.63
Std Dev	3.492

Gaus mean : 44.0034

Gaus Width: 3.1057

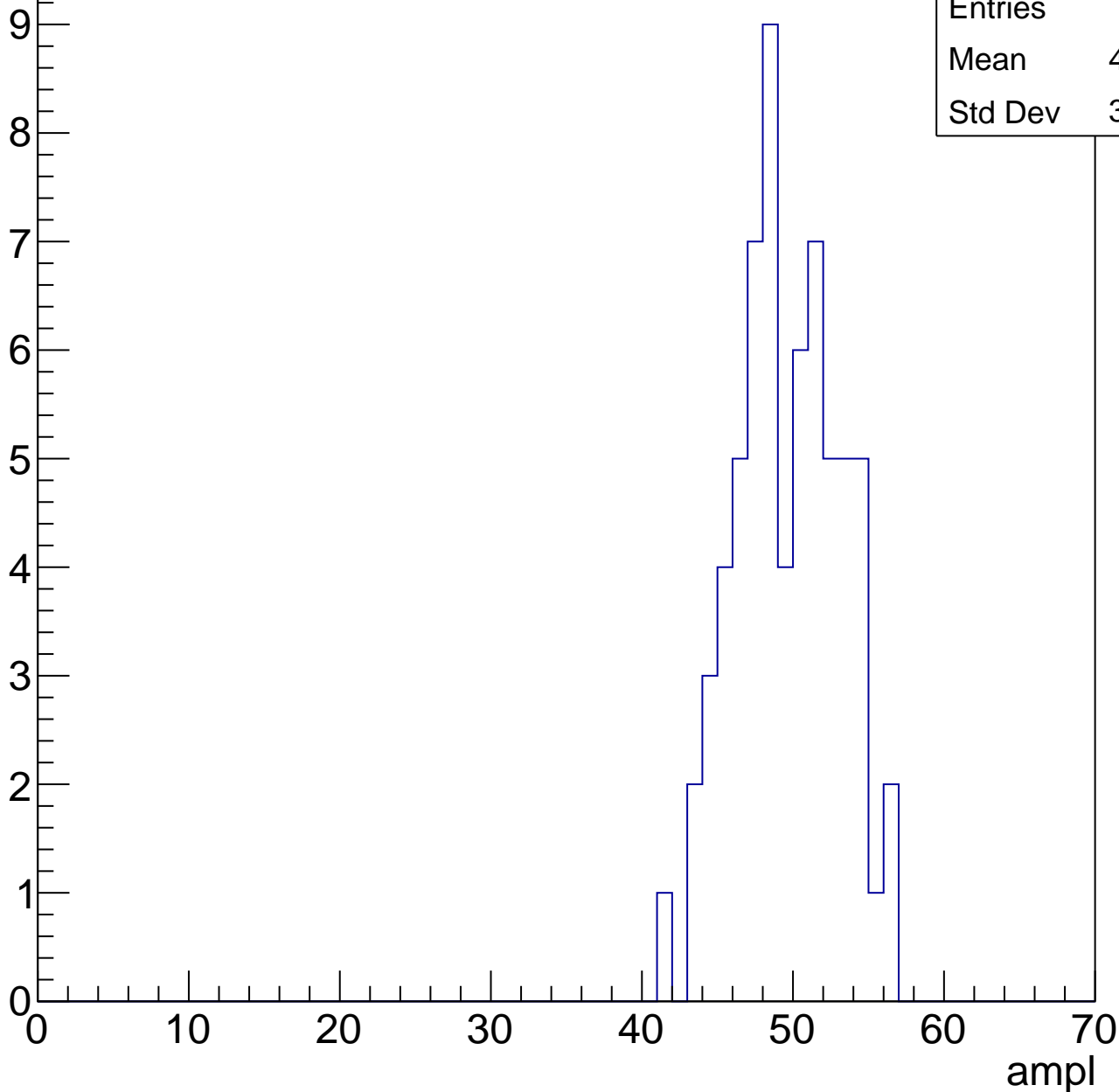


B1L100S, U19-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.17
Std Dev	3.423

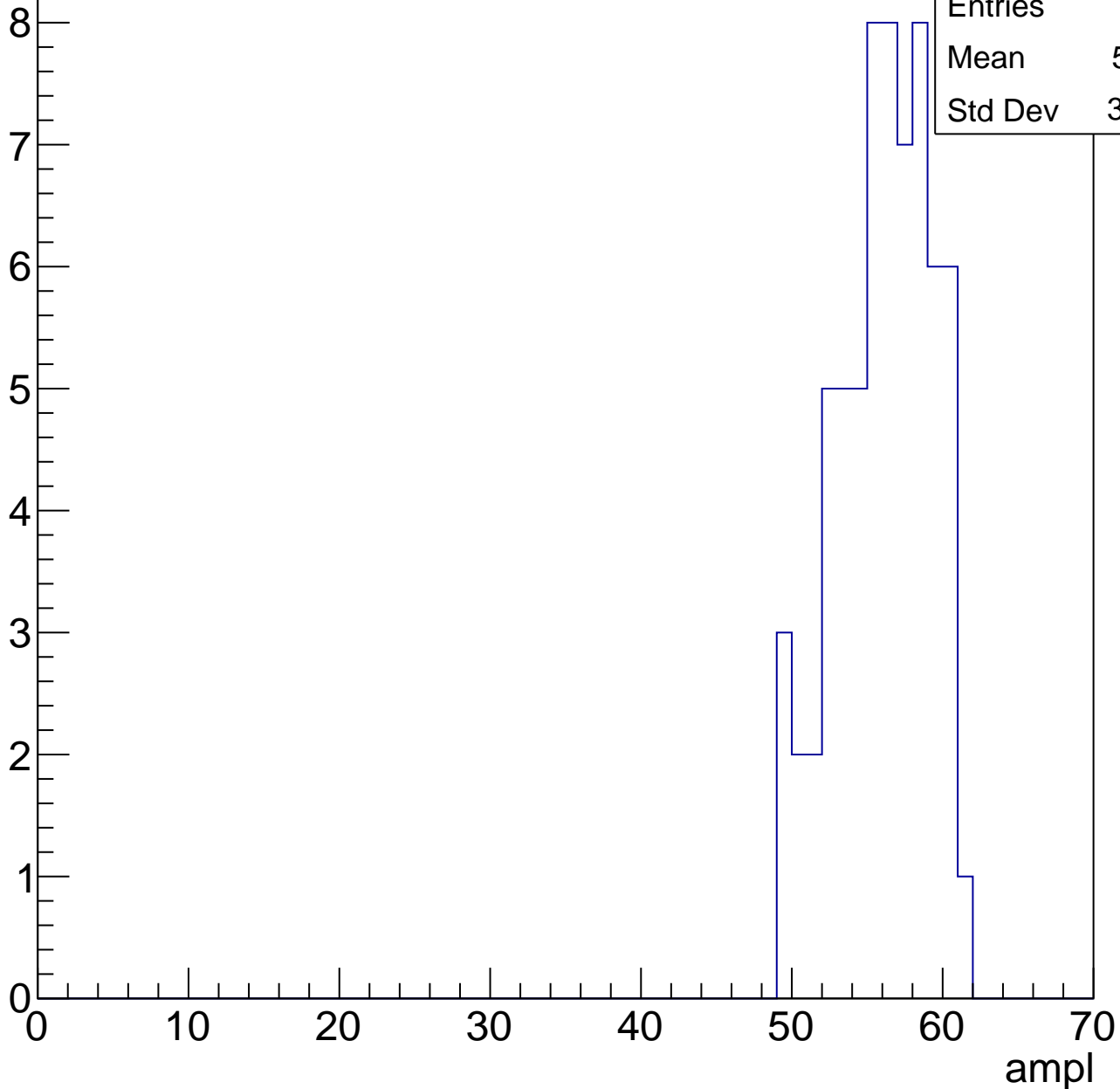


B1L100S, U19-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

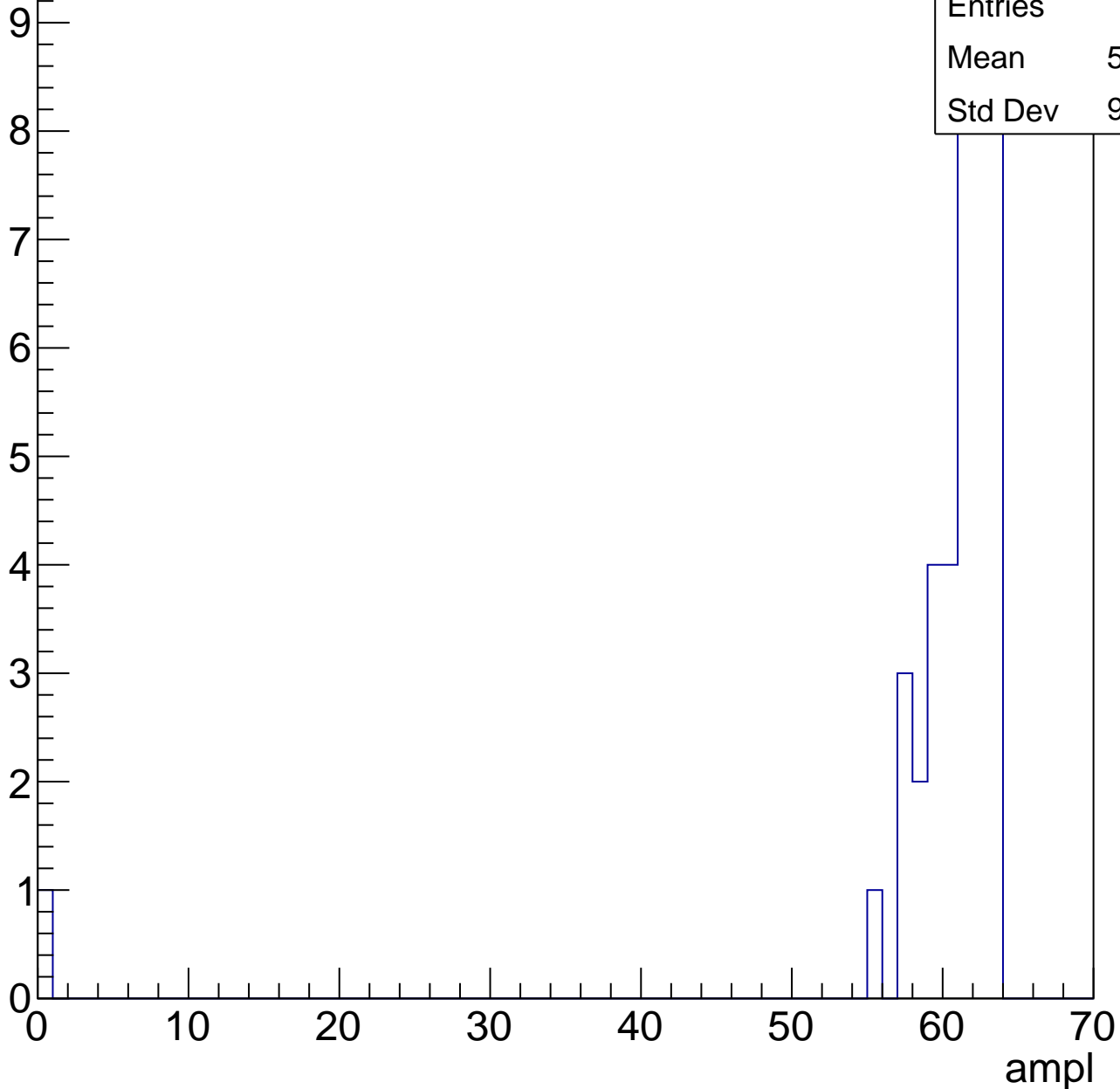
Entries	66
Mean	55.61
Std Dev	3.079



B1L100S, U19-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L100S, U19-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U19-ch48, adc0

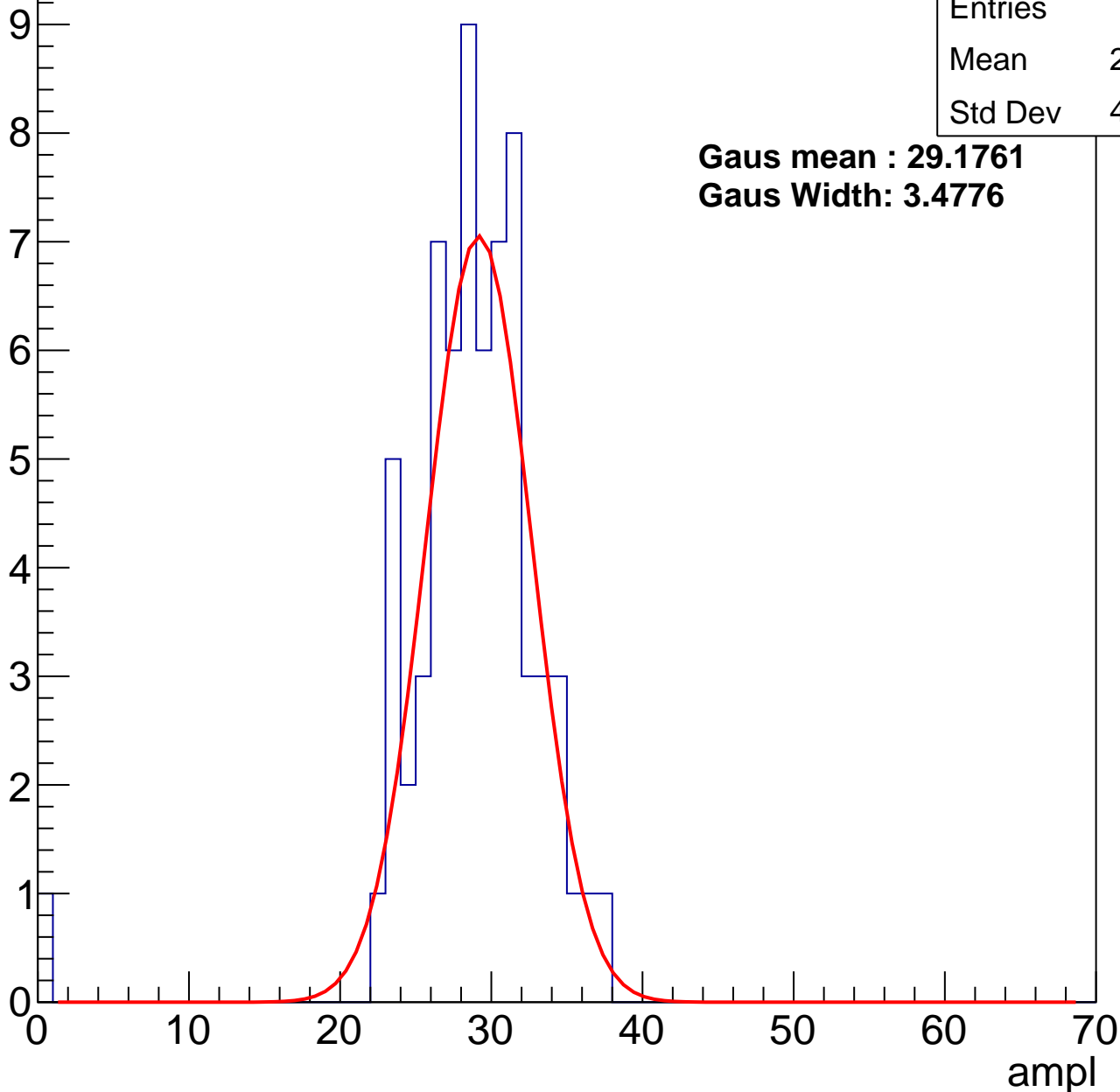
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.25
Std Dev	4.833

Gaus mean : 29.1761

Gaus Width: 3.4776



B1L100S, U19-ch48, adc1

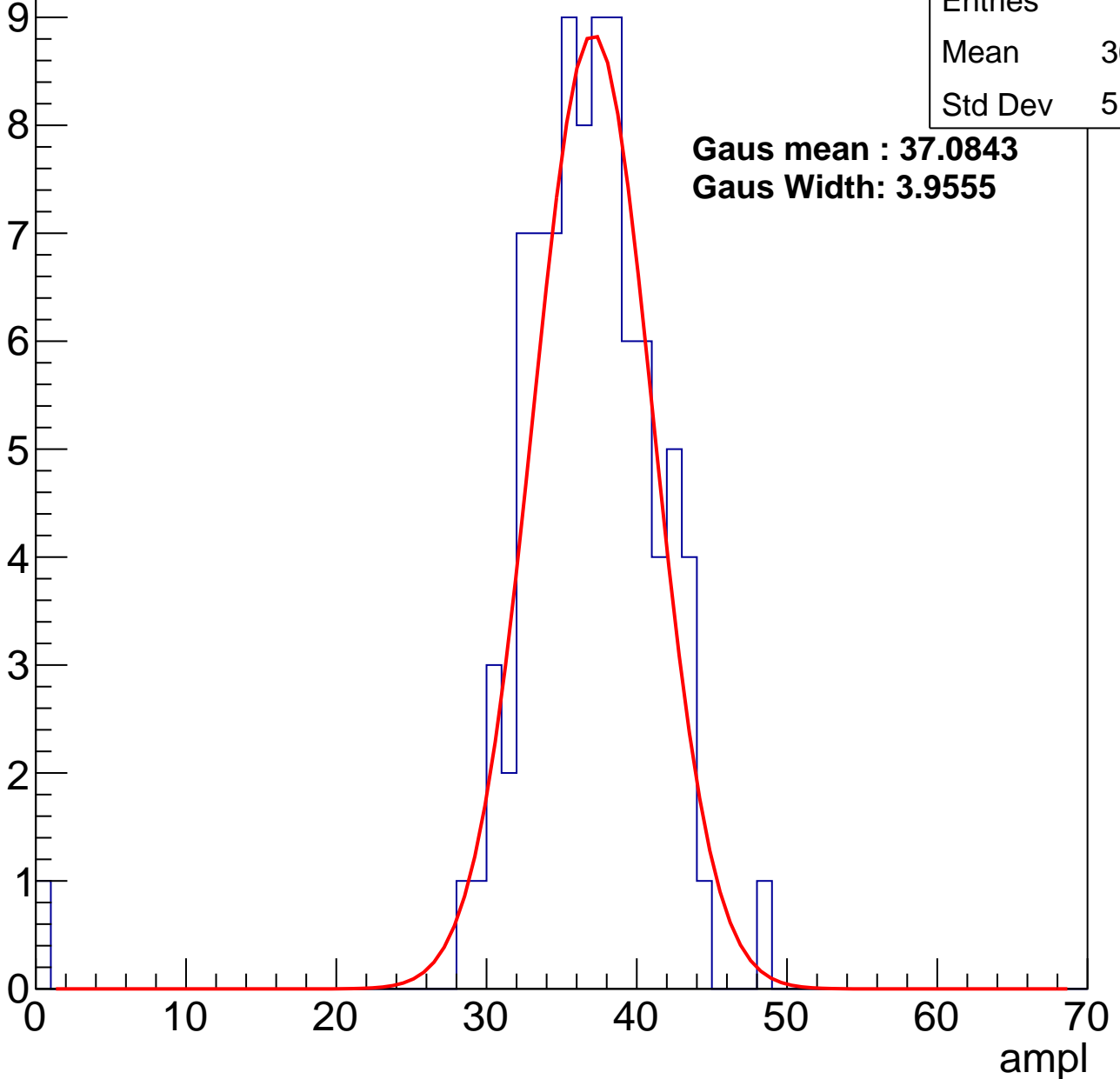
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	91
Mean	36.18
Std Dev	5.402

Gaus mean : 37.0843

Gaus Width: 3.9555



B1L100S, U19-ch48, adc2

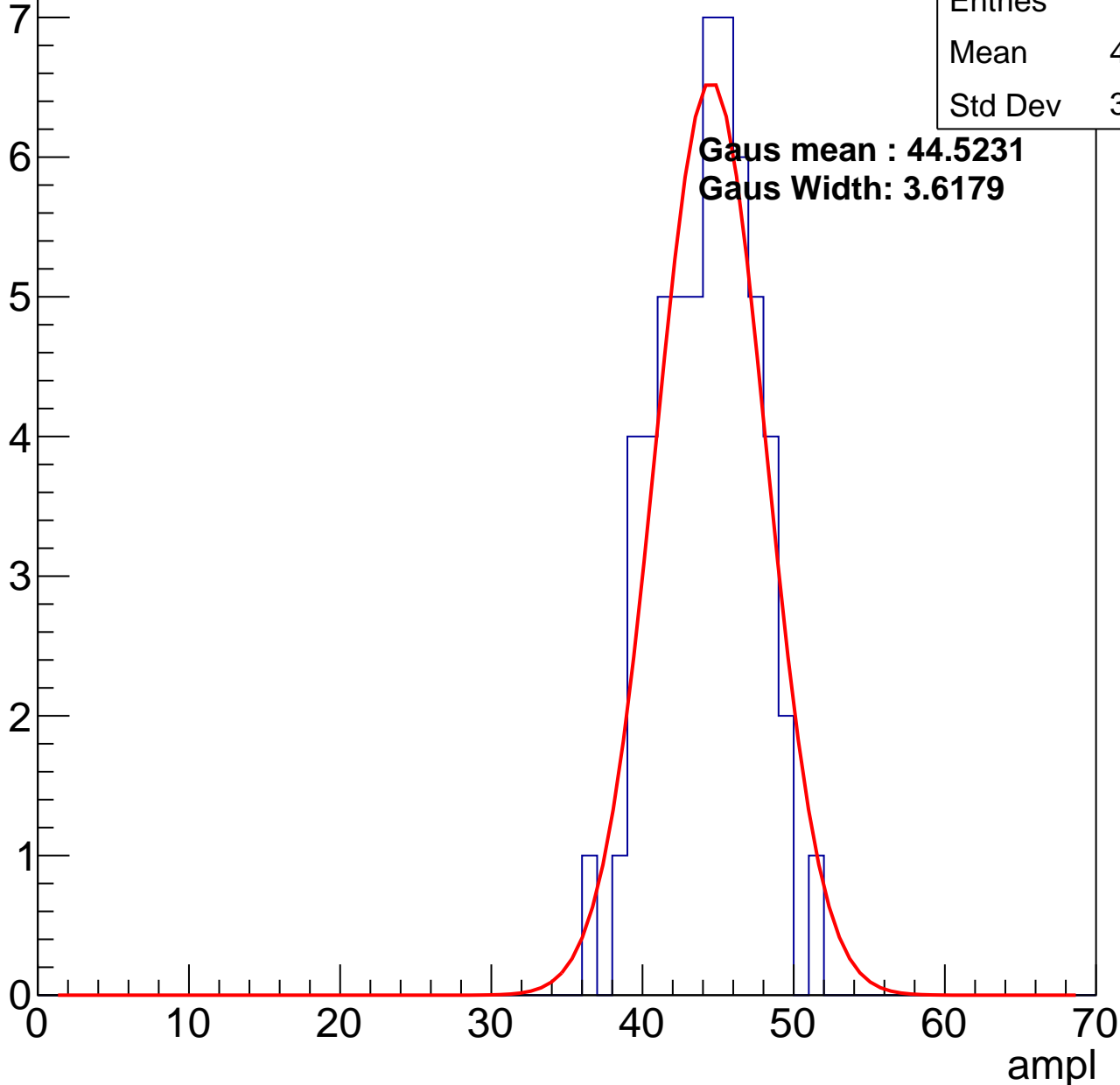
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	43.77
Std Dev	3.162

Gaus mean : 44.5231

Gaus Width: 3.6179

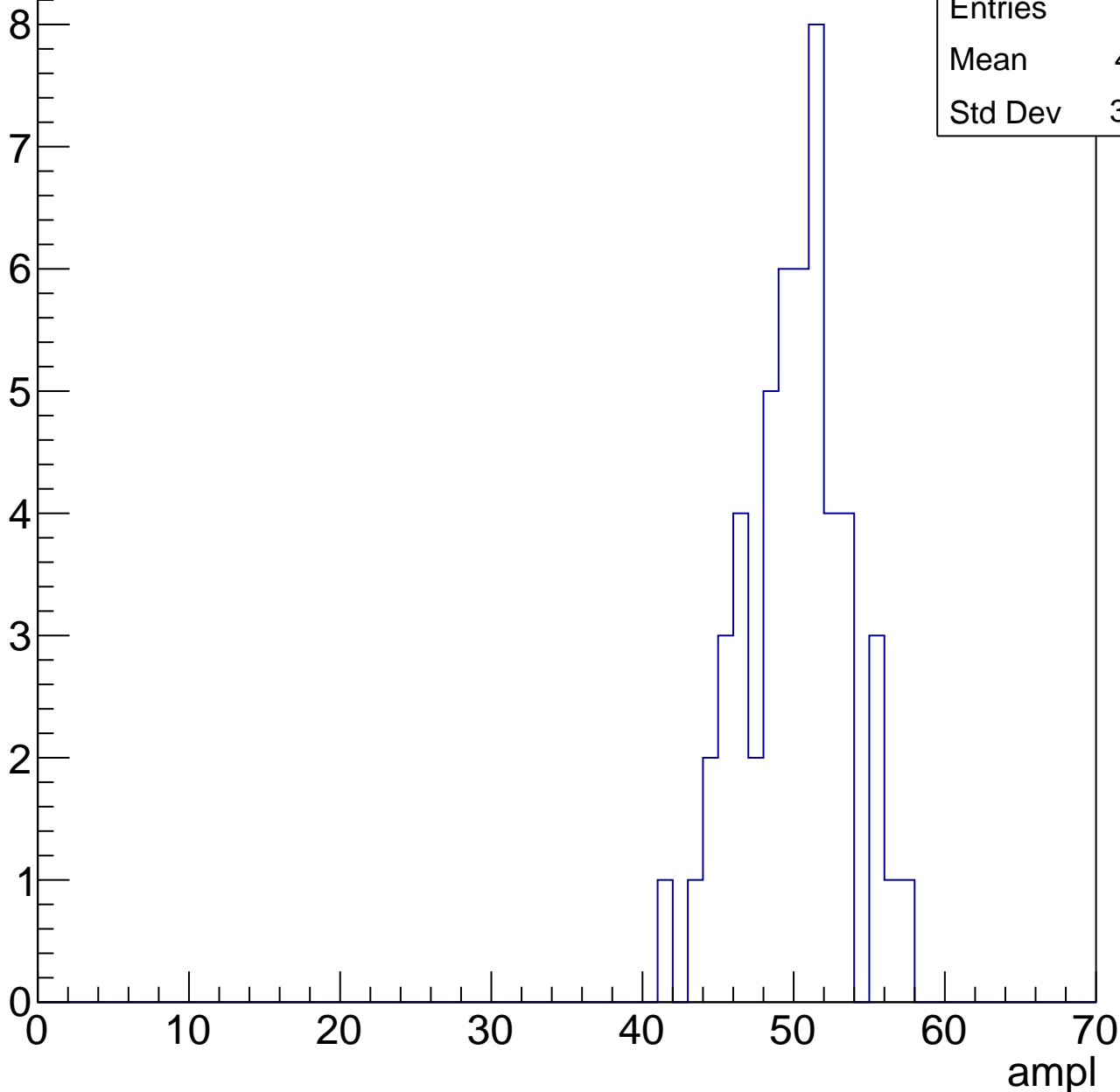


B1L100S, U19-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	49.51
Std Dev	3.409

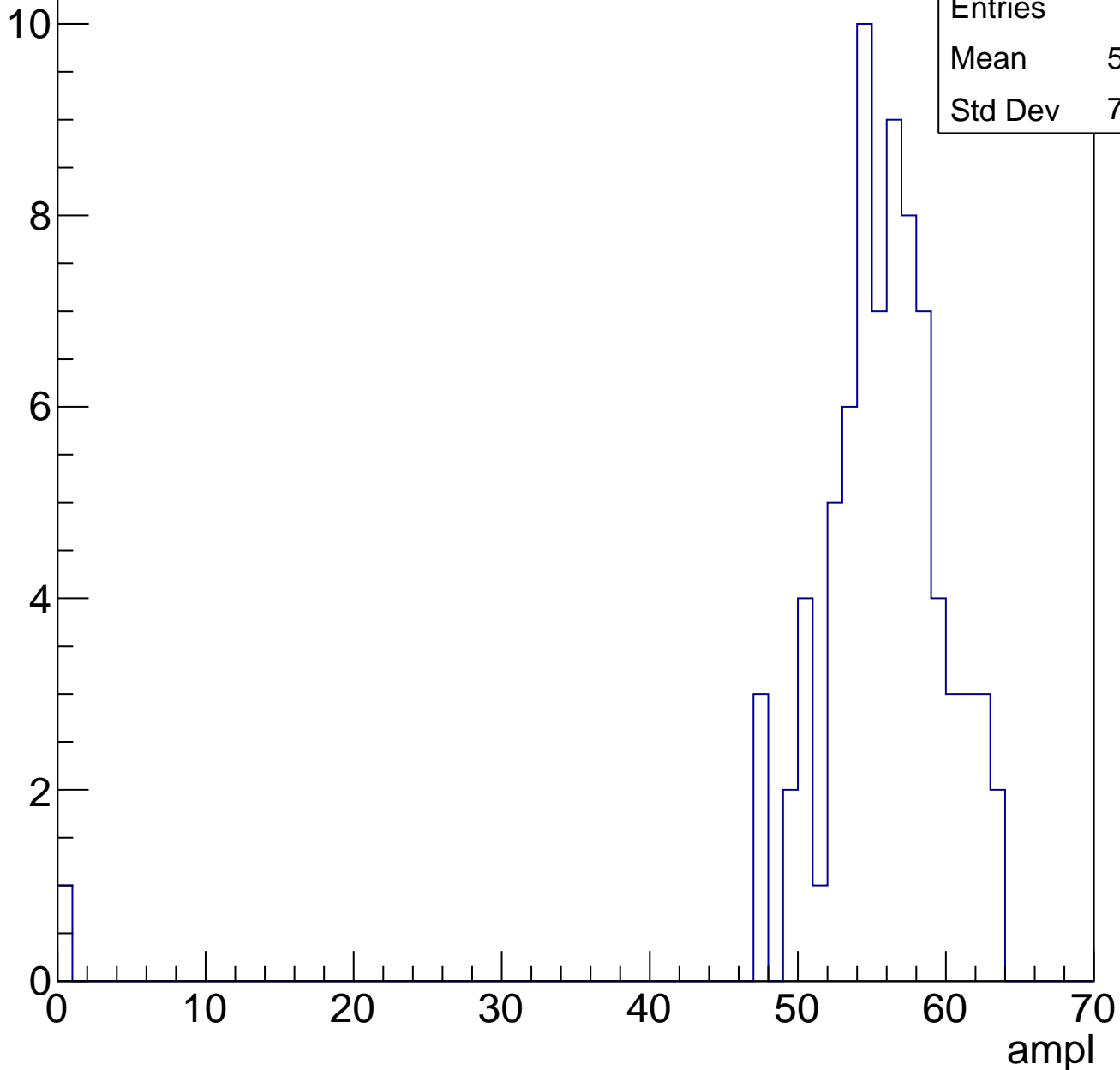


B1L100S, U19-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	54.74
Std Dev	7.254

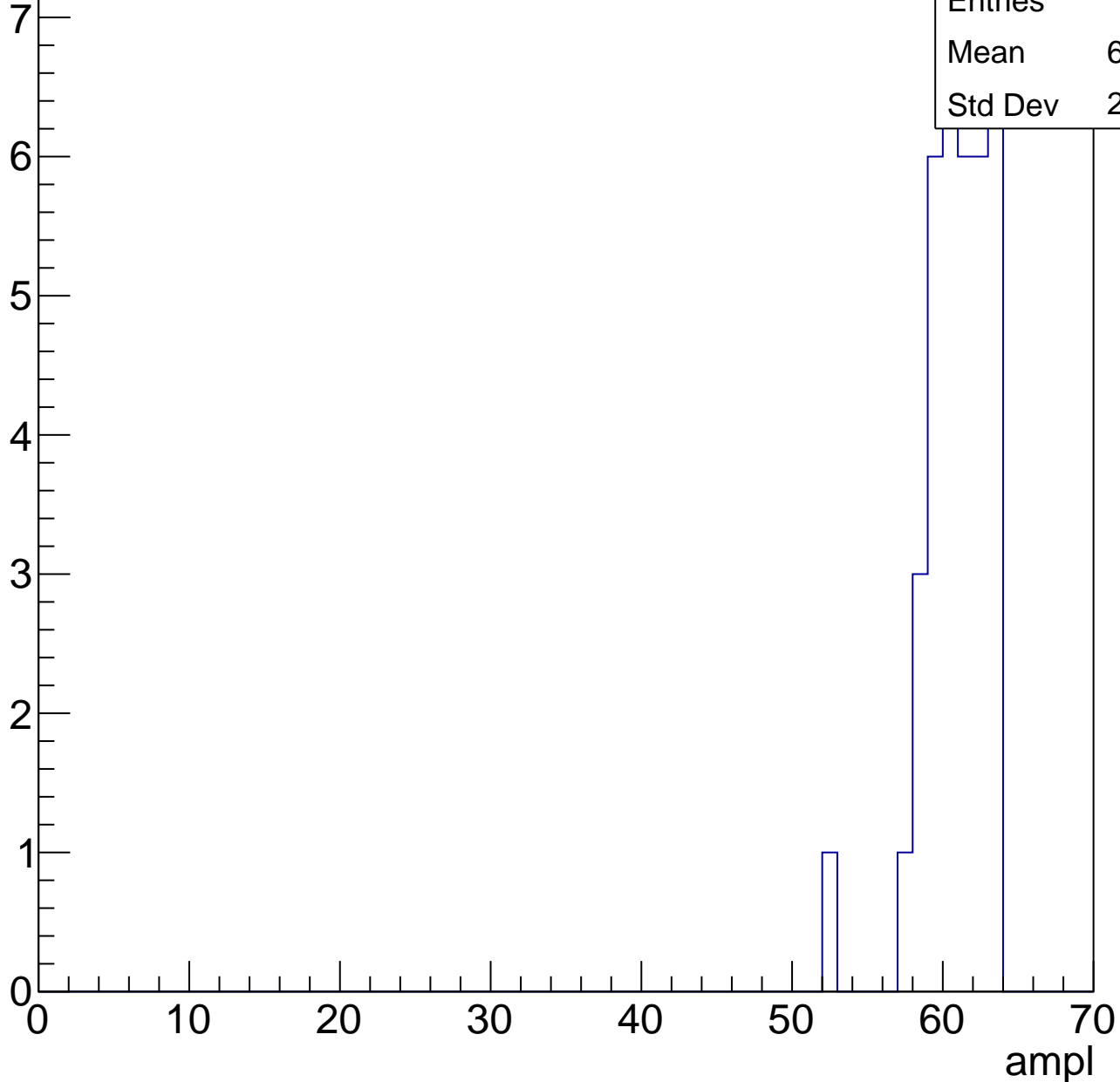
Entry



B1L100S, U19-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

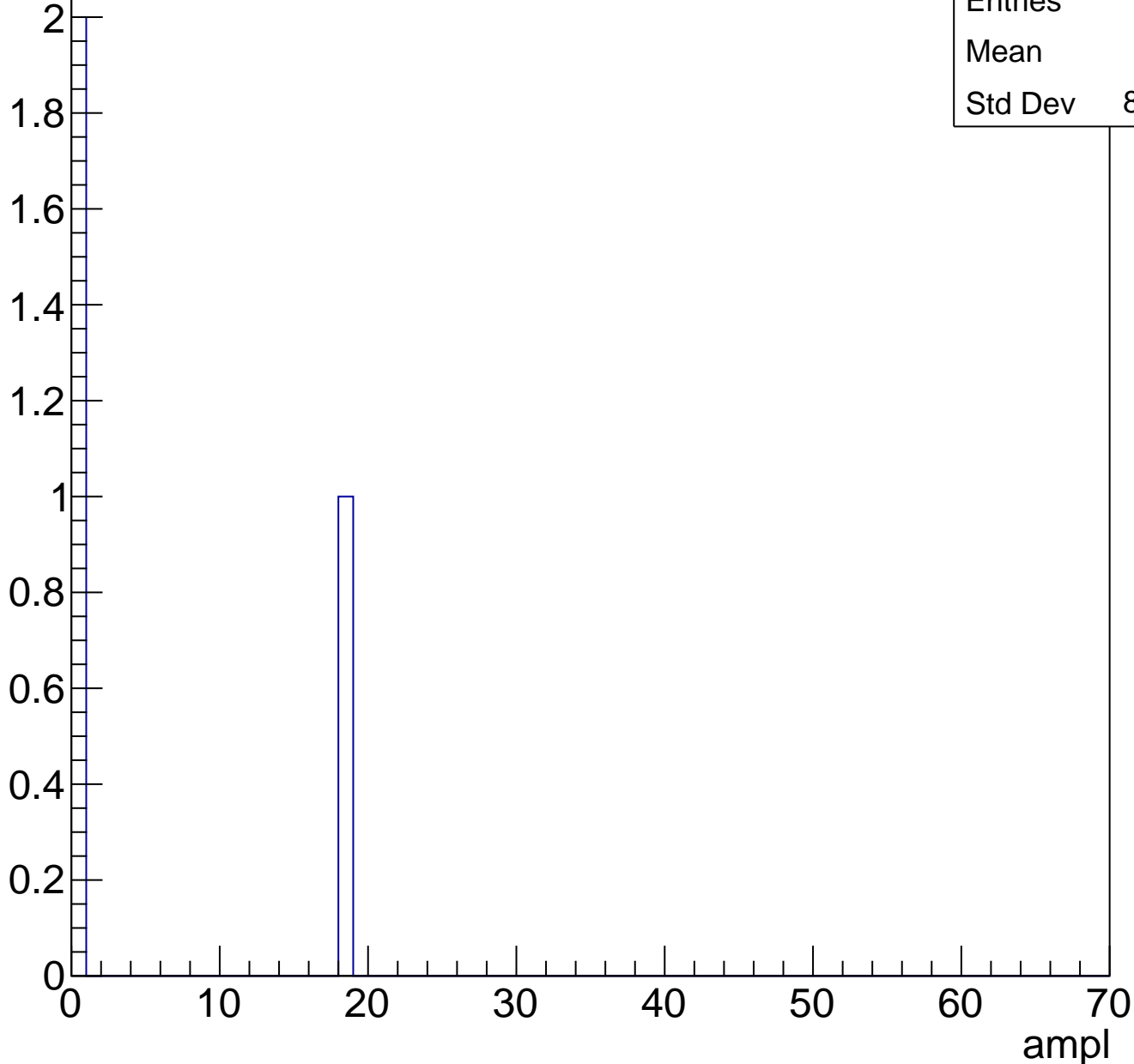
Entry



B1L100S, U19-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L100S, U19-ch49, adc0

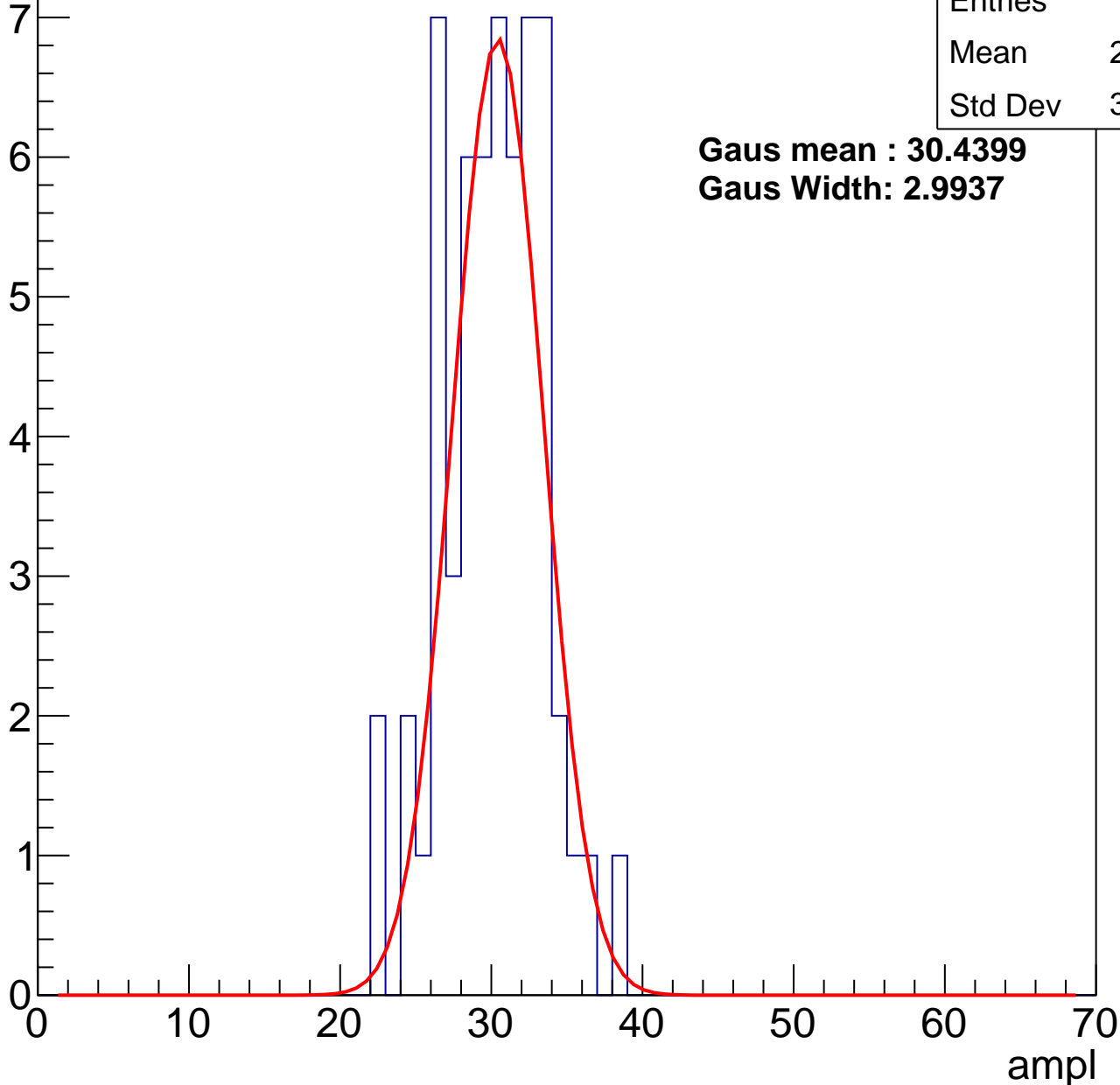
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.66
Std Dev	3.297

Gaus mean : 30.4399

Gaus Width: 2.9937



B1L100S, U19-ch49, adc1

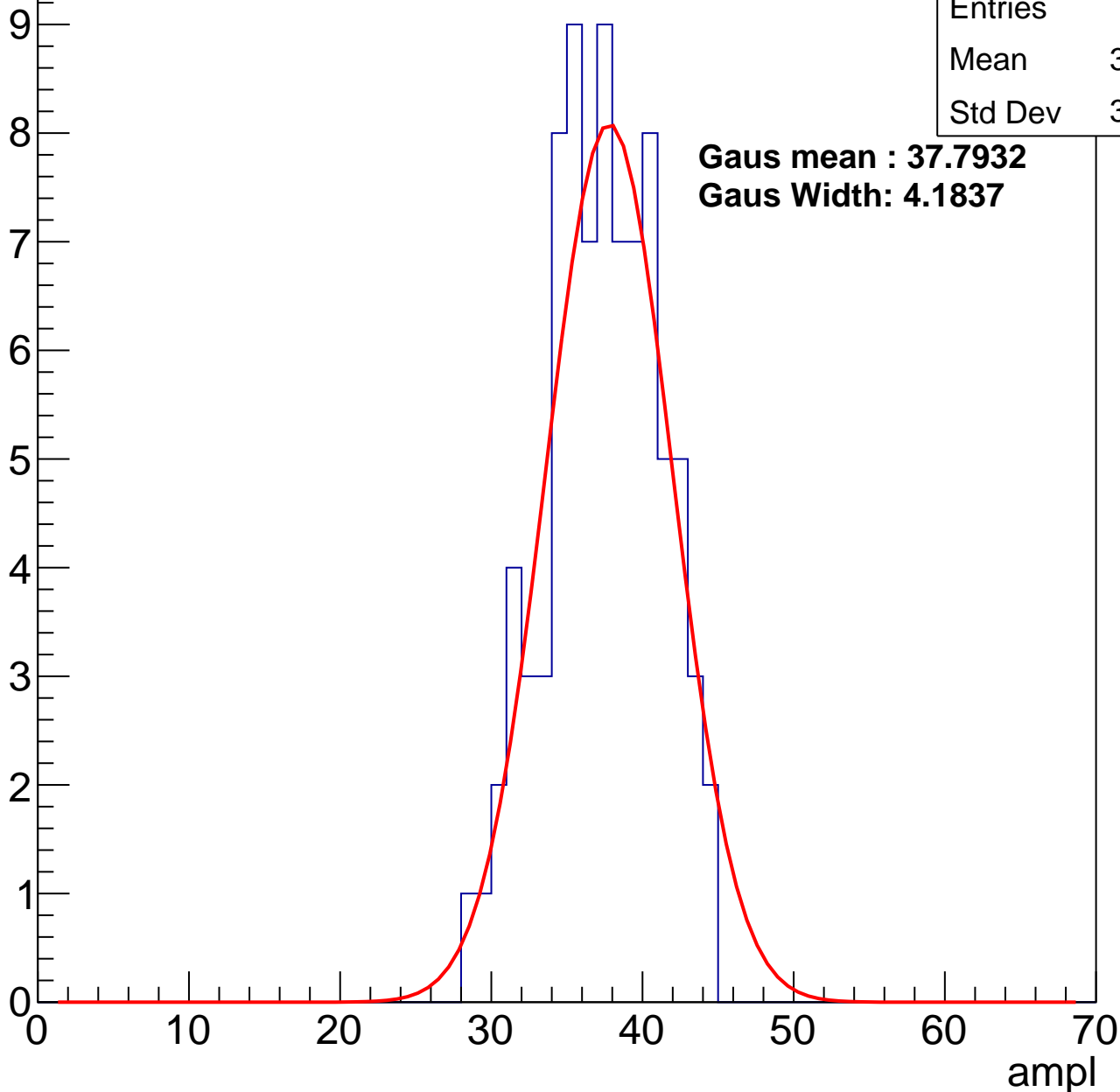
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	36.89
Std Dev	3.687

Gaus mean : 37.7932

Gaus Width: 4.1837



B1L100S, U19-ch49, adc2

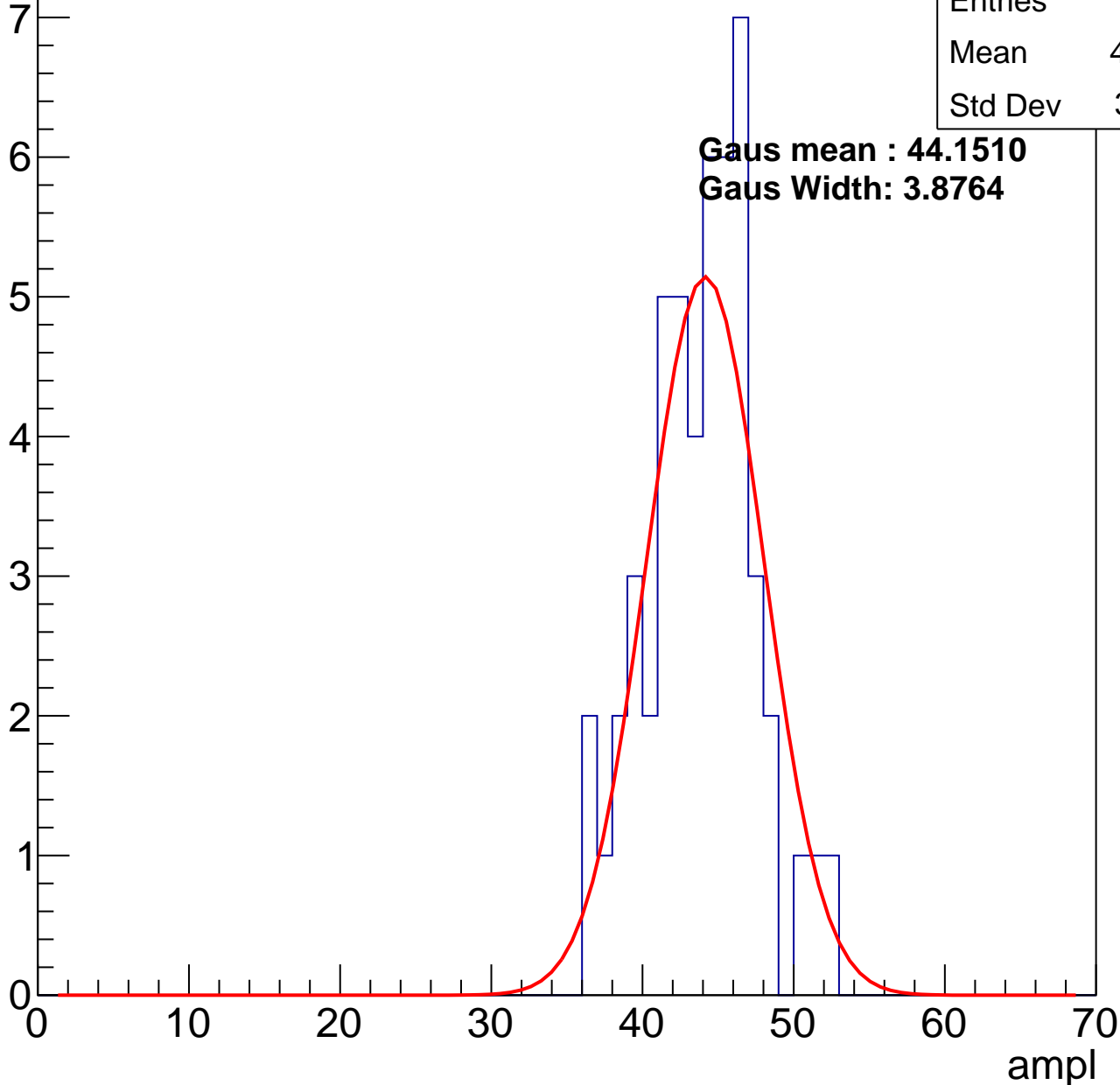
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	43.43
Std Dev	3.571

Gaus mean : 44.1510

Gaus Width: 3.8764

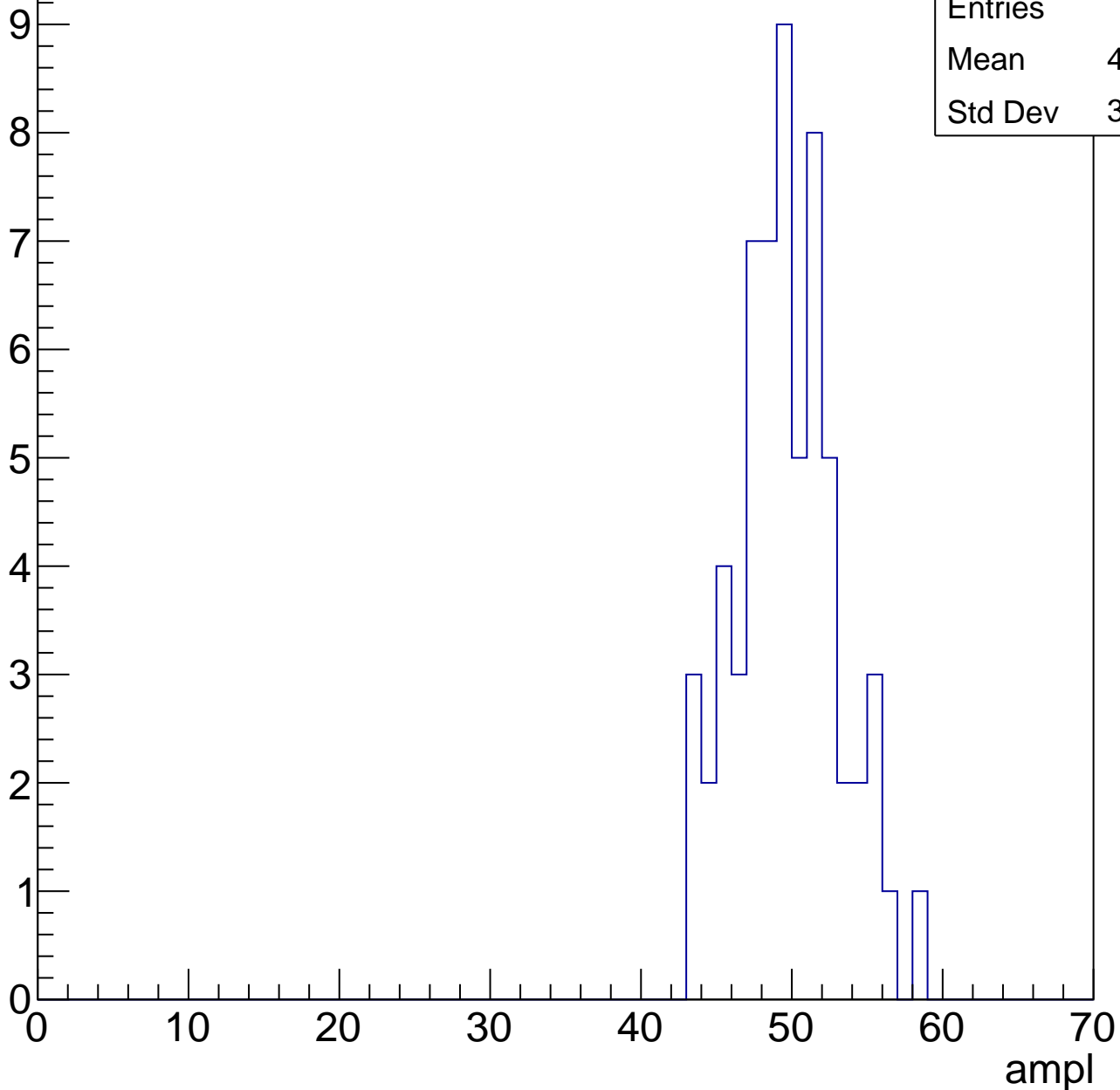


B1L100S, U19-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

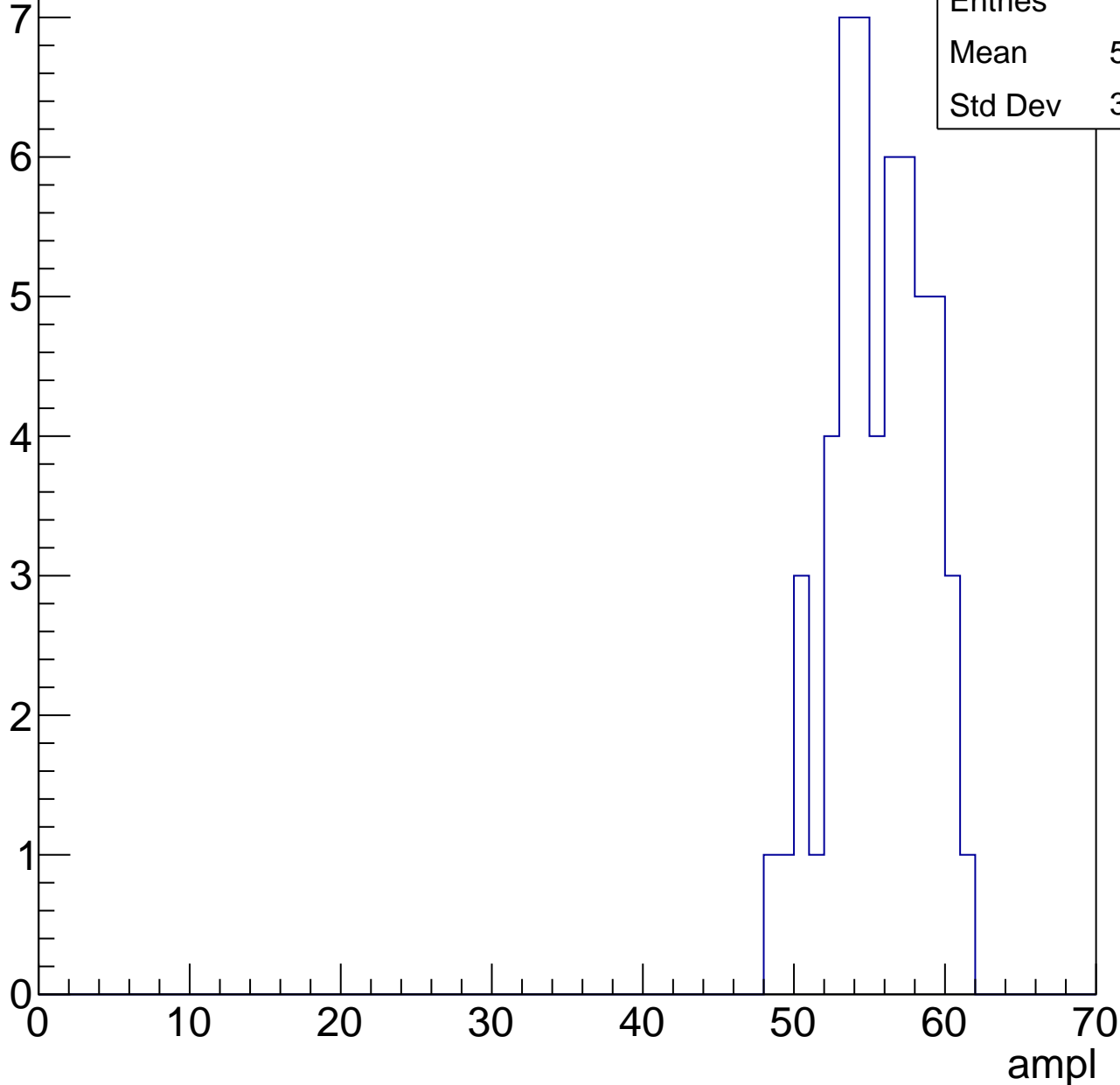
Entries	62
Mean	49.23
Std Dev	3.319



B1L100S, U19-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	54
Mean	55.17
Std Dev	3.078

B1L100S, U19-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	58.52
Std Dev	8.411

Entry

10

8

6

4

2

0

0

10

20

30

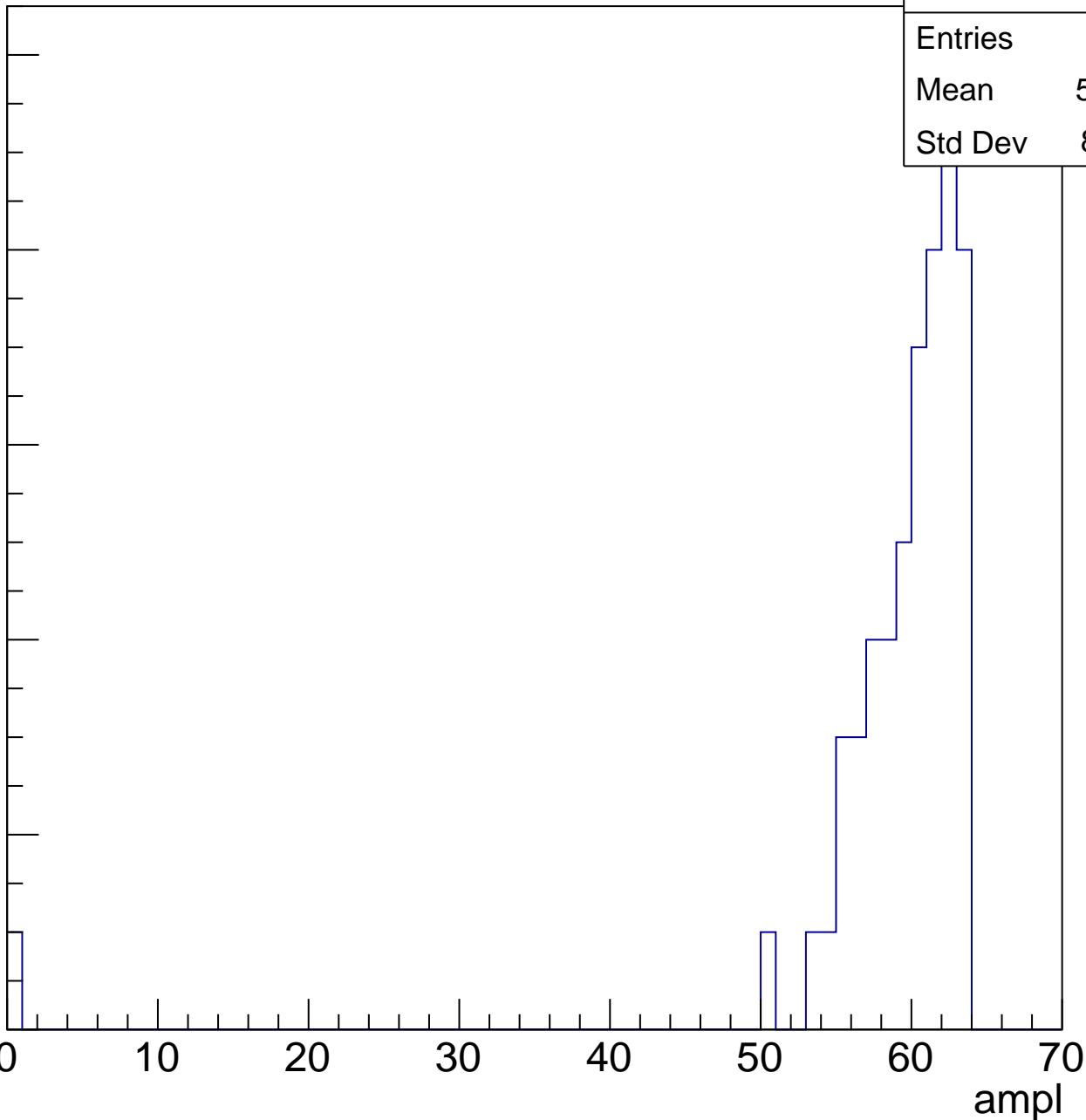
40

50

60

70

ampl



B1L100S, U19-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch50, adc0

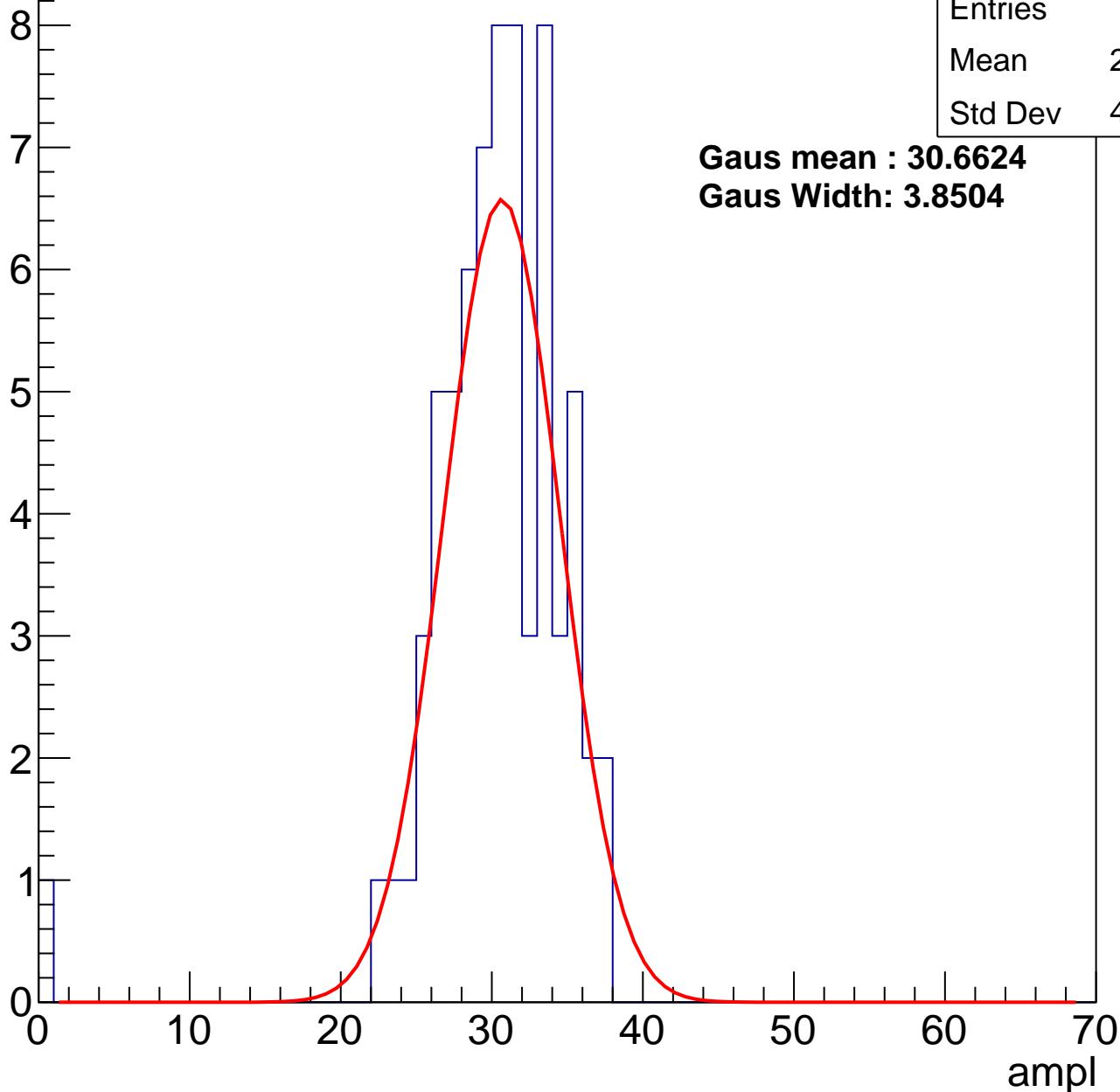
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.72
Std Dev	4.978

Gaus mean : 30.6624

Gaus Width: 3.8504



B1L100S, U19-ch50, adc1

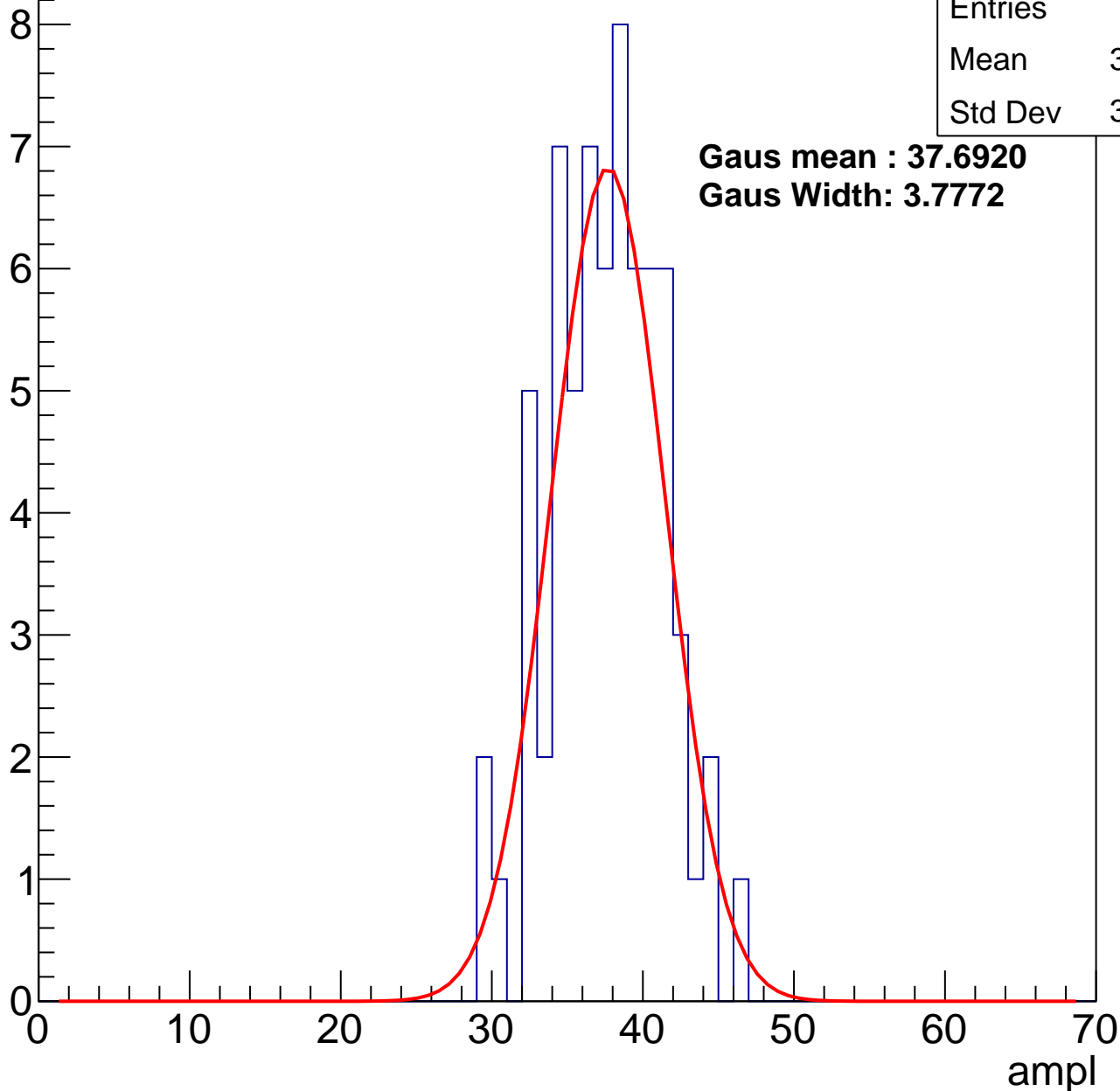
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.18
Std Dev	3.626

Gaus mean : 37.6920

Gaus Width: 3.7772



B1L100S, U19-ch50, adc2

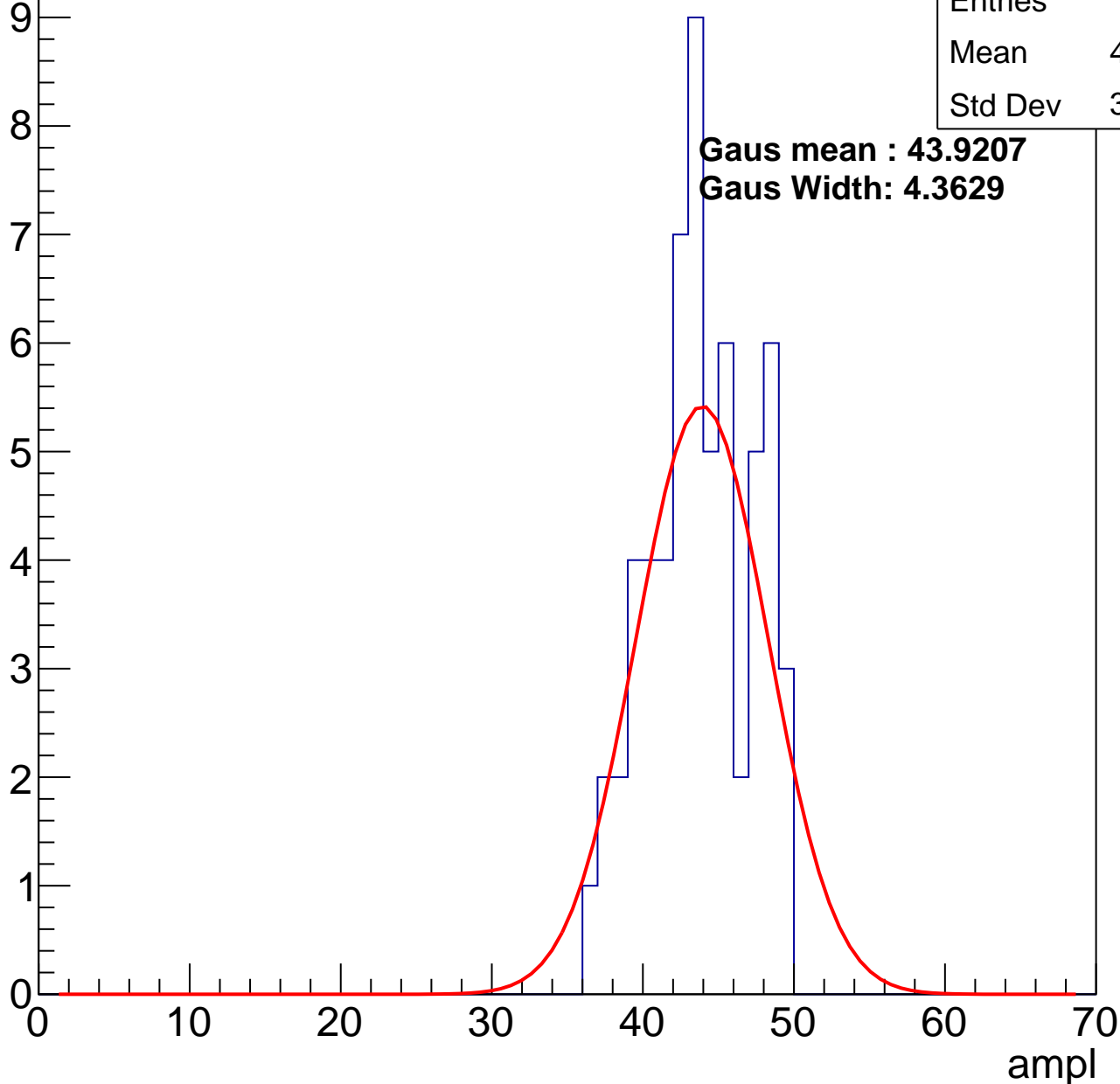
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.32
Std Dev	3.349

Gaus mean : 43.9207

Gaus Width: 4.3629

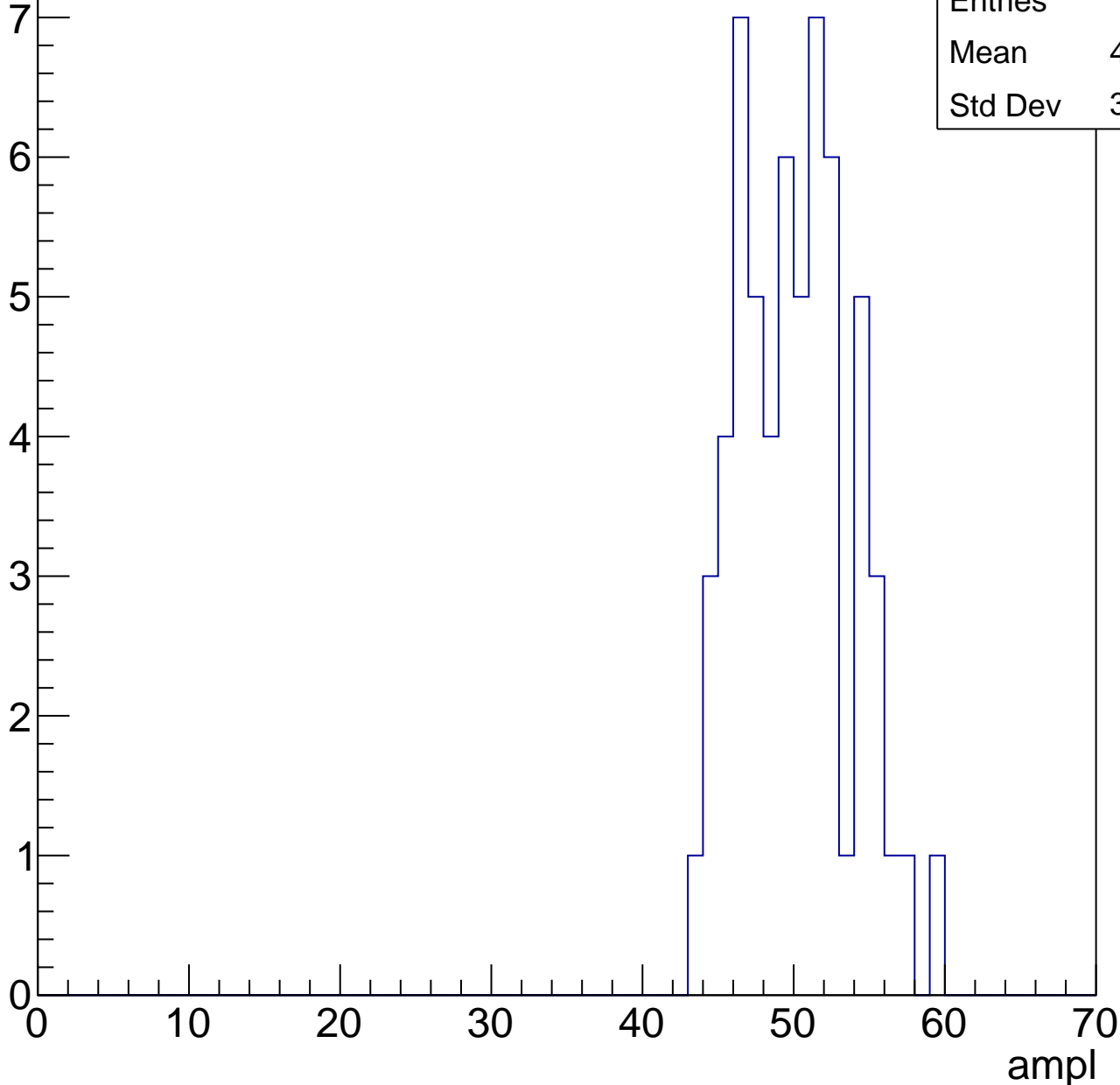


B1L100S, U19-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.62
Std Dev	3.624

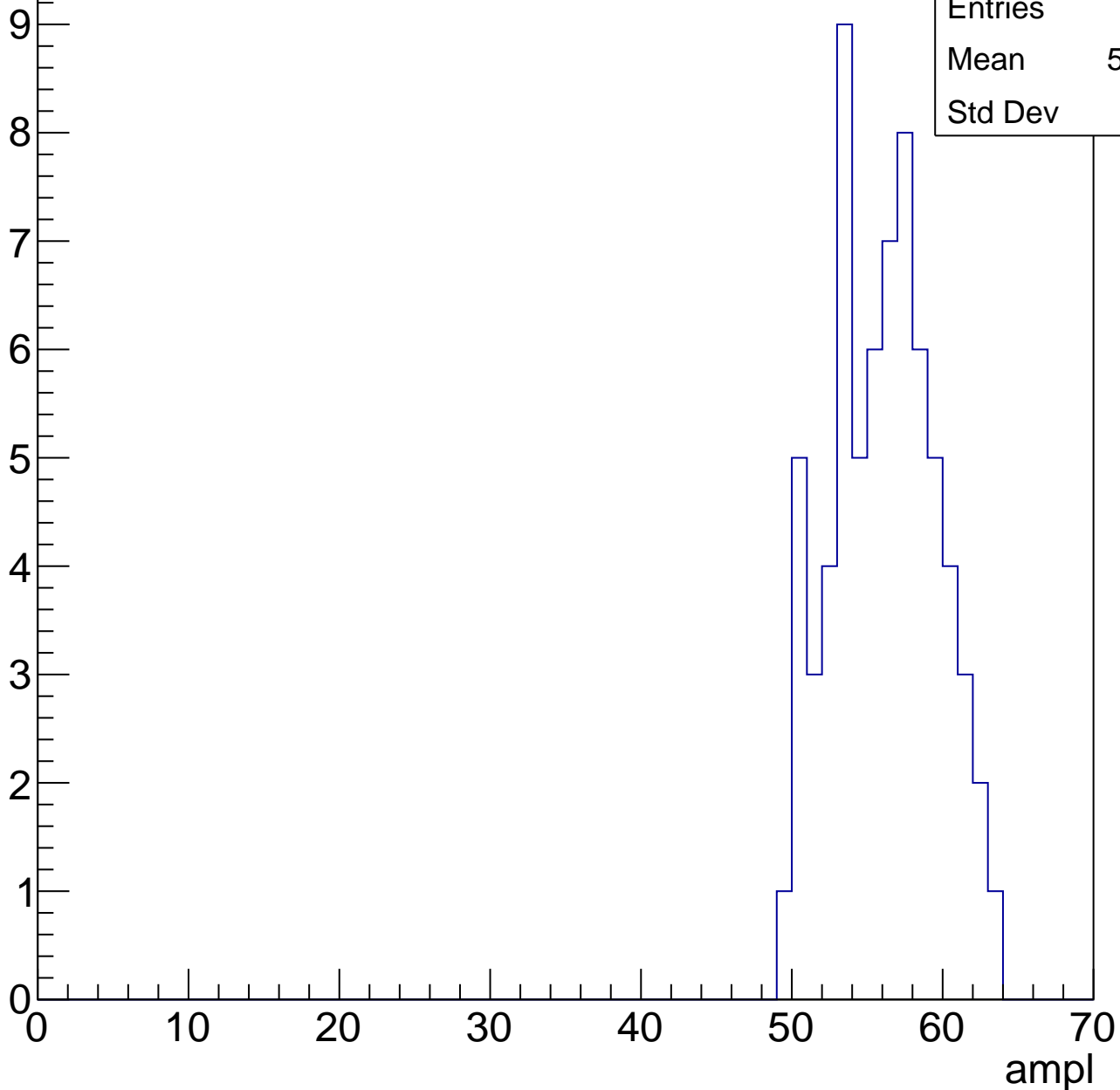


B1L100S, U19-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

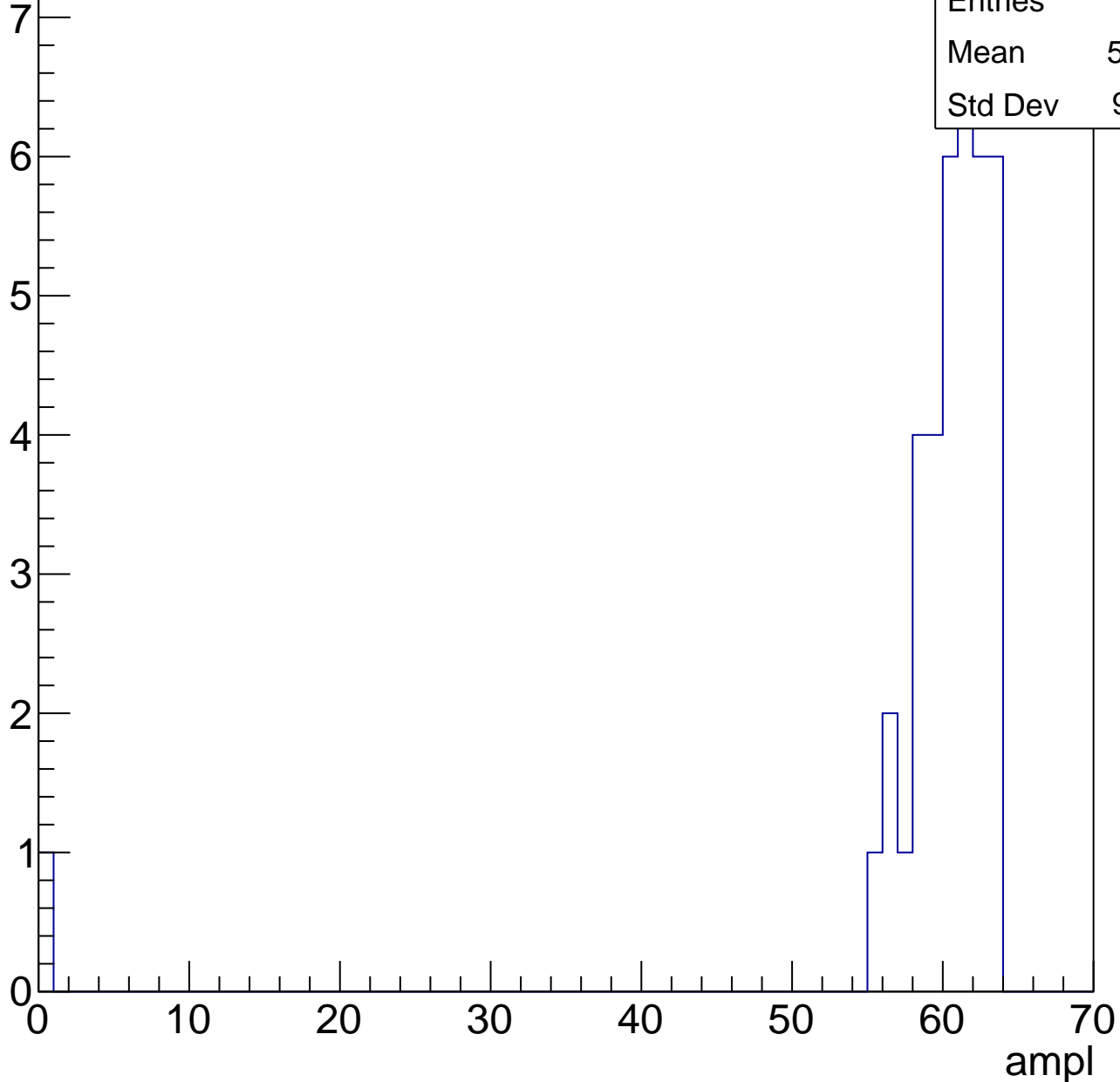
Entries	69
Mean	55.62
Std Dev	3.41



B1L100S, U19-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch51, adc0

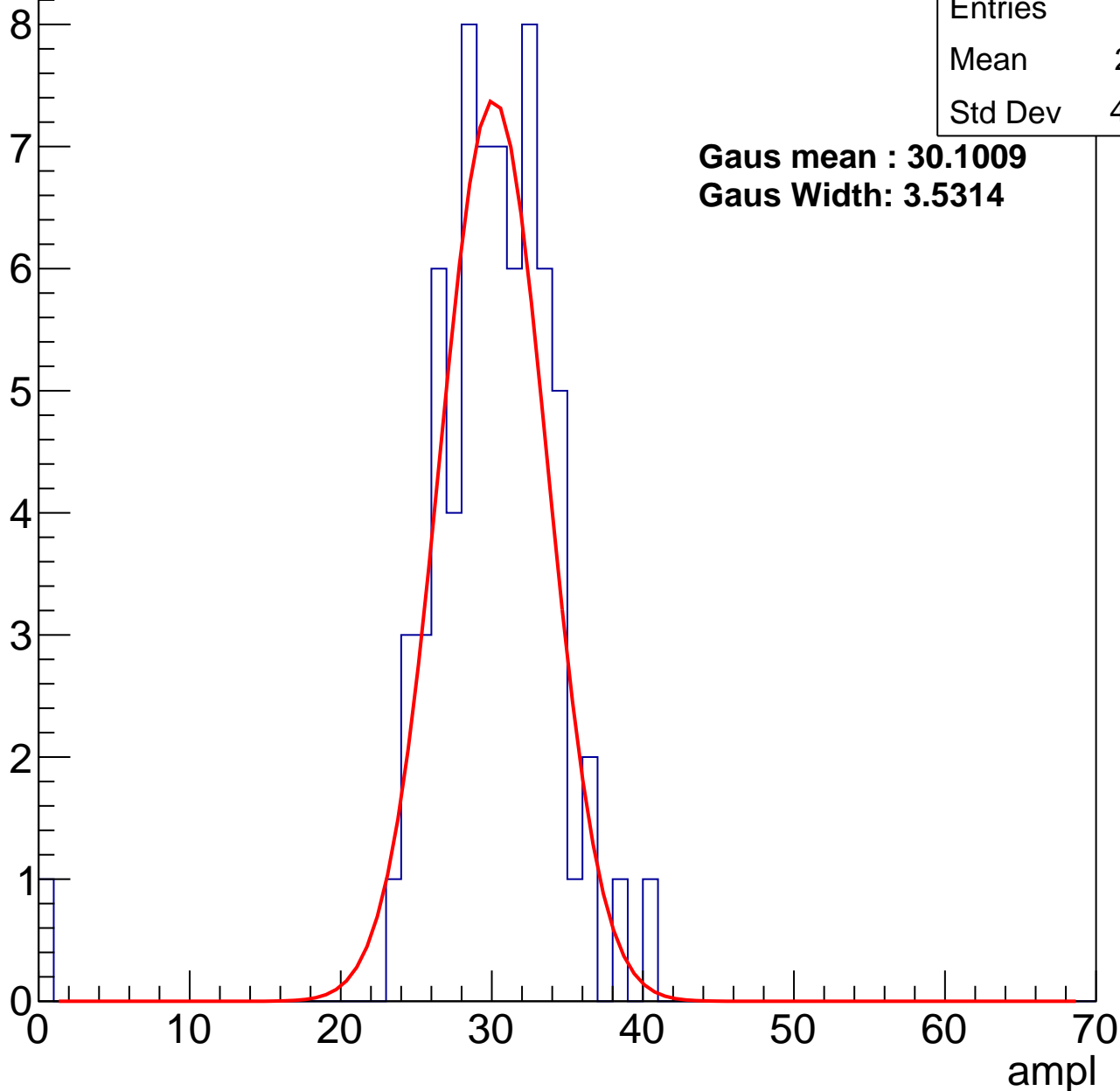
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.51
Std Dev	4.956

Gaus mean : 30.1009

Gaus Width: 3.5314



B1L100S, U19-ch51, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

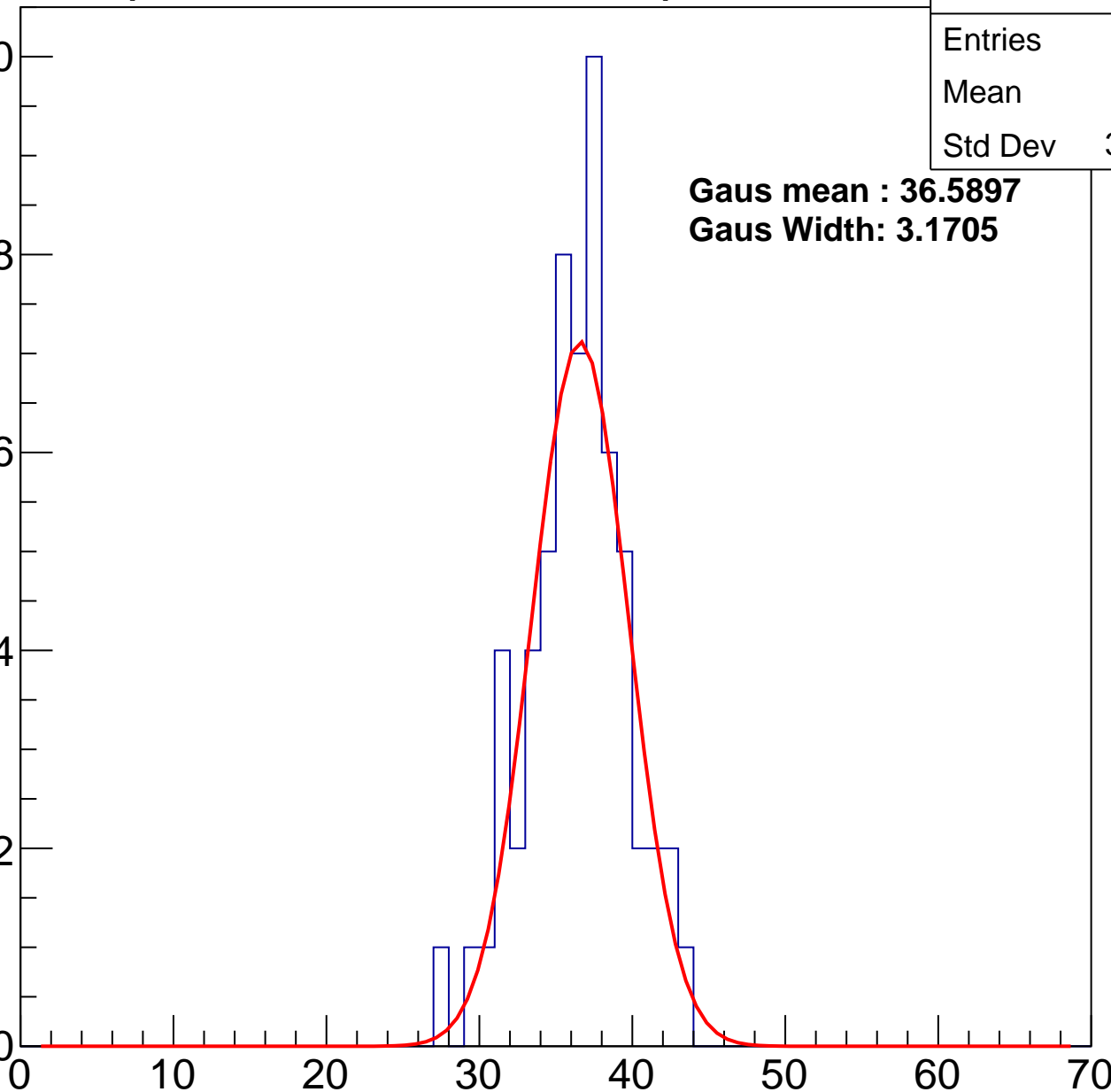
Entries	61
Mean	35.9
Std Dev	3.248

Gaus mean : 36.5897

Gaus Width: 3.1705

10
8
6
4
2
0

ampl



B1L100S, U19-ch51, adc2

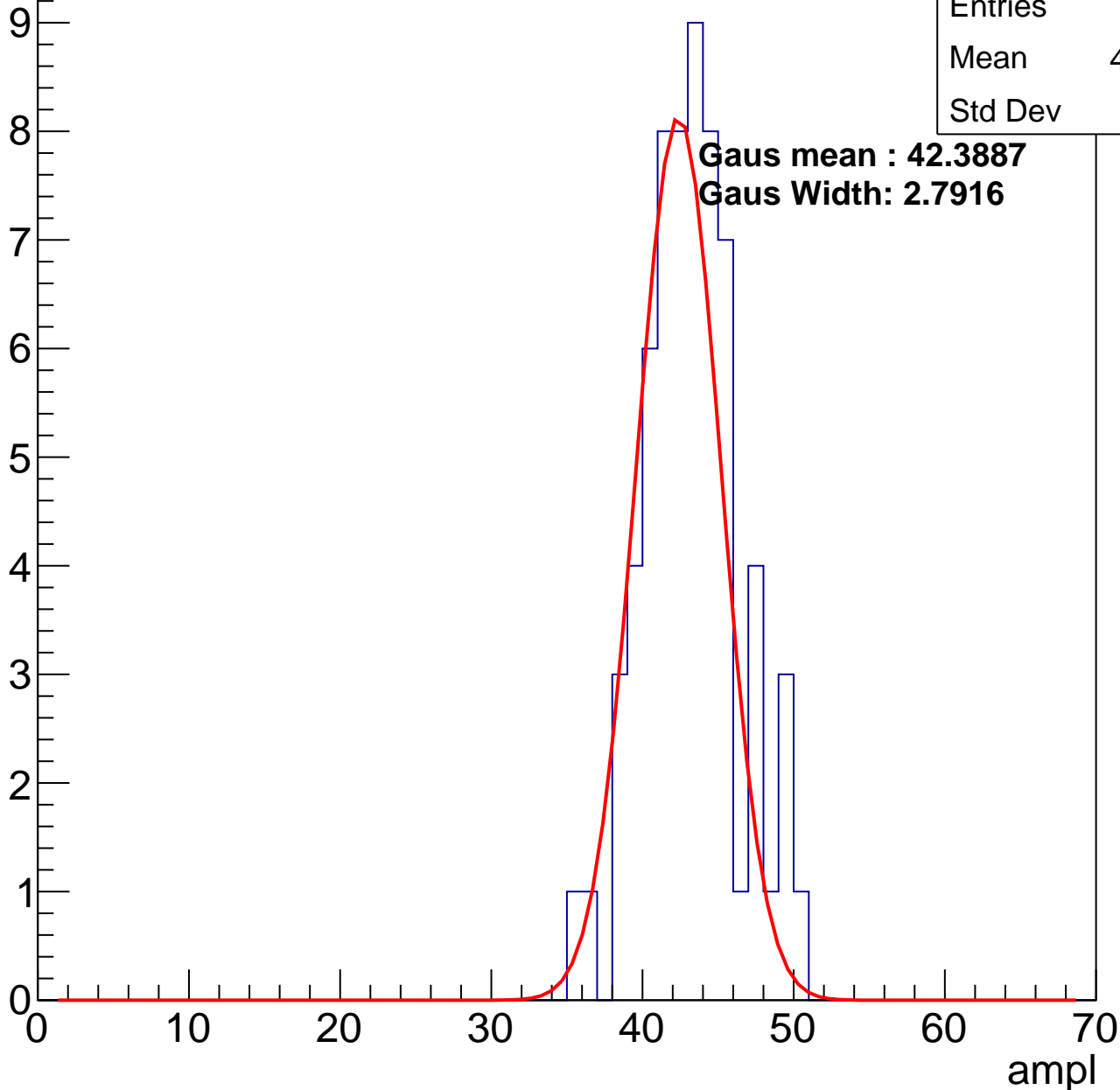
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.74
Std Dev	3.12

Gaus mean : 42.3887

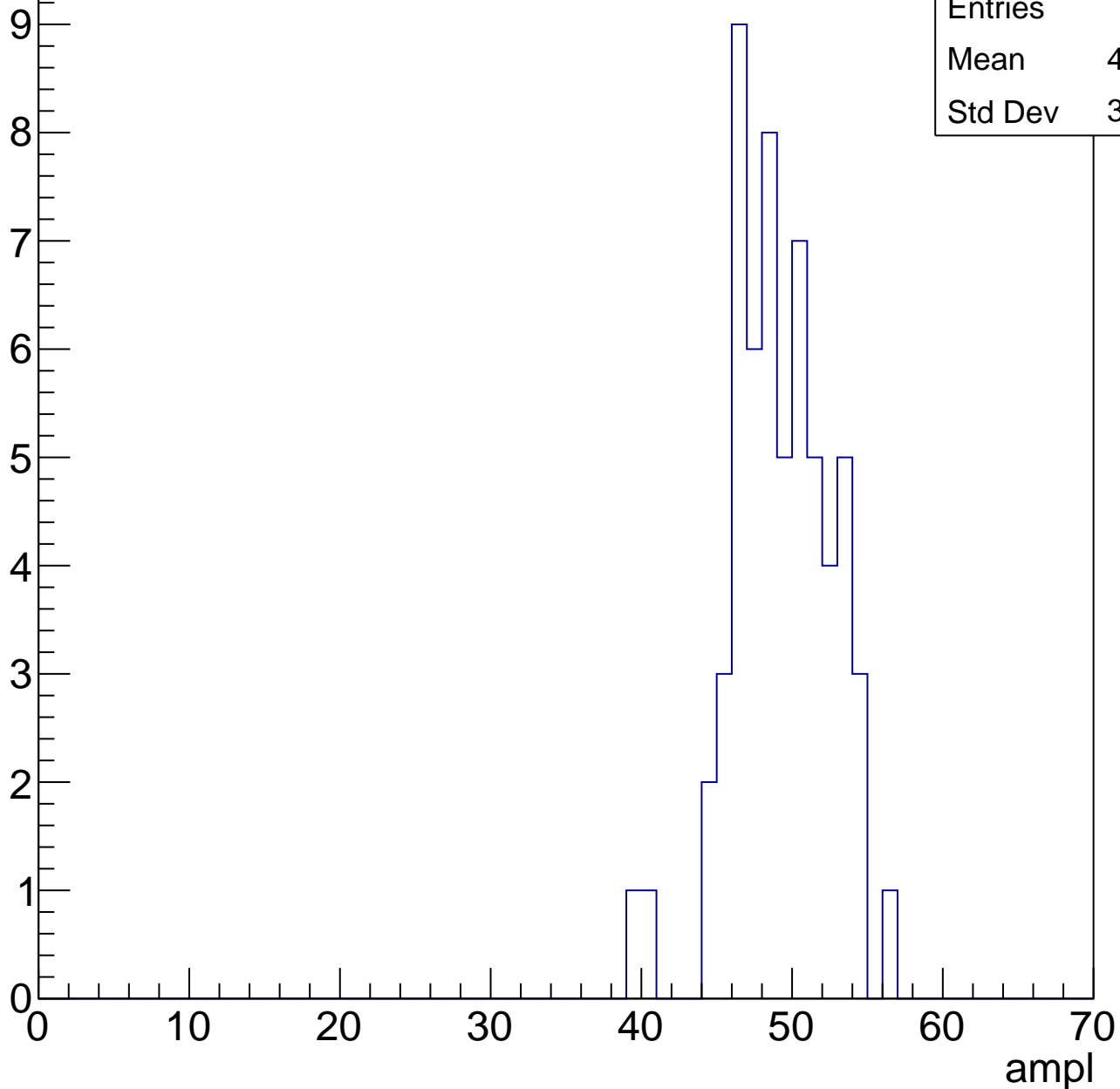
Gaus Width: 2.7916



B1L100S, U19-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

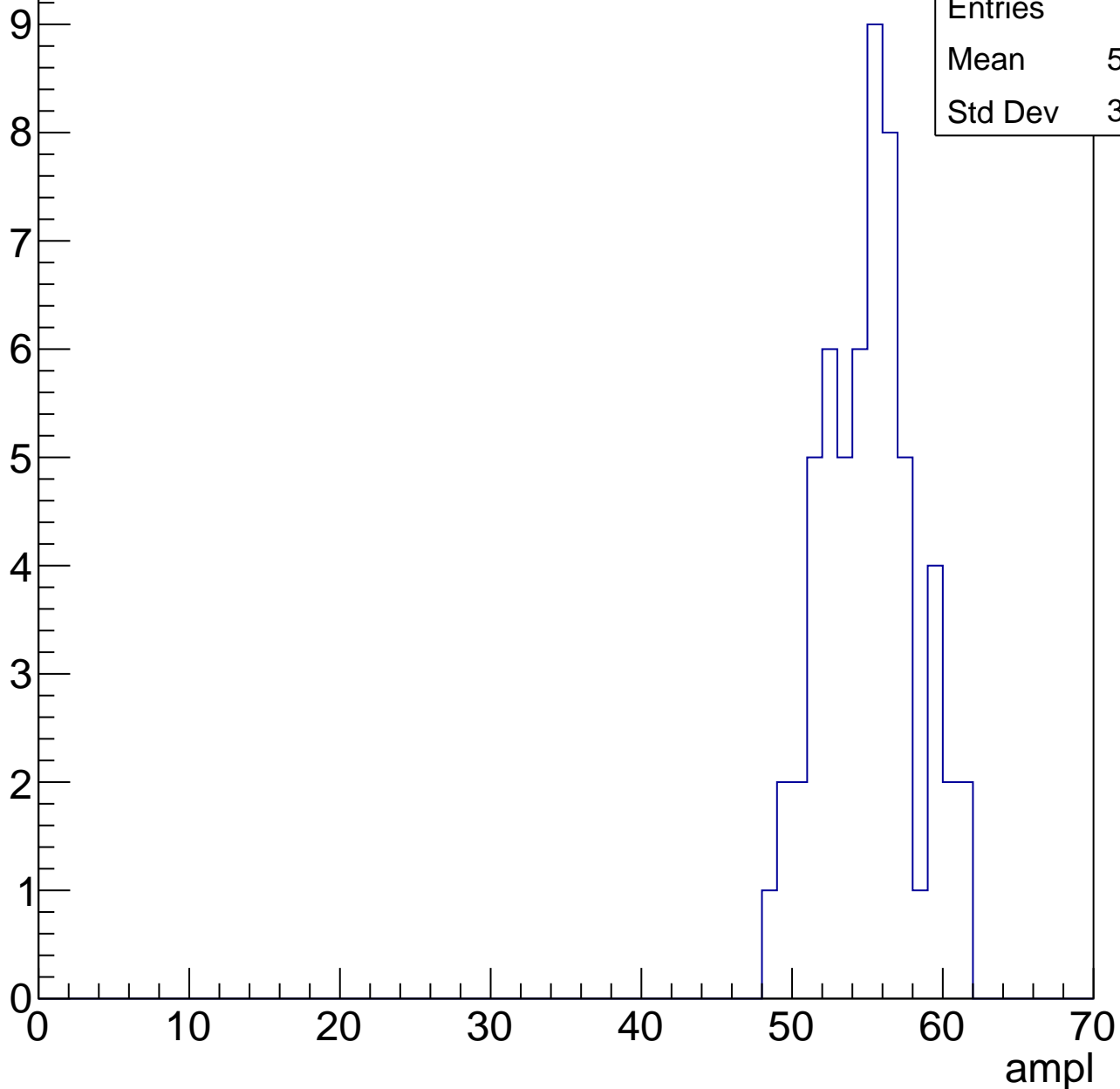
Entry



B1L100S, U19-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

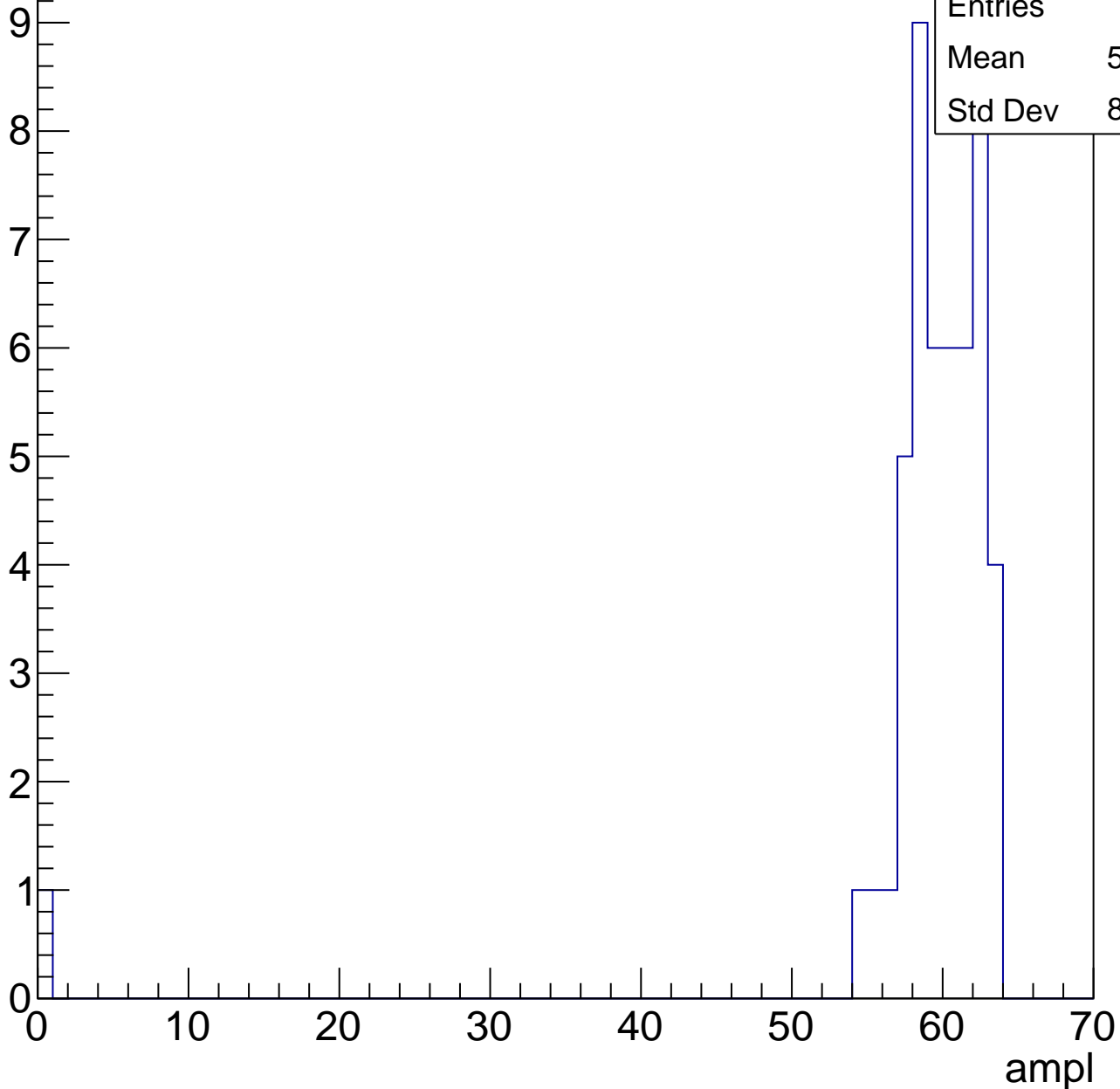
Entry



B1L100S, U19-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

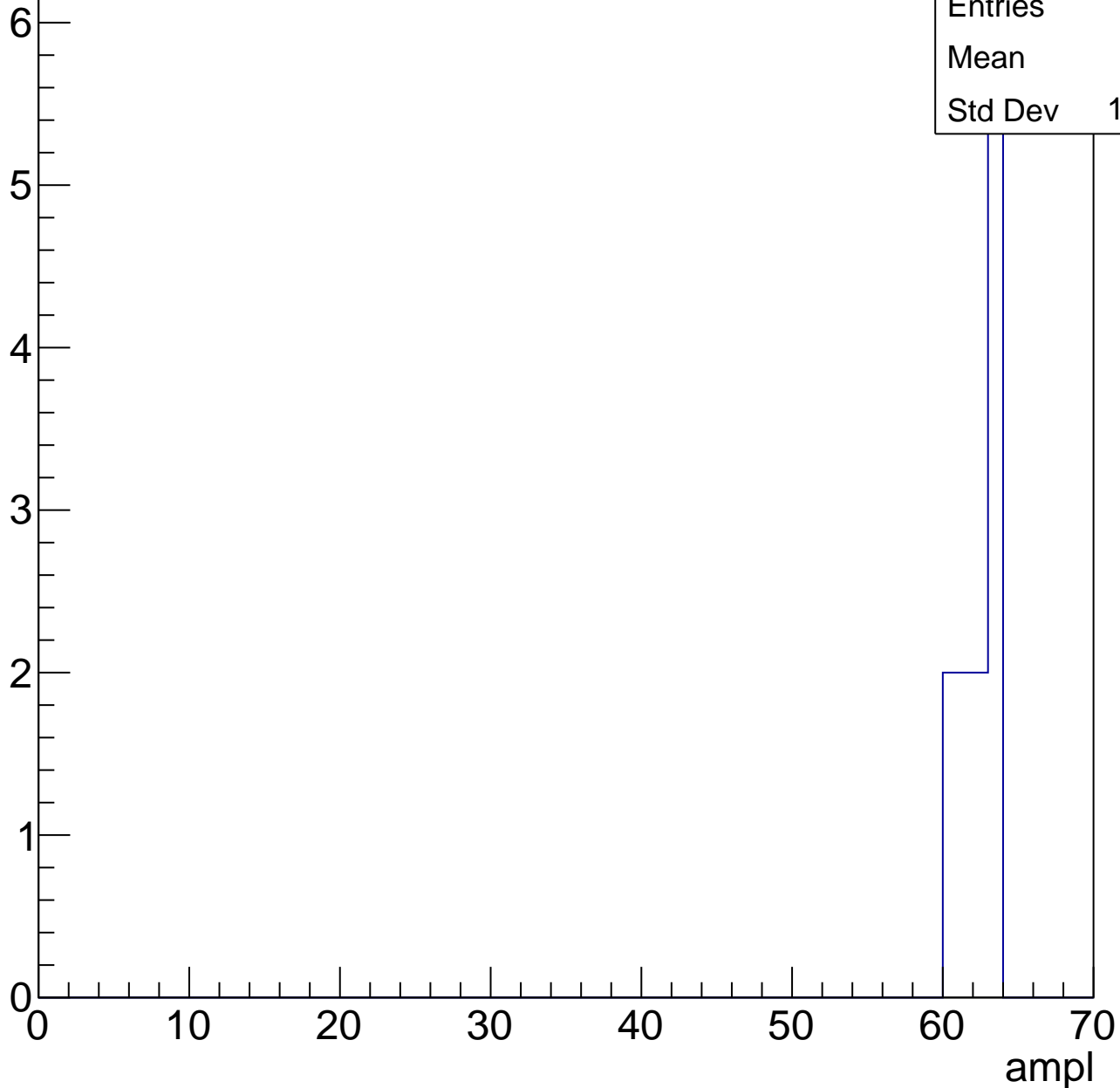
Entry



B1L100S, U19-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch52, adc0

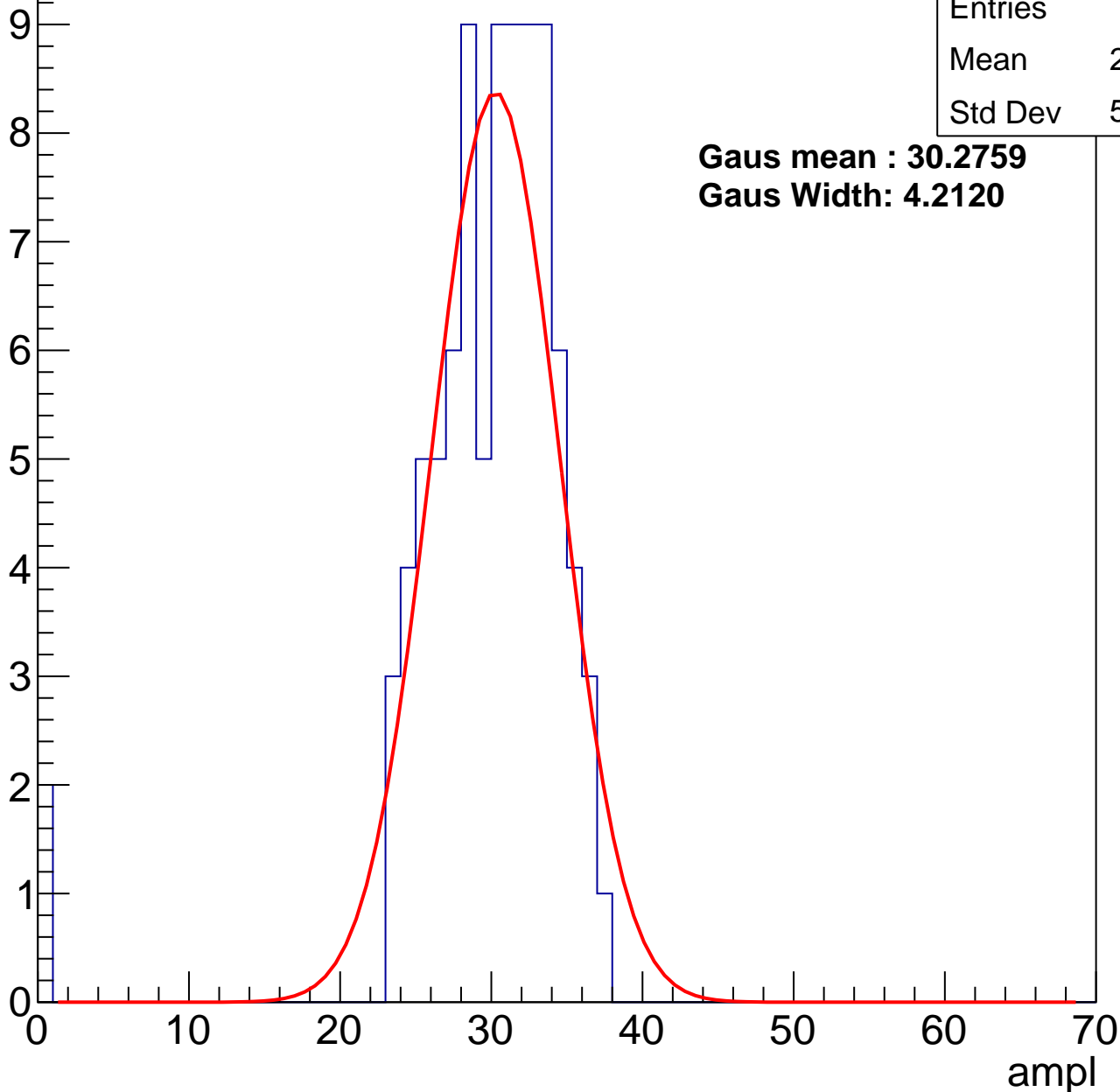
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	29.24
Std Dev	5.627

Gaus mean : 30.2759

Gaus Width: 4.2120



B1L100S, U19-ch52, adc1

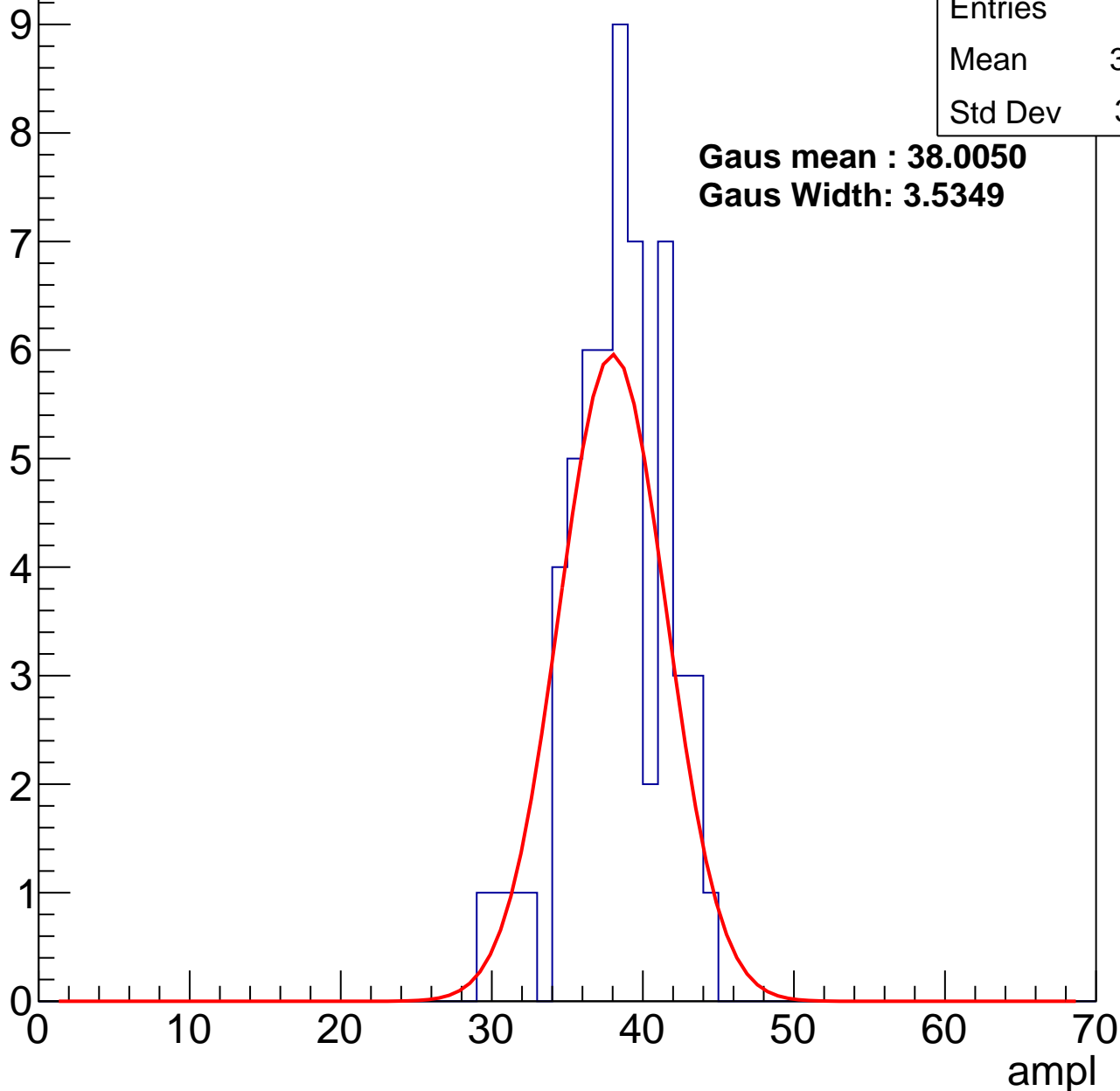
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	37.75
Std Dev	3.251

Gaus mean : 38.0050

Gaus Width: 3.5349



B1L100S, U19-ch52, adc2

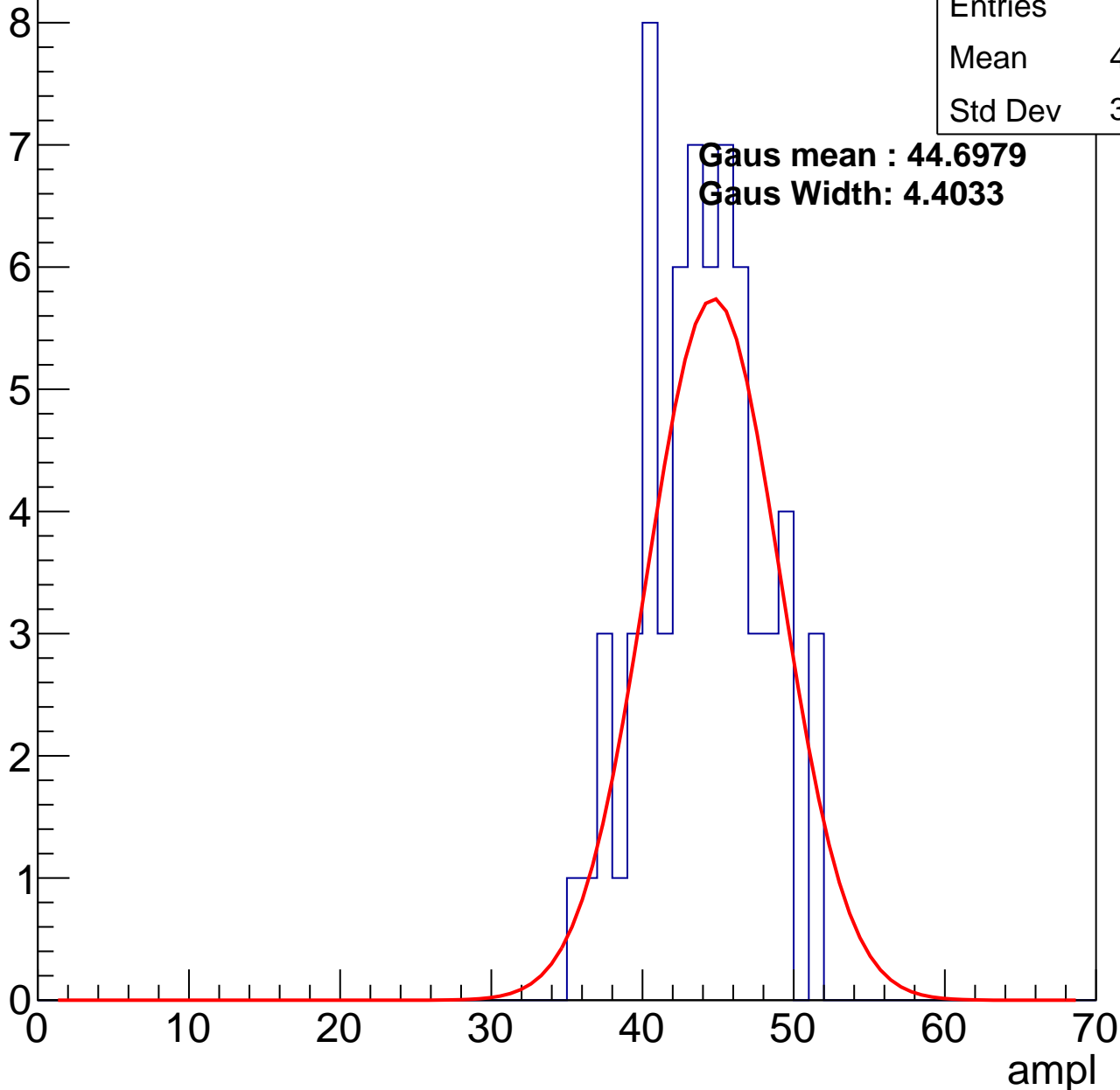
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.42
Std Dev	3.762

Gaus mean : 44.6979

Gaus Width: 4.4033

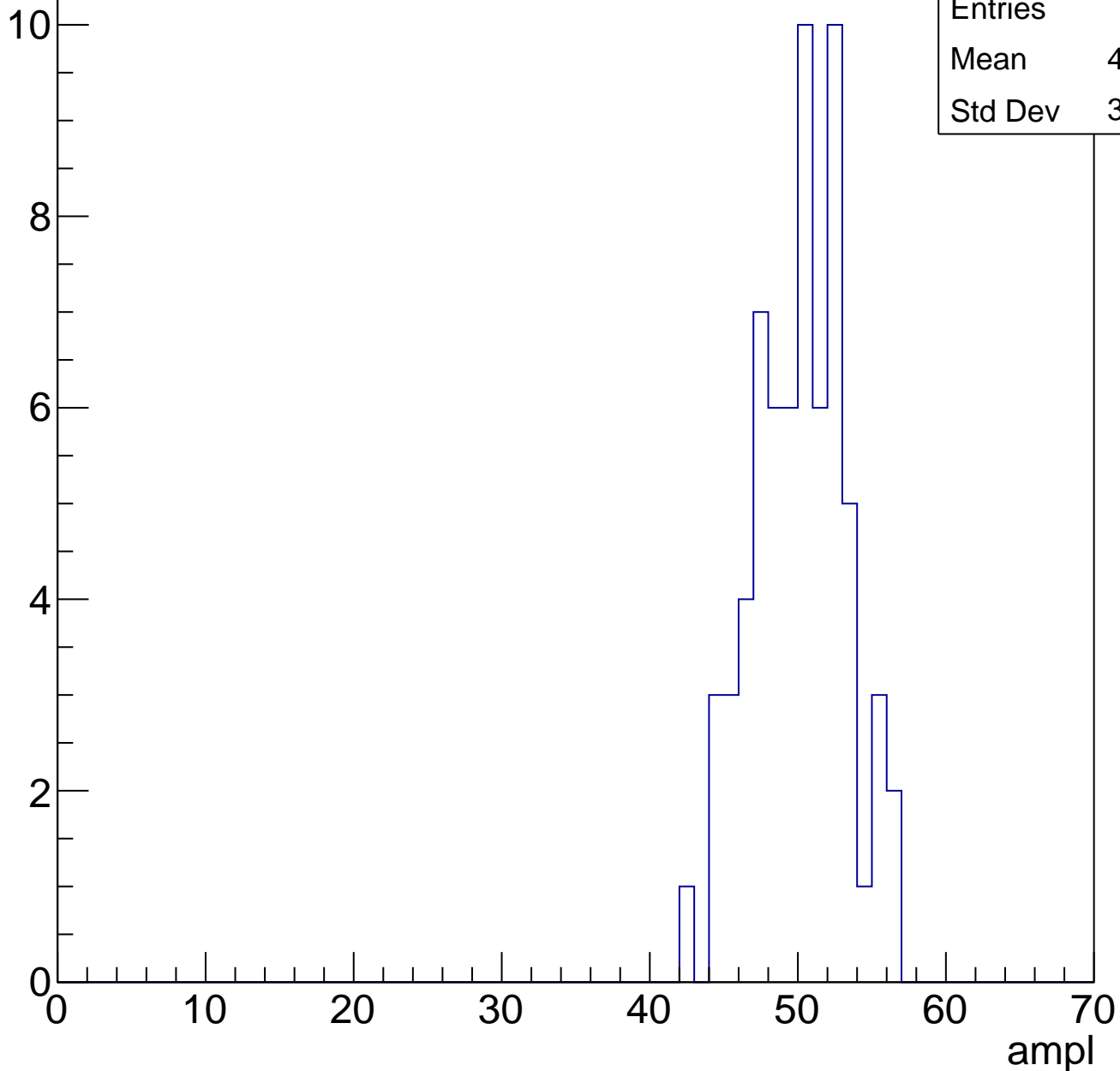


B1L100S, U19-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	49.64
Std Dev	3.113

Entry

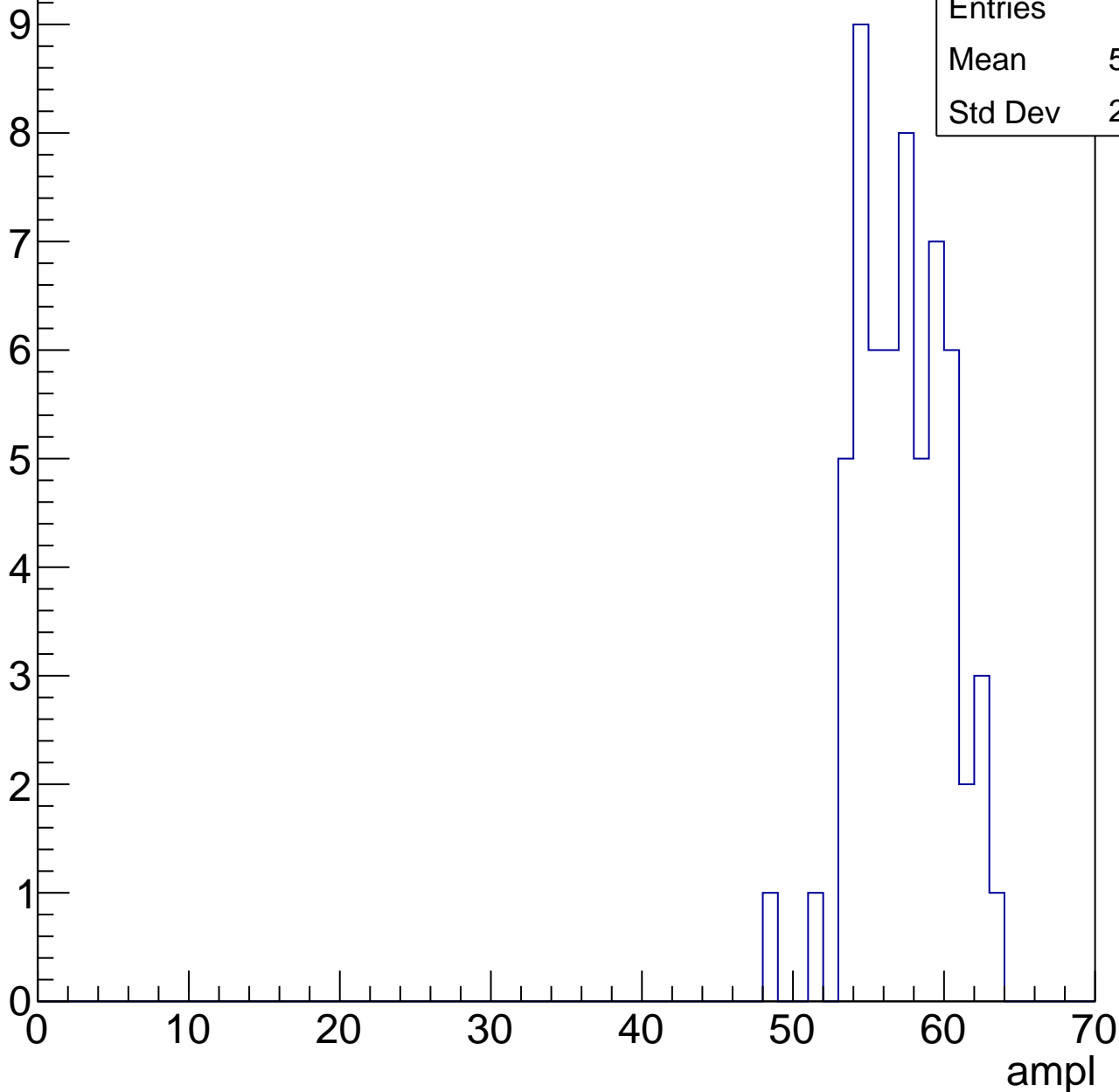


B1L100S, U19-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.77
Std Dev	2.985

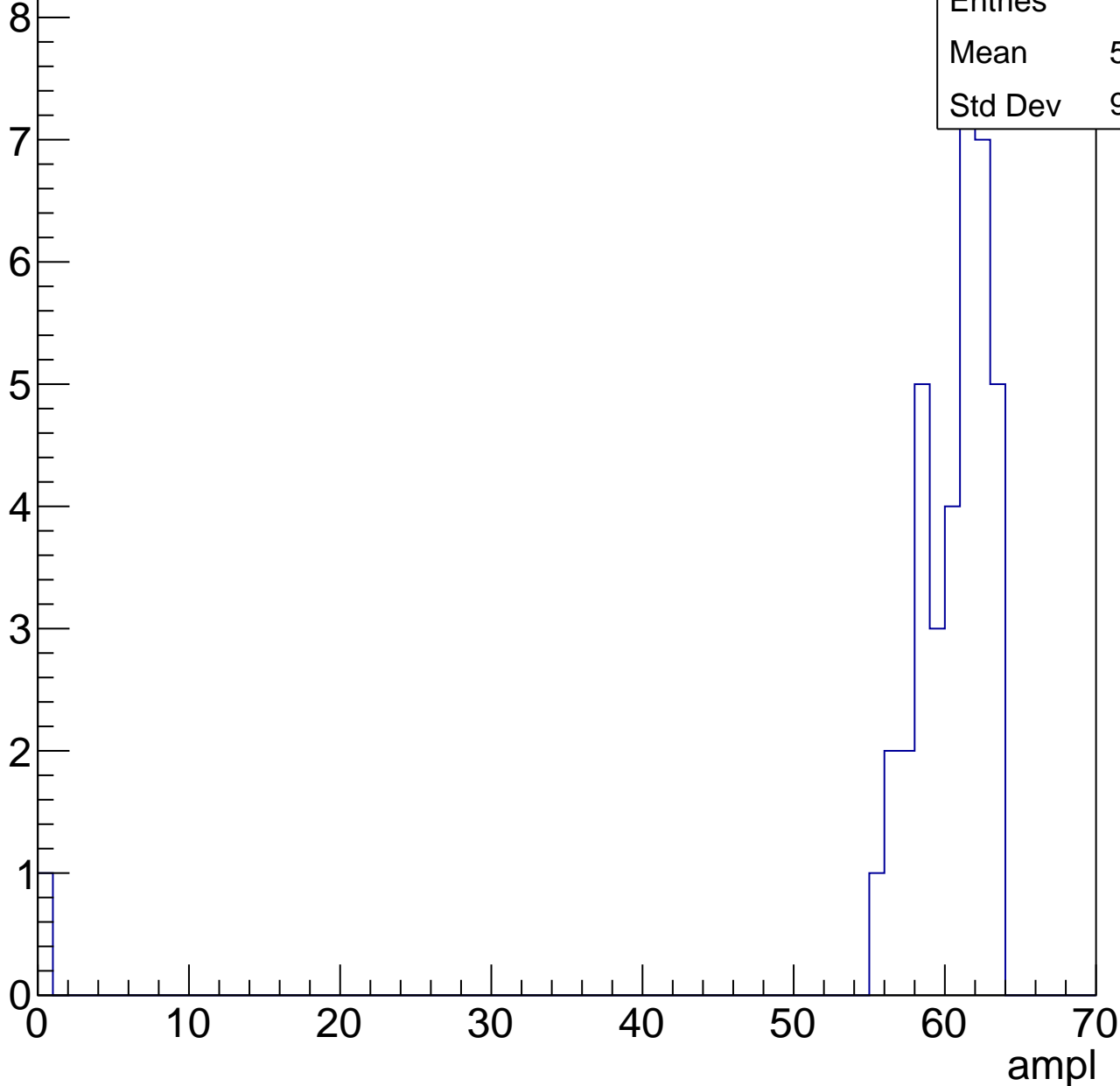


B1L100S, U19-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.55
Std Dev	9.867



B1L100S, U19-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch53, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	30.75
Std Dev	4.815

Gaus mean : 31.5912

Gaus Width: 3.7158

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

10

8

6

4

2

0

B1L100S, U19-ch53, adc1

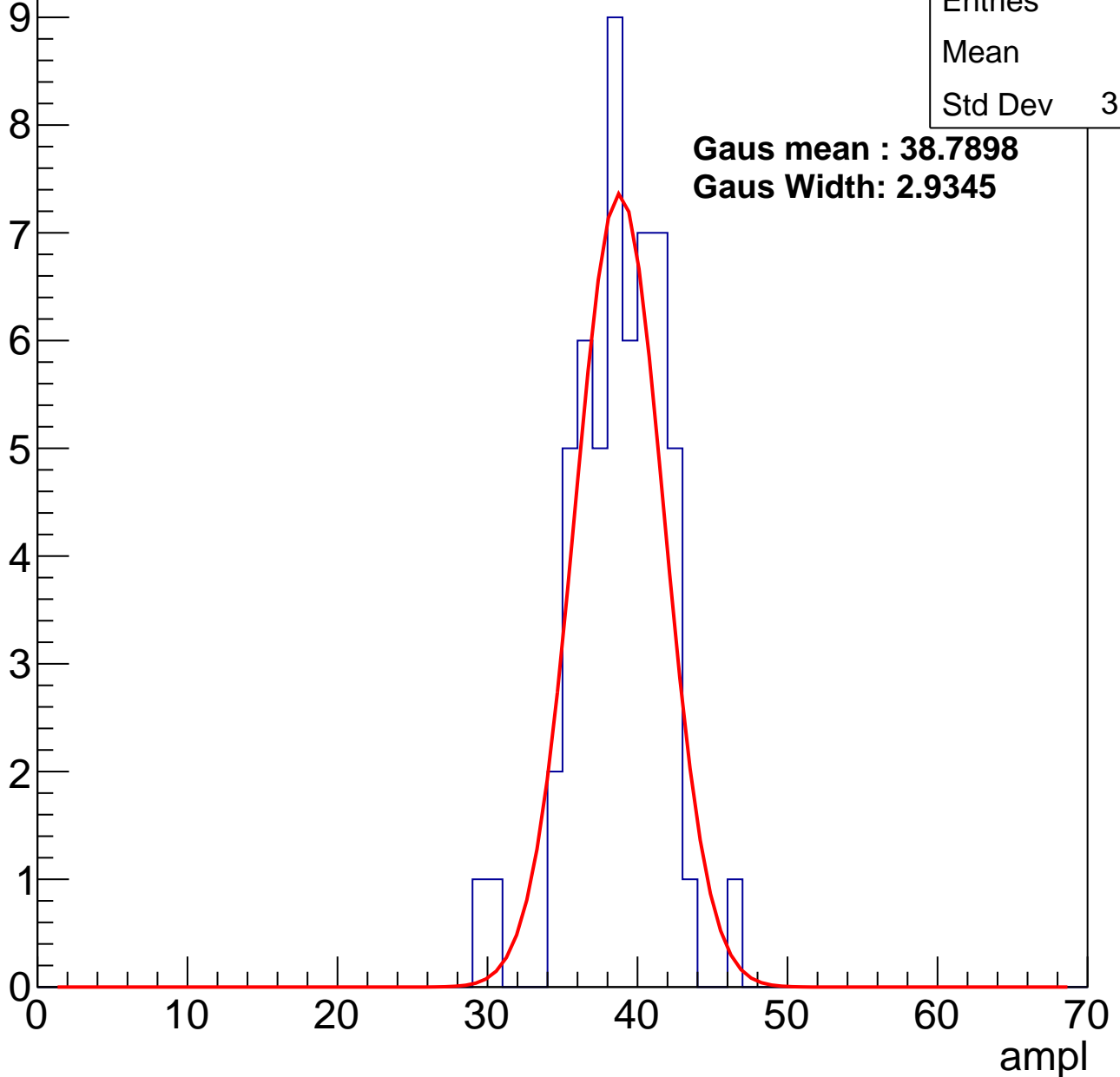
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	38.3
Std Dev	3.023

Gaus mean : 38.7898

Gaus Width: 2.9345



B1L100S, U19-ch53, adc2

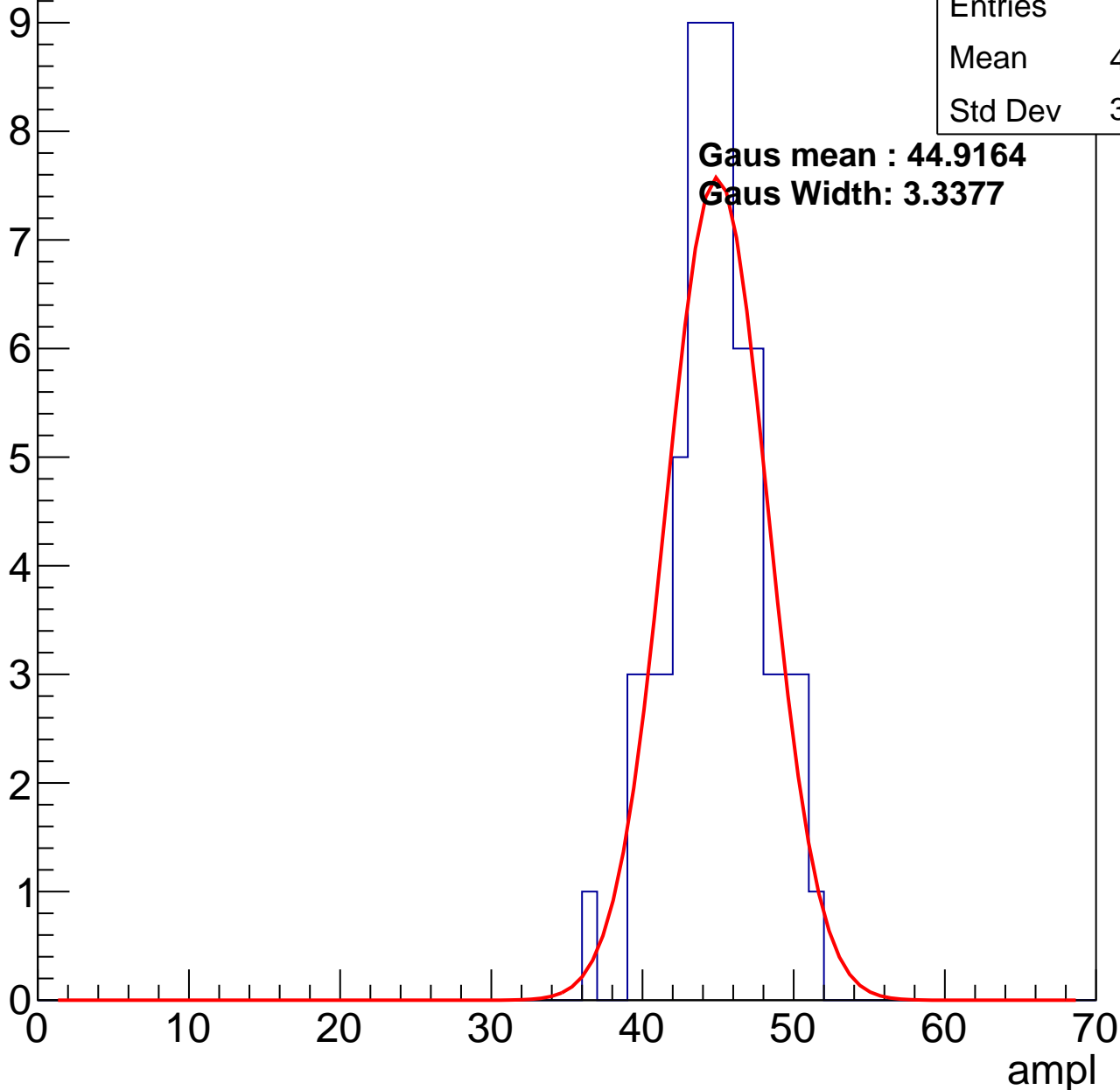
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.44
Std Dev	3.056

Gaus mean : 44.9164

Gaus Width: 3.3377

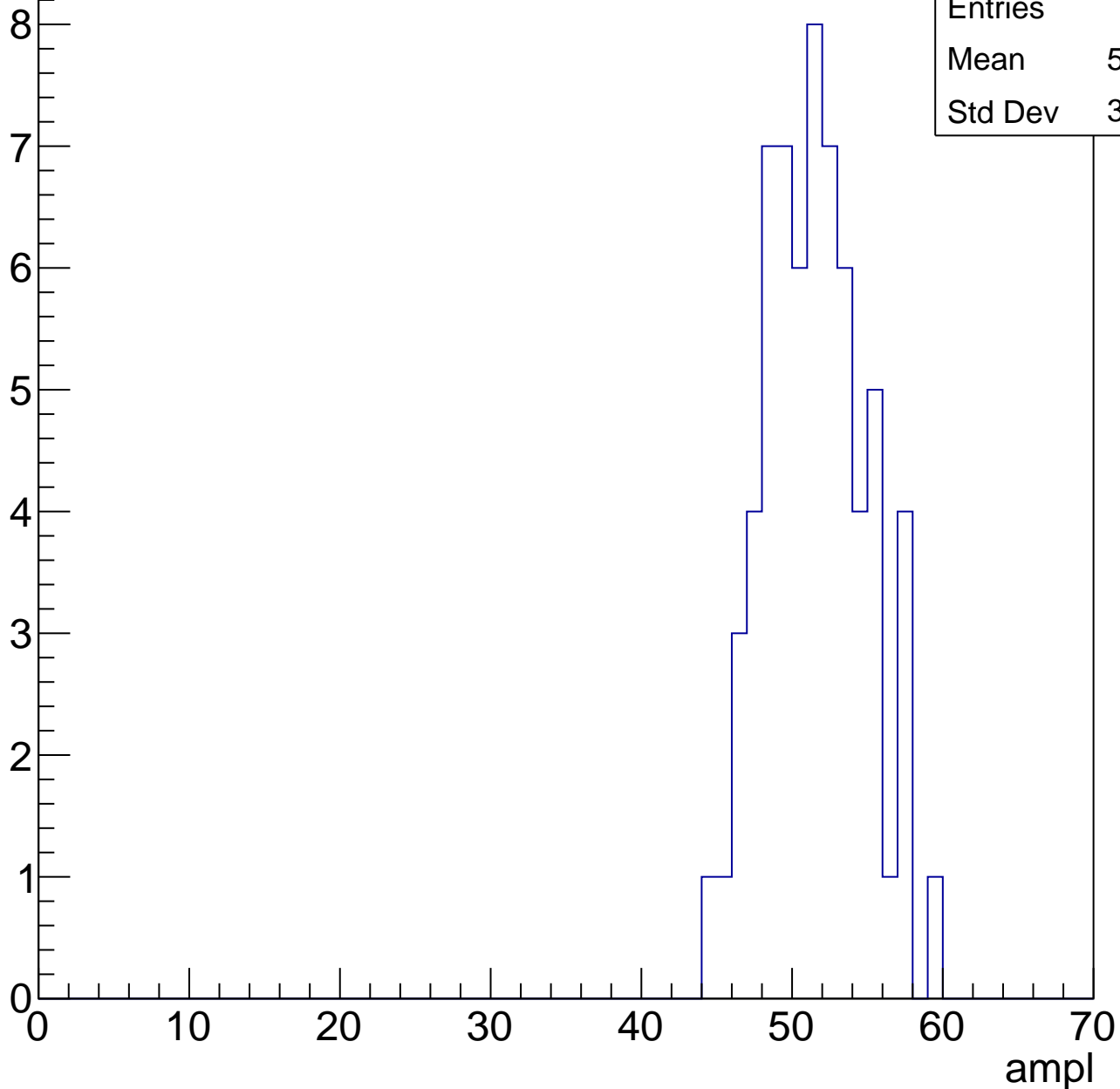


B1L100S, U19-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

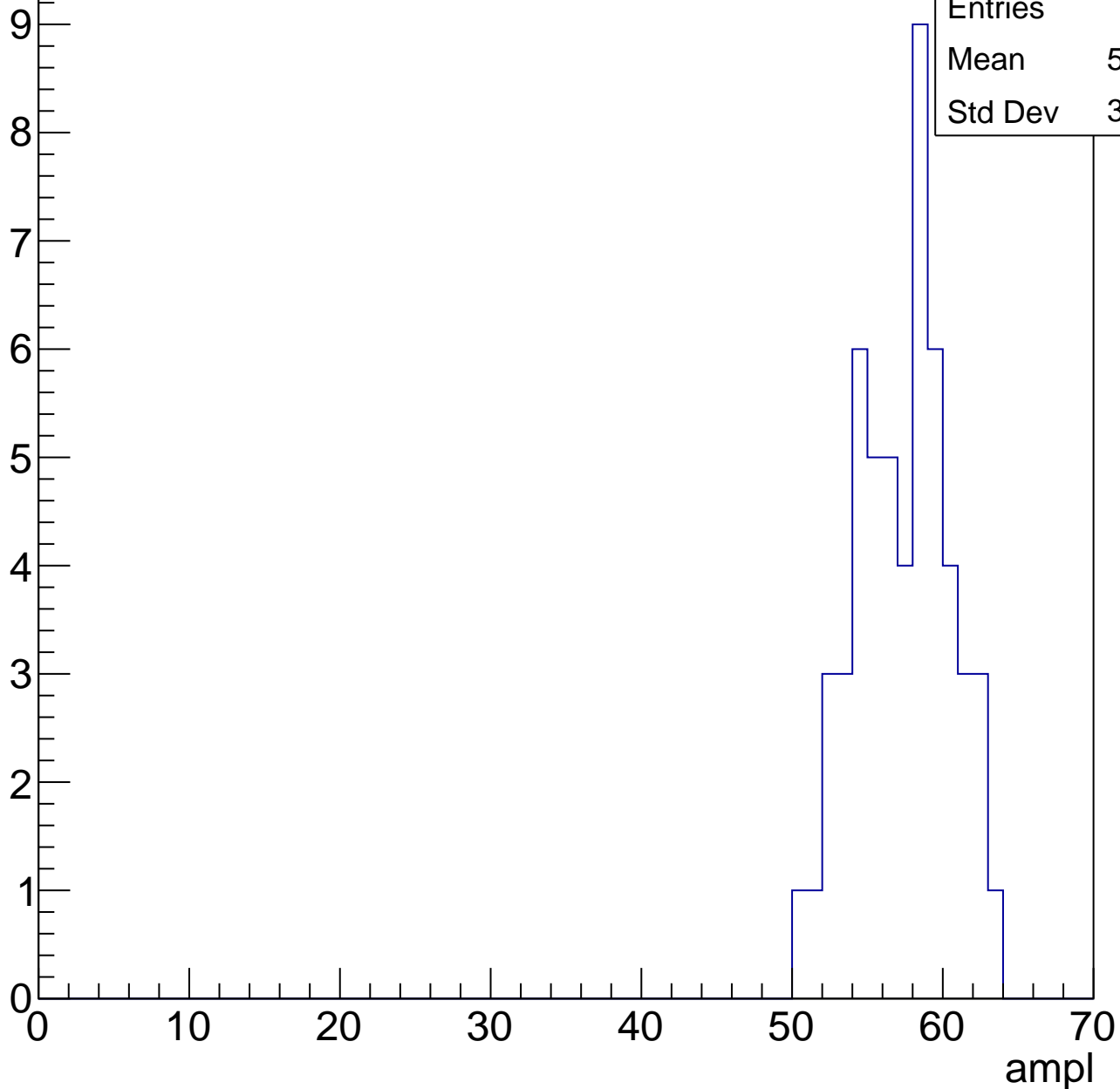
Entries	65
Mean	51.05
Std Dev	3.284



B1L100S, U19-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



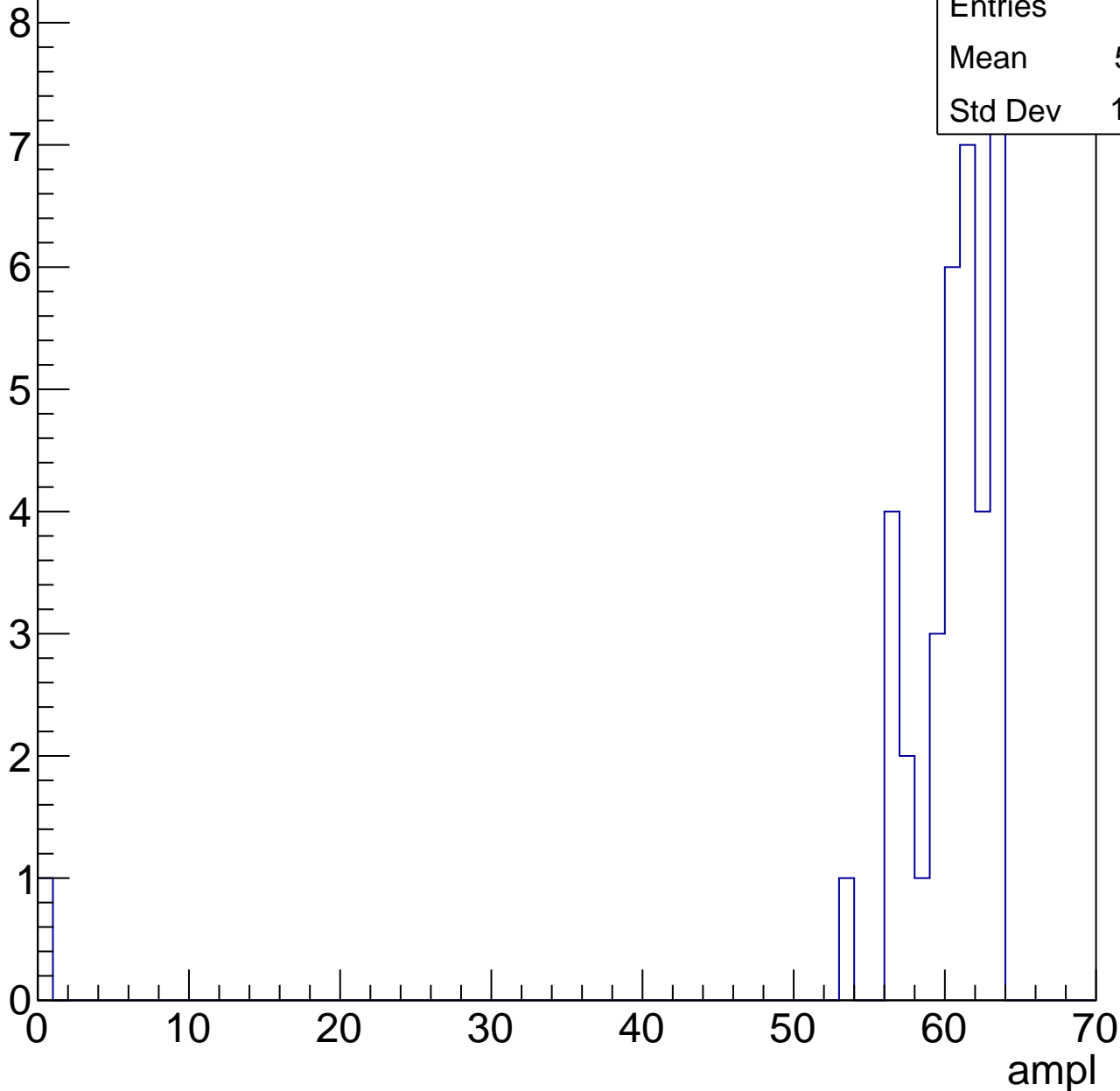
Entries	54
Mean	56.87
Std Dev	3.085

B1L100S, U19-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.51
Std Dev	10.07



B1L100S, U19-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch53, adc7

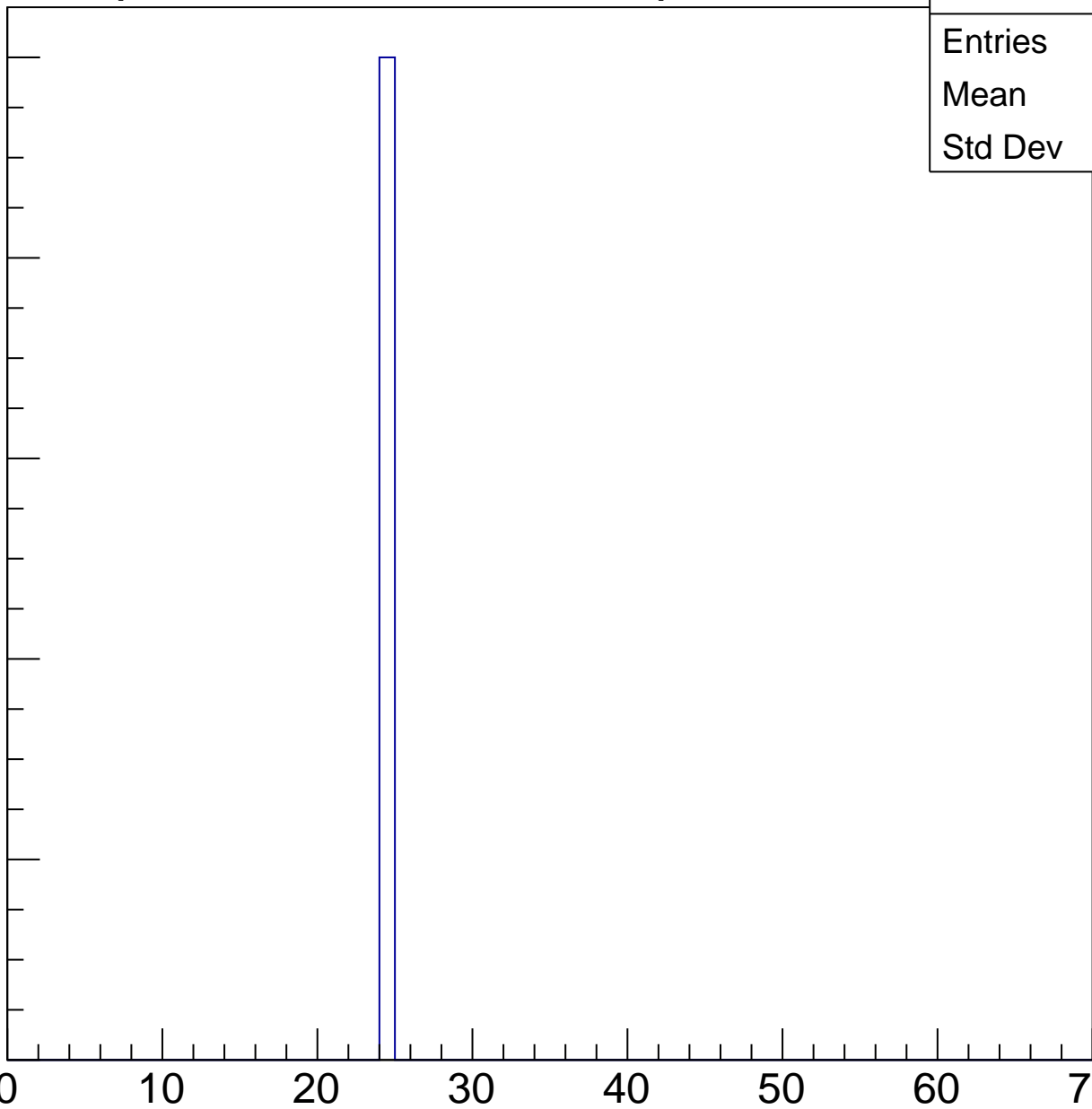
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L100S, U19-ch54, adc0

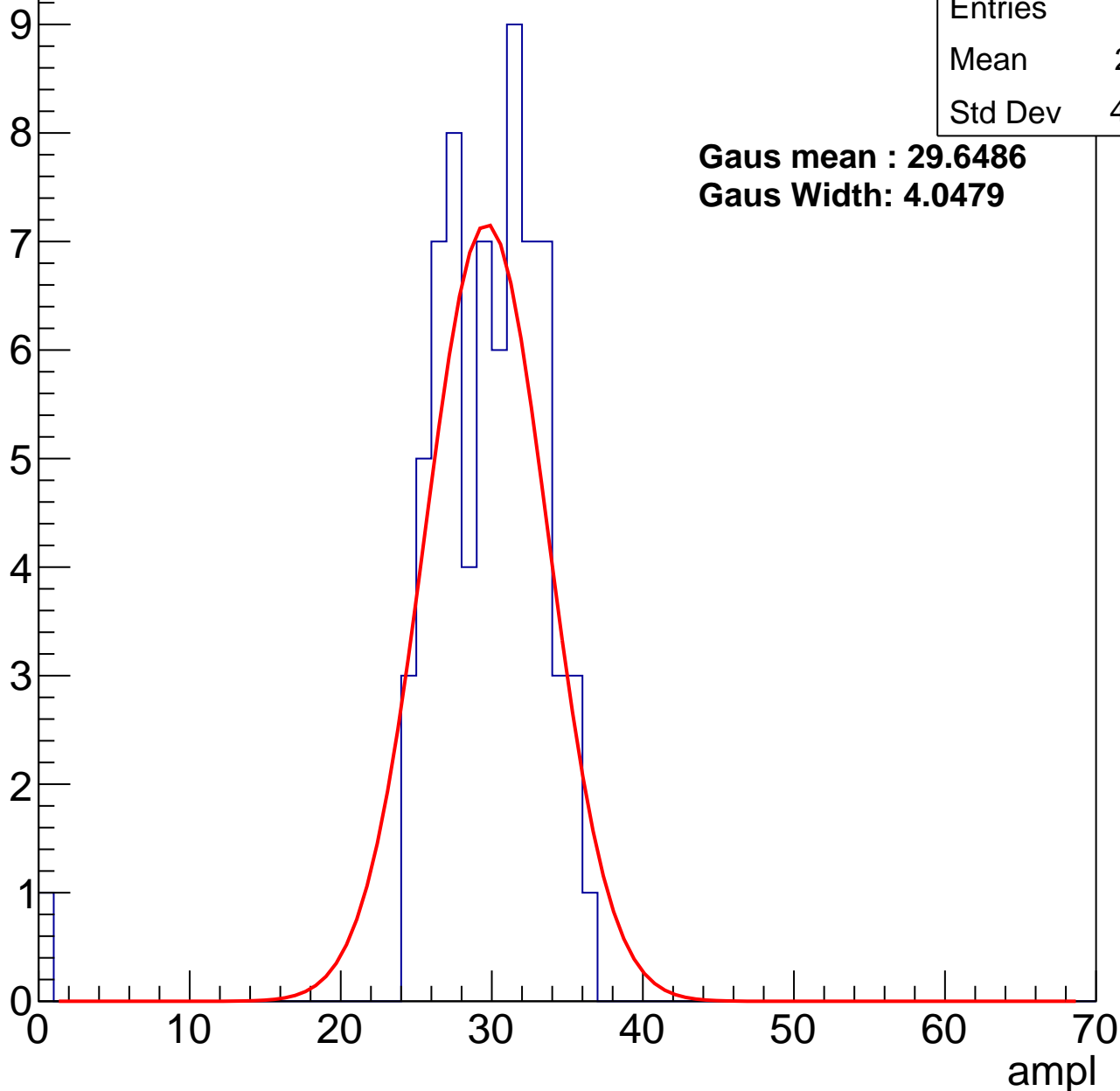
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.11
Std Dev	4.662

Gaus mean : 29.6486

Gaus Width: 4.0479



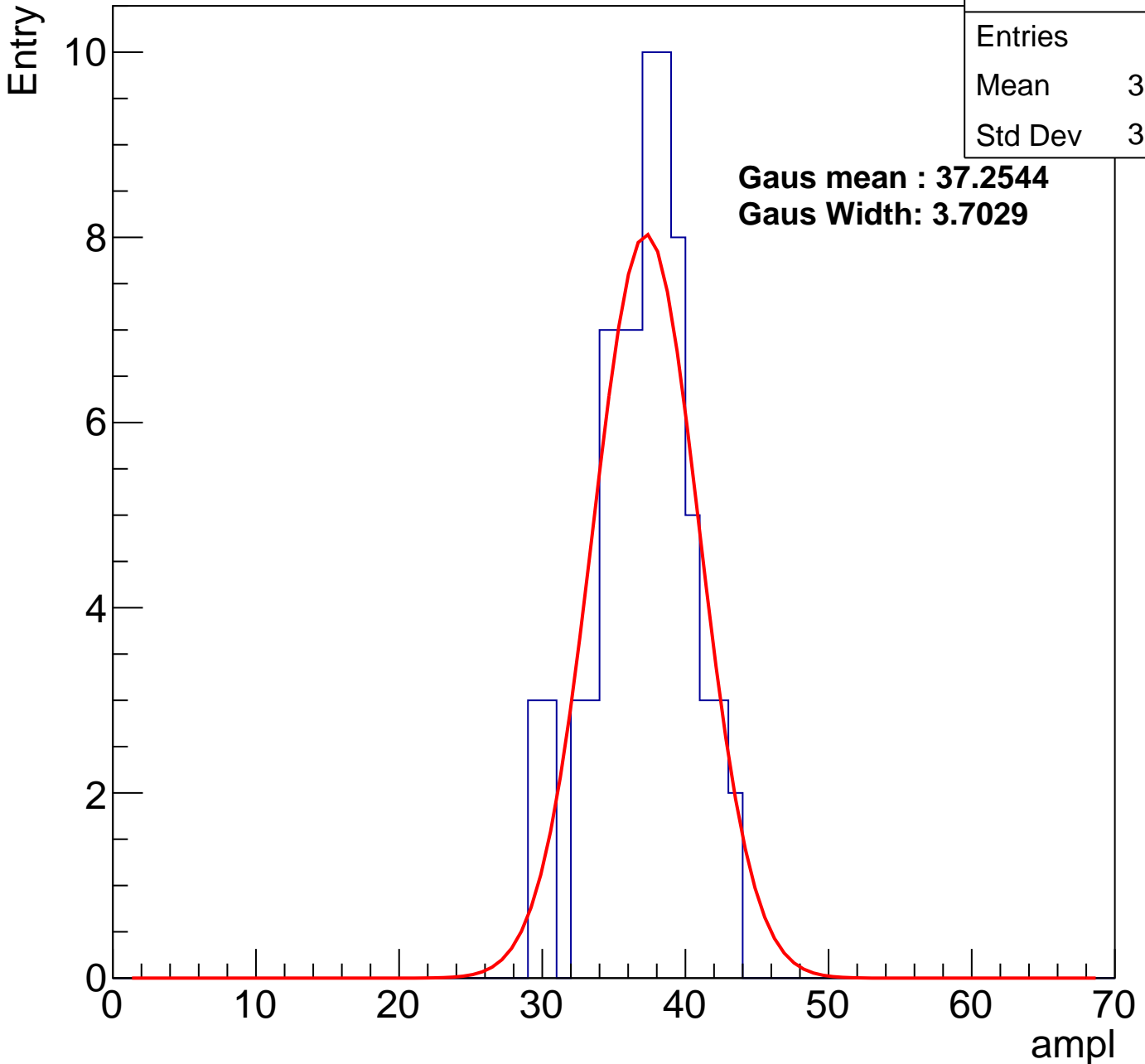
B1L100S, U19-ch54, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	36.54
Std Dev	3.342

Gaus mean : 37.2544

Gaus Width: 3.7029



B1L100S, U19-ch54, adc2

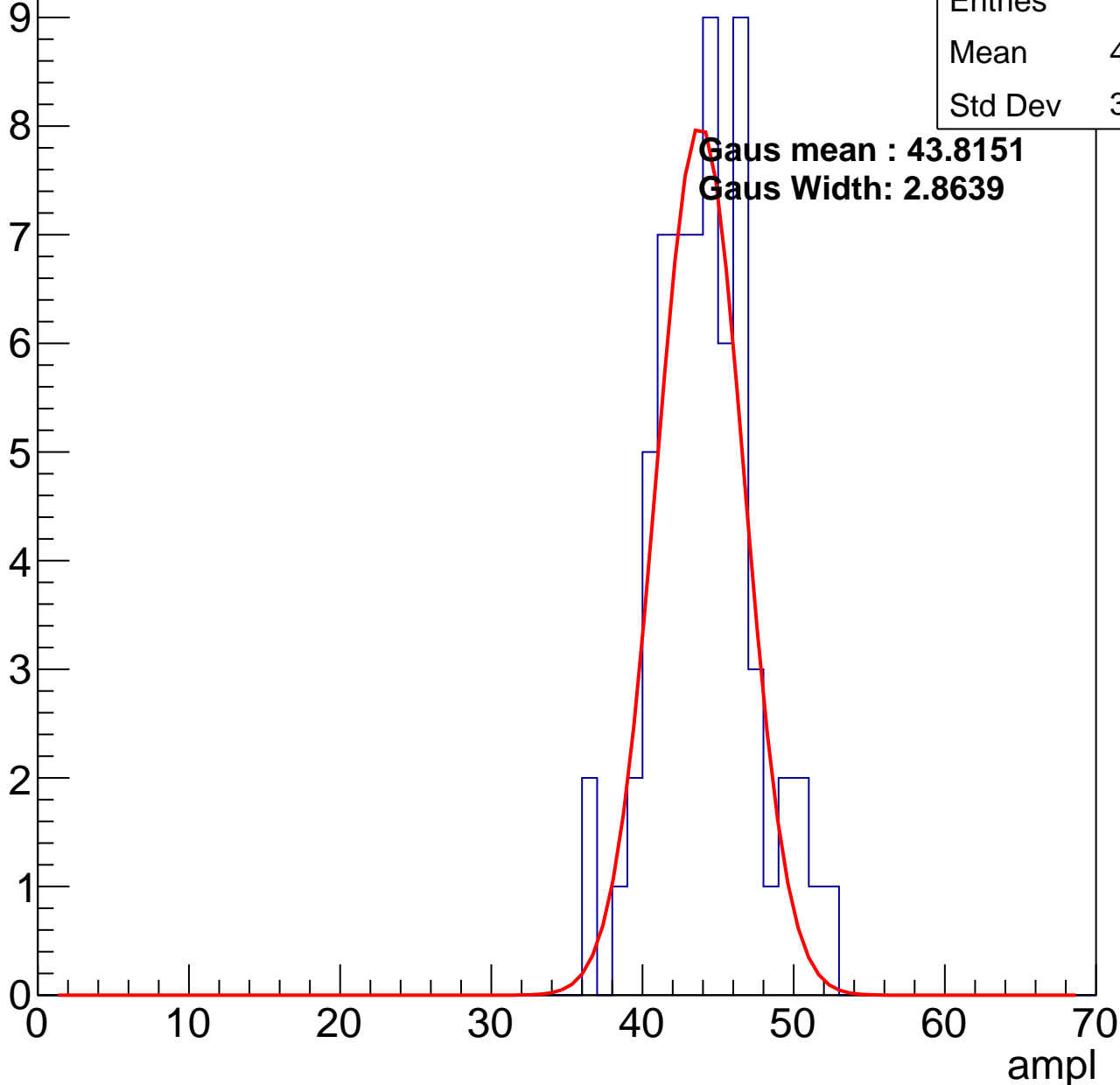
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.69
Std Dev	3.295

Gaus mean : 43.8151

Gaus Width: 2.8639

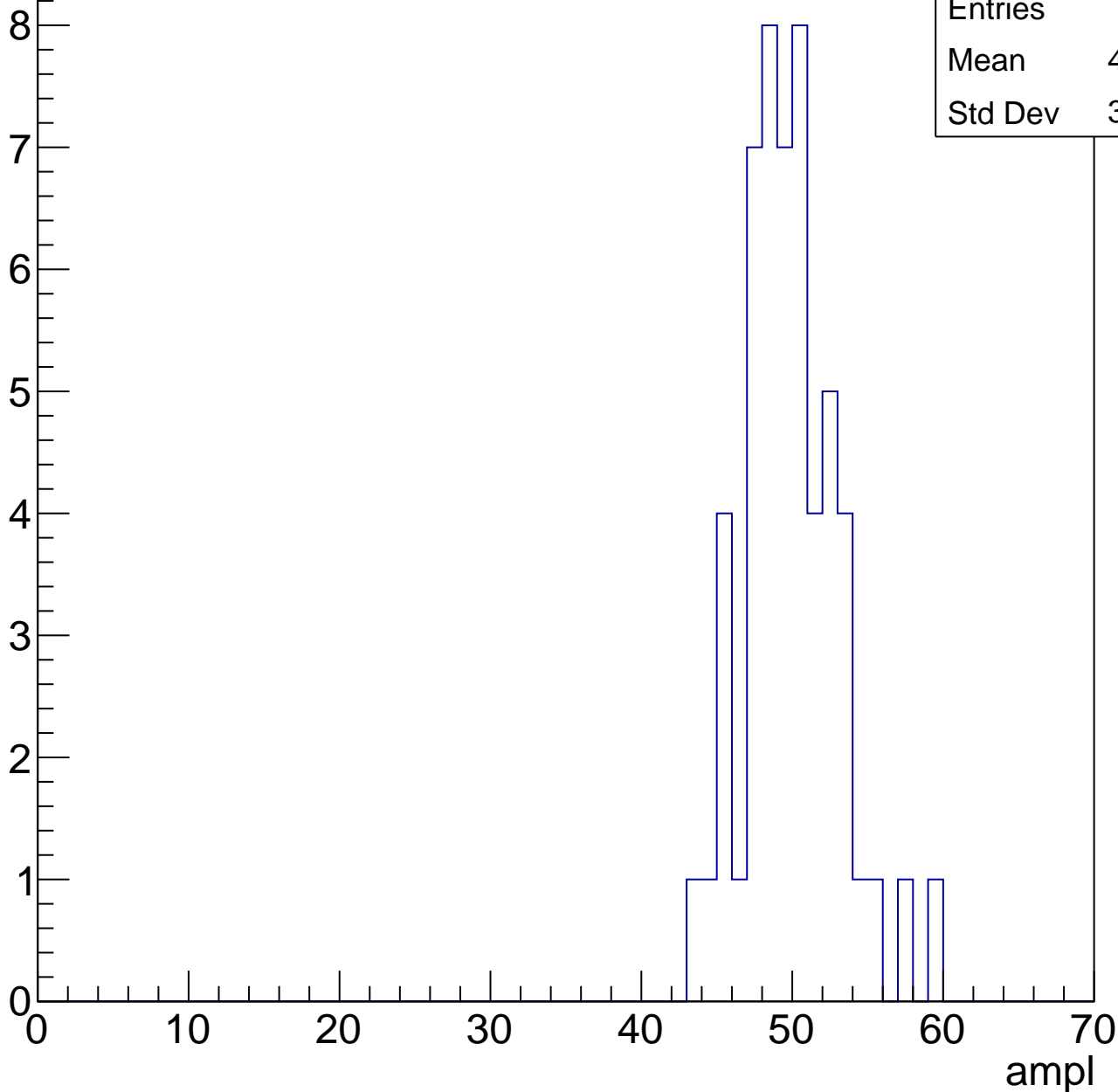


B1L100S, U19-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

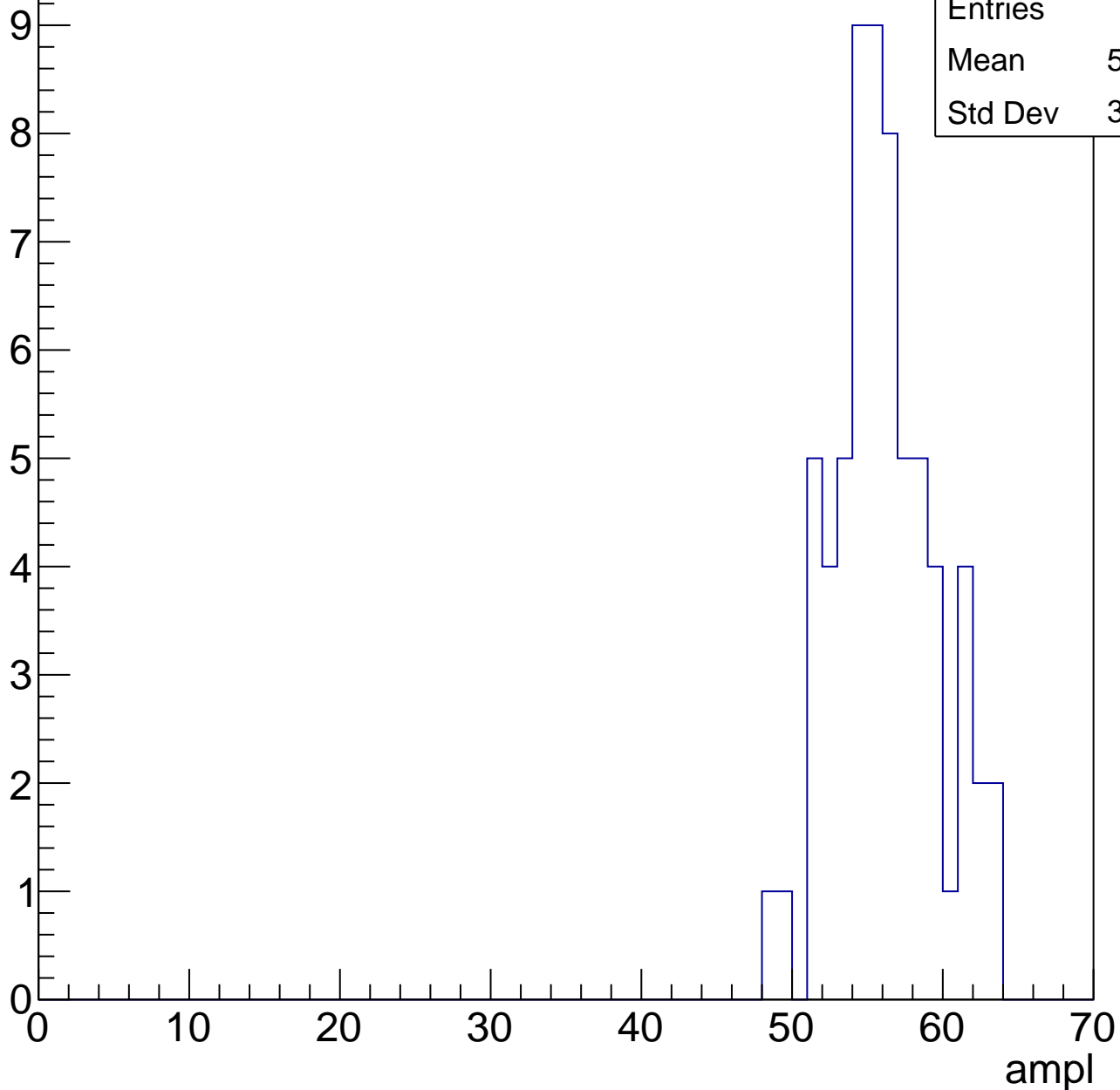
Entries	54
Mean	49.44
Std Dev	3.095



B1L100S, U19-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

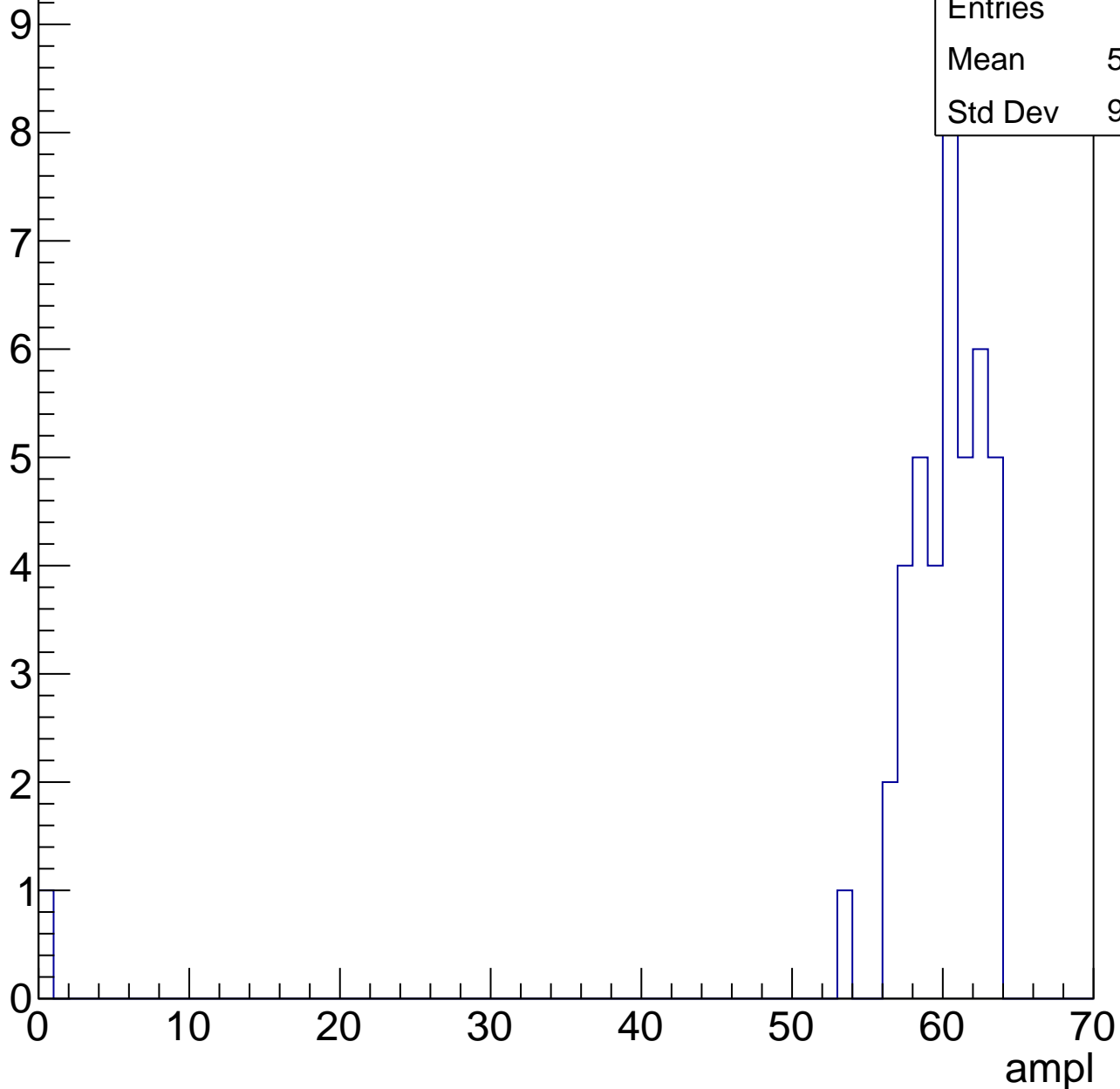


Entries	65
Mean	55.68
Std Dev	3.356

B1L100S, U19-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch55, adc0

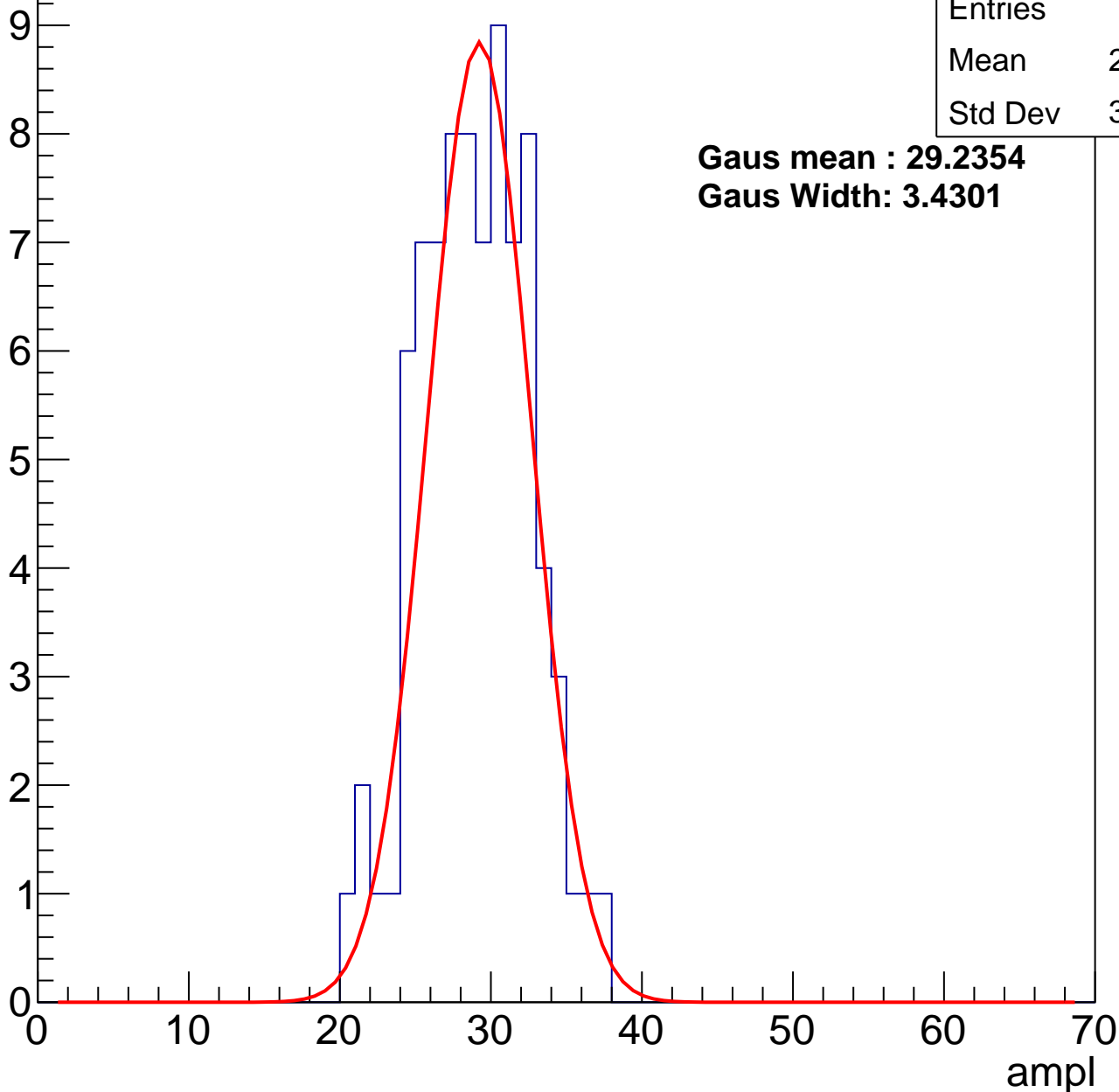
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	28.49
Std Dev	3.552

Gaus mean : 29.2354

Gaus Width: 3.4301



B1L100S, U19-ch55, adc1

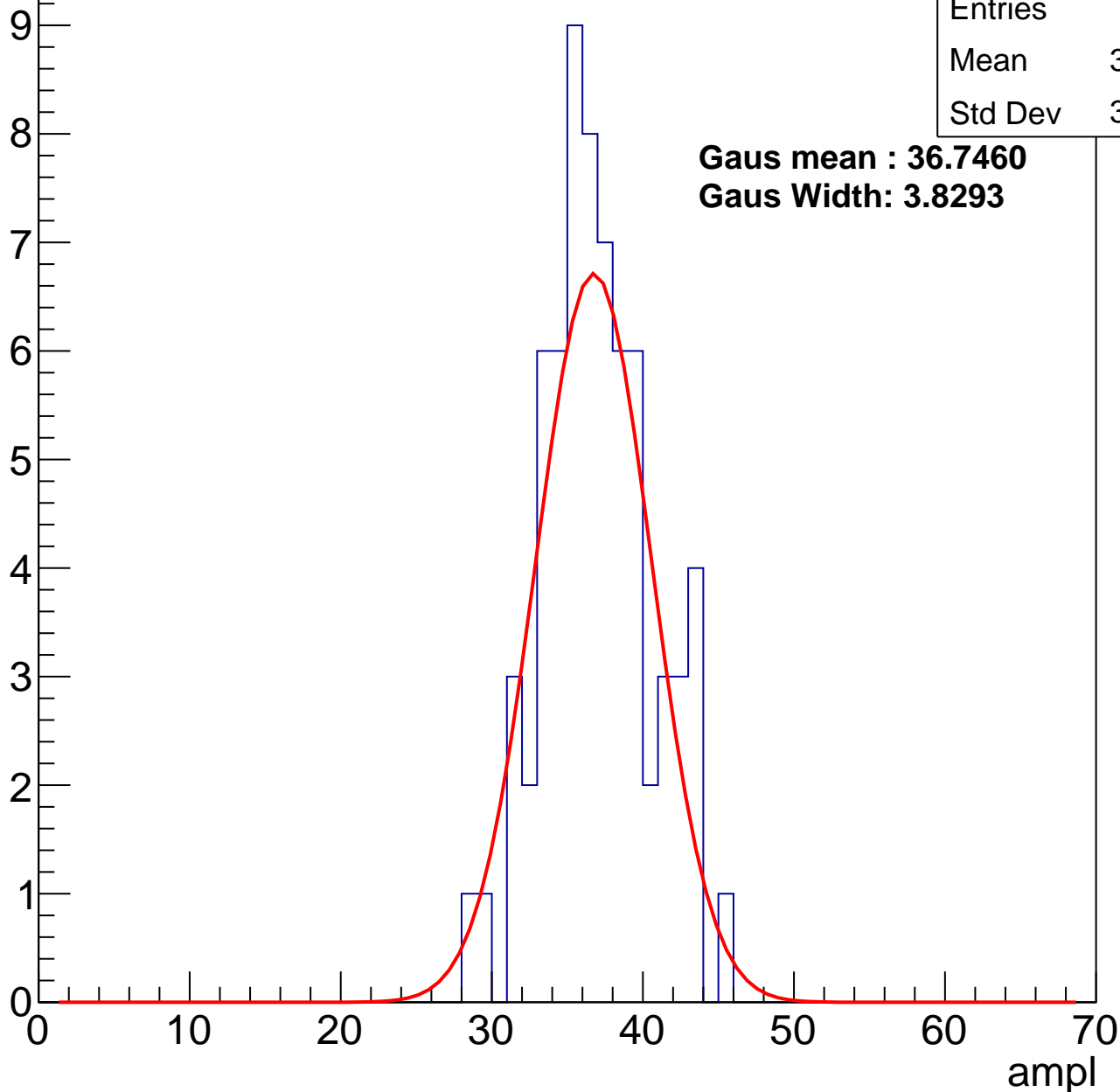
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.56
Std Dev	3.562

Gaus mean : 36.7460

Gaus Width: 3.8293



B1L100S, U19-ch55, adc2

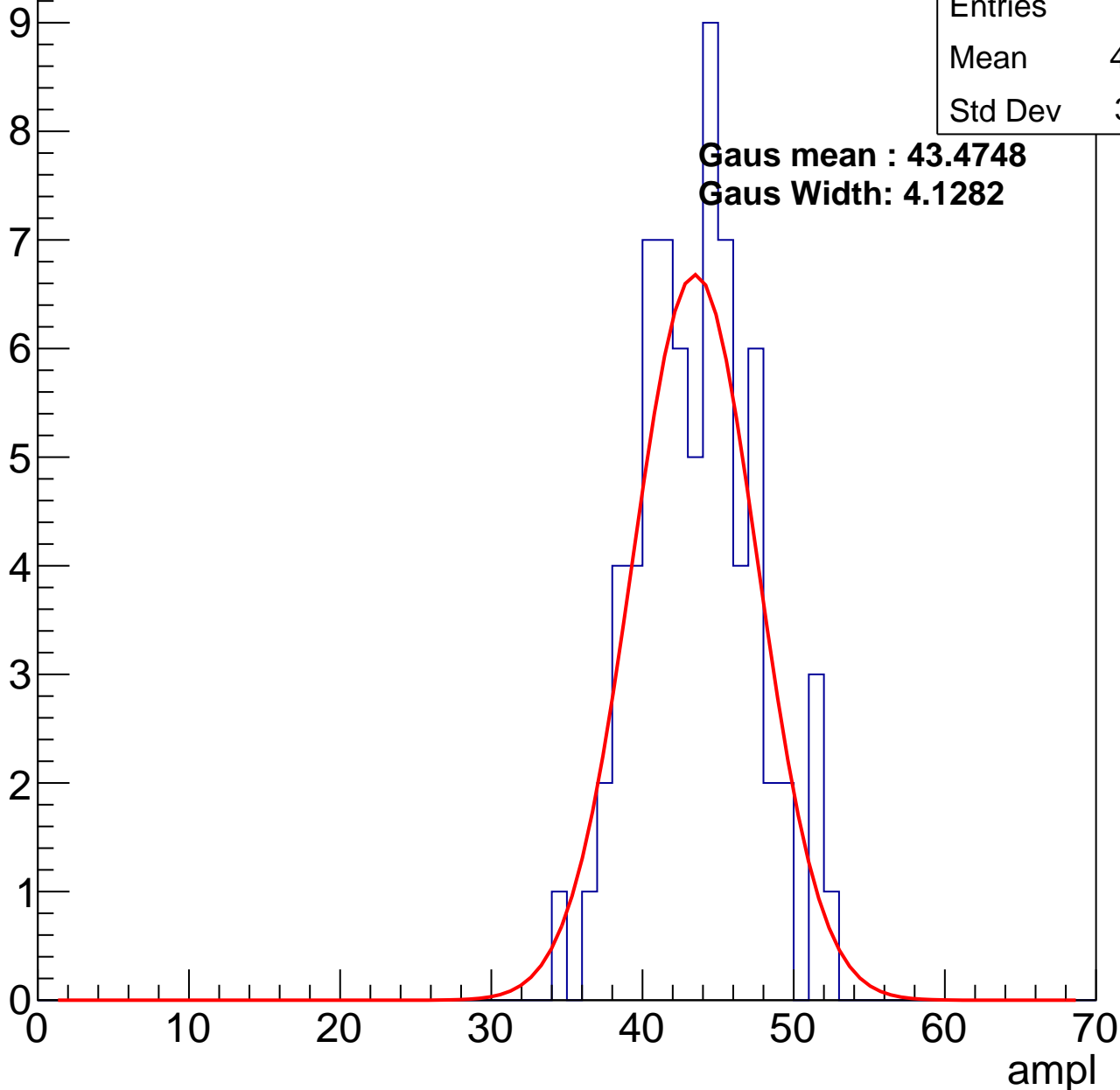
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.13
Std Dev	3.801

Gaus mean : 43.4748

Gaus Width: 4.1282

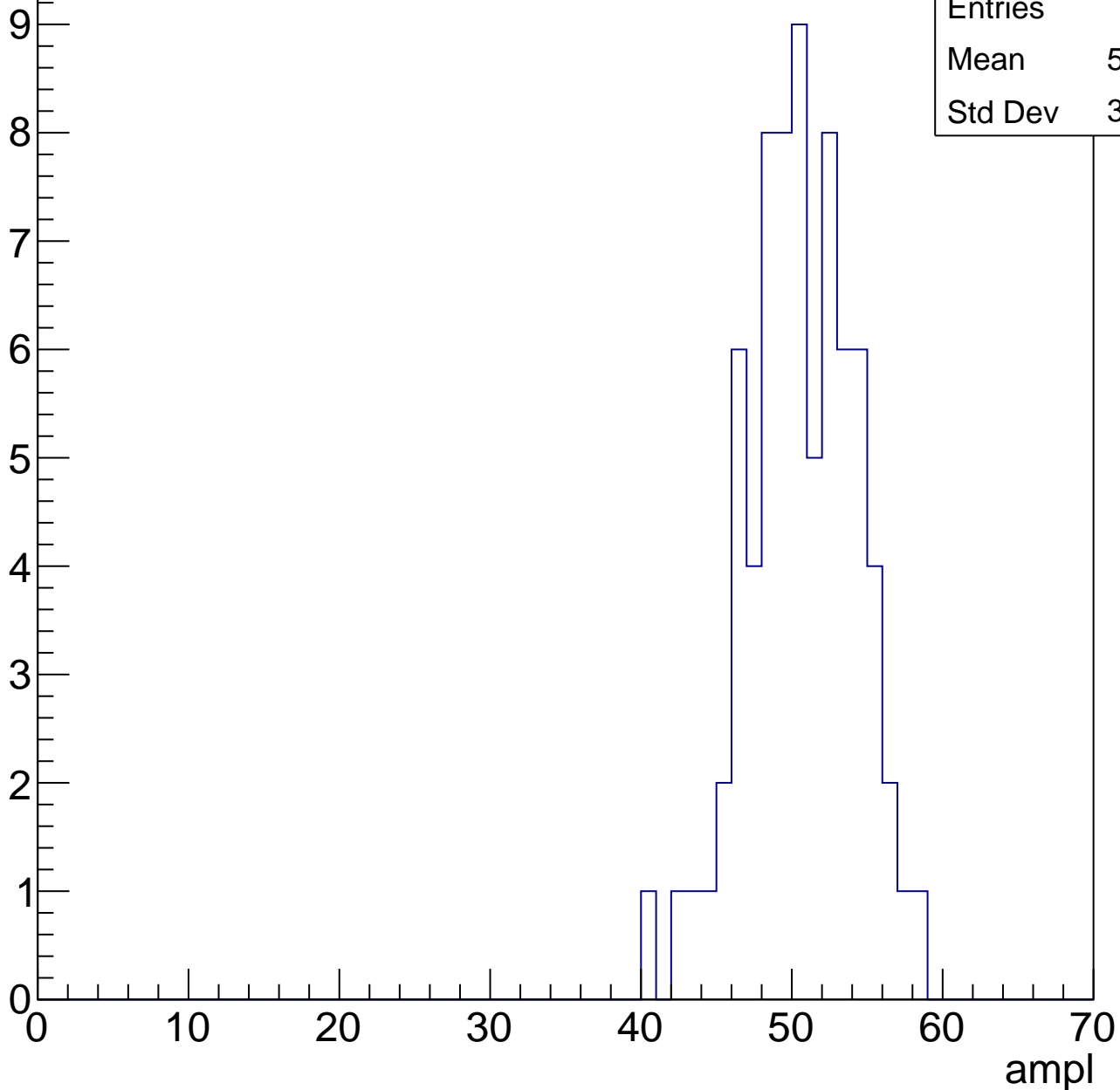


B1L100S, U19-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.12
Std Dev	3.568

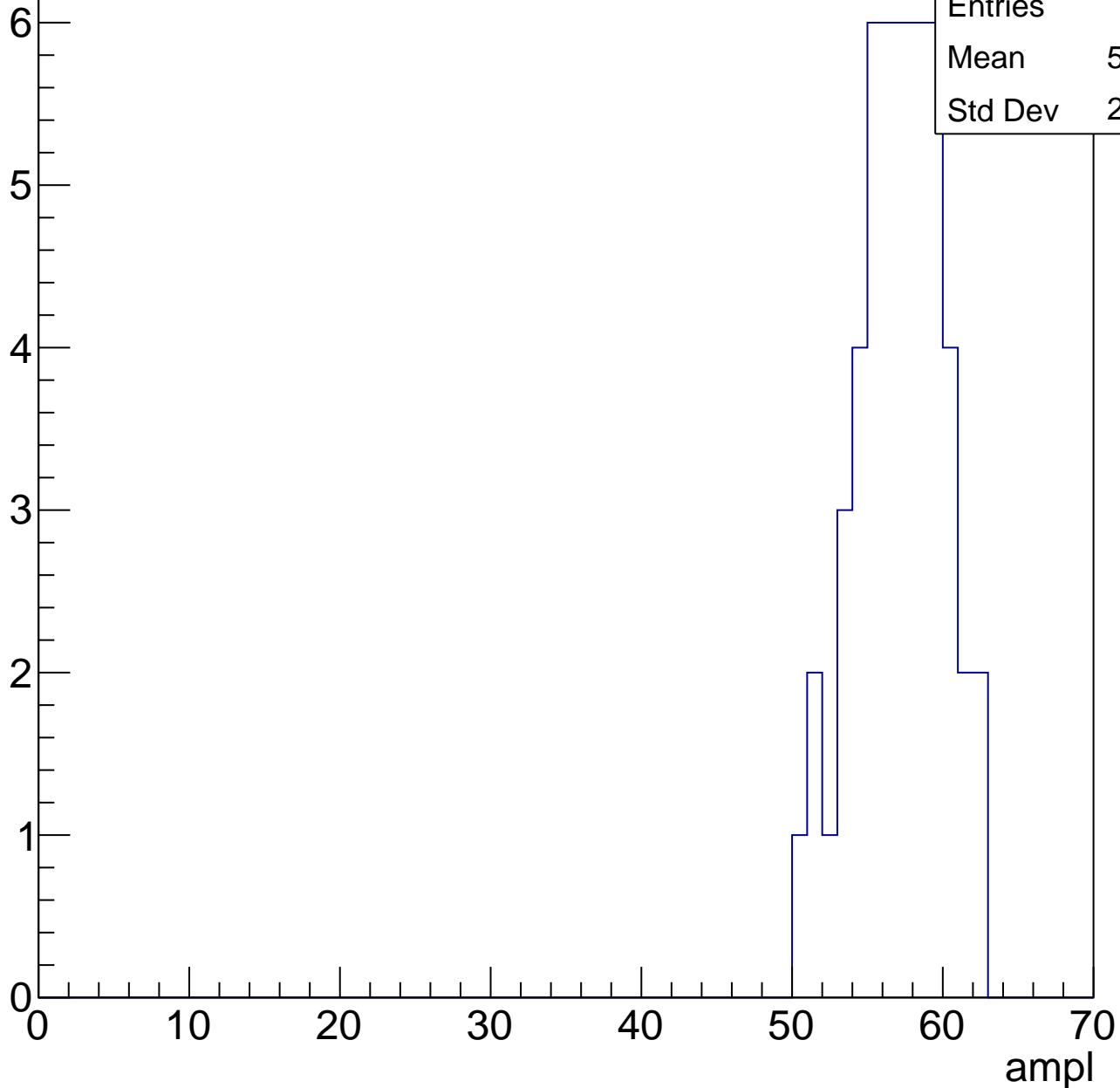


B1L100S, U19-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	56.63
Std Dev	2.862

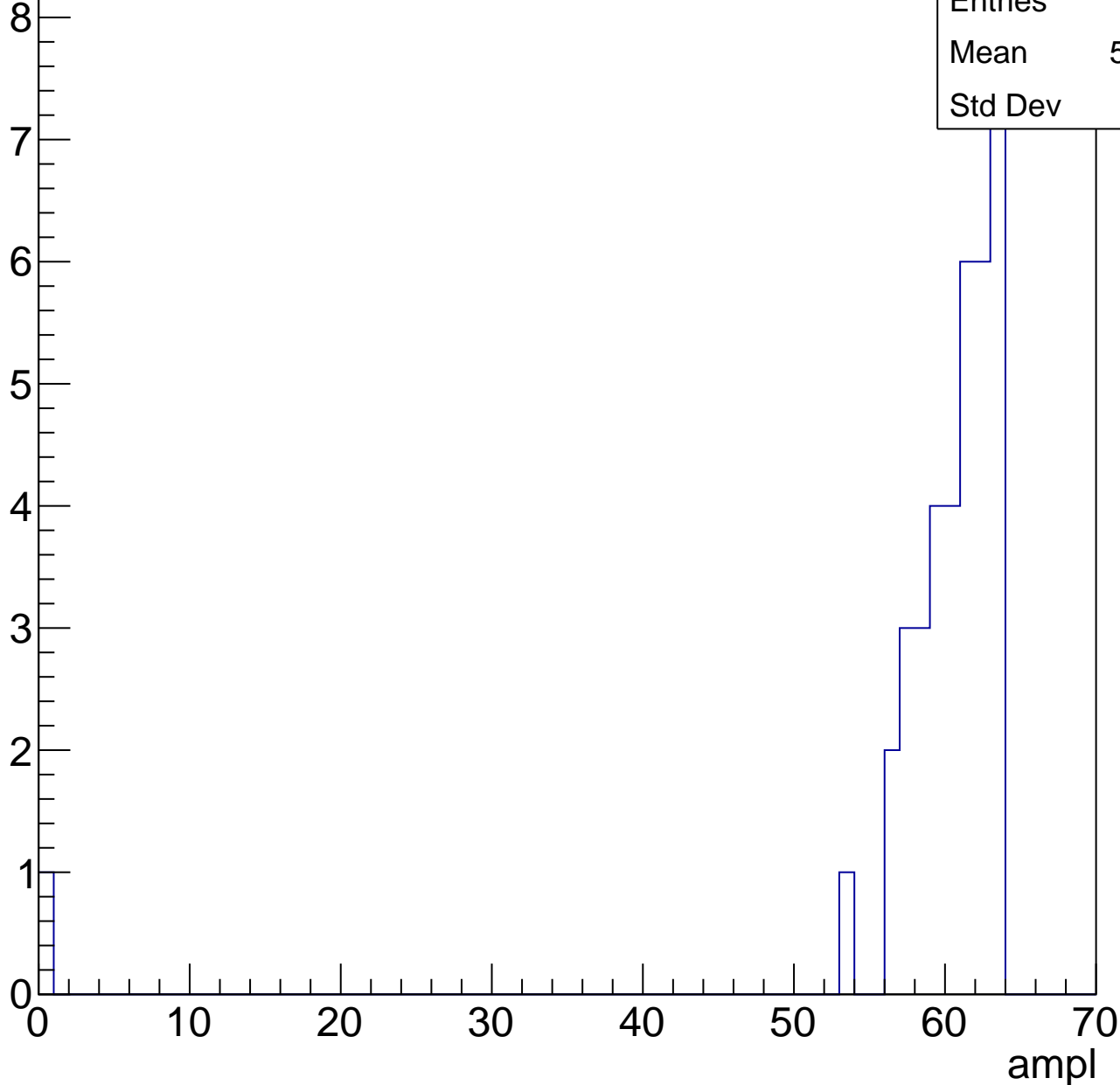


B1L100S, U19-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.63
Std Dev	9.94



B1L100S, U19-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	8
Mean	61.5
Std Dev	1.118

B1L100S, U19-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch56, adc0

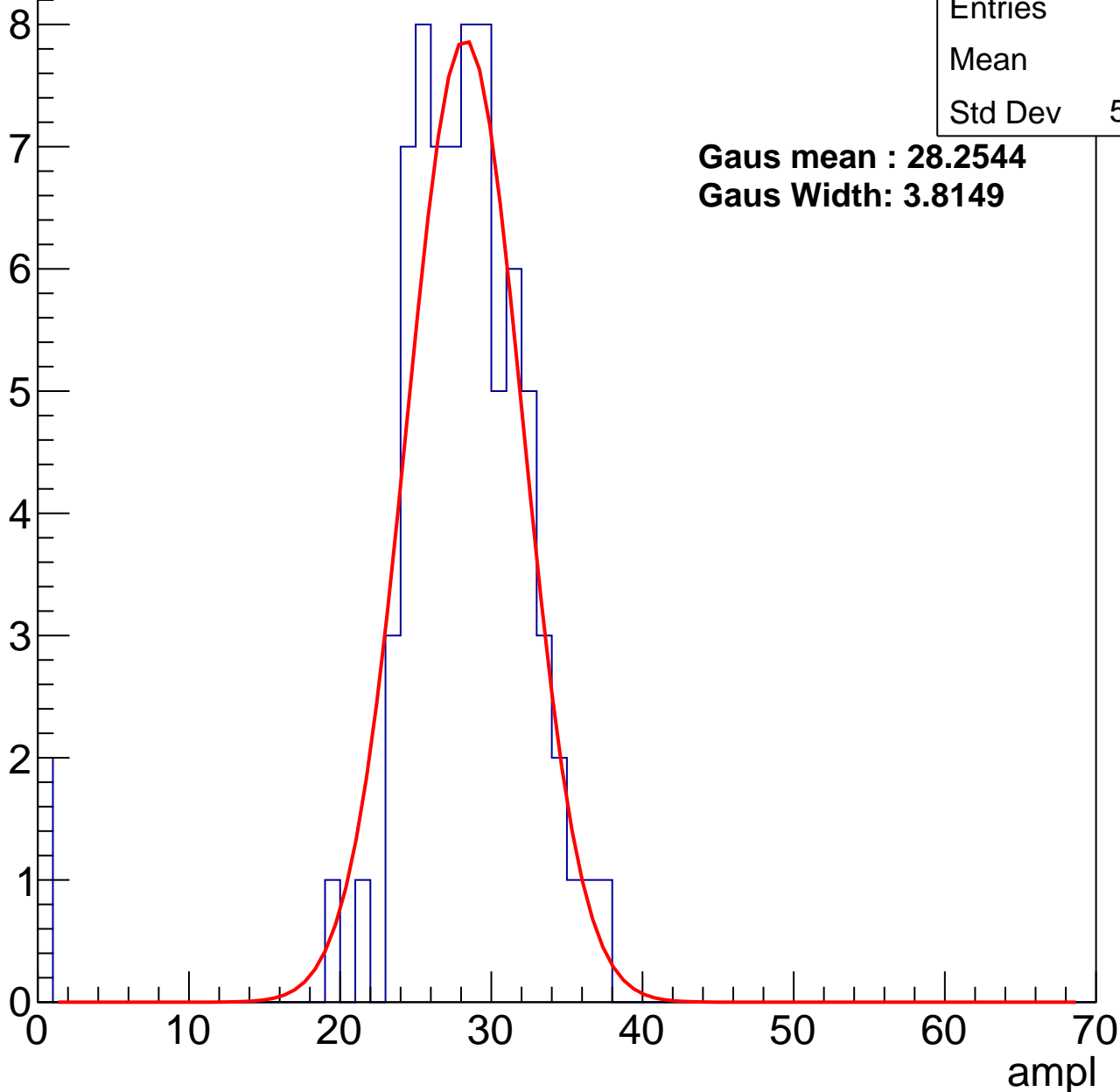
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	27.3
Std Dev	5.687

Gaus mean : 28.2544

Gaus Width: 3.8149



B1L100S, U19-ch56, adc1

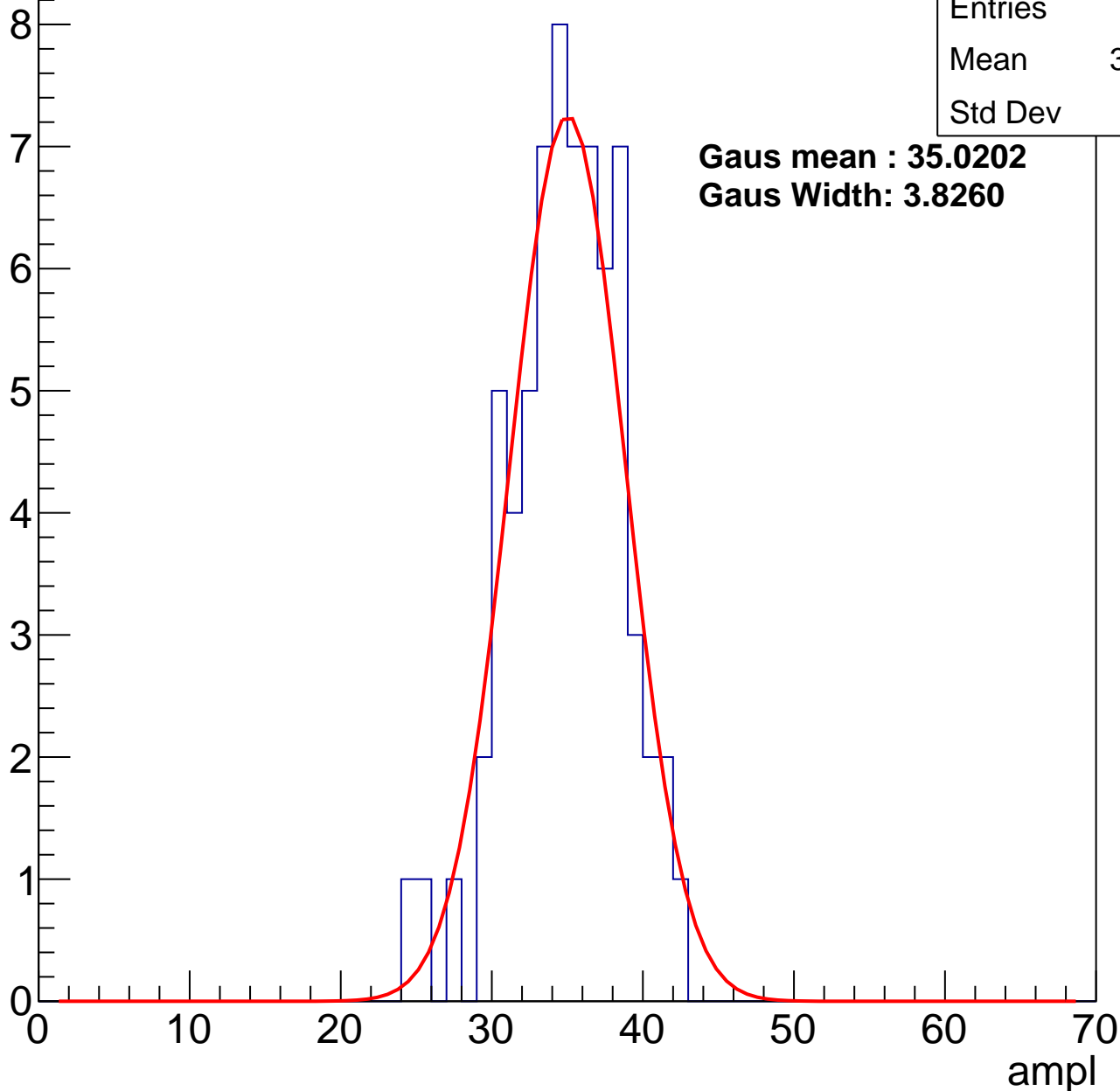
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.45
Std Dev	3.65

Gaus mean : 35.0202

Gaus Width: 3.8260



B1L100S, U19-ch56, adc2

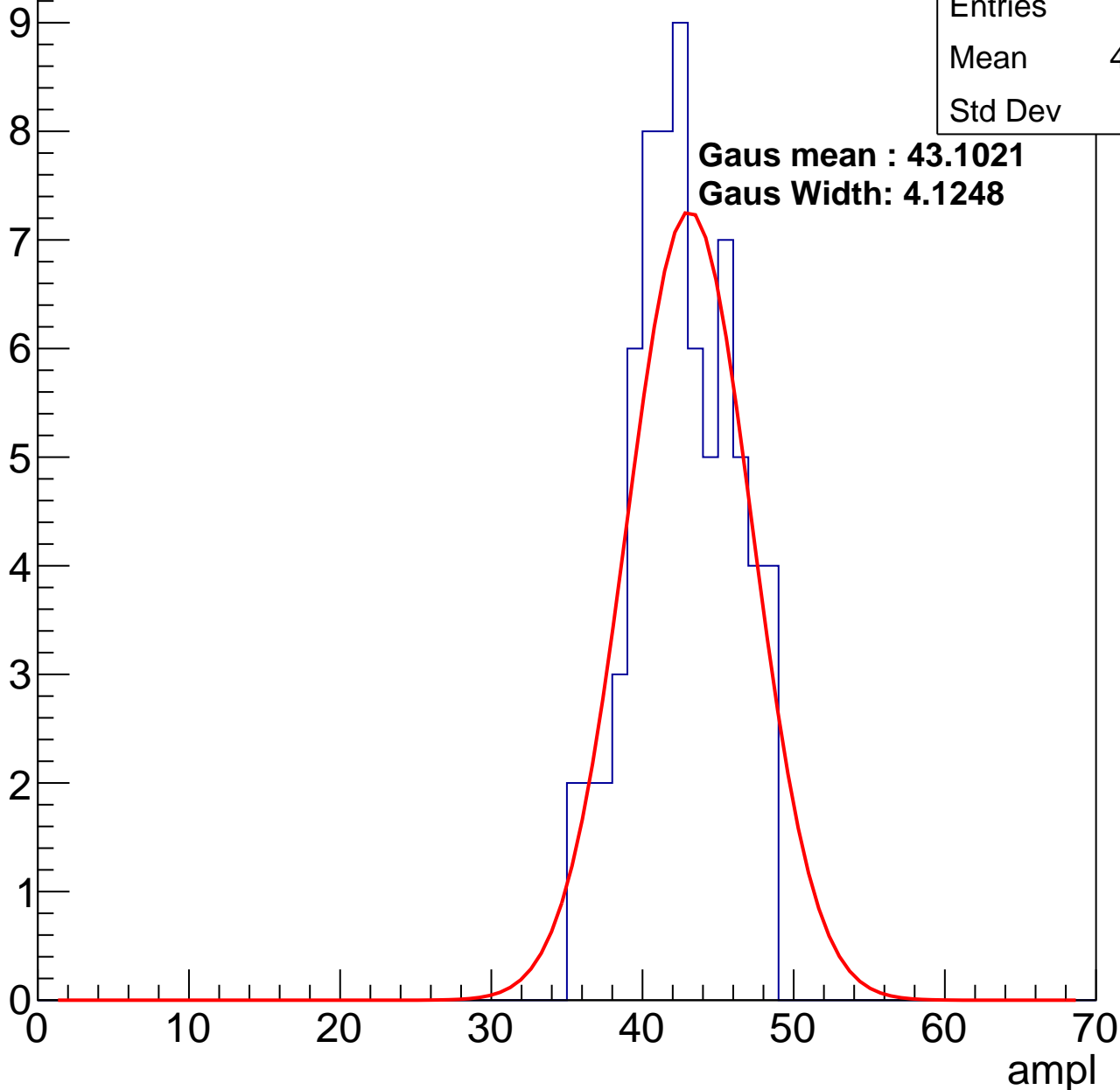
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	42.15
Std Dev	3.3

Gaus mean : 43.1021

Gaus Width: 4.1248

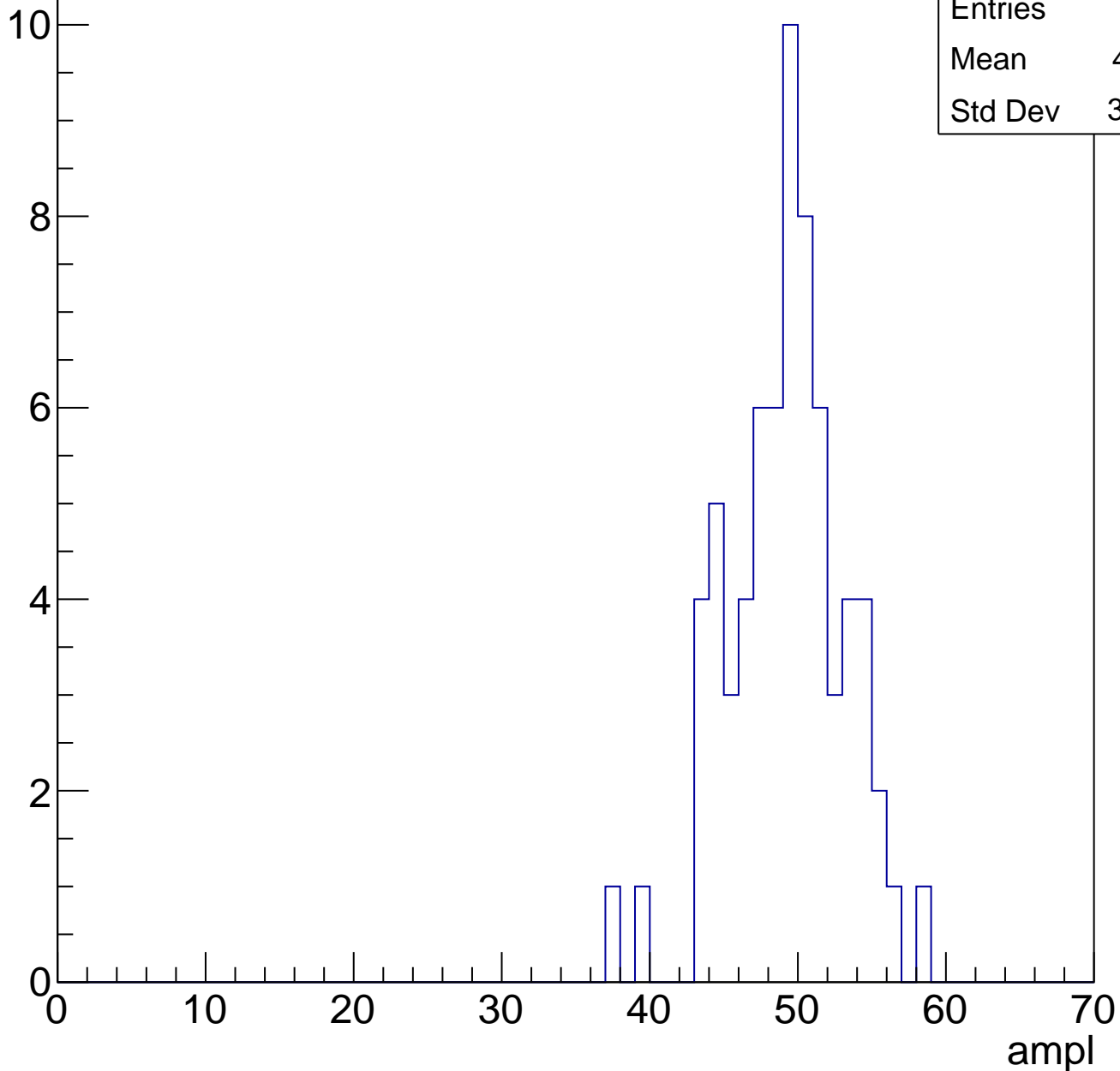


B1L100S, U19-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

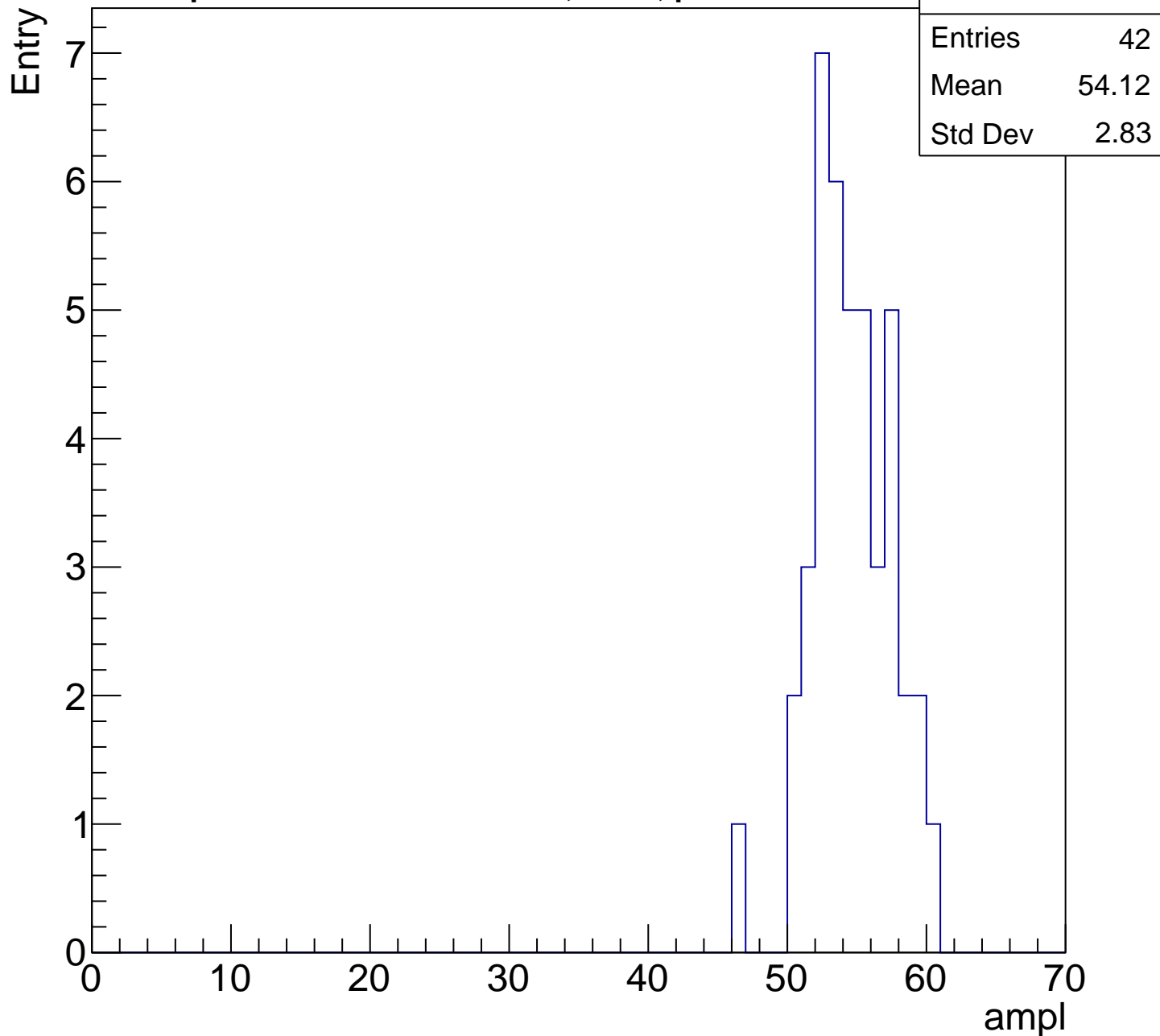
Entries	69
Mean	48.71
Std Dev	3.894

Entry



B1L100S, U19-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

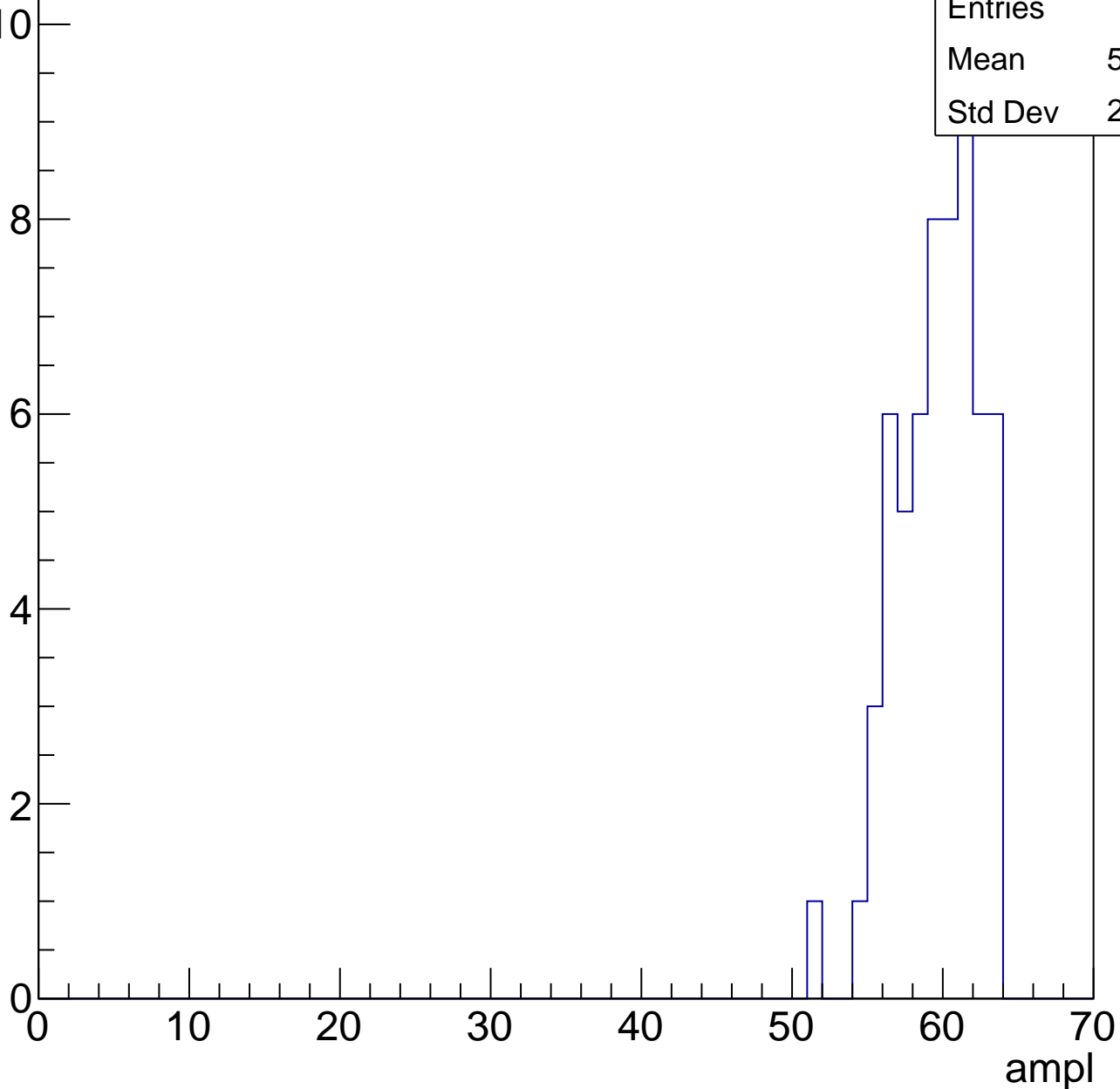


B1L100S, U19-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

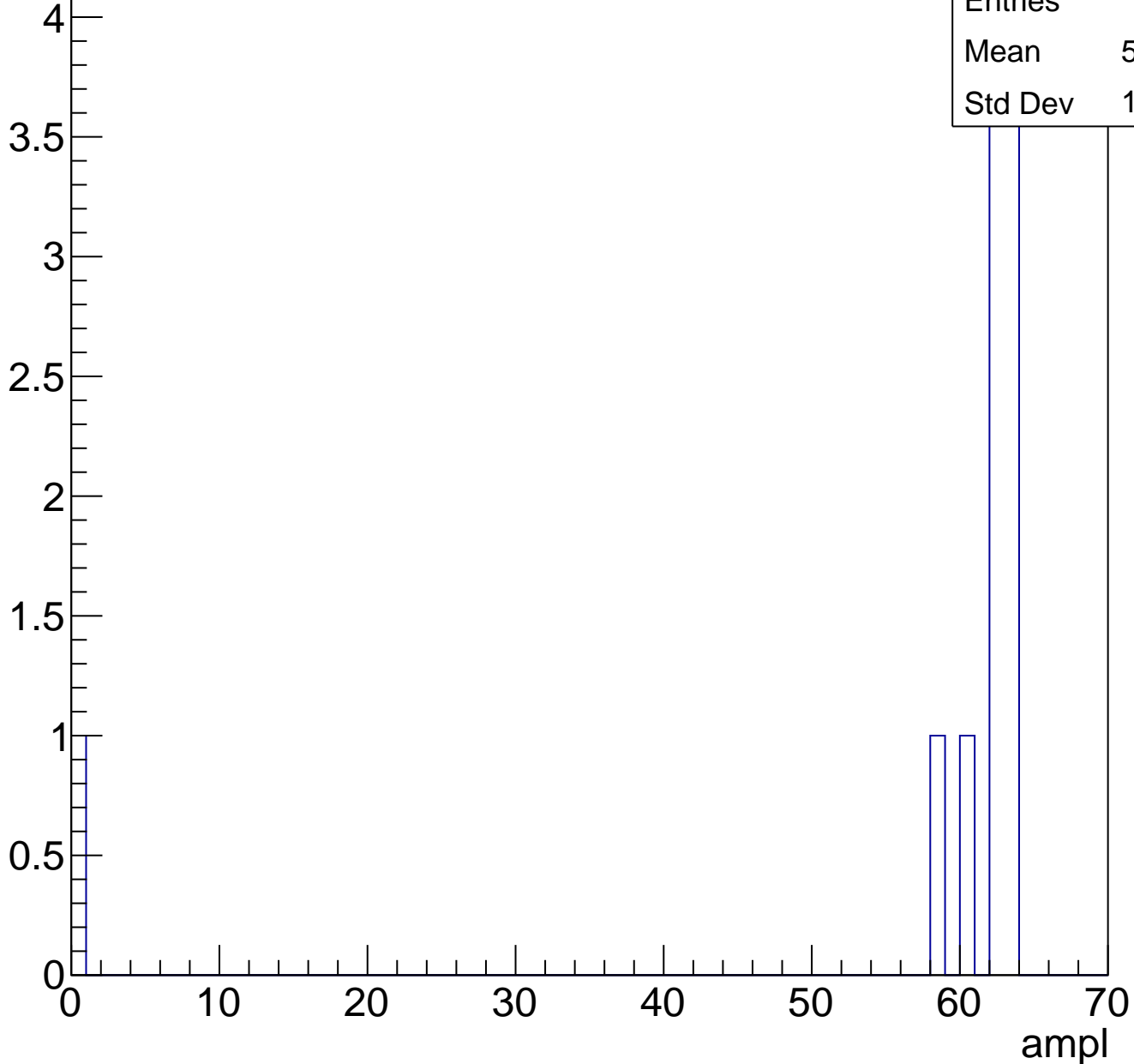
Entries	60
Mean	59.18
Std Dev	2.624



B1L100S, U19-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch57, adc0

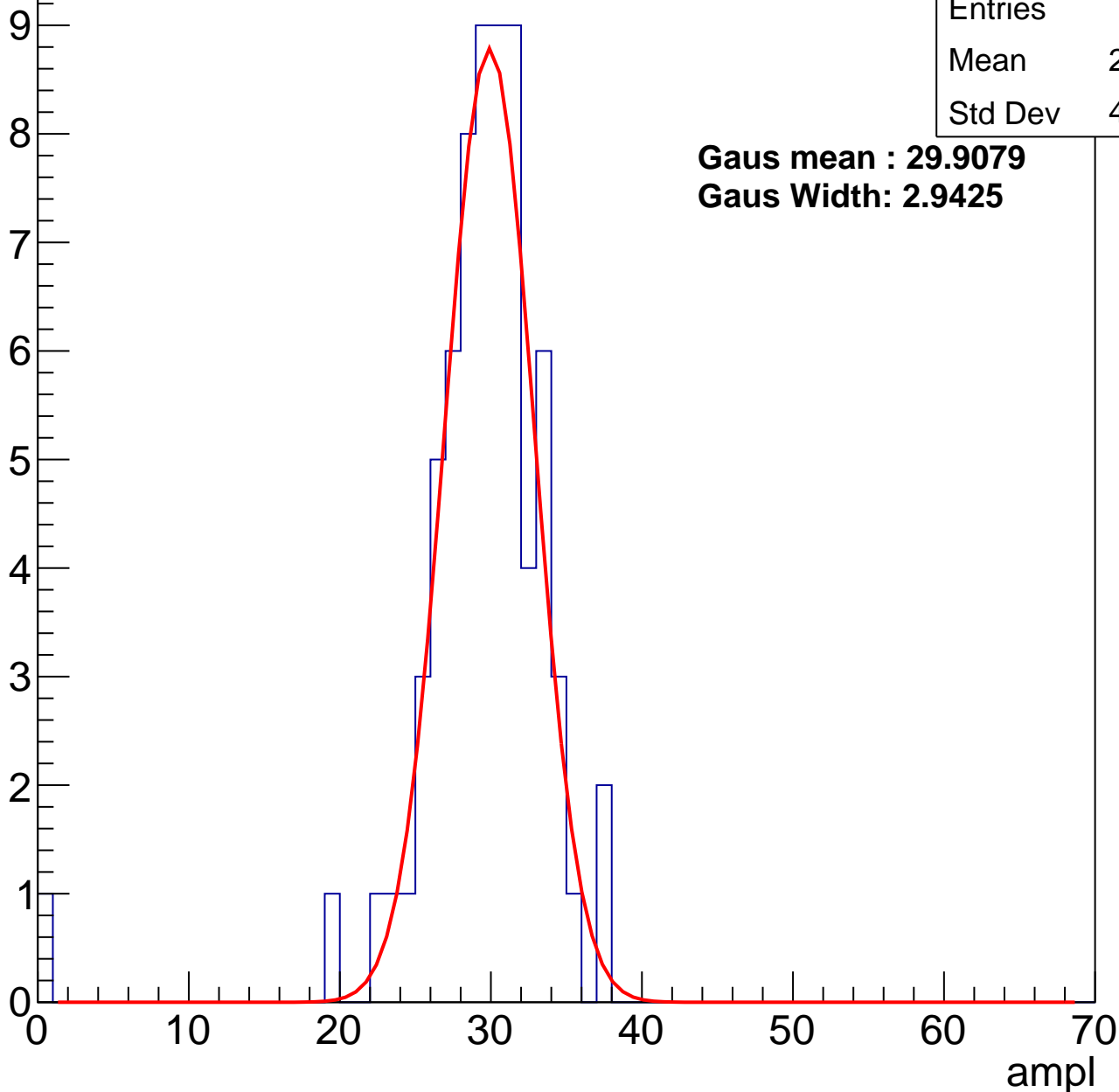
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.94
Std Dev	4.769

Gaus mean : 29.9079

Gaus Width: 2.9425

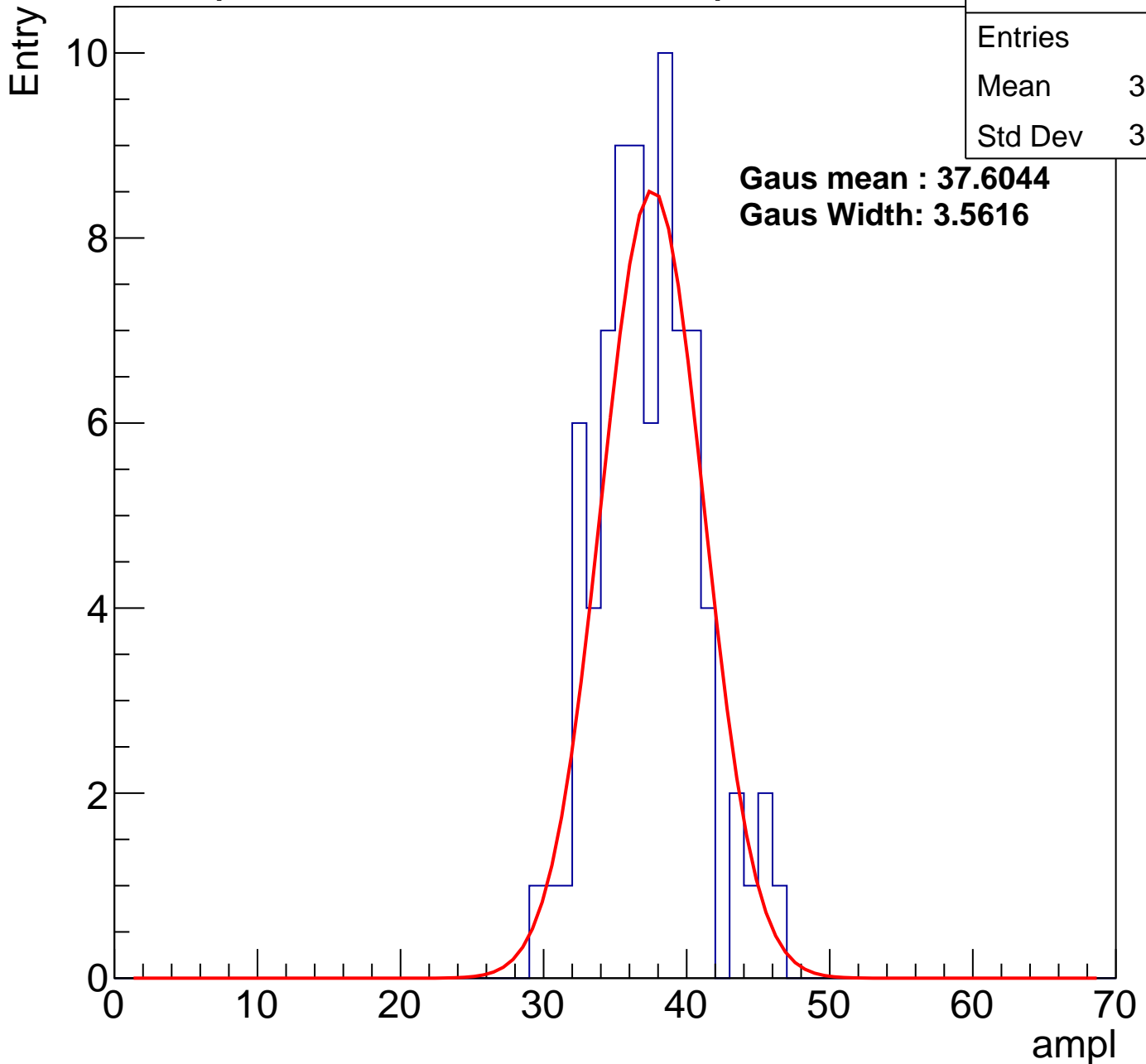


B1L100S, U19-ch57, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	36.87
Std Dev	3.517

Gaus mean : 37.6044
Gaus Width: 3.5616



B1L100S, U19-ch57, adc2

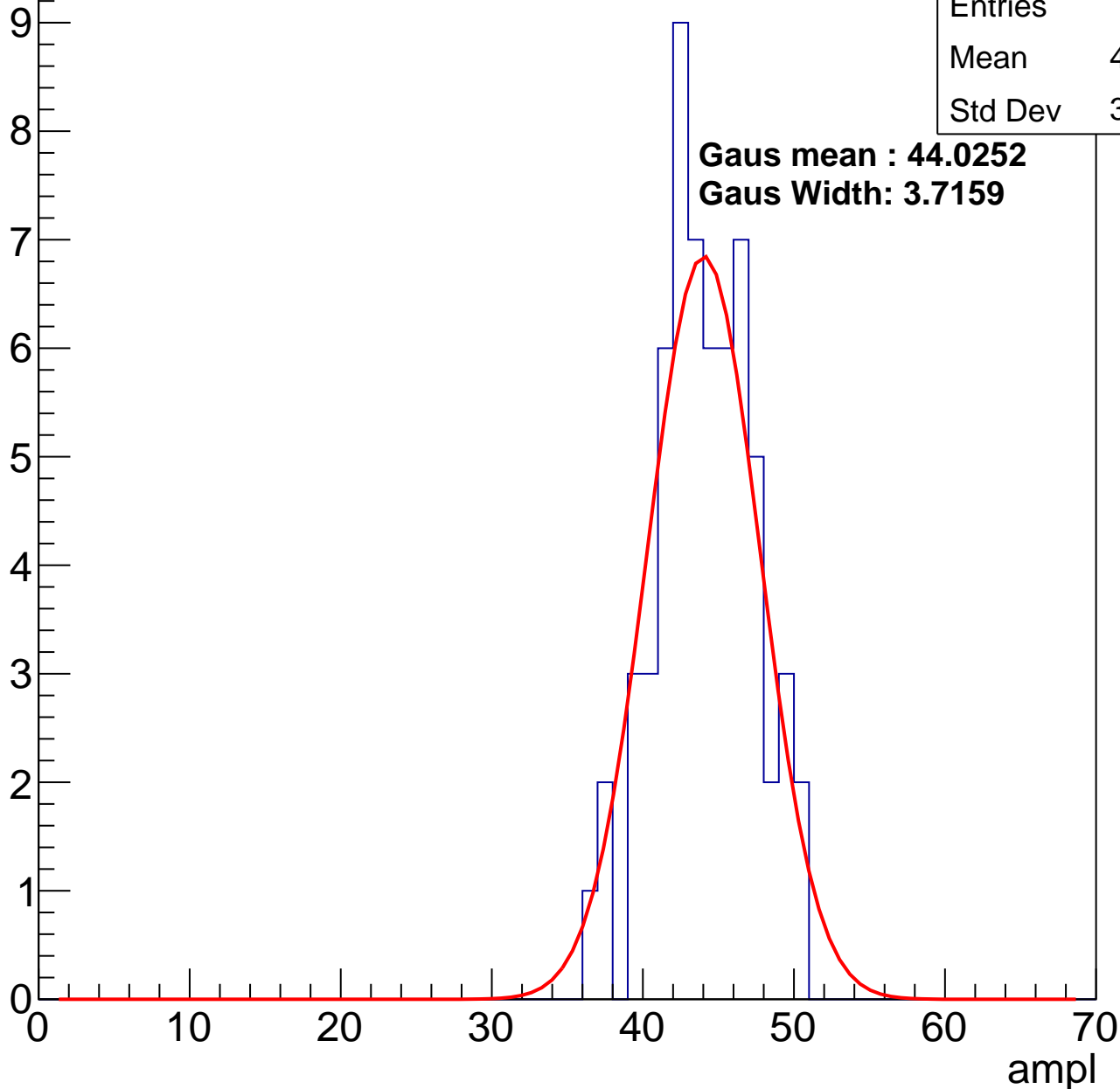
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.65
Std Dev	3.193

Gaus mean : 44.0252

Gaus Width: 3.7159

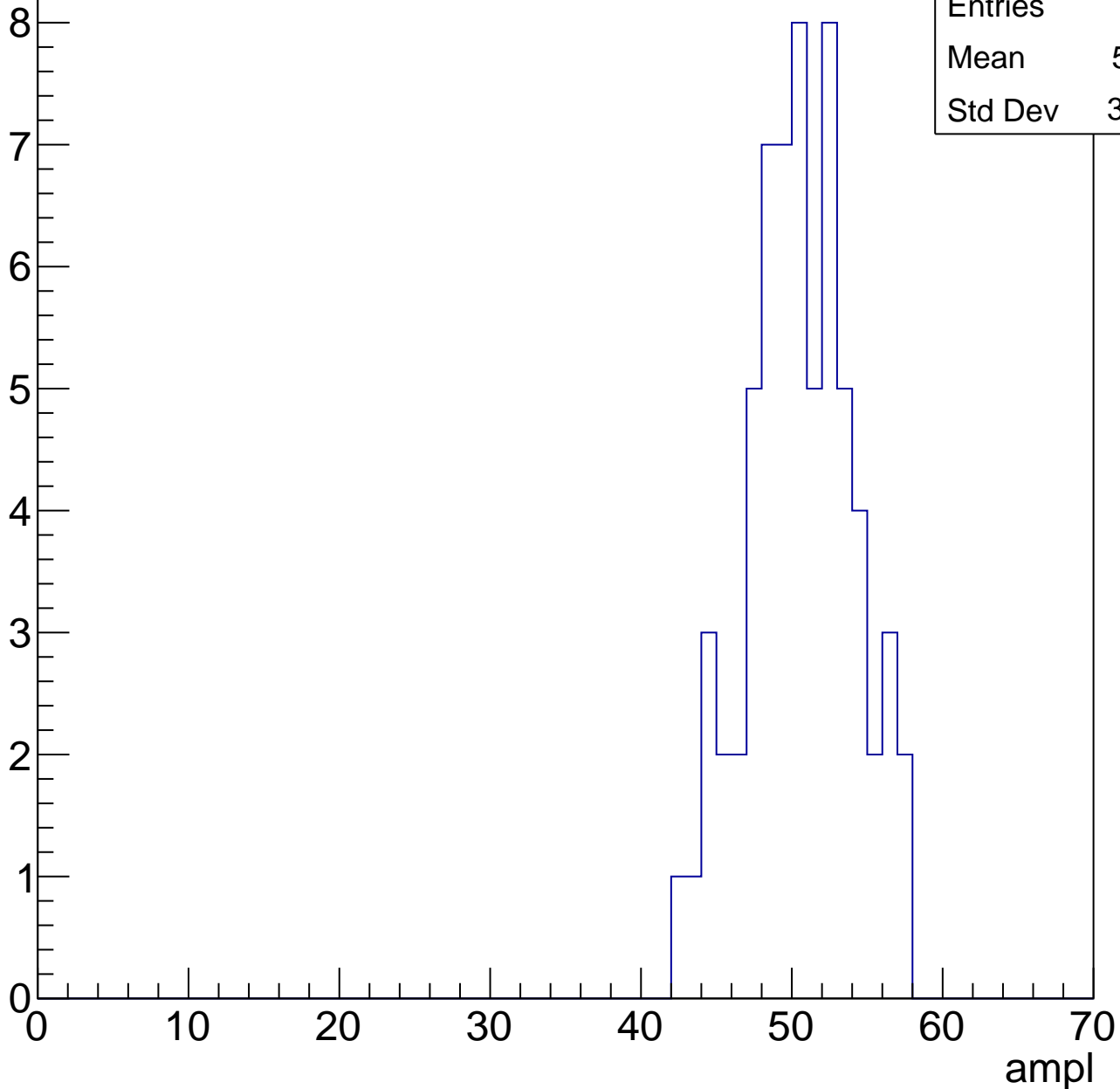


B1L100S, U19-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.11
Std Dev	3.474

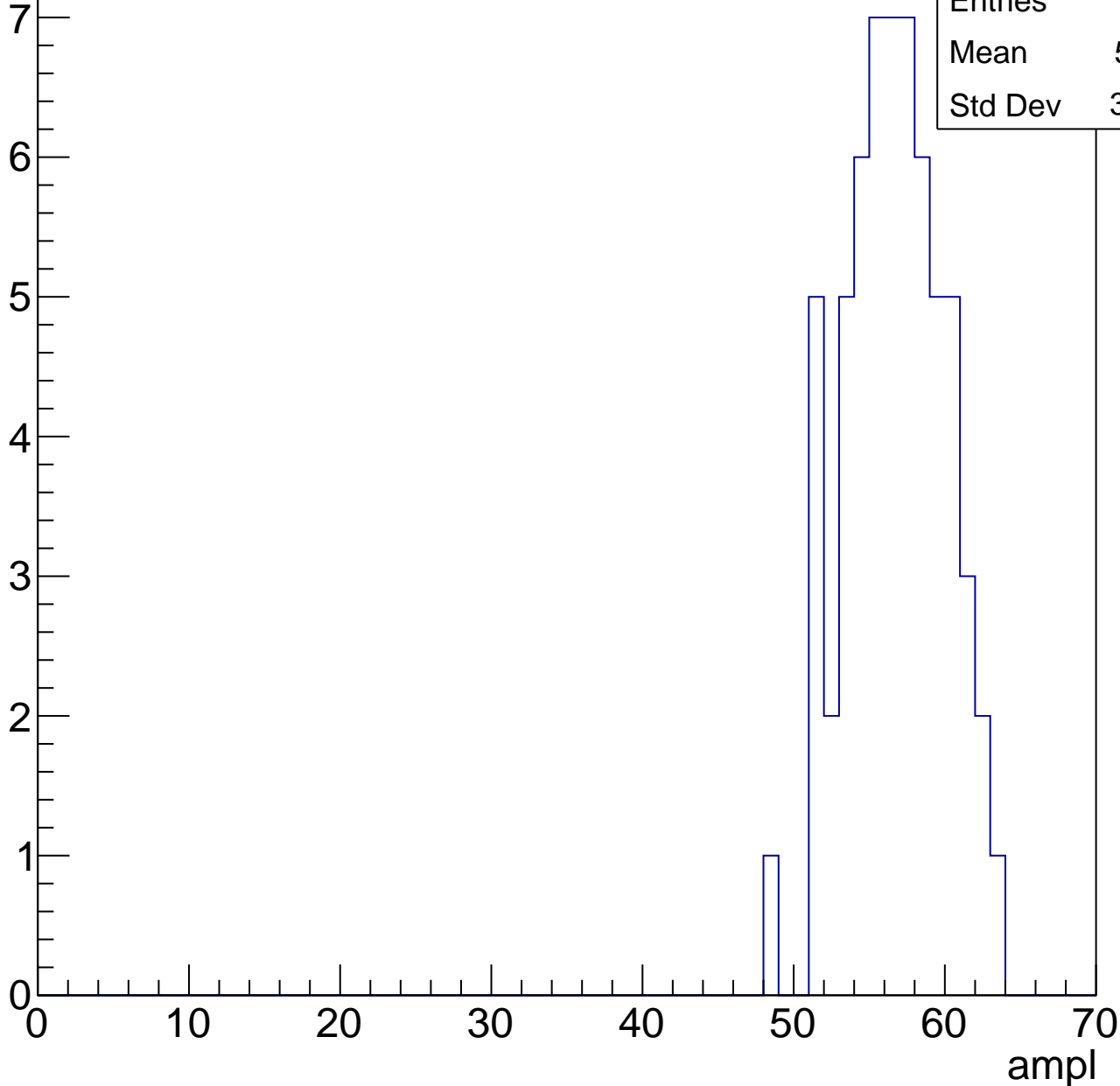


B1L100S, U19-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	56.21
Std Dev	3.229

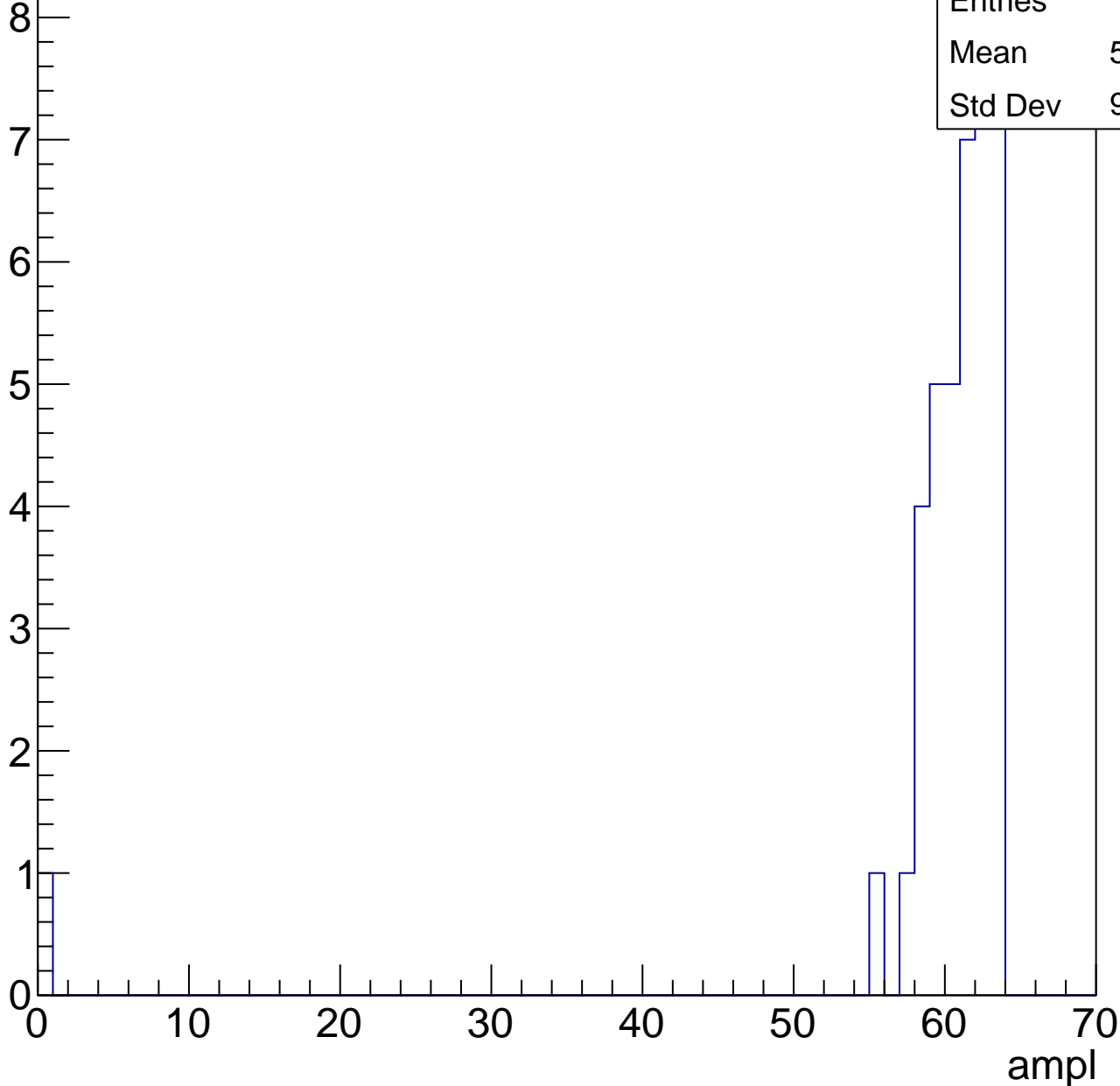


B1L100S, U19-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59.15
Std Dev	9.666



B1L100S, U19-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch58, adc0

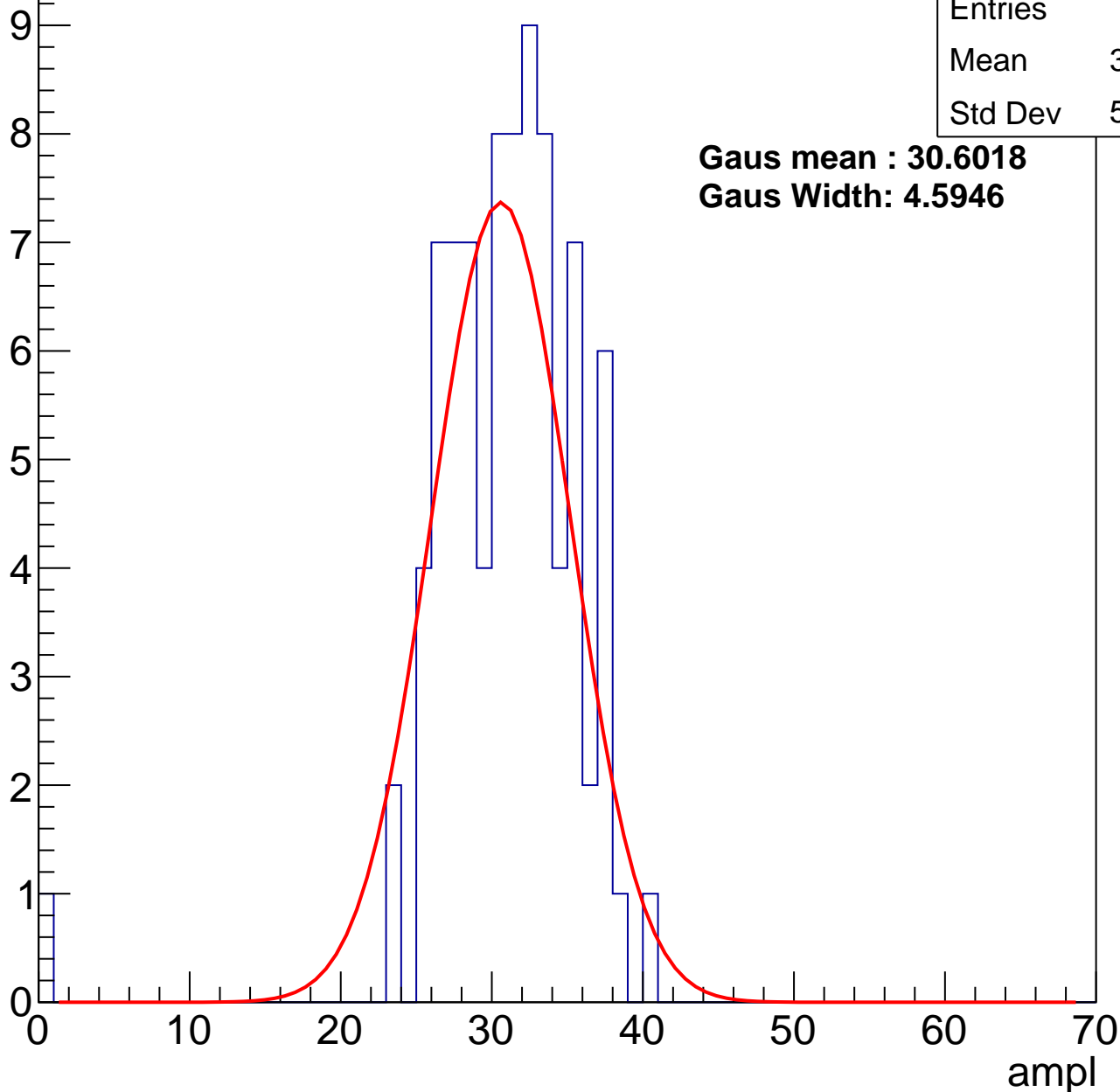
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	30.49
Std Dev	5.023

Gaus mean : 30.6018

Gaus Width: 4.5946



B1L100S, U19-ch58, adc1

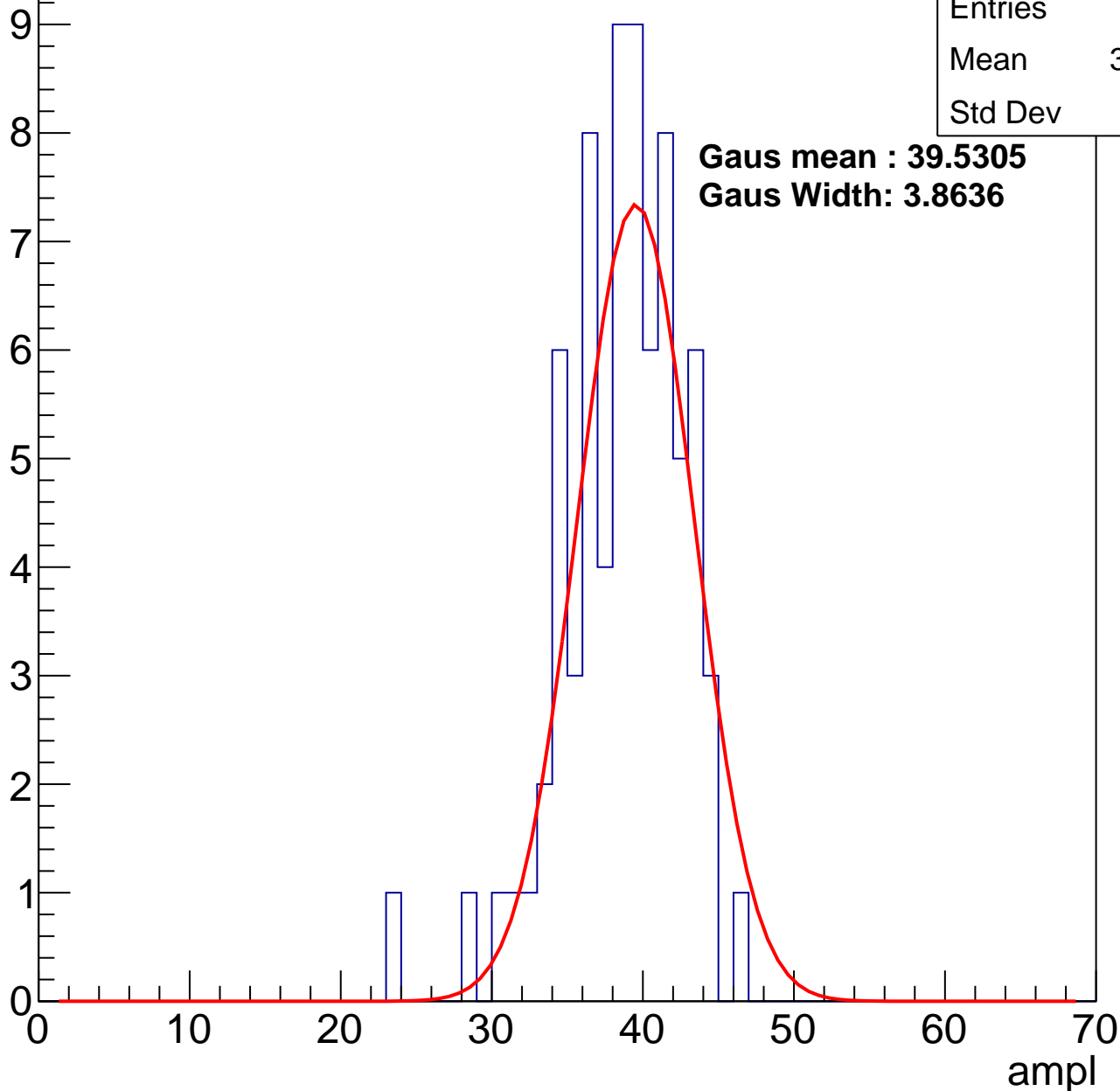
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	38.16
Std Dev	3.98

Gaus mean : 39.5305

Gaus Width: 3.8636



B1L100S, U19-ch58, adc2

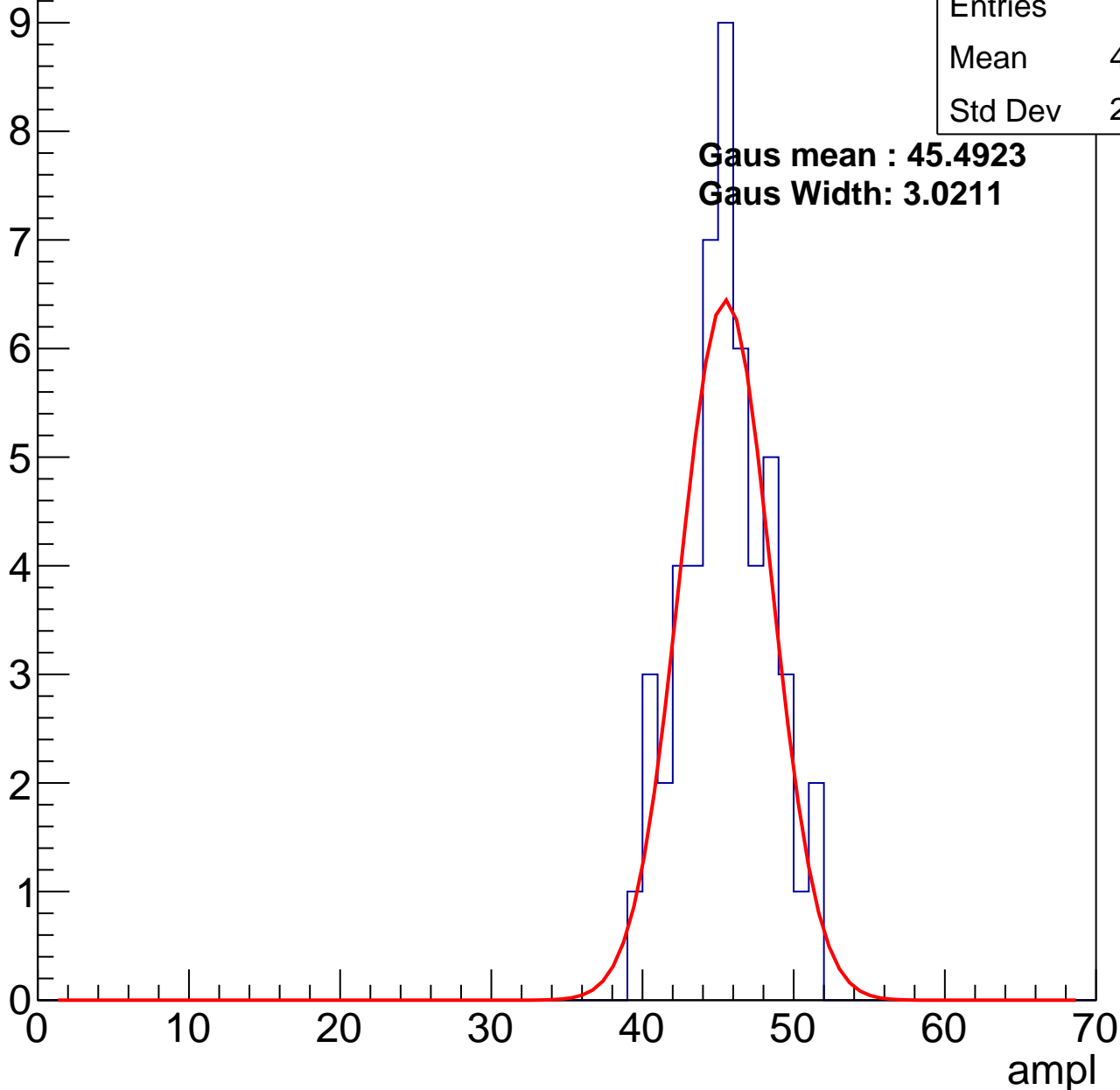
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	45.04
Std Dev	2.849

Gaus mean : 45.4923

Gaus Width: 3.0211

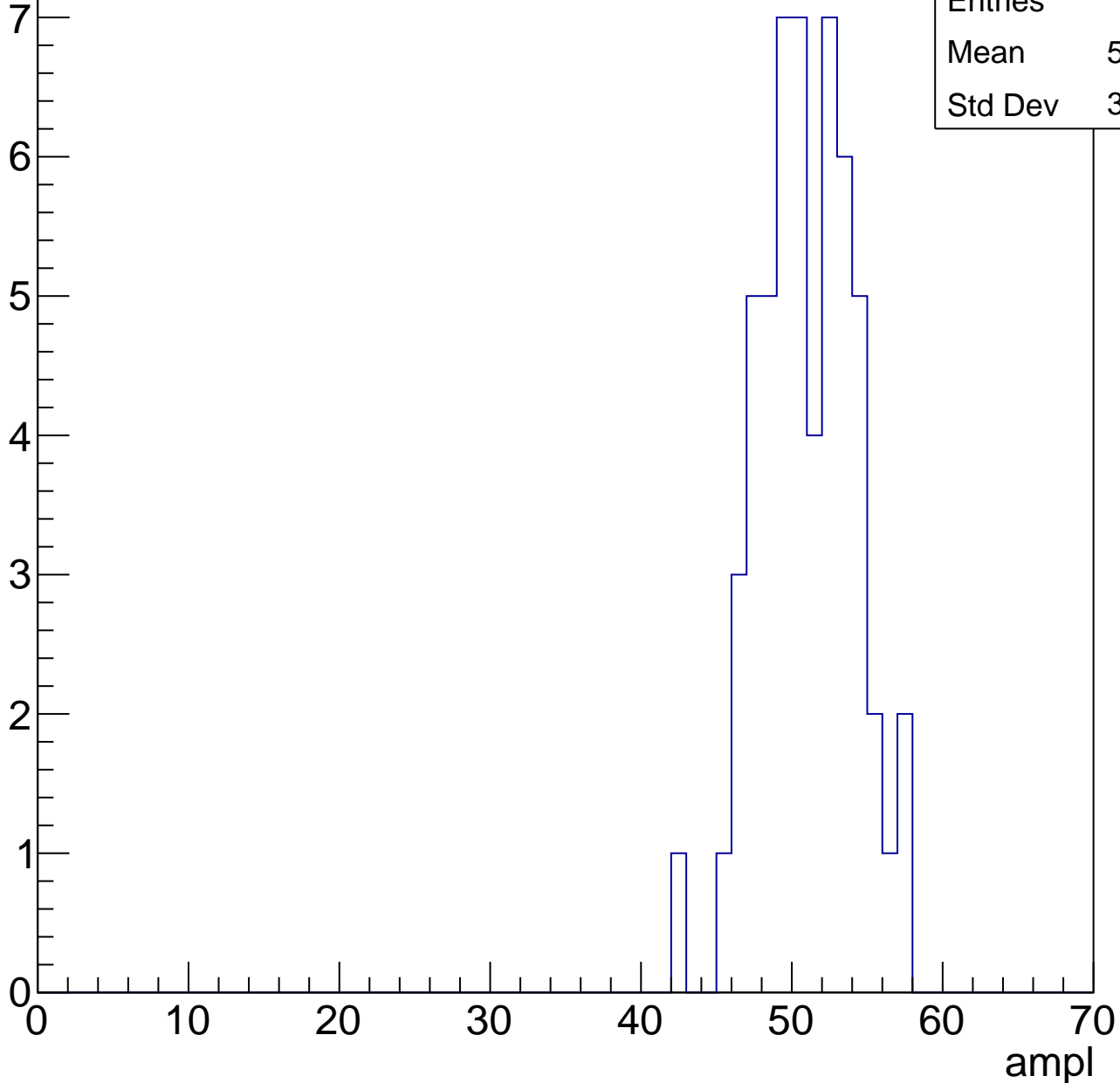


B1L100S, U19-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	50.52
Std Dev	3.117

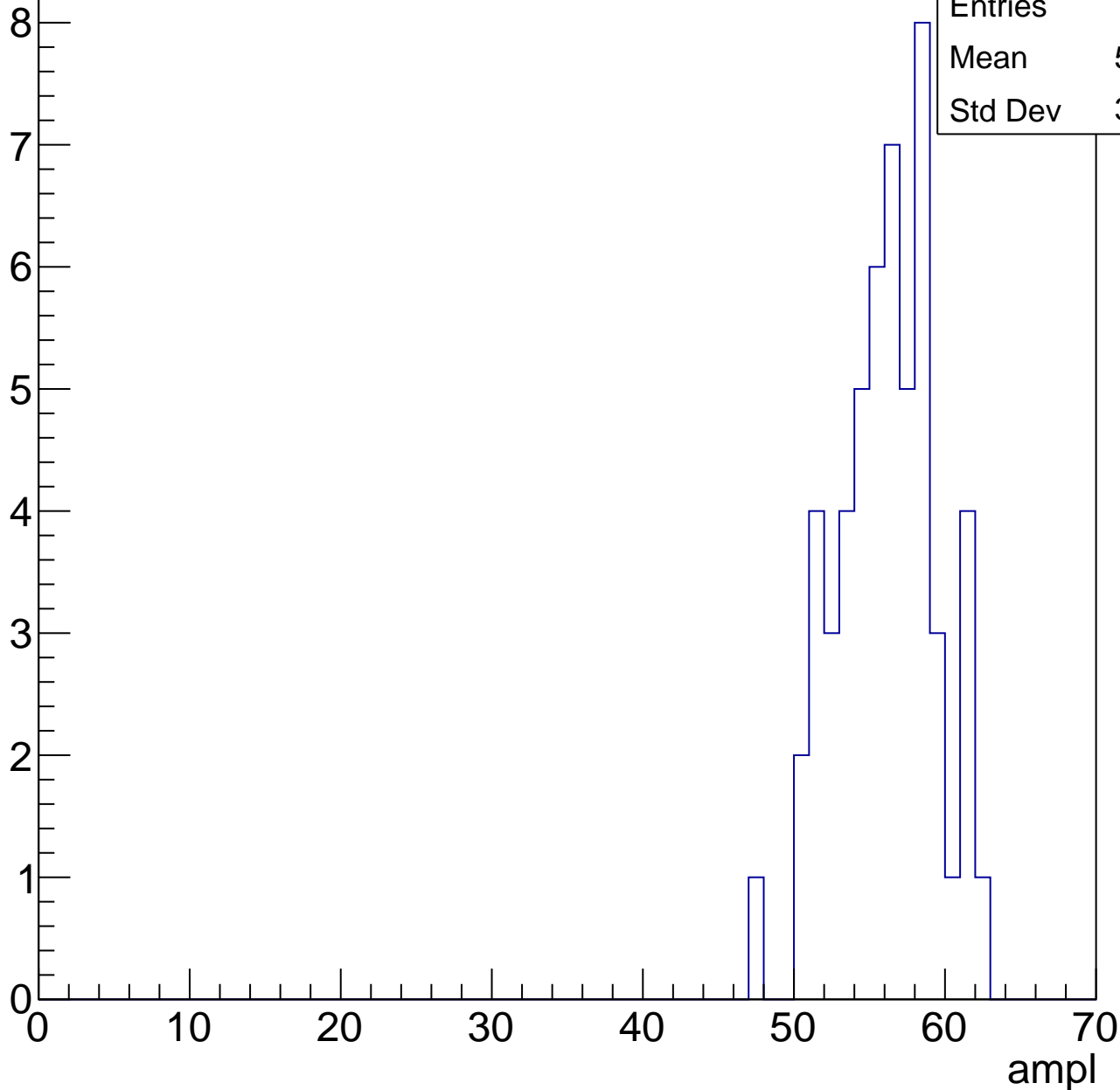


B1L100S, U19-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	55.61
Std Dev	3.251



B1L100S, U19-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 48

Mean 59.02

Std Dev 8.903

8

6

4

2

0

0

10

20

30

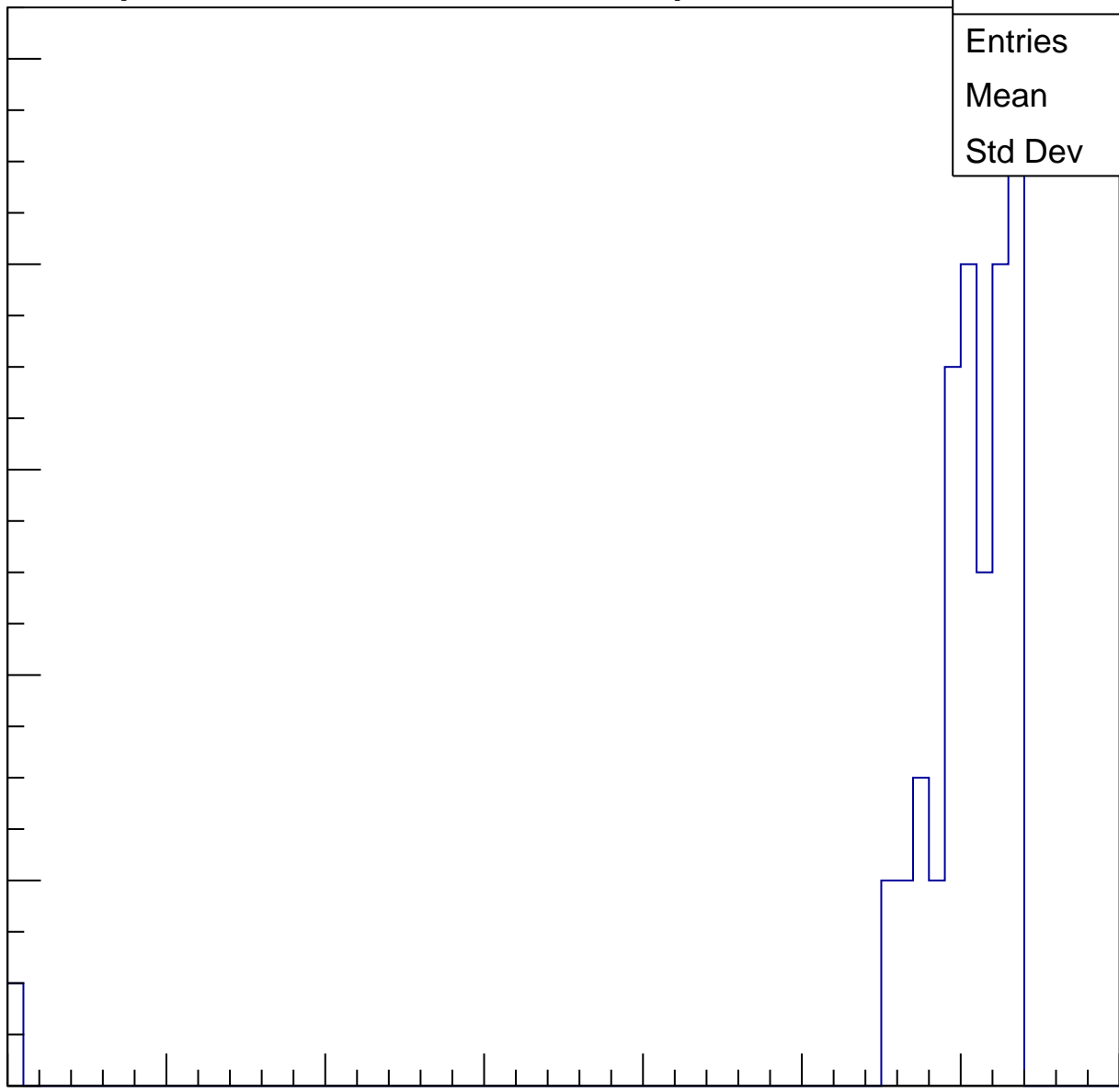
40

50

60

70

ampl



B1L100S, U19-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch59, adc0

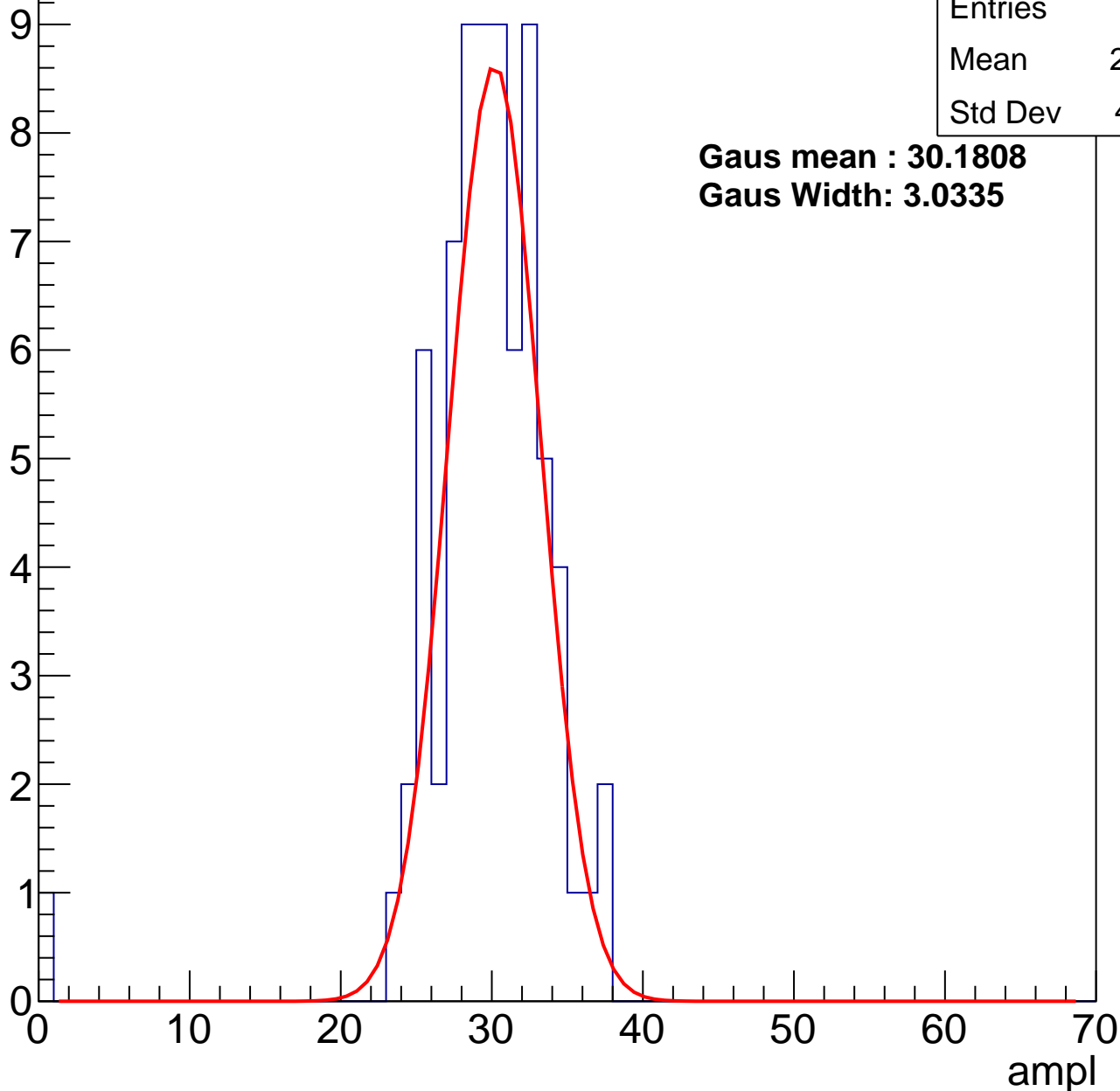
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.26
Std Dev	4.621

Gaus mean : 30.1808

Gaus Width: 3.0335



B1L100S, U19-ch59, adc1

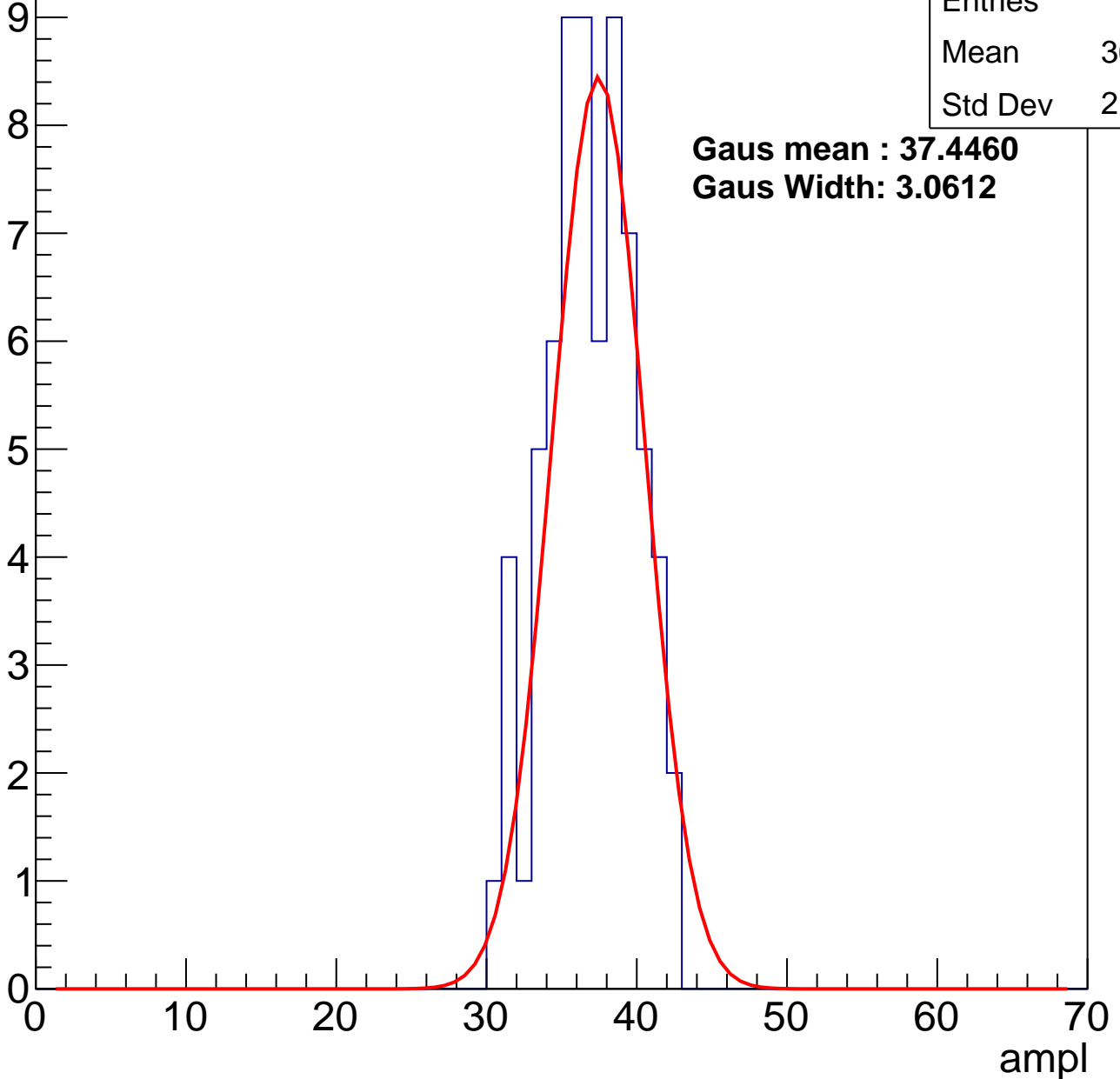
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.46
Std Dev	2.903

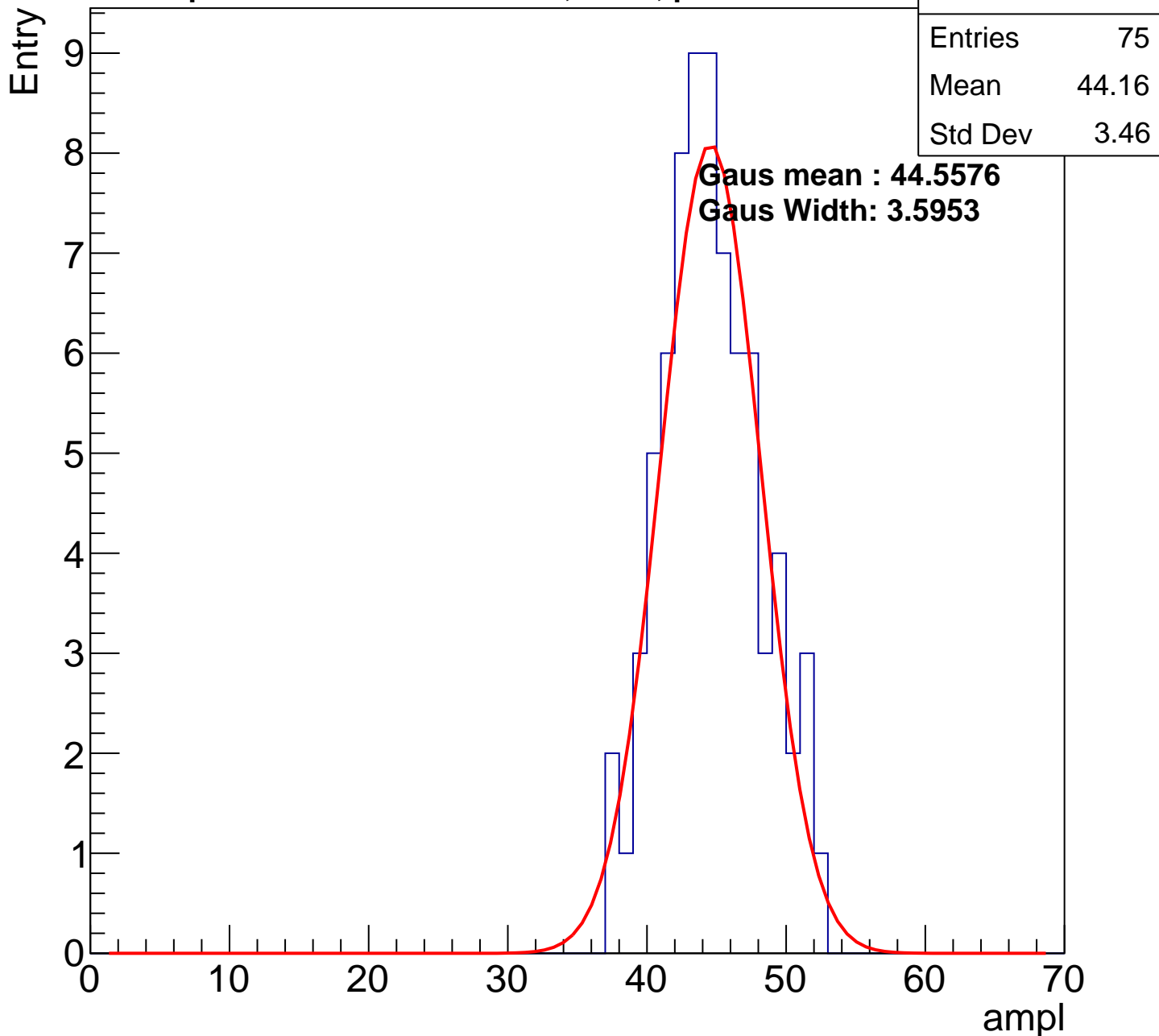
Gaus mean : 37.4460

Gaus Width: 3.0612



B1L100S, U19-ch59, adc2

calib_packv5_042523_0143.root, FC#4, port A2

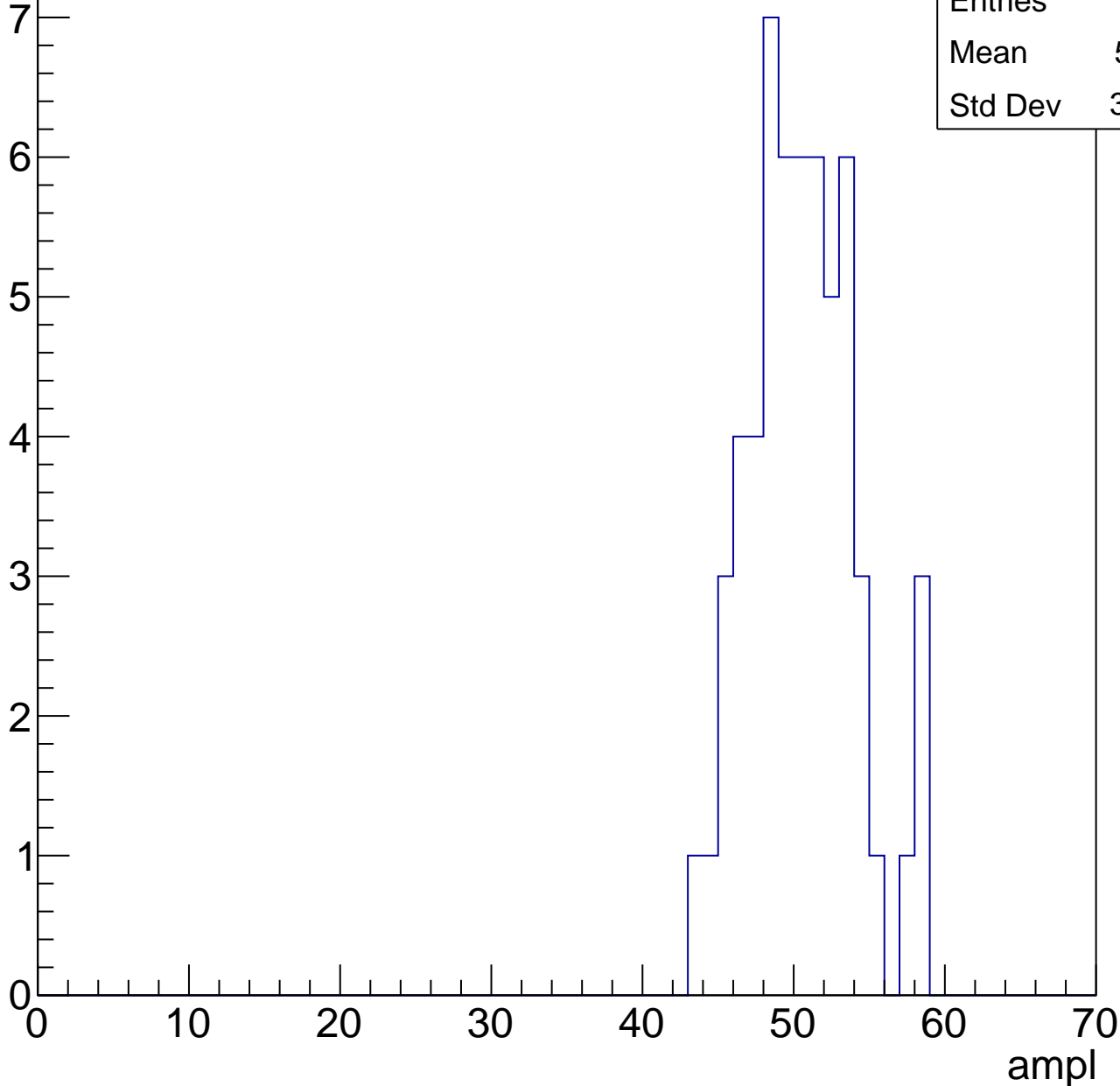


B1L100S, U19-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	50.11
Std Dev	3.473

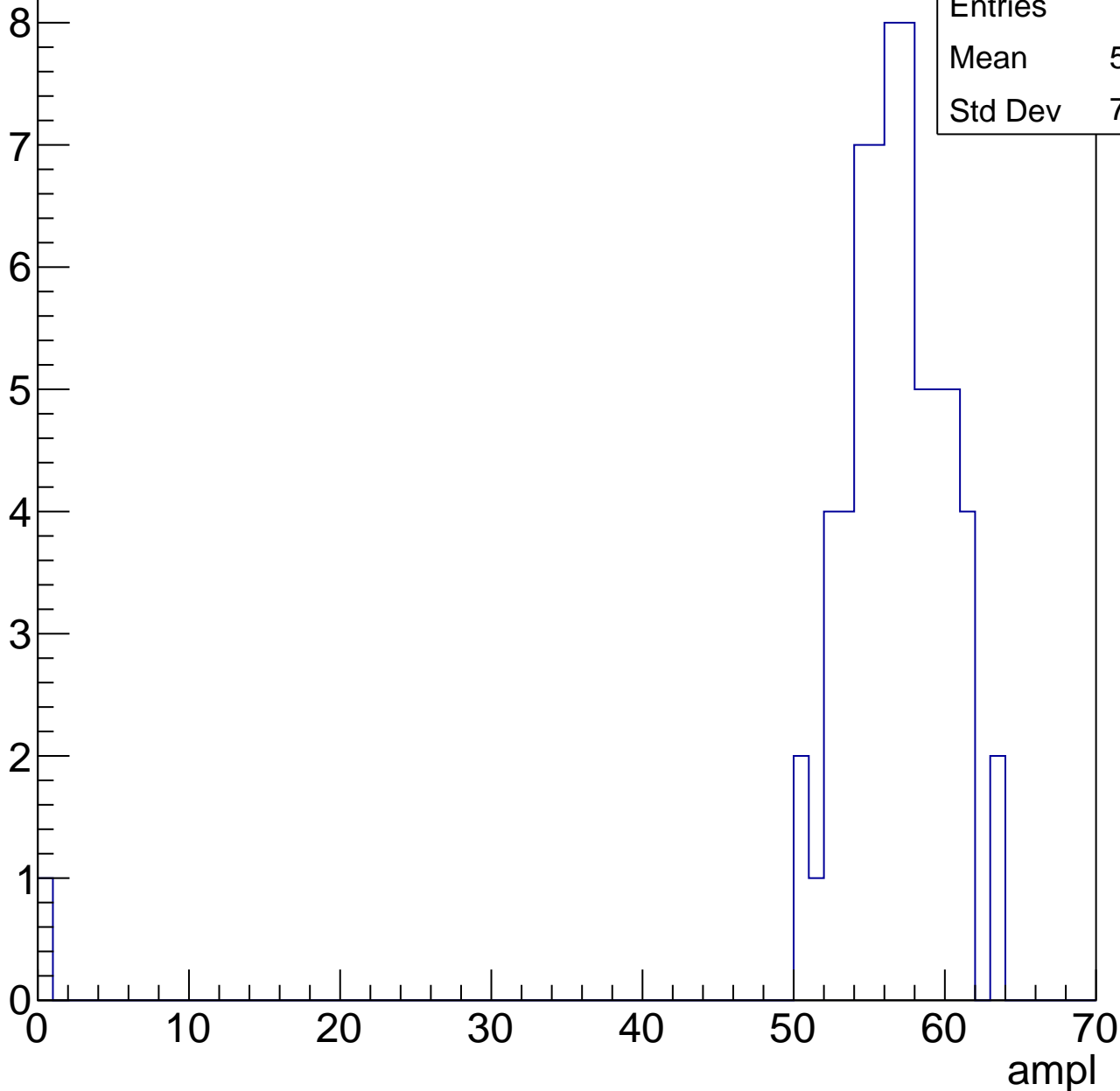


B1L100S, U19-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.44
Std Dev	7.663



B1L100S, U19-ch59, adc5

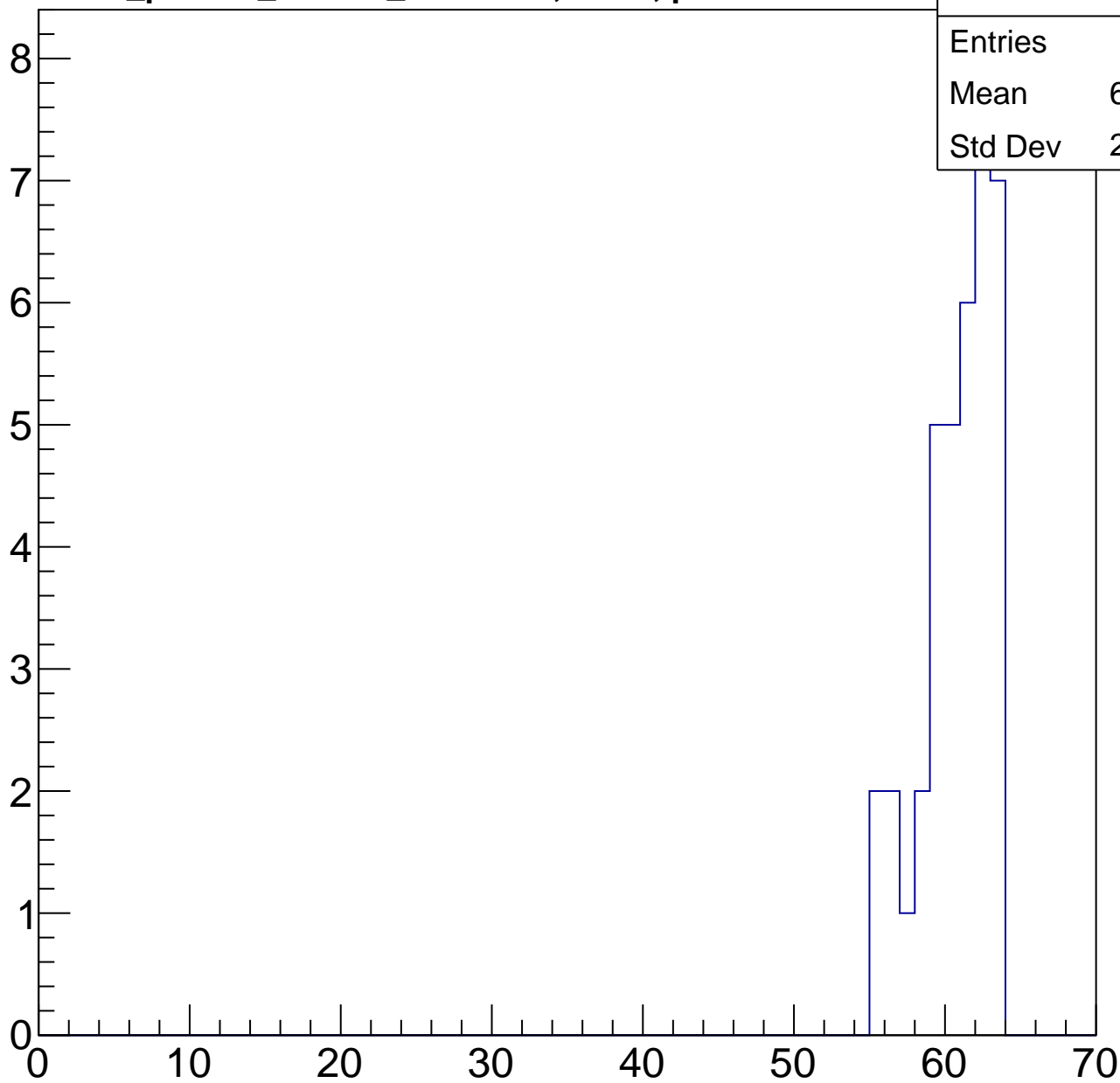
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.34
Std Dev	2.297

ampl



B1L100S, U19-ch59, adc6

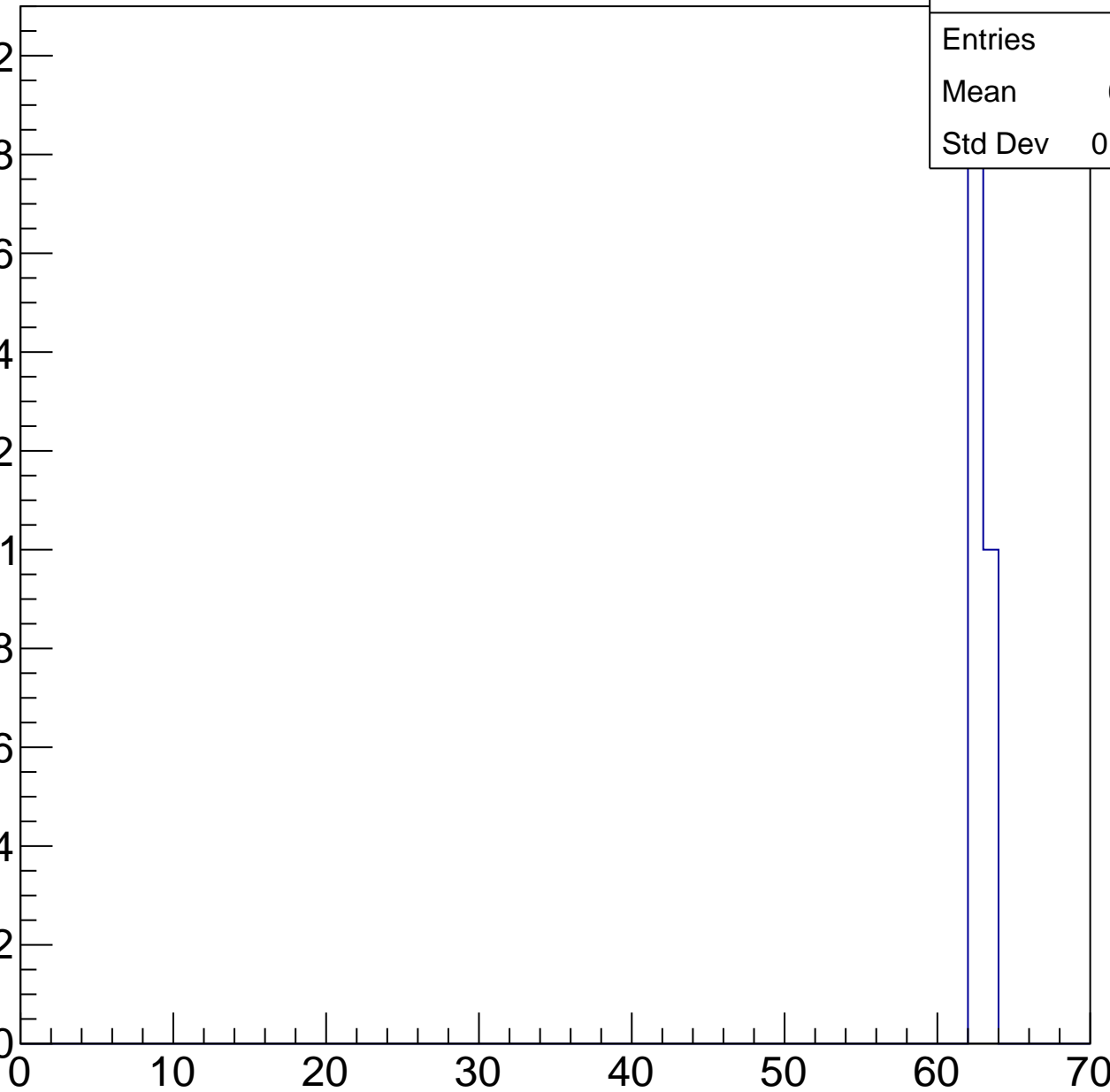
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

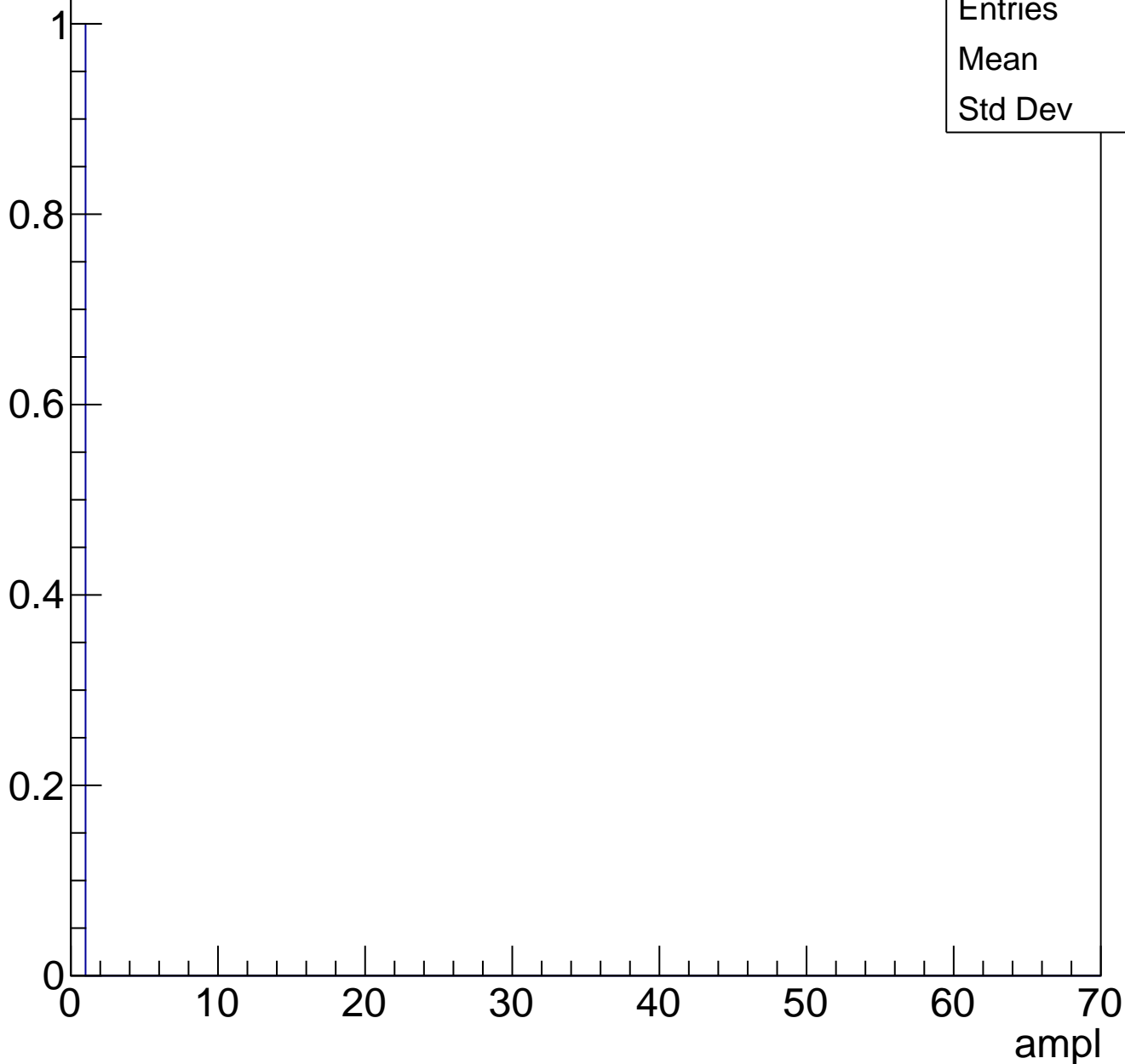
ampl



B1L100S, U19-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch60, adc0

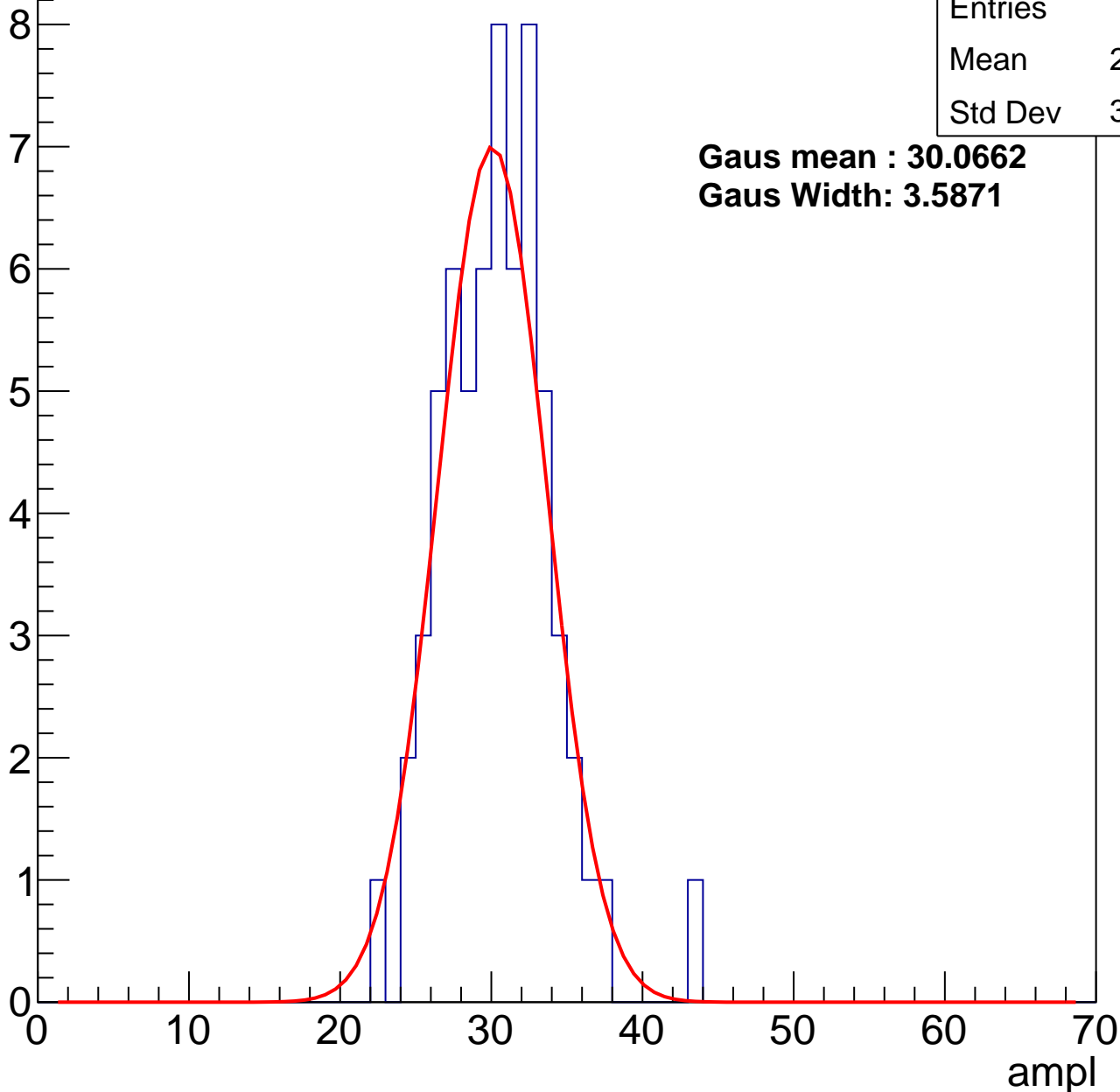
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.94
Std Dev	3.576

Gaus mean : 30.0662

Gaus Width: 3.5871



B1L100S, U19-ch60, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	94
Mean	37.39
Std Dev	3.863

Gaus mean : 37.7229

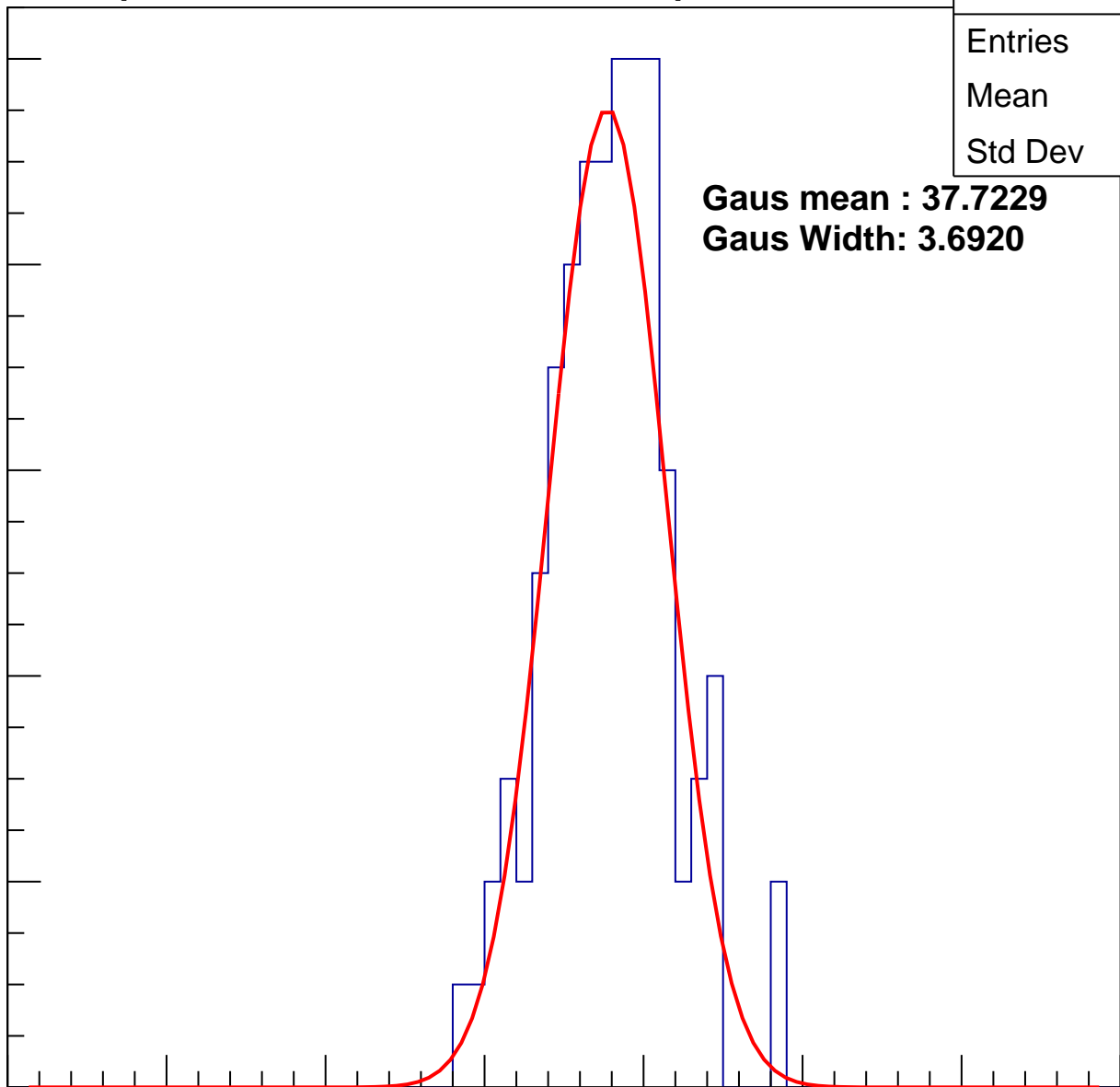
Gaus Width: 3.6920

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch60, adc2

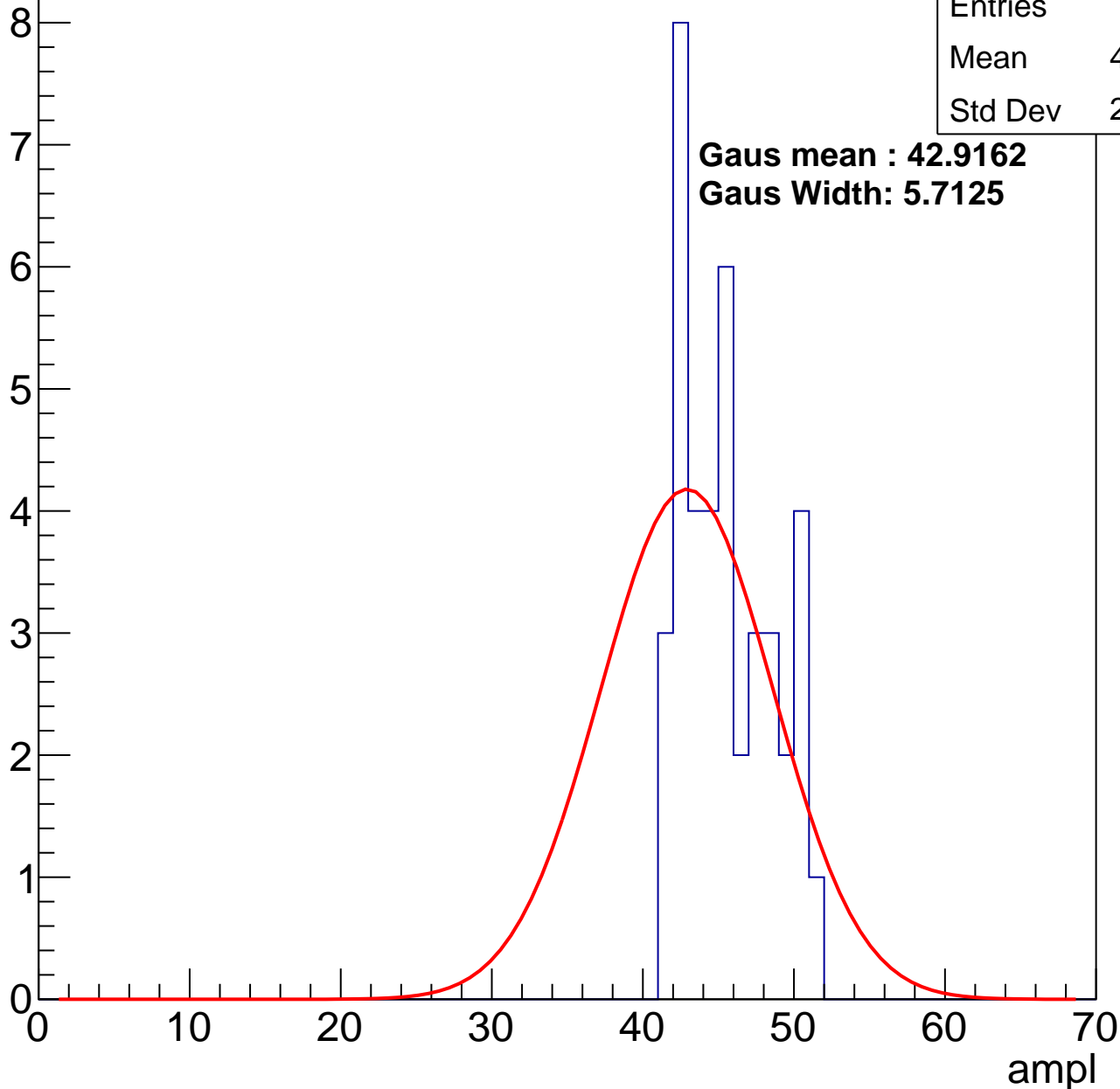
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	45.08
Std Dev	2.953

Gaus mean : 42.9162

Gaus Width: 5.7125

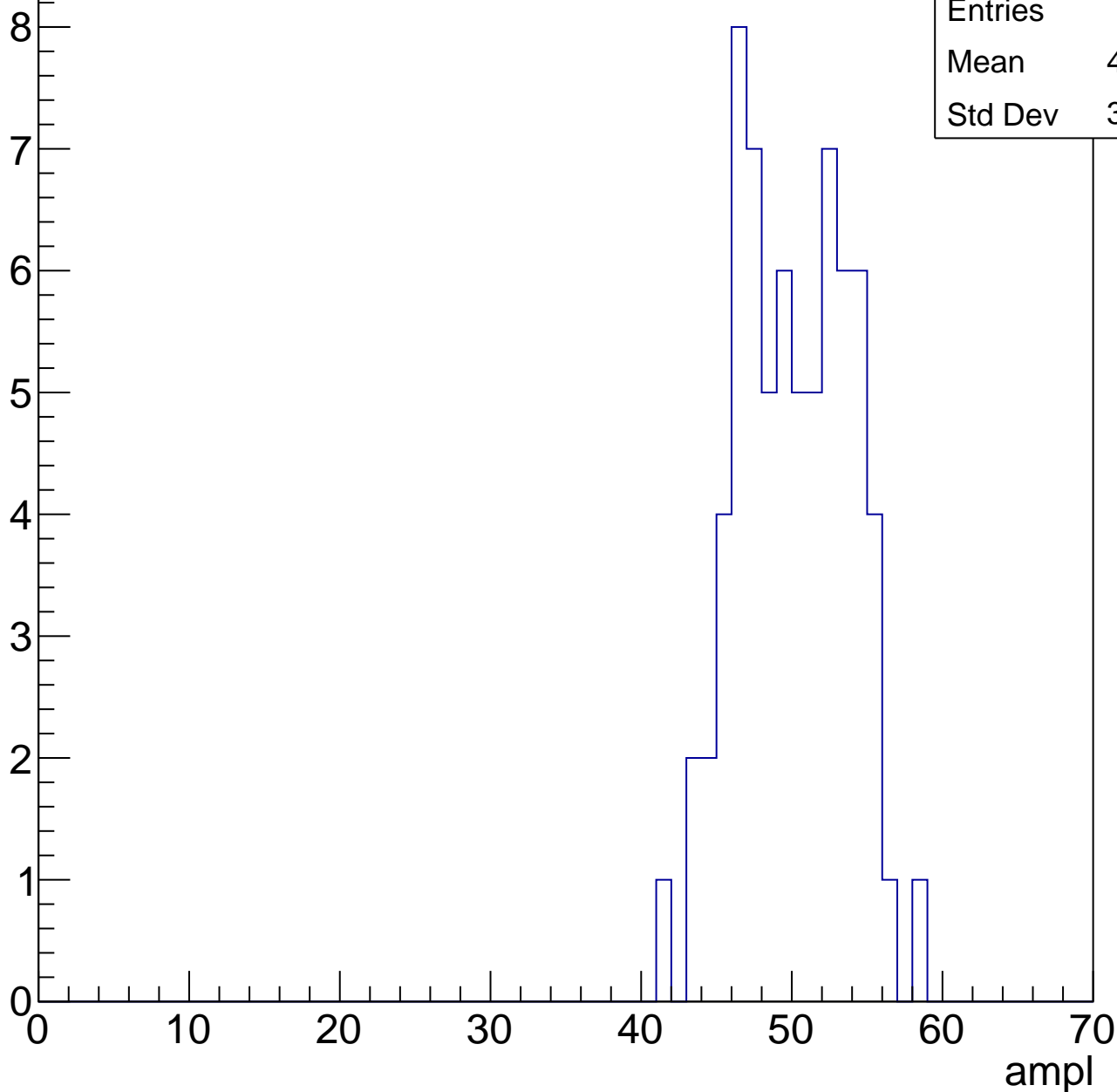


B1L100S, U19-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

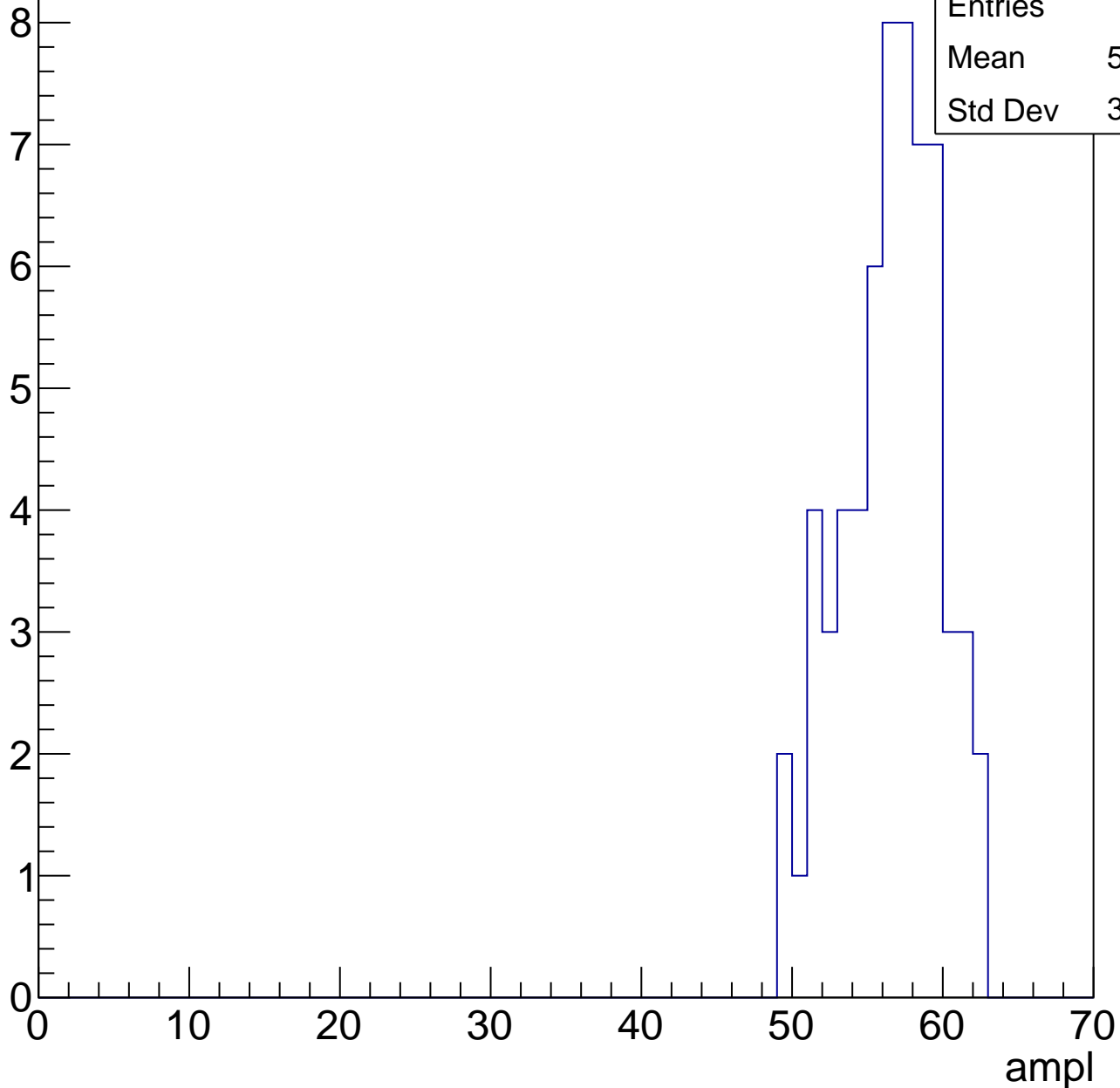
Entries	70
Mean	49.59
Std Dev	3.663



B1L100S, U19-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	62
Mean	56.06
Std Dev	3.197

B1L100S, U19-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 40

Mean 59.38

Std Dev 9.692

8

6

4

2

0

0

10

20

30

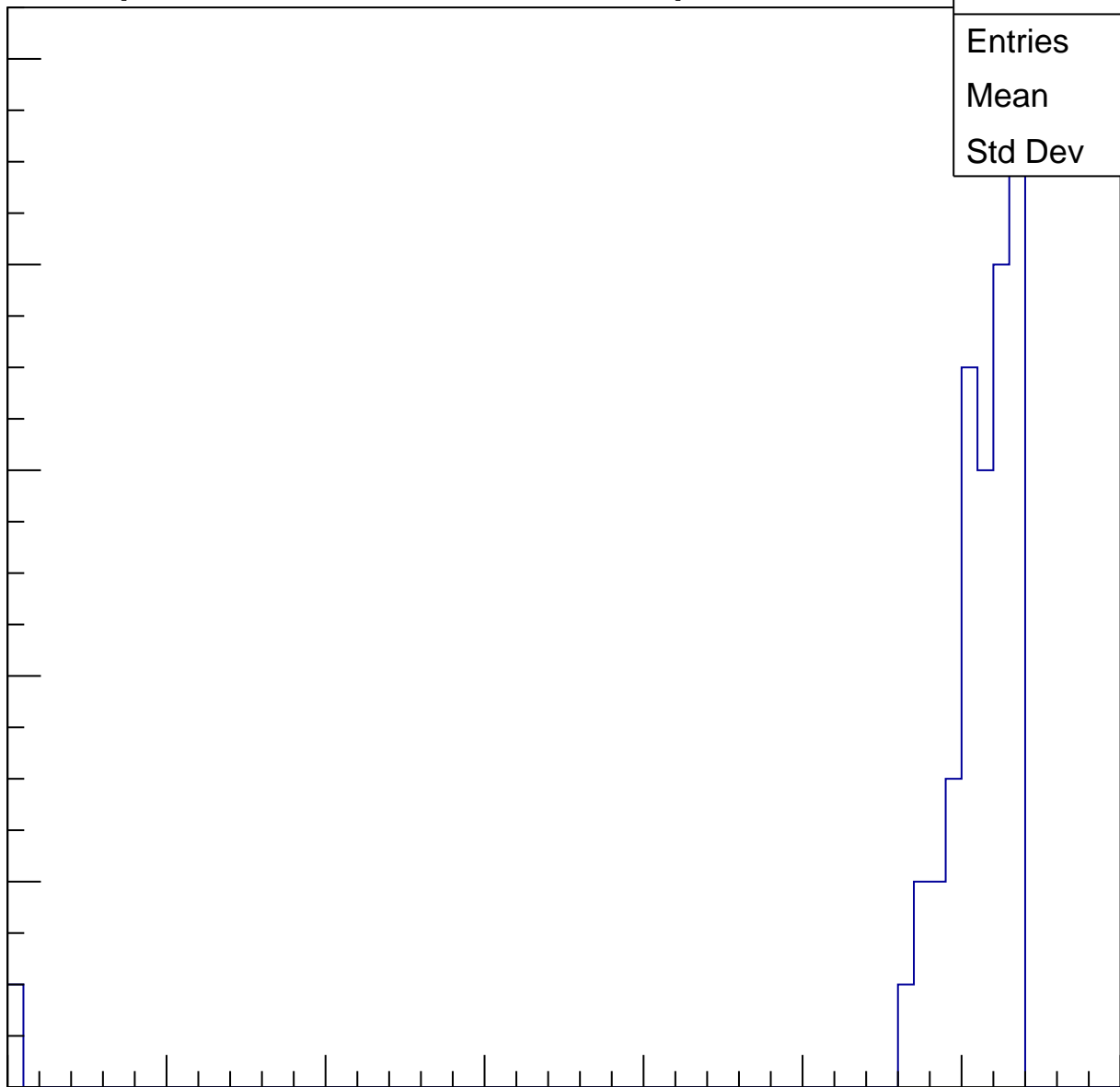
40

50

60

70

ampl



B1L100S, U19-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch61, adc0

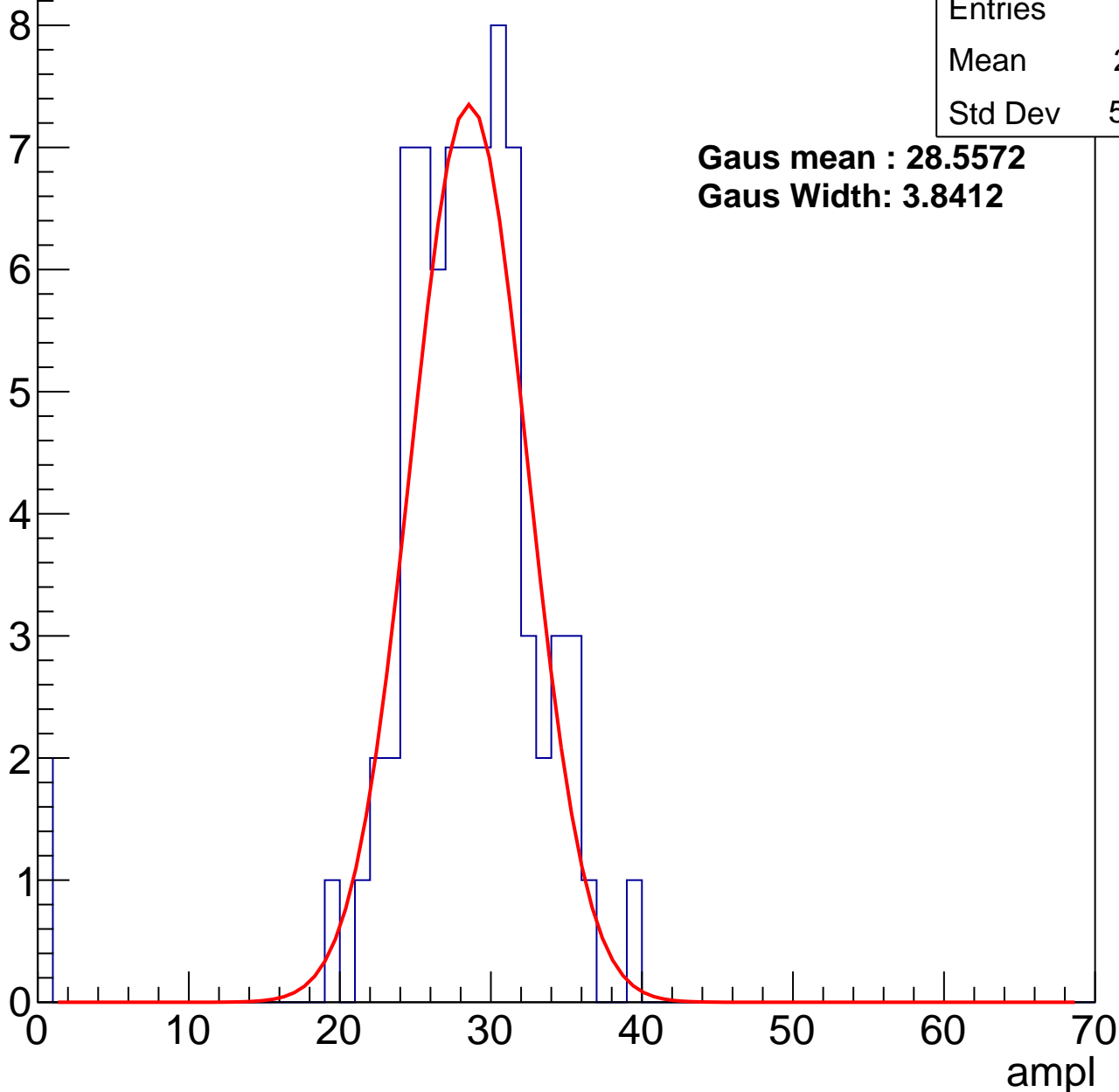
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	27.51
Std Dev	5.863

Gaus mean : 28.5572

Gaus Width: 3.8412



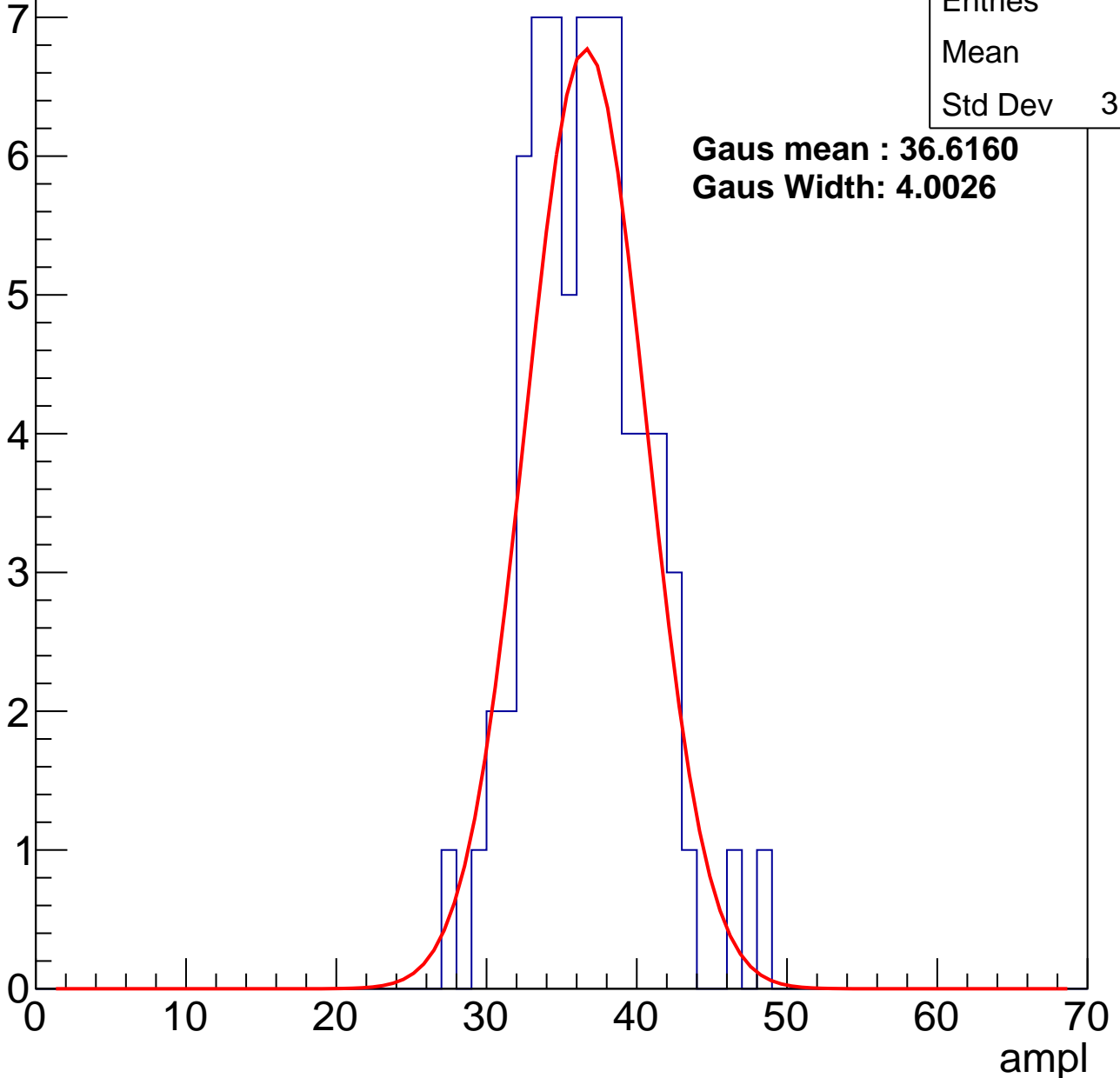
B1L100S, U19-ch61, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.2
Std Dev	3.923

Gaus mean : 36.6160
Gaus Width: 4.0026



B1L100S, U19-ch61, adc2

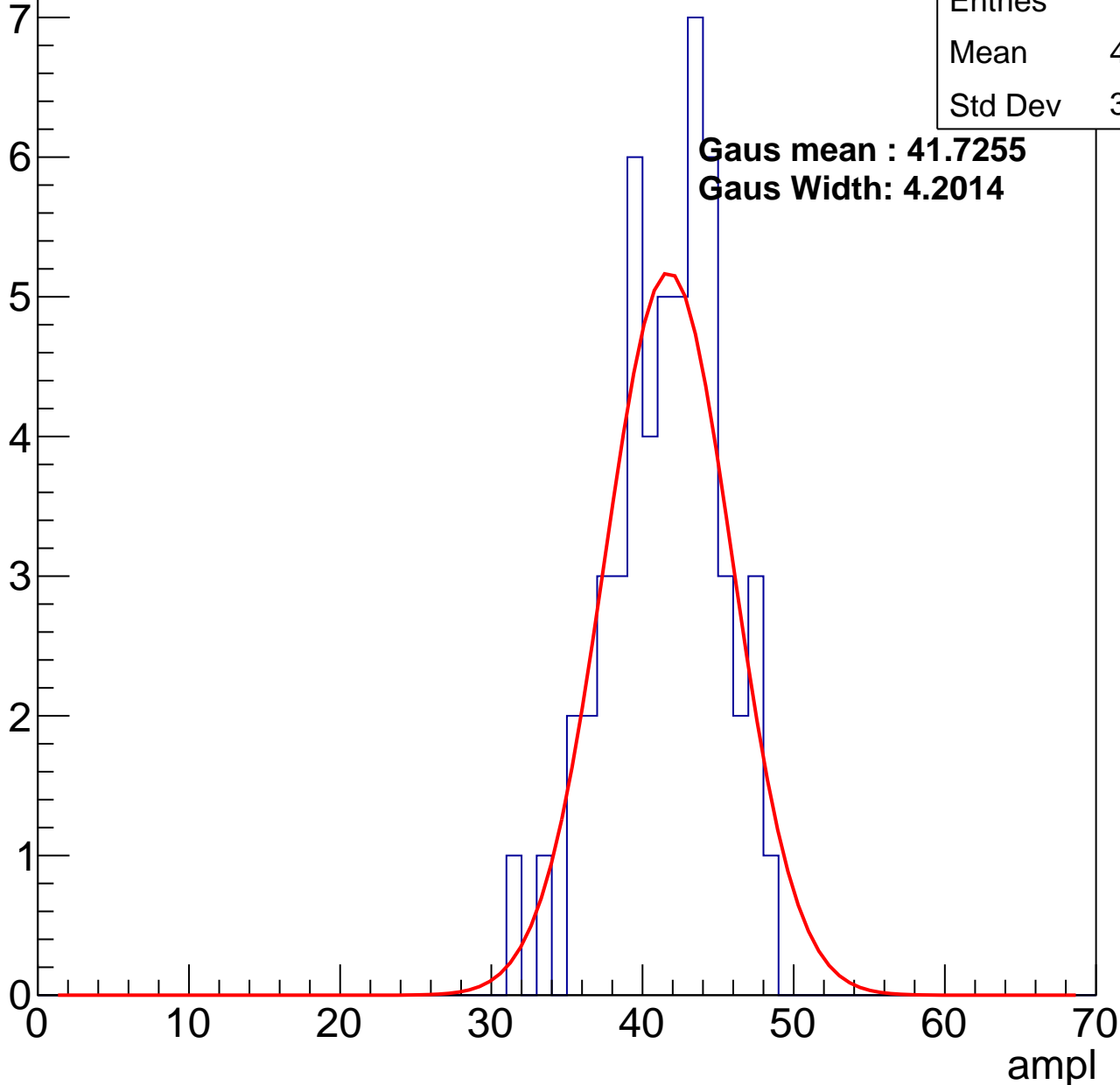
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	41.13
Std Dev	3.677

Gaus mean : 41.7255

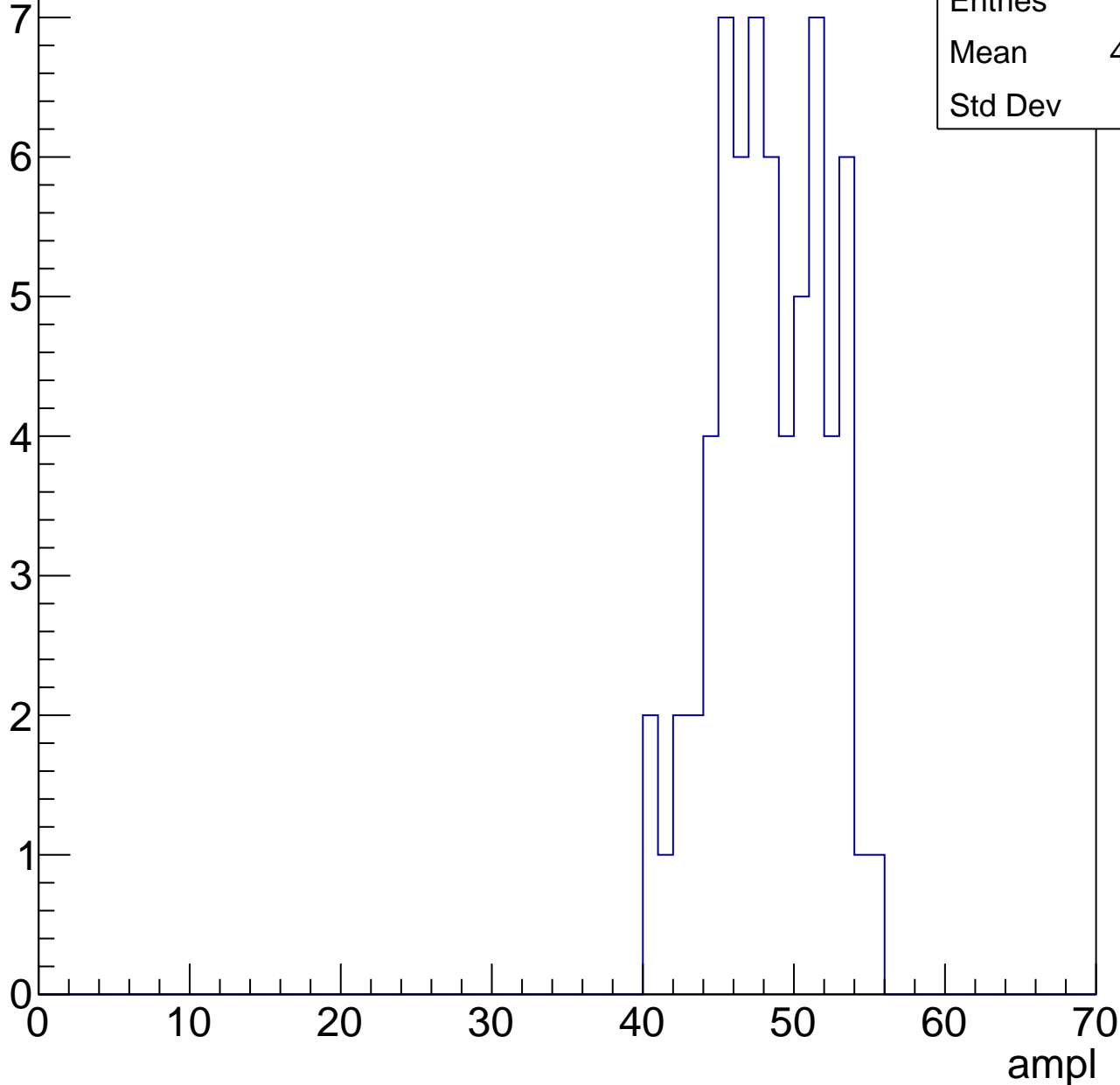
Gaus Width: 4.2014



B1L100S, U19-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

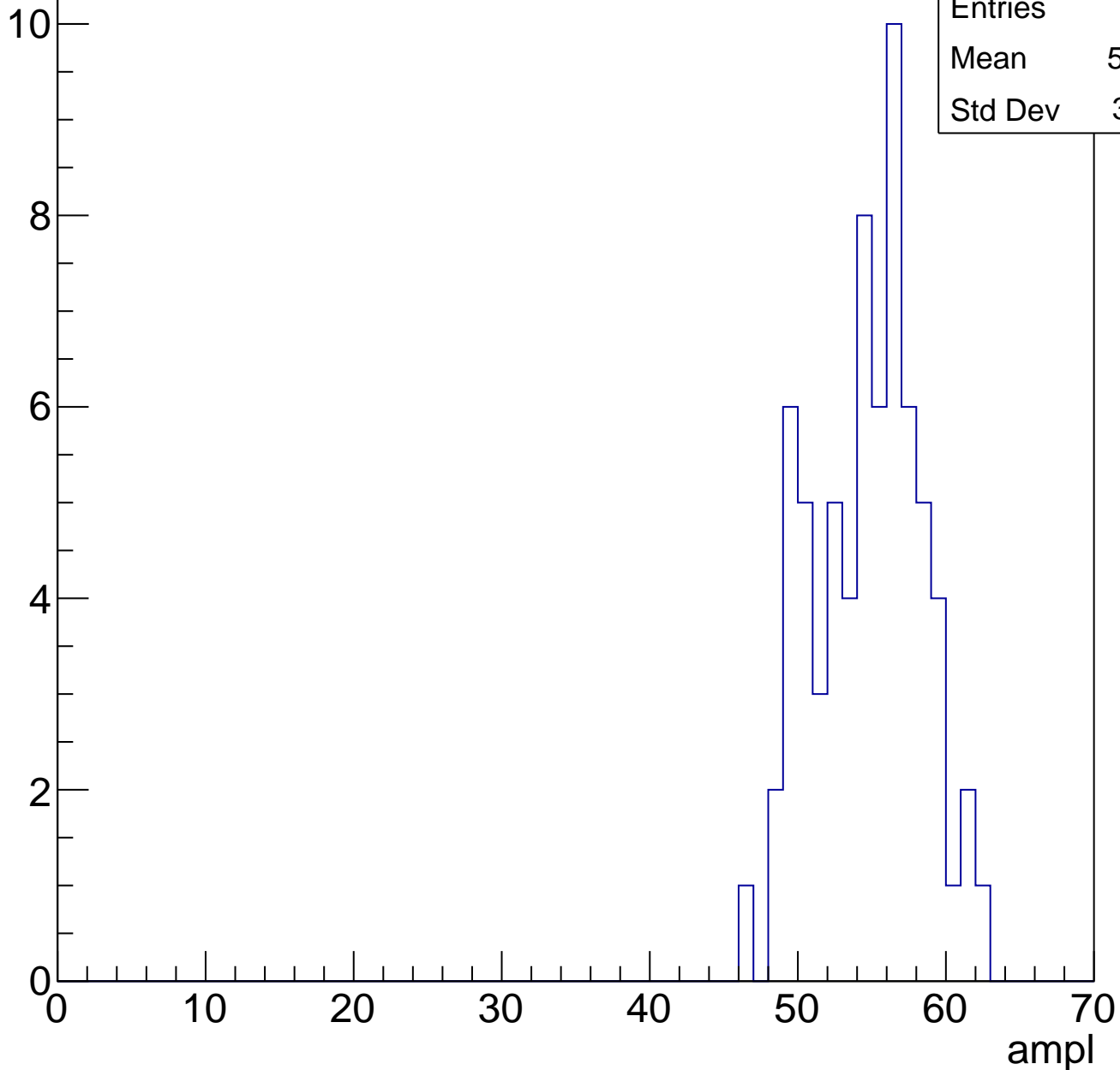


B1L100S, U19-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	54.28
Std Dev	3.591

Entry



B1L100S, U19-ch61, adc5

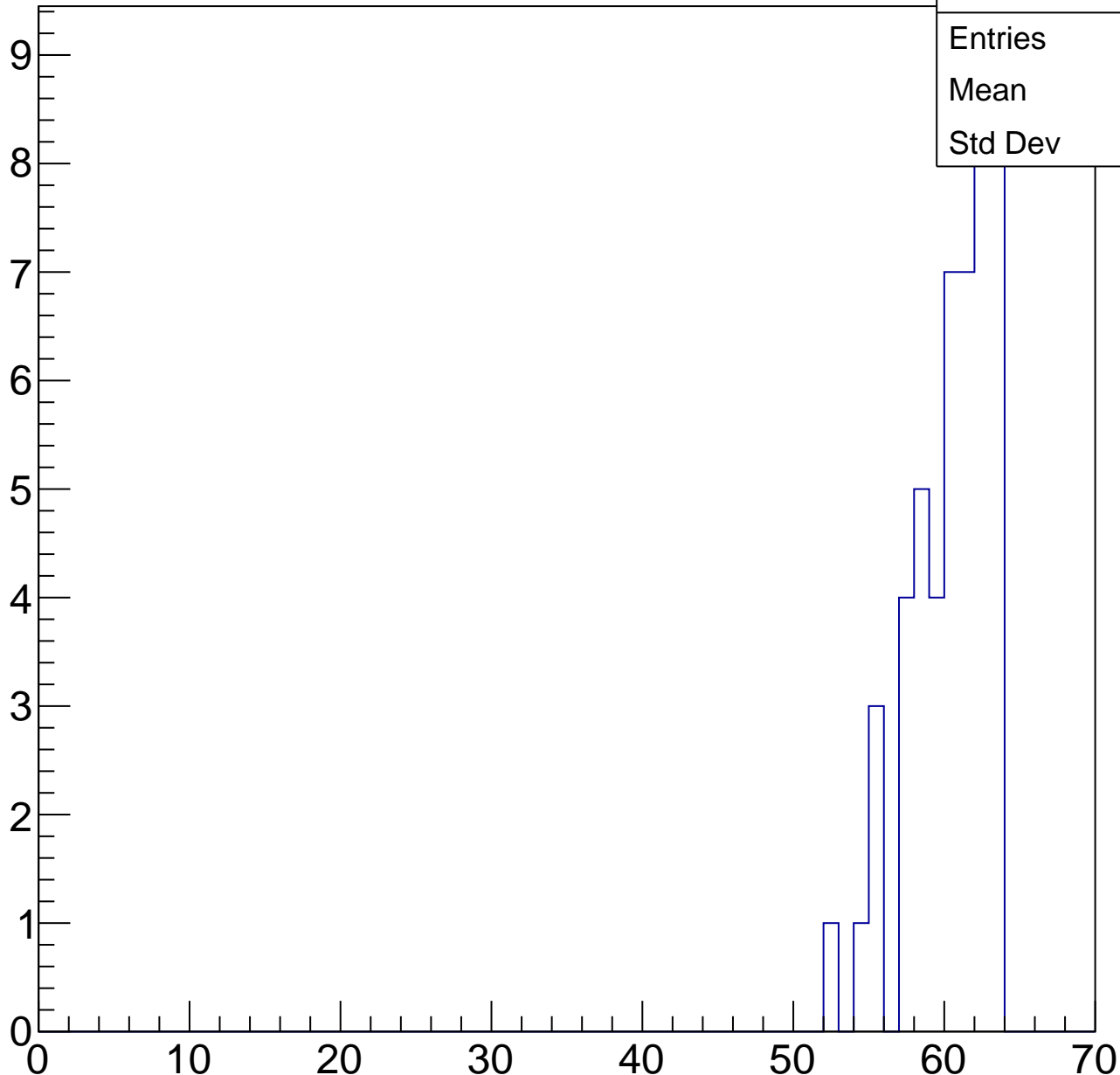
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.9
Std Dev	2.69

ampl



B1L100S, U19-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53
Std Dev	20.07

ampl

0 10 20 30 40 50 60 70

B1L100S, U19-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch62, adc0

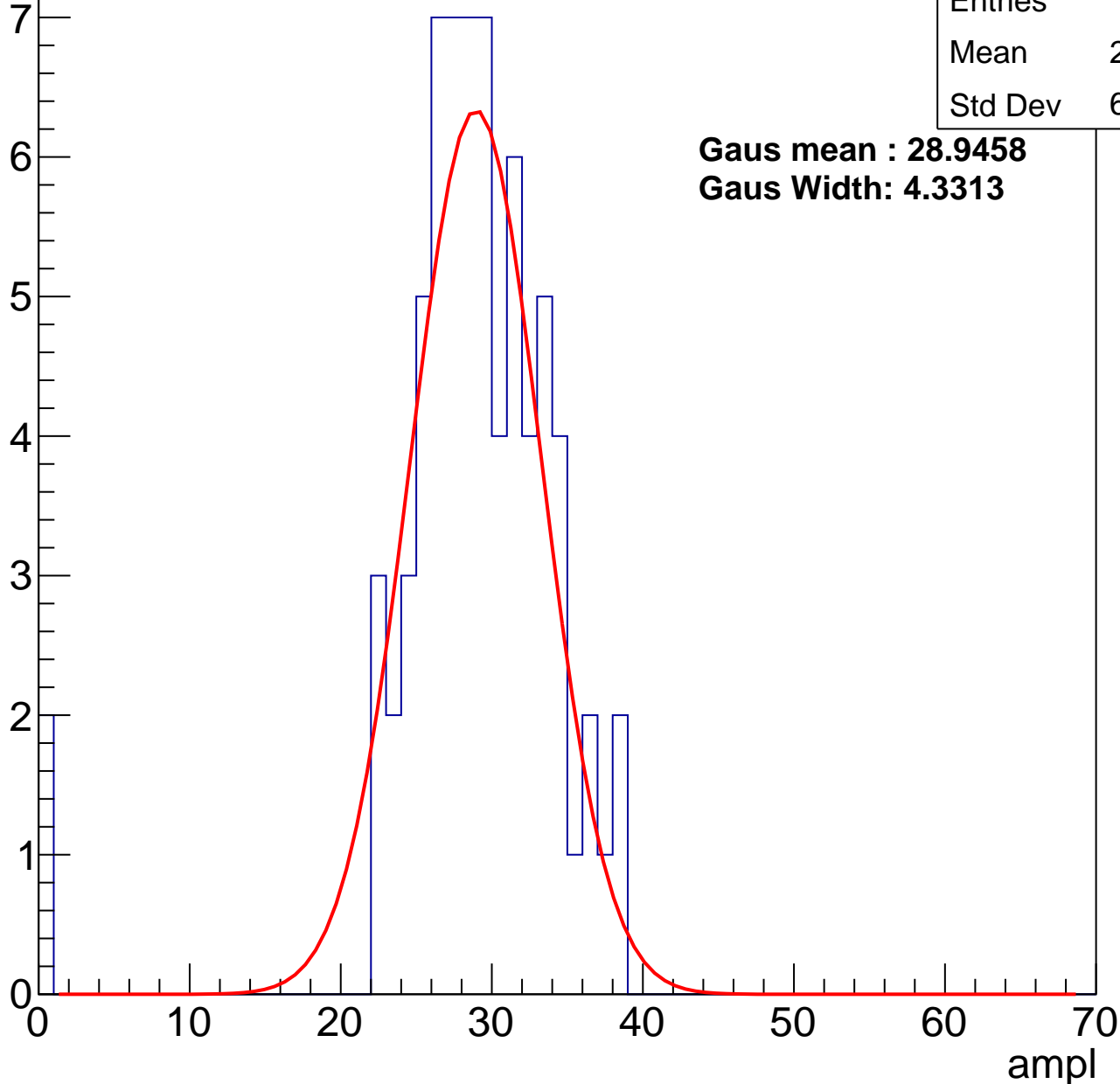
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.25
Std Dev	6.144

Gaus mean : 28.9458

Gaus Width: 4.3313



B1L100S, U19-ch62, adc1

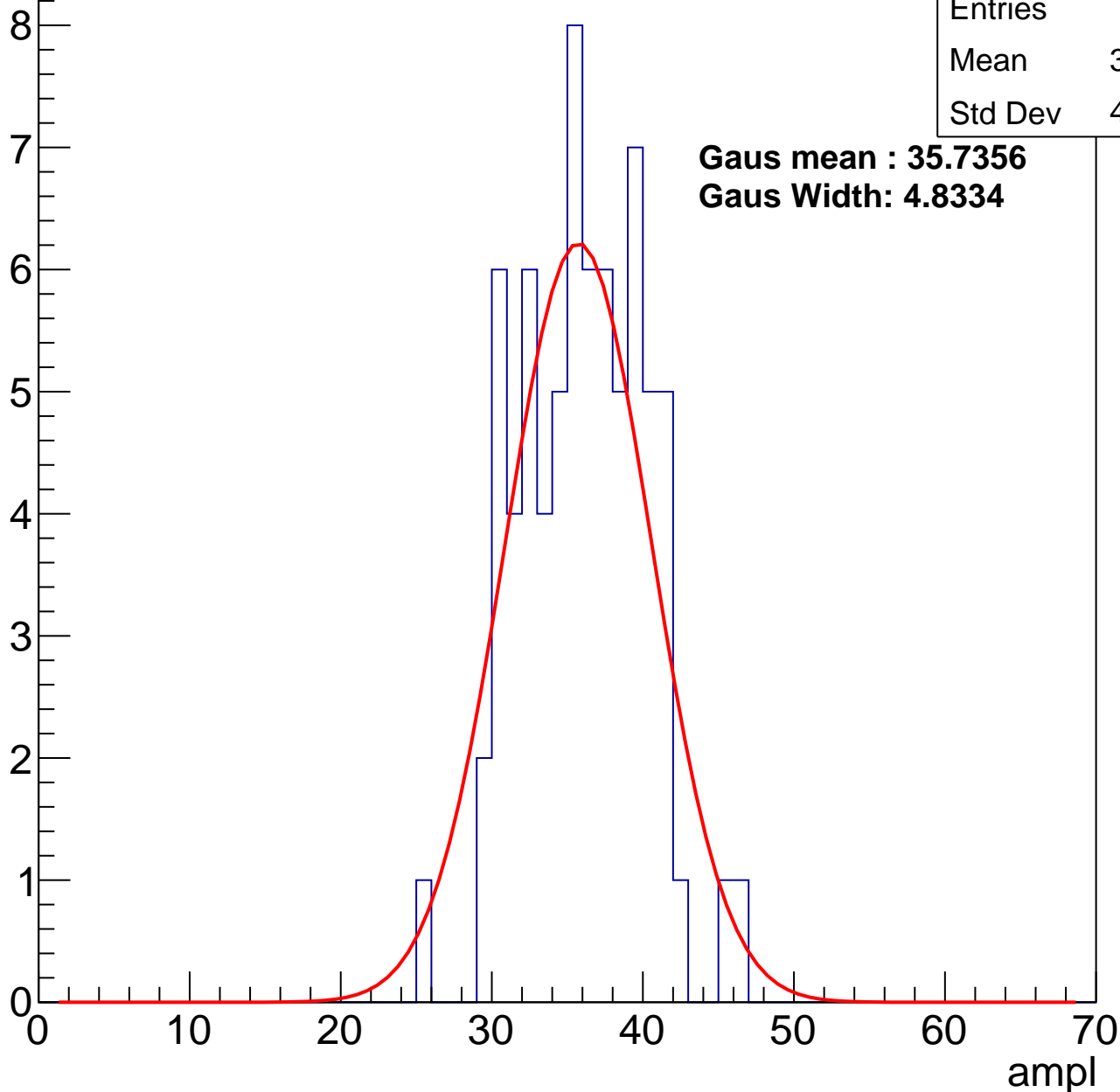
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	35.62
Std Dev	4.046

Gaus mean : 35.7356

Gaus Width: 4.8334



B1L100S, U19-ch62, adc2

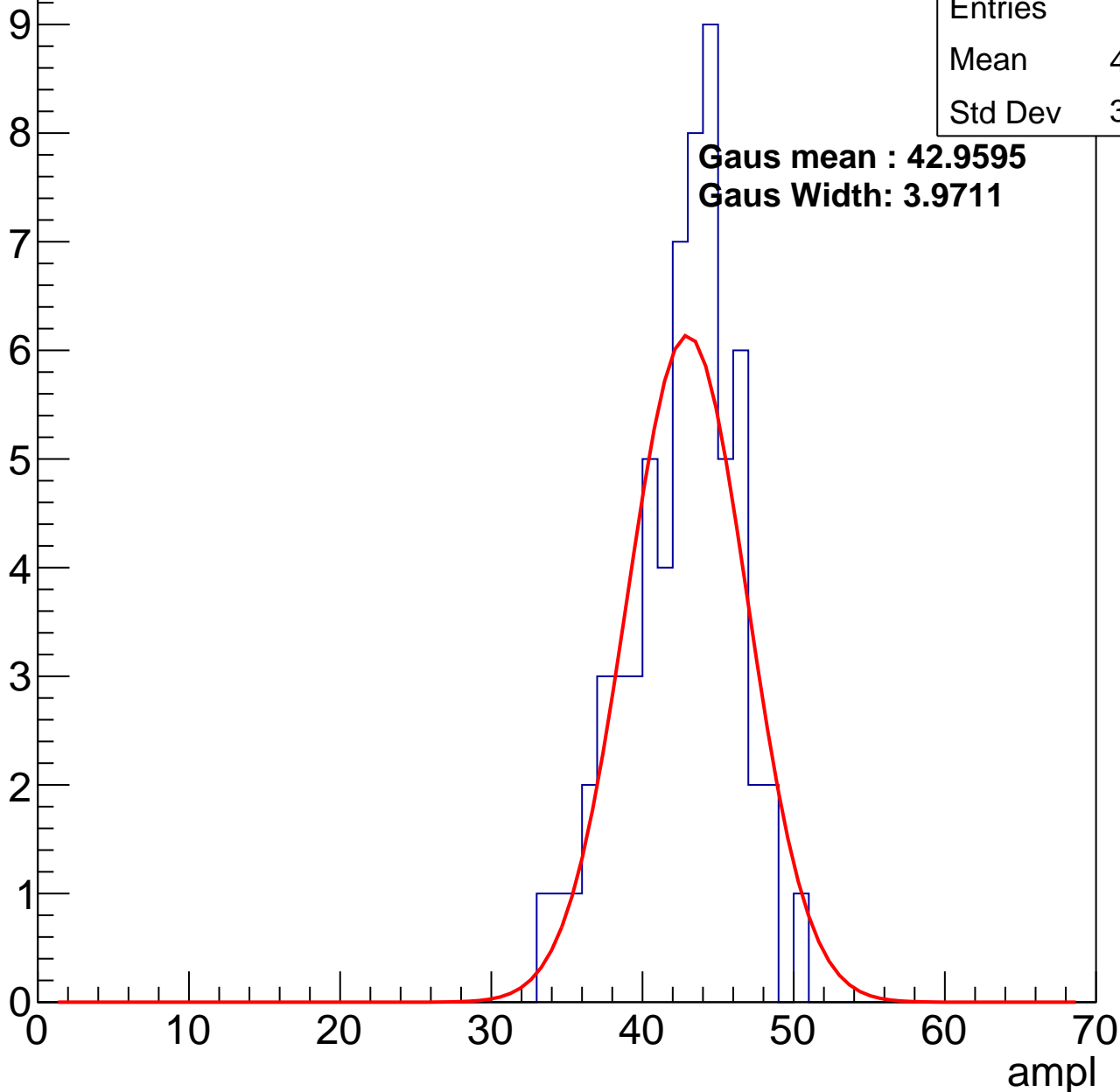
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.14
Std Dev	3.585

Gaus mean : 42.9595

Gaus Width: 3.9711

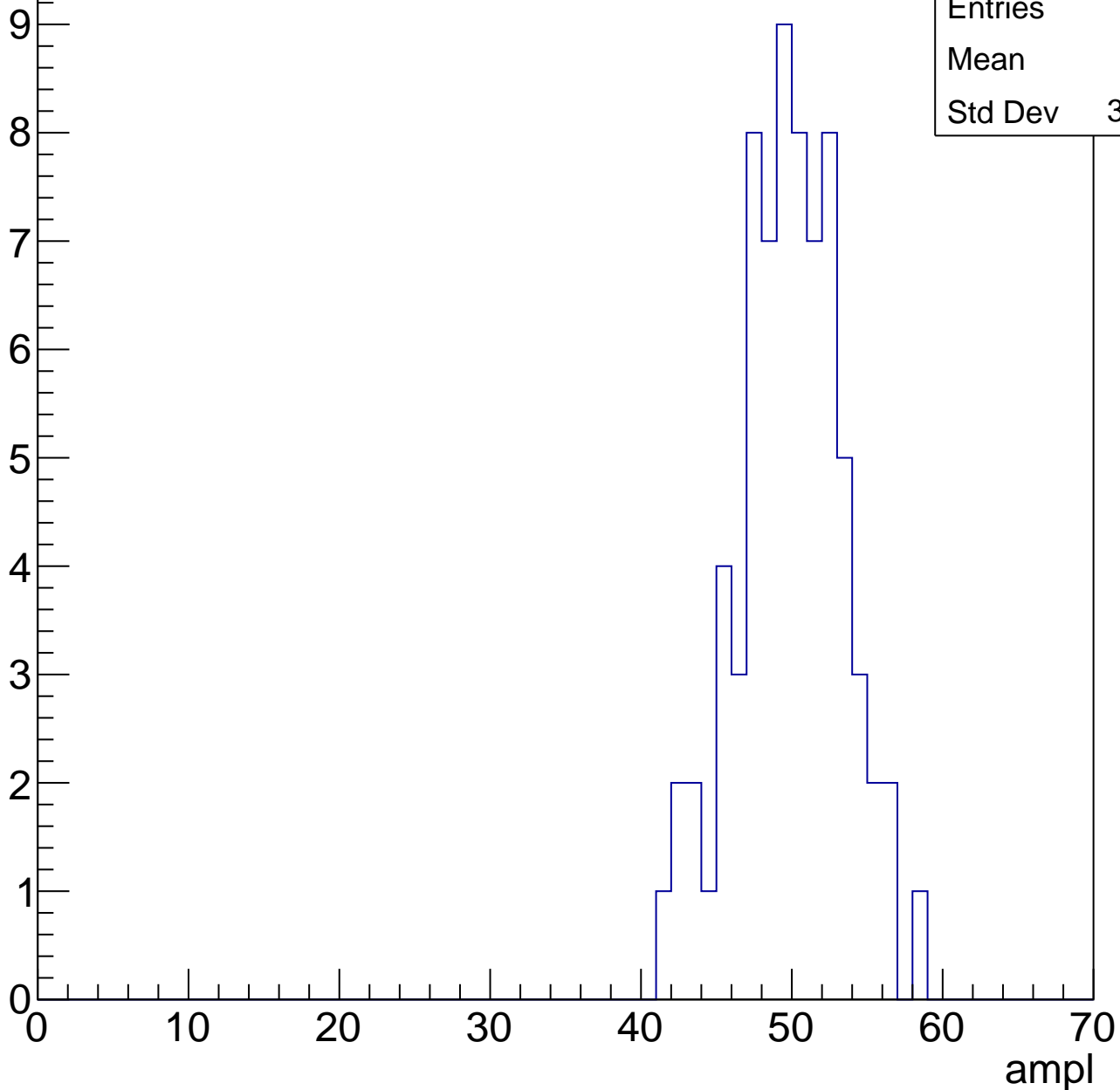


B1L100S, U19-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.4
Std Dev	3.495

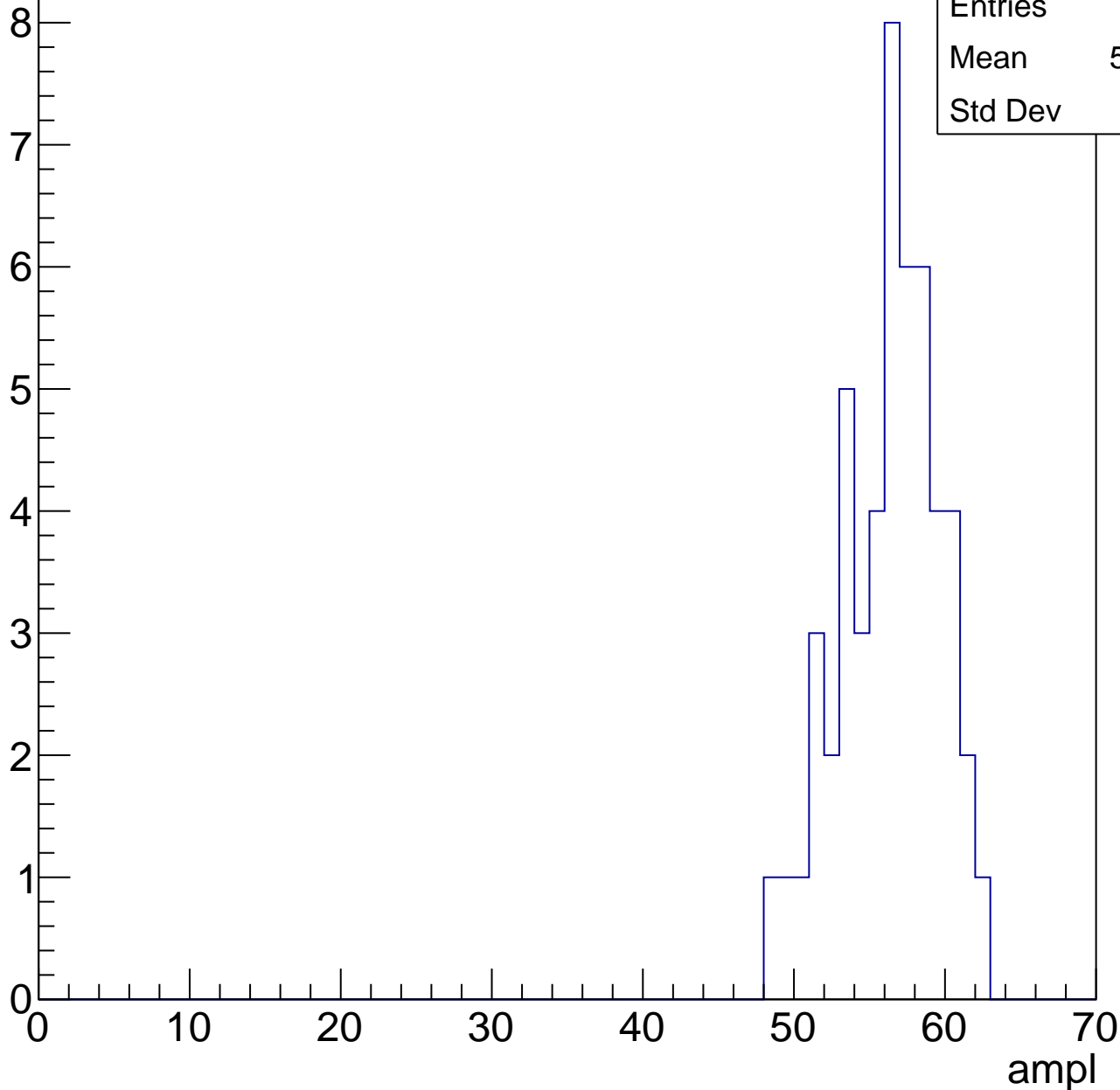


B1L100S, U19-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

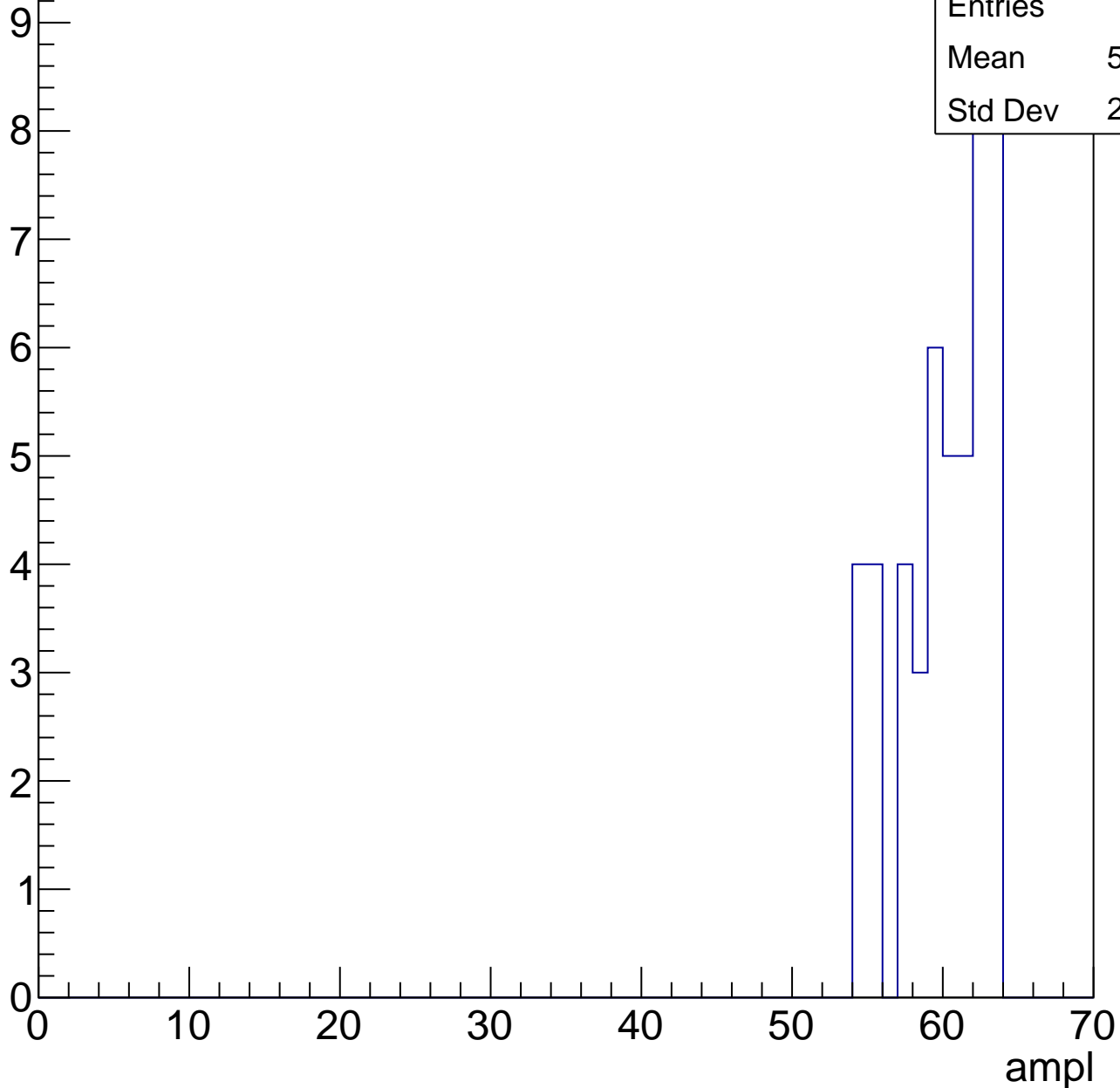
Entries	51
Mean	55.86
Std Dev	3.23



B1L100S, U19-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

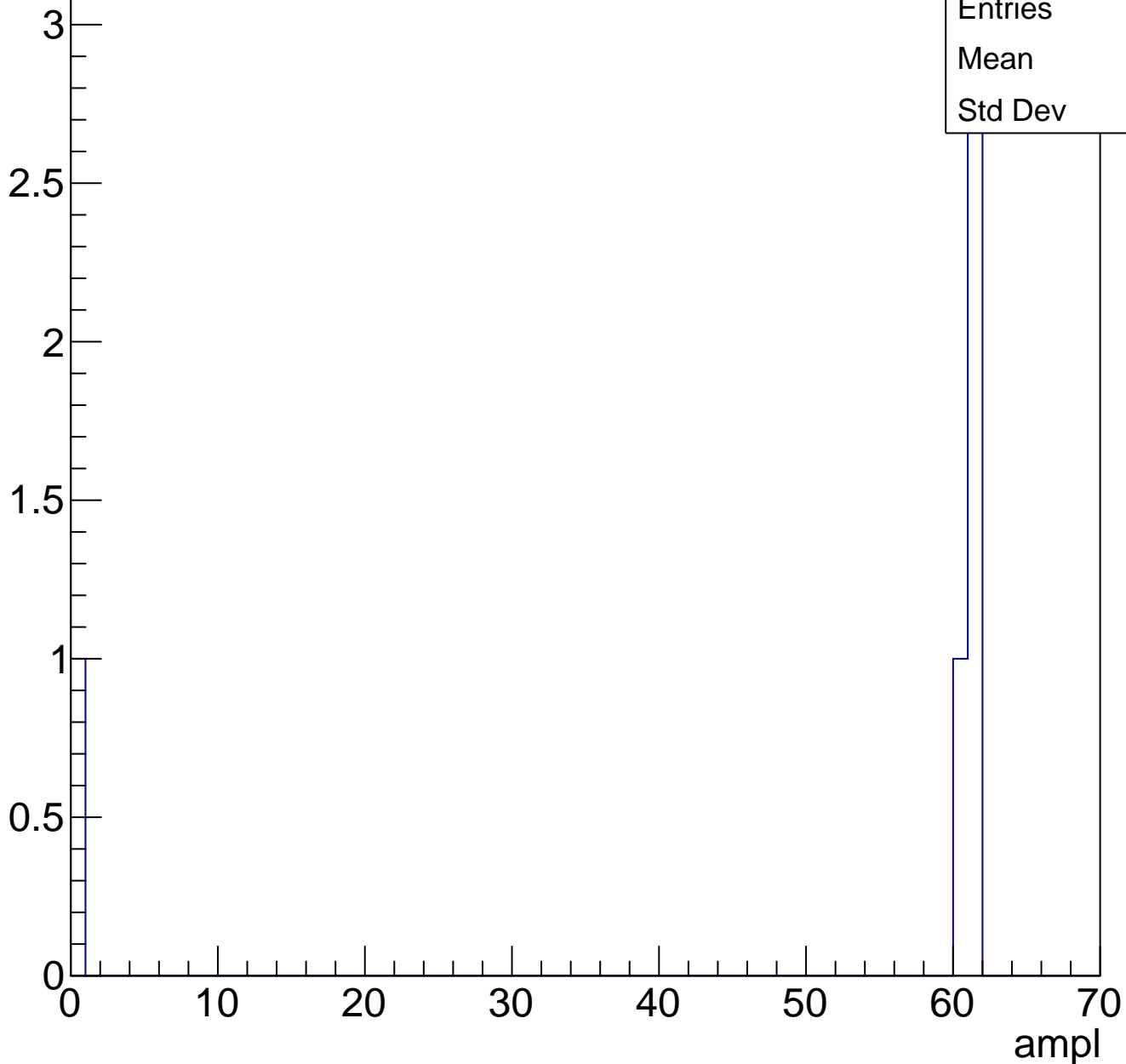
Entry



B1L100S, U19-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

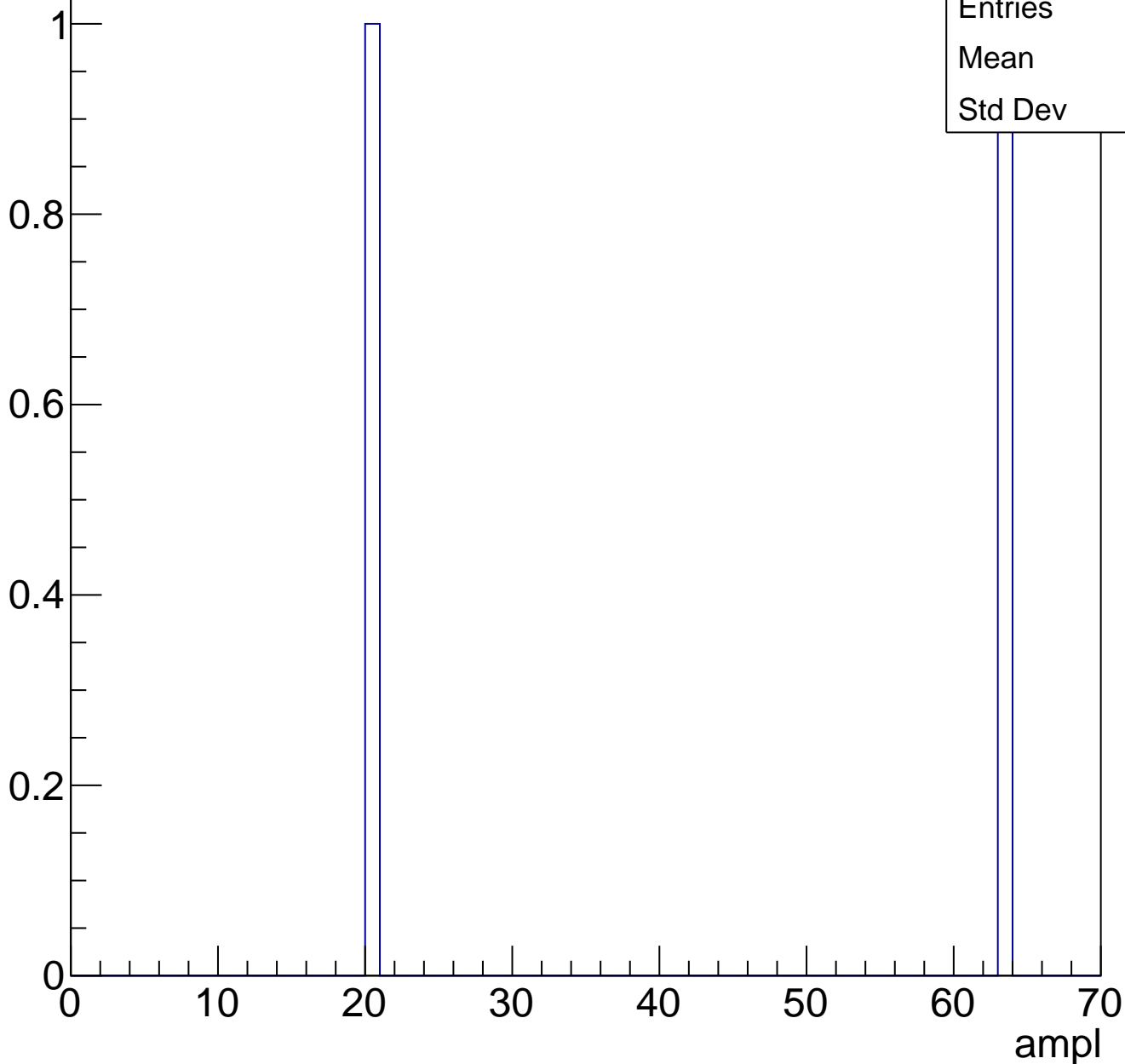
Entry



B1L100S, U19-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch63, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	31.91
Std Dev	3.072

Gaus mean : 32.2723

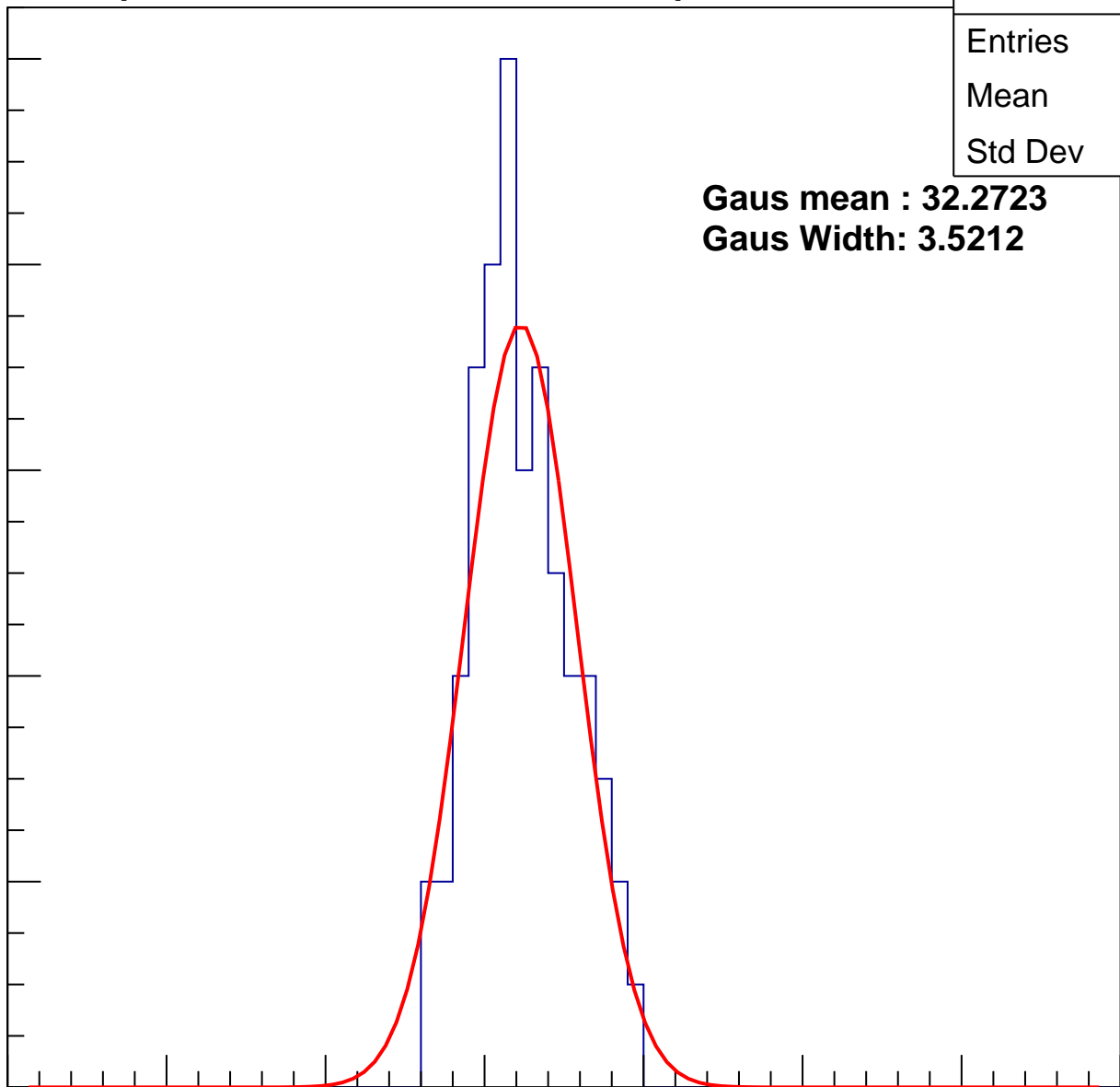
Gaus Width: 3.5212

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch63, adc1

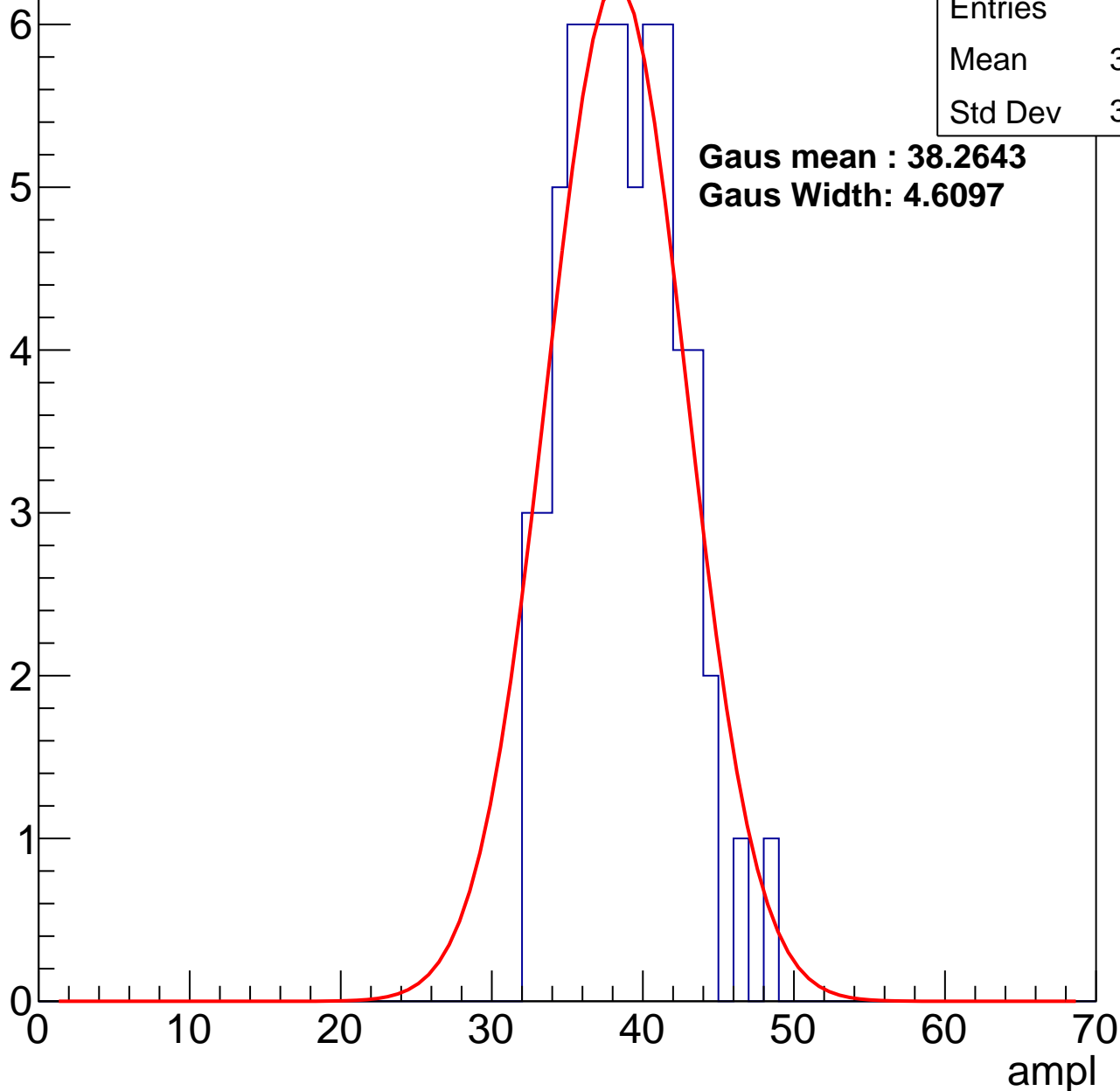
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	38.19
Std Dev	3.596

Gaus mean : 38.2643

Gaus Width: 4.6097



B1L100S, U19-ch63, adc2

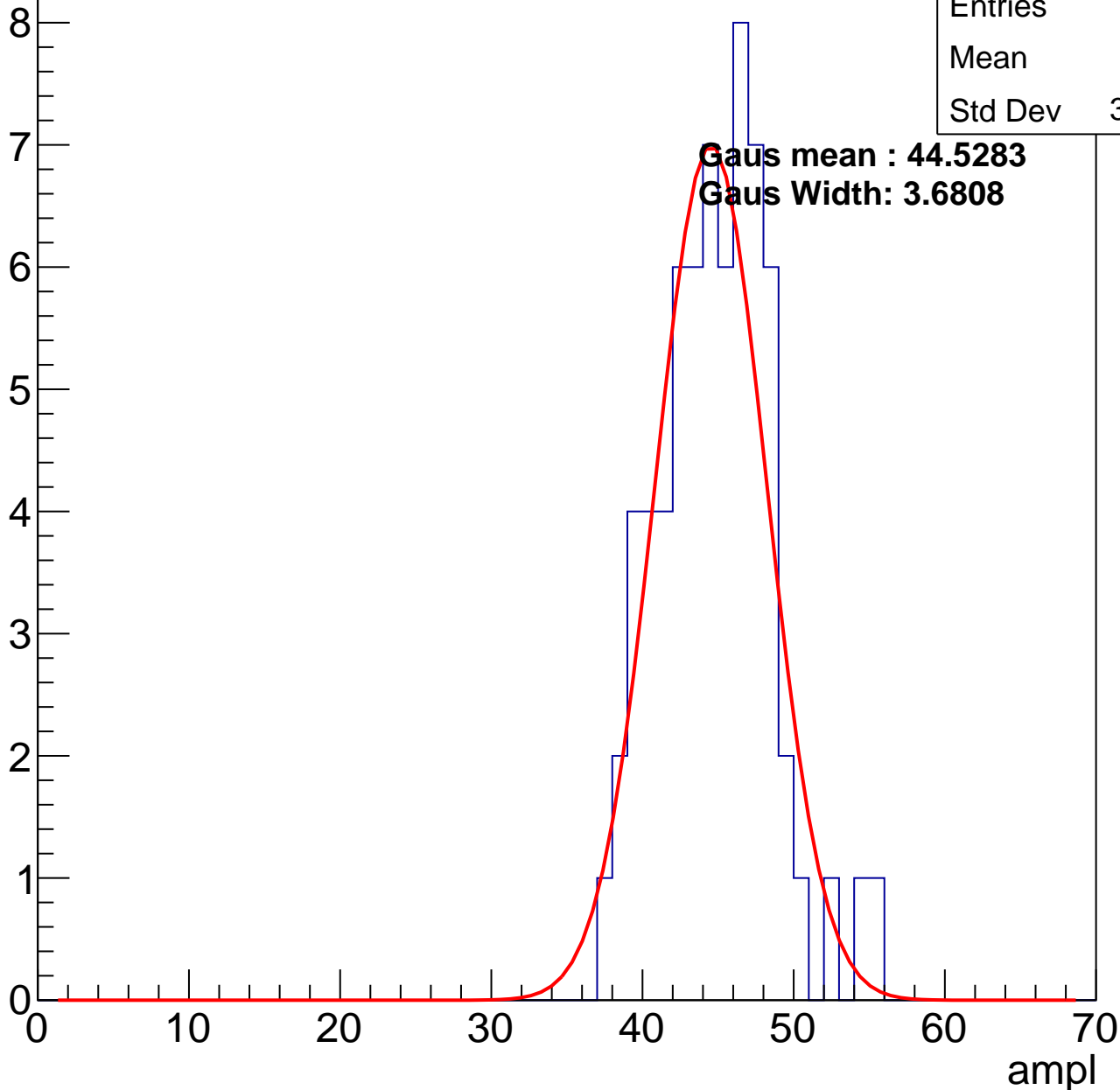
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.4
Std Dev	3.682

Gaus mean : 44.5283

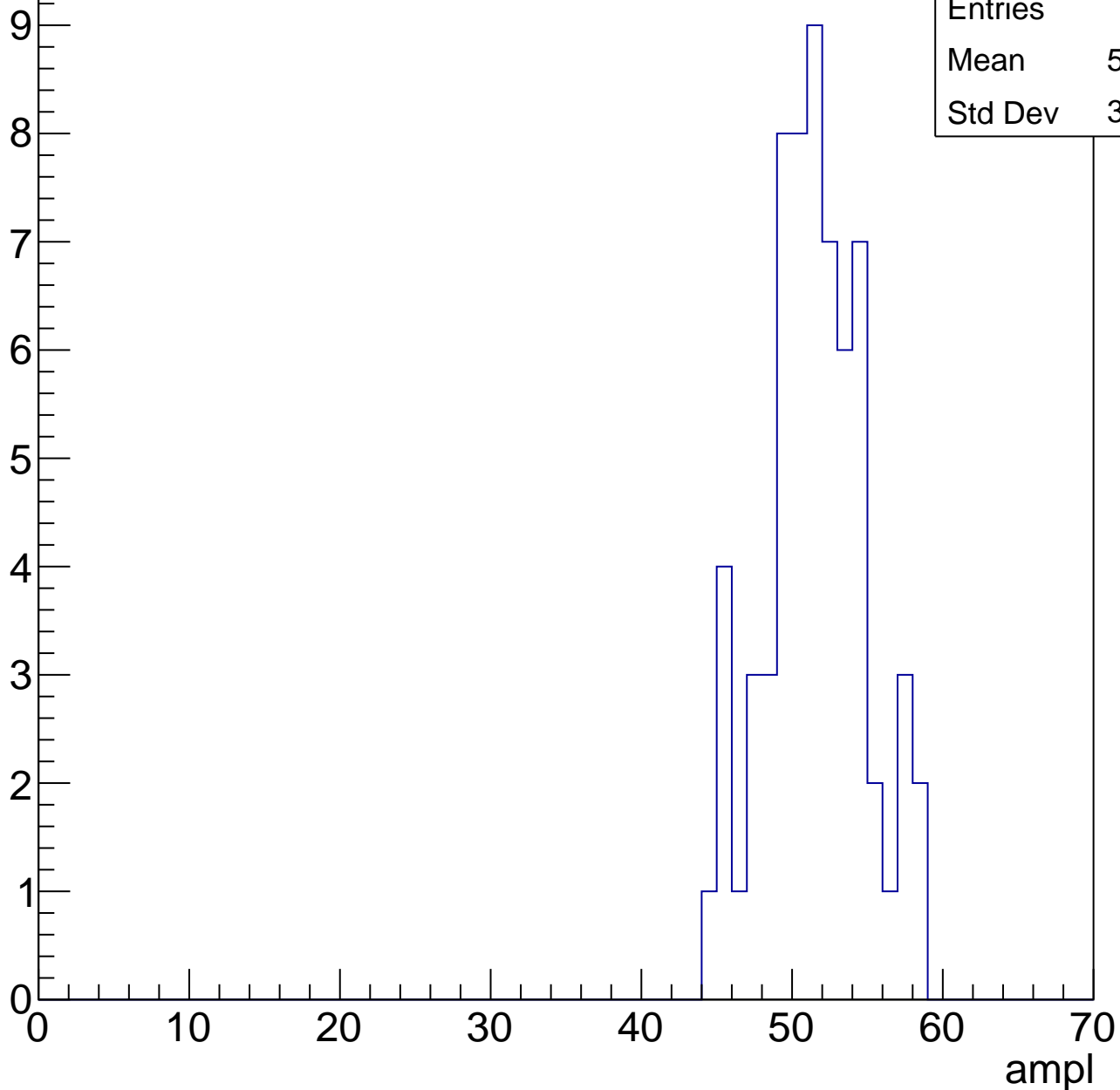
Gaus Width: 3.6808



B1L100S, U19-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

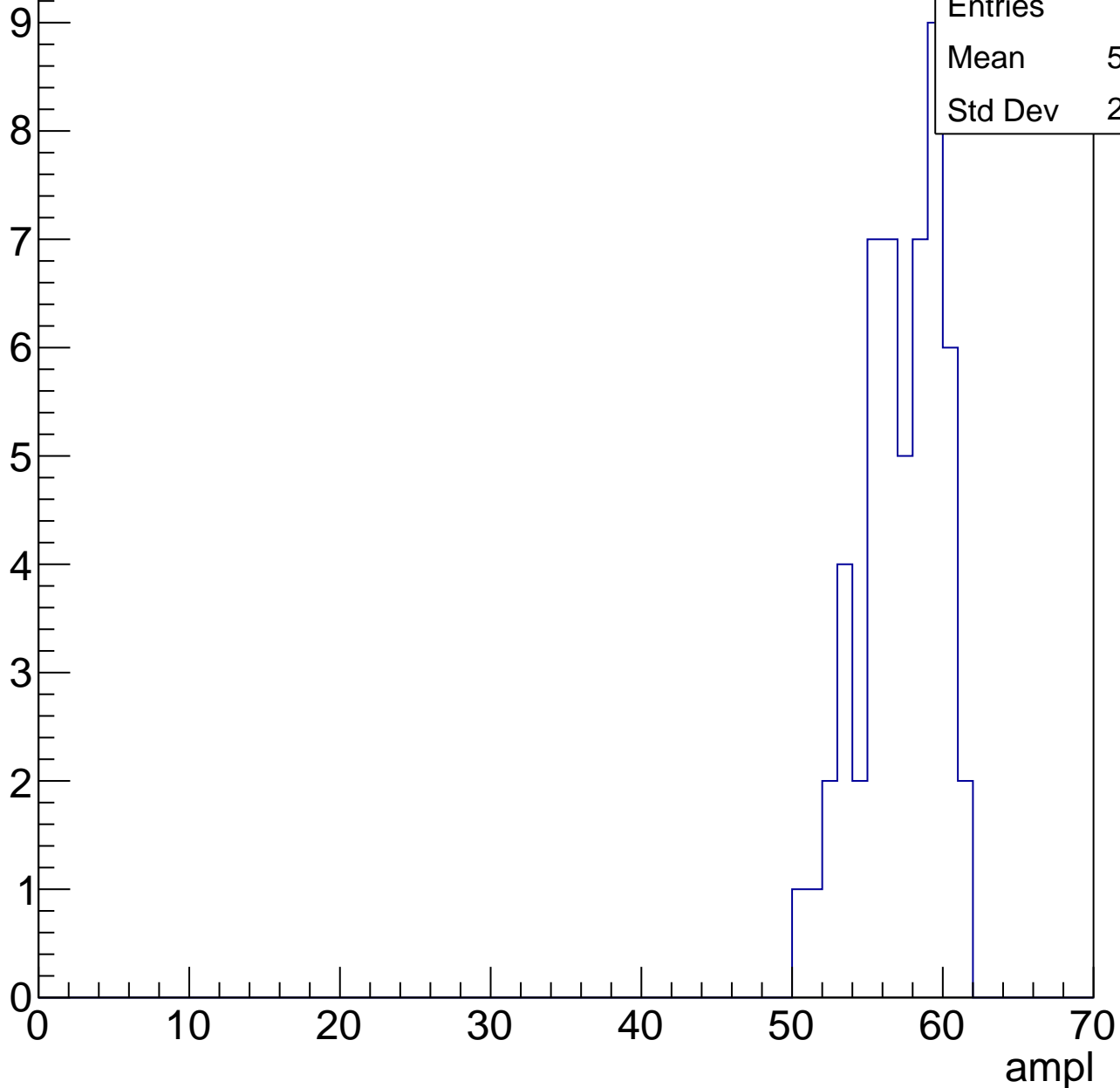
Entry



B1L100S, U19-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	53
Mean	56.72
Std Dev	2.666

B1L100S, U19-ch63, adc5

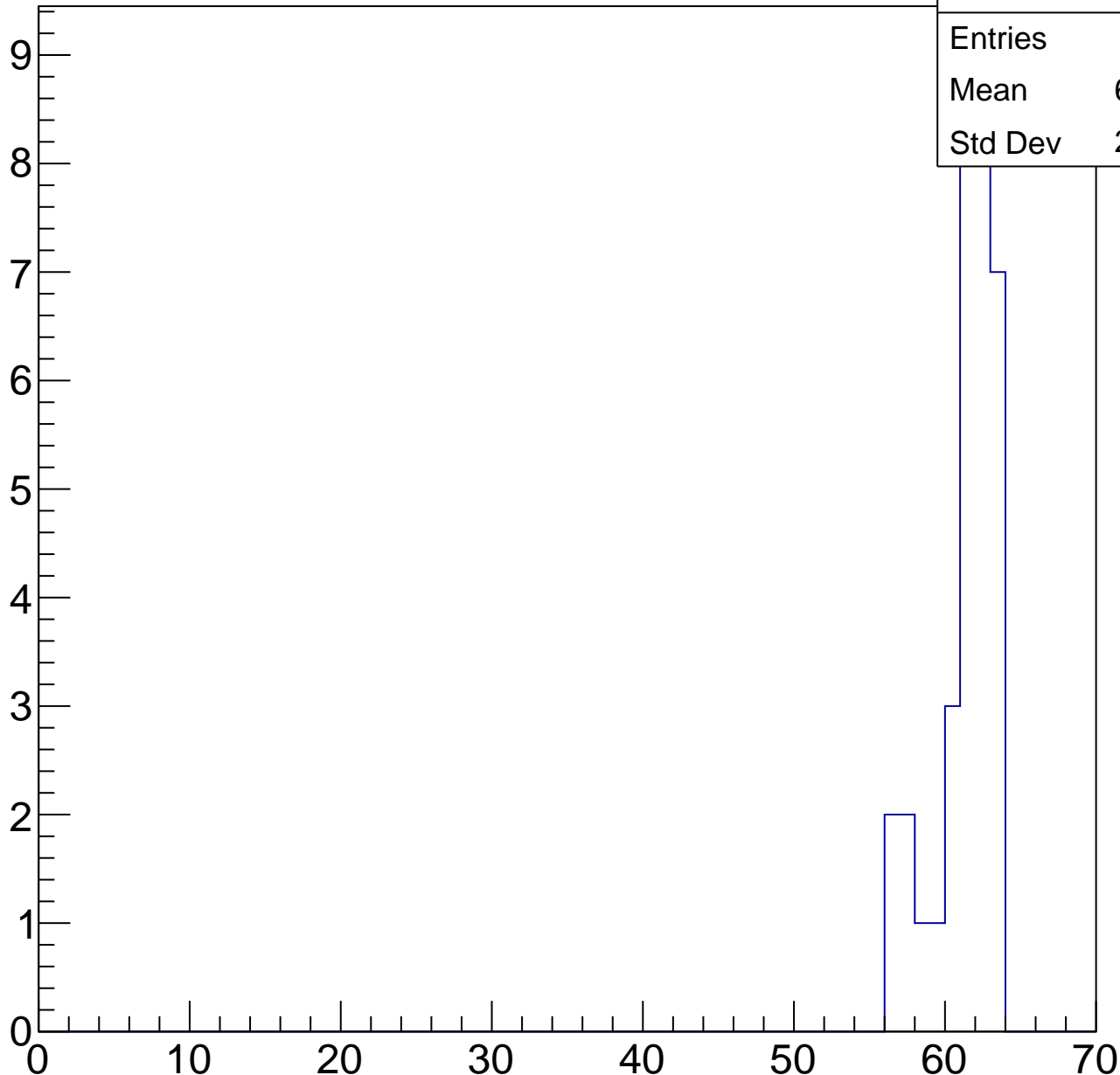
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	33
Mean	60.91
Std Dev	2.021

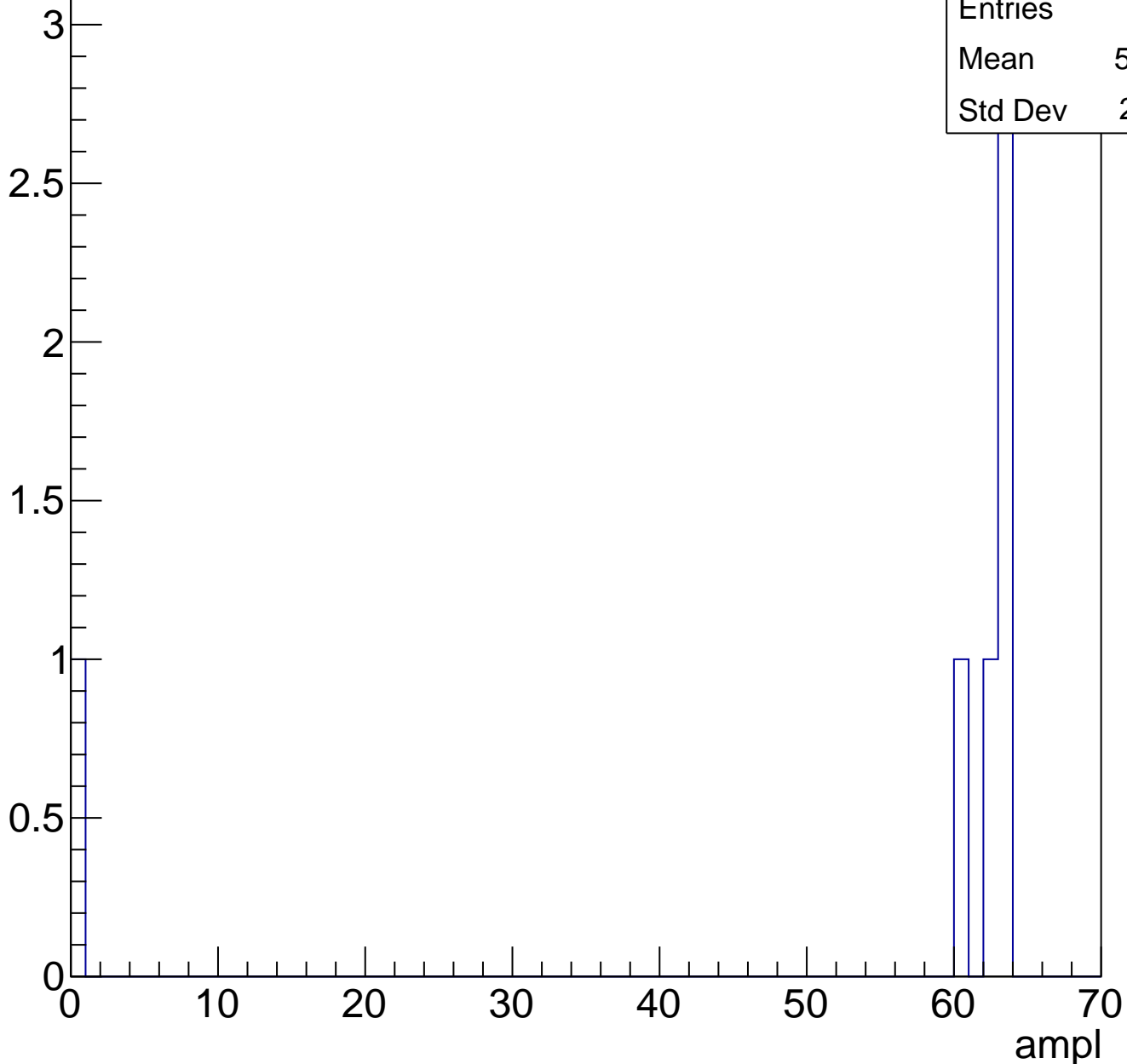
ampl



B1L100S, U19-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch64, adc0

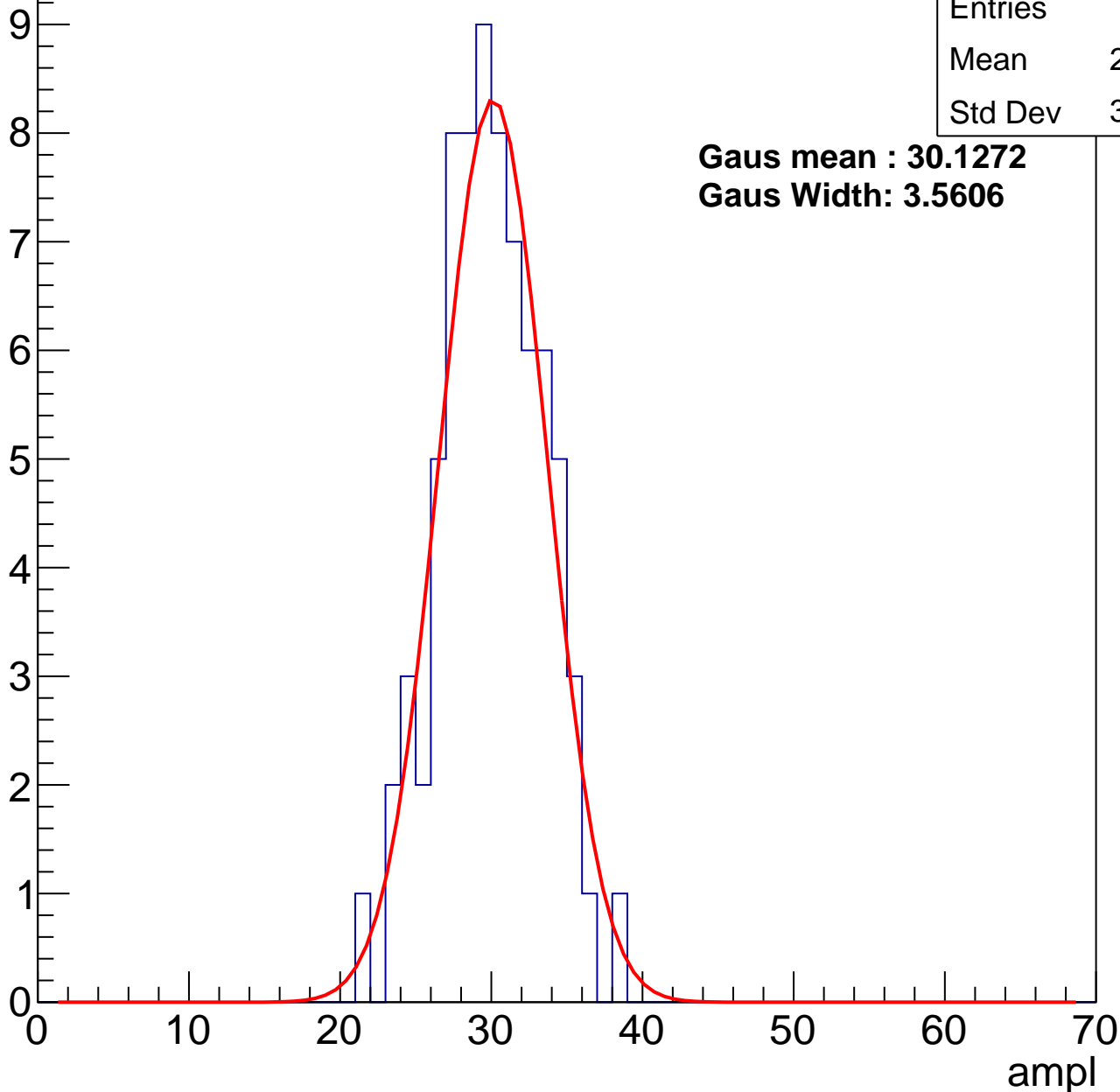
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.55
Std Dev	3.376

Gaus mean : 30.1272

Gaus Width: 3.5606



B1L100S, U19-ch64, adc1

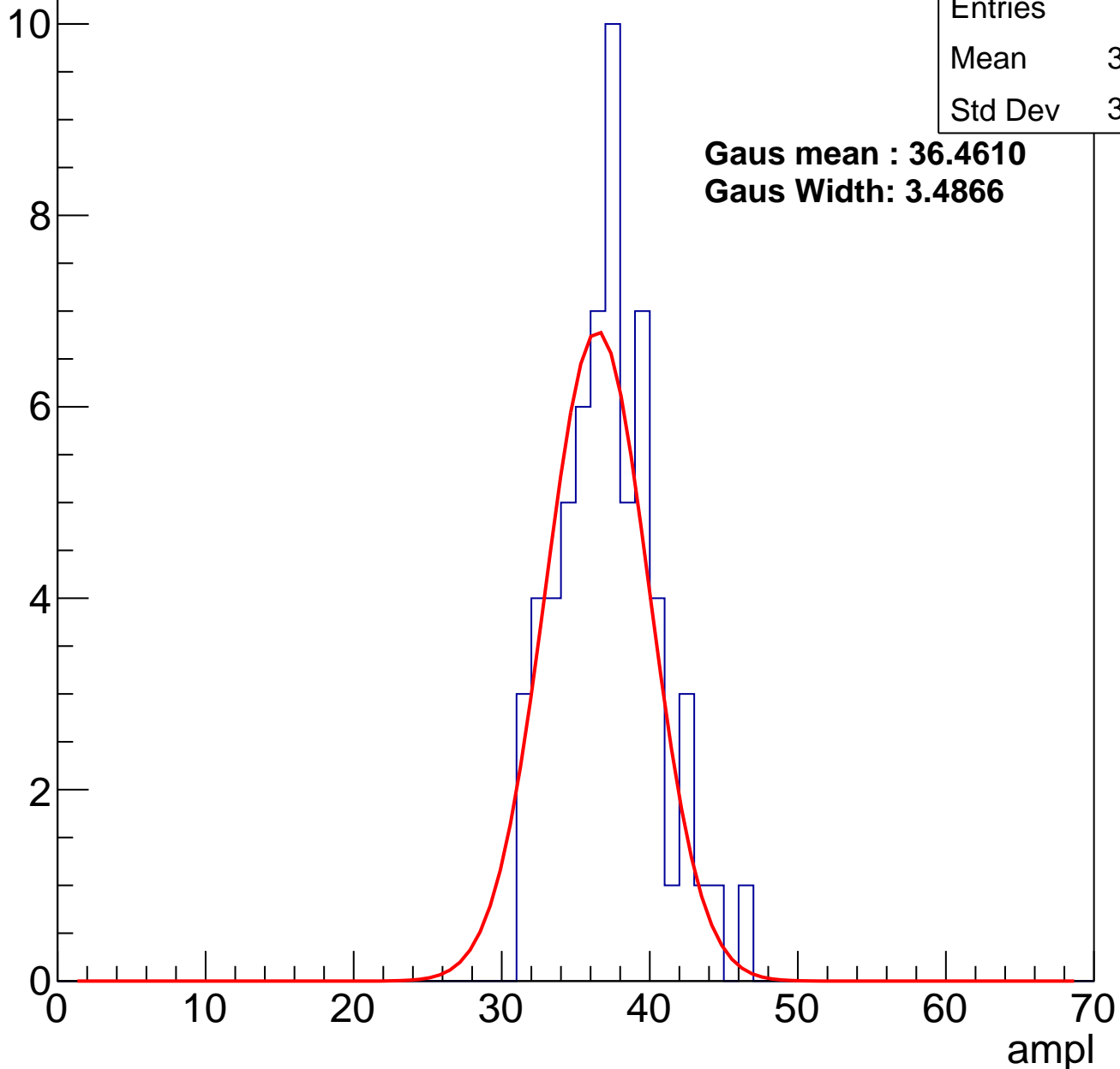
calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	36.74
Std Dev	3.287

Gaus mean : 36.4610

Gaus Width: 3.4866

Entry



B1L100S, U19-ch64, adc2

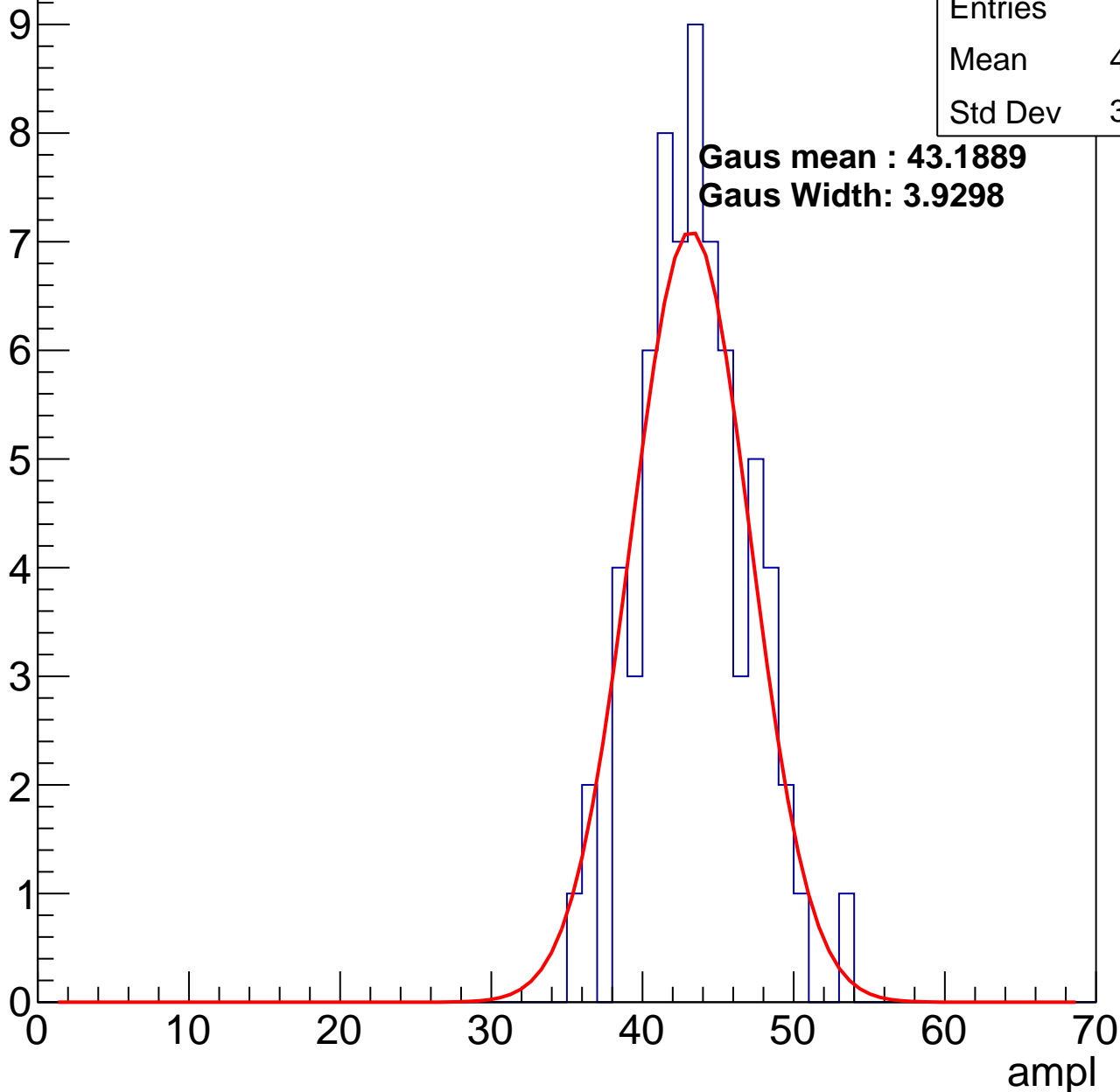
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.03
Std Dev	3.534

Gaus mean : 43.1889

Gaus Width: 3.9298

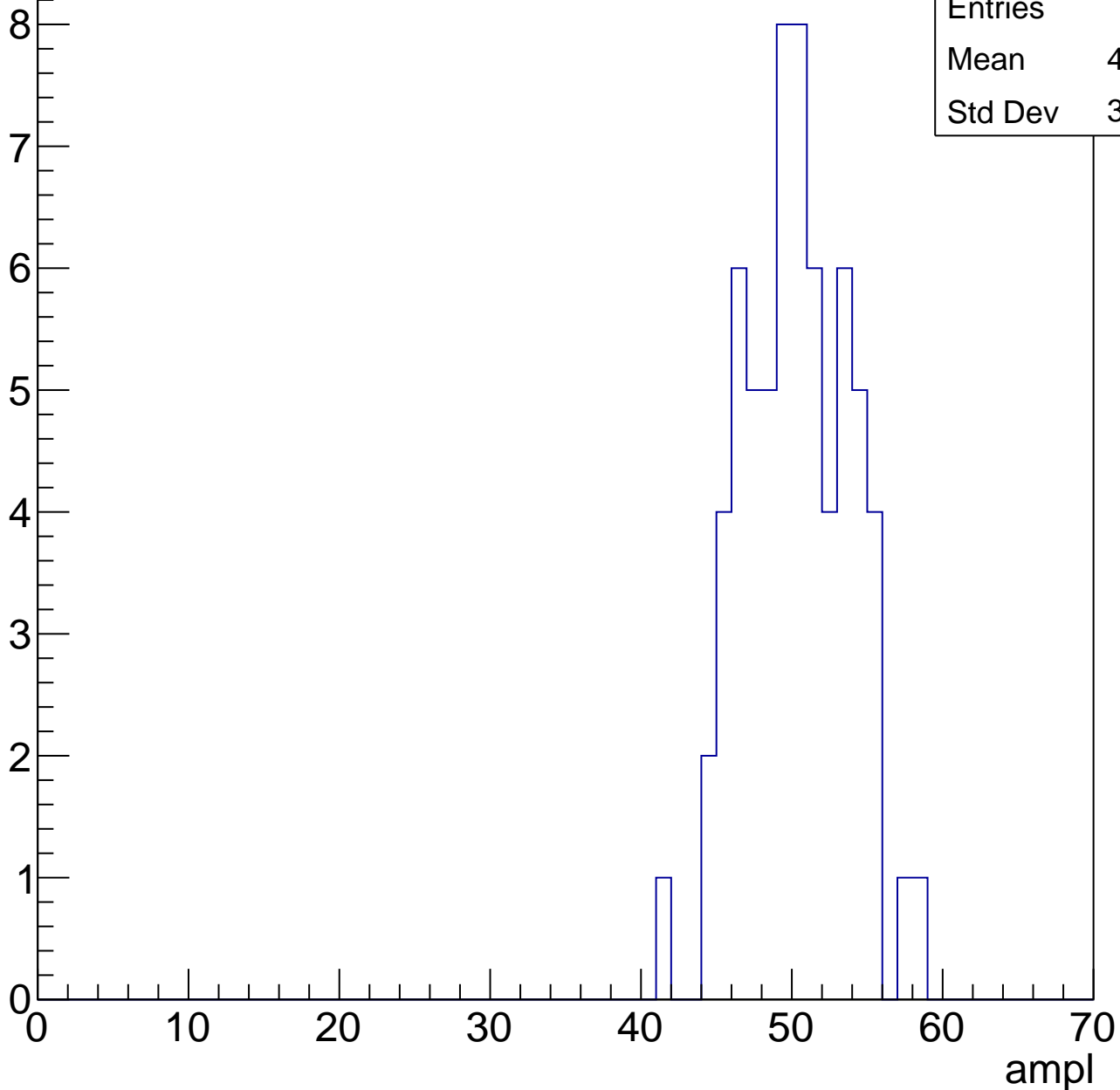


B1L100S, U19-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.83
Std Dev	3.458

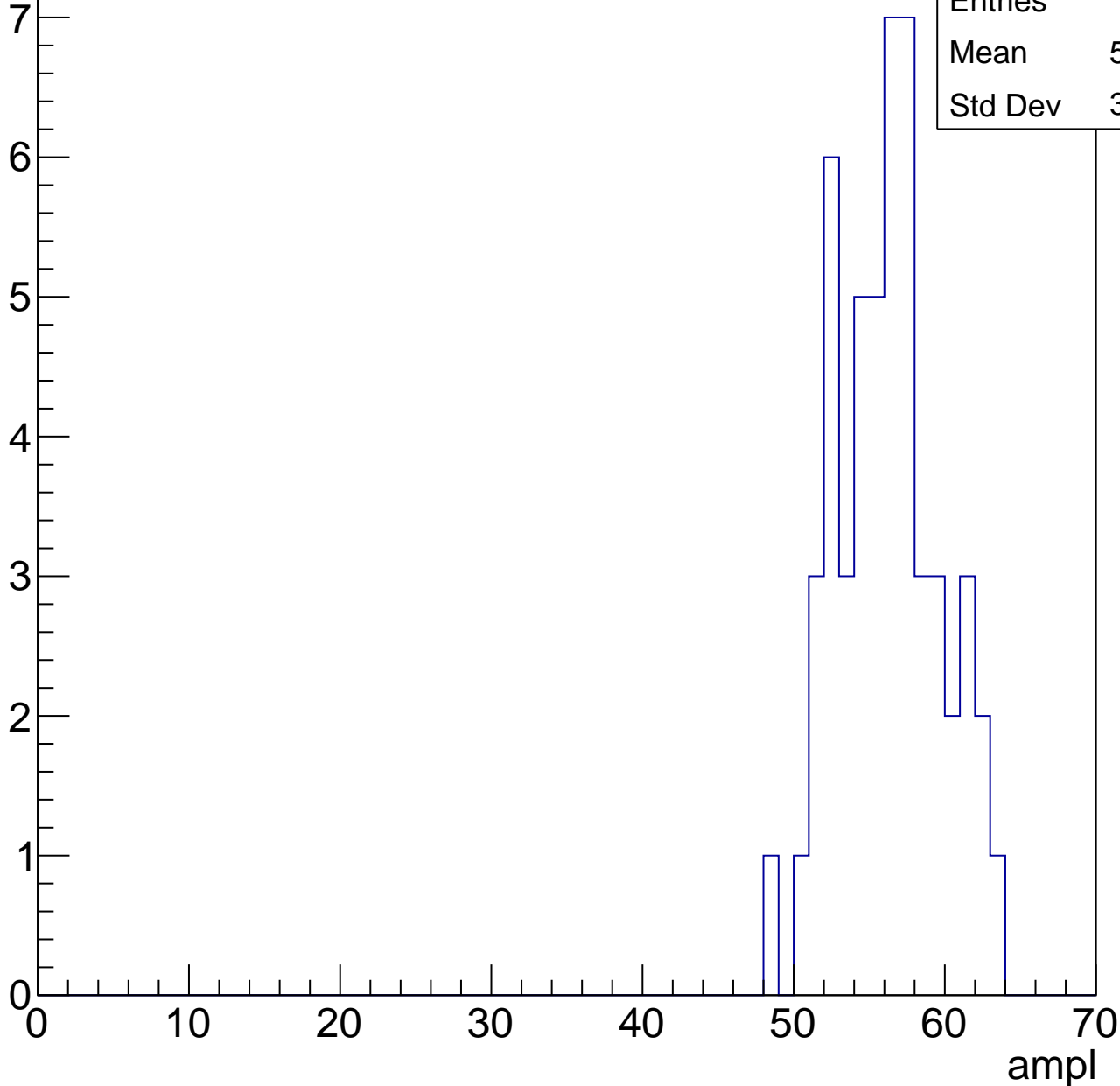


B1L100S, U19-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.75
Std Dev	3.379

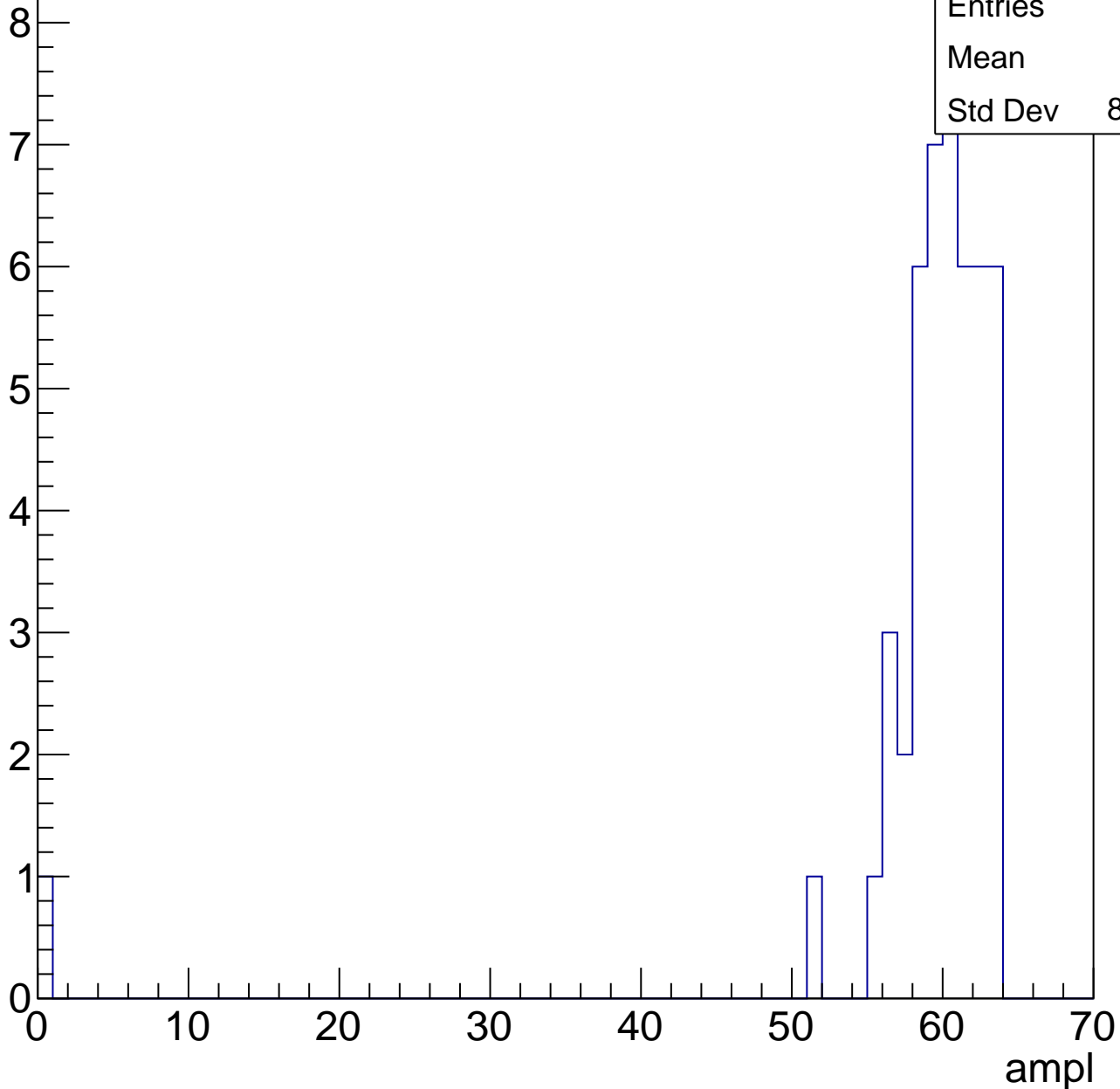


B1L100S, U19-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.4
Std Dev	8.953



B1L100S, U19-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch65, adc0

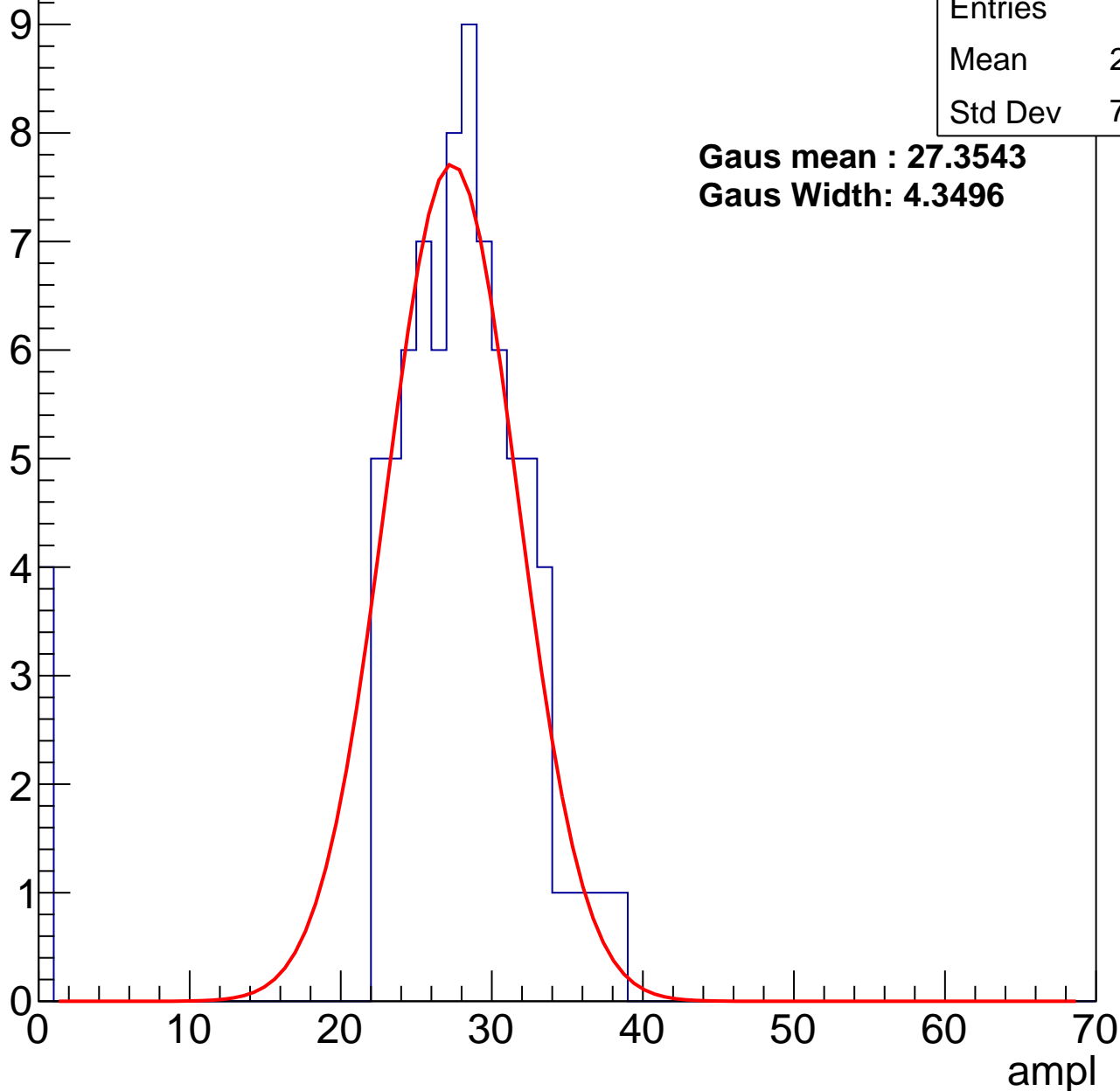
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	26.56
Std Dev	7.026

Gaus mean : 27.3543

Gaus Width: 4.3496



B1L100S, U19-ch65, adc1

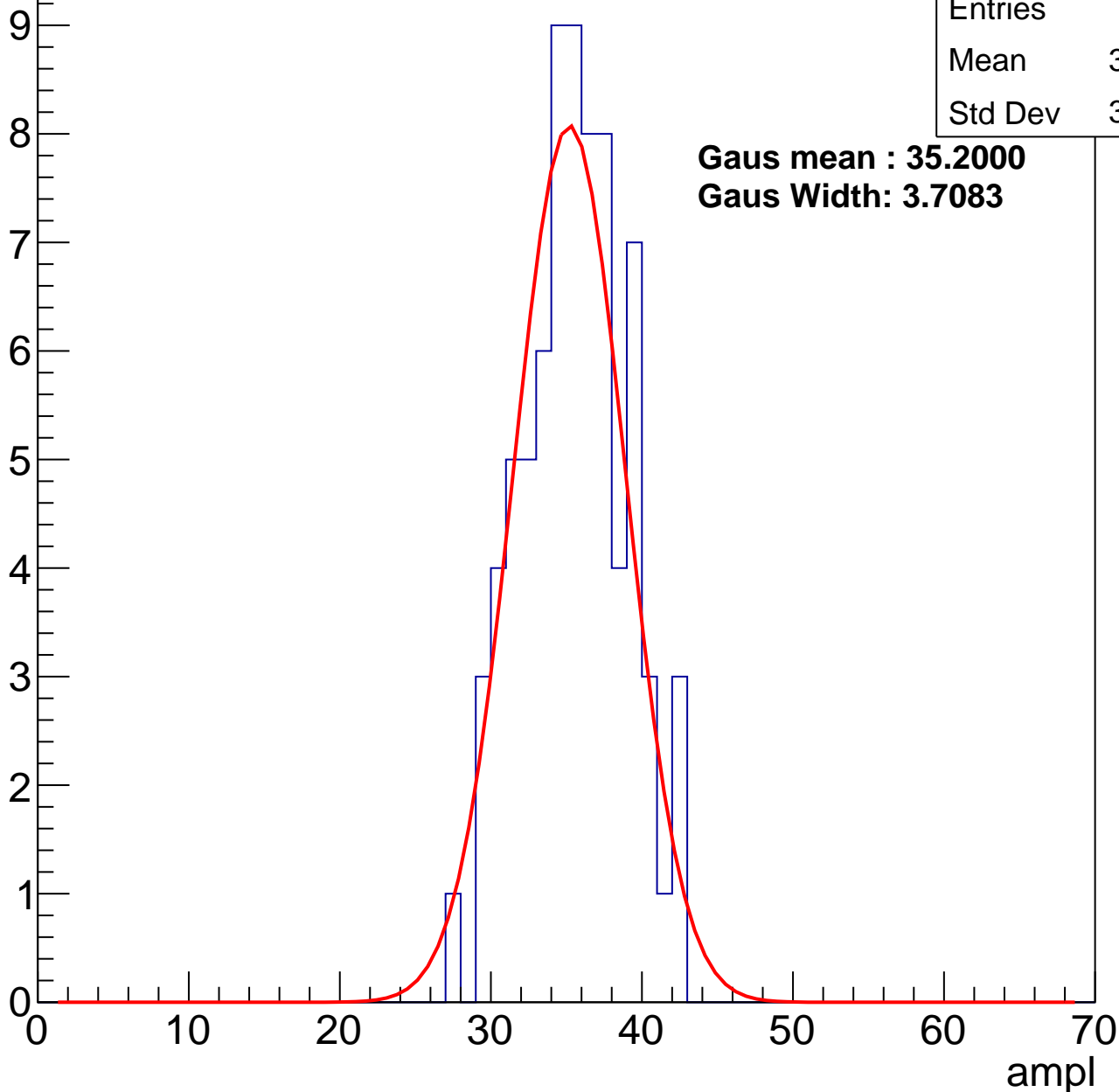
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	35.05
Std Dev	3.395

Gaus mean : 35.2000

Gaus Width: 3.7083



B1L100S, U19-ch65, adc2

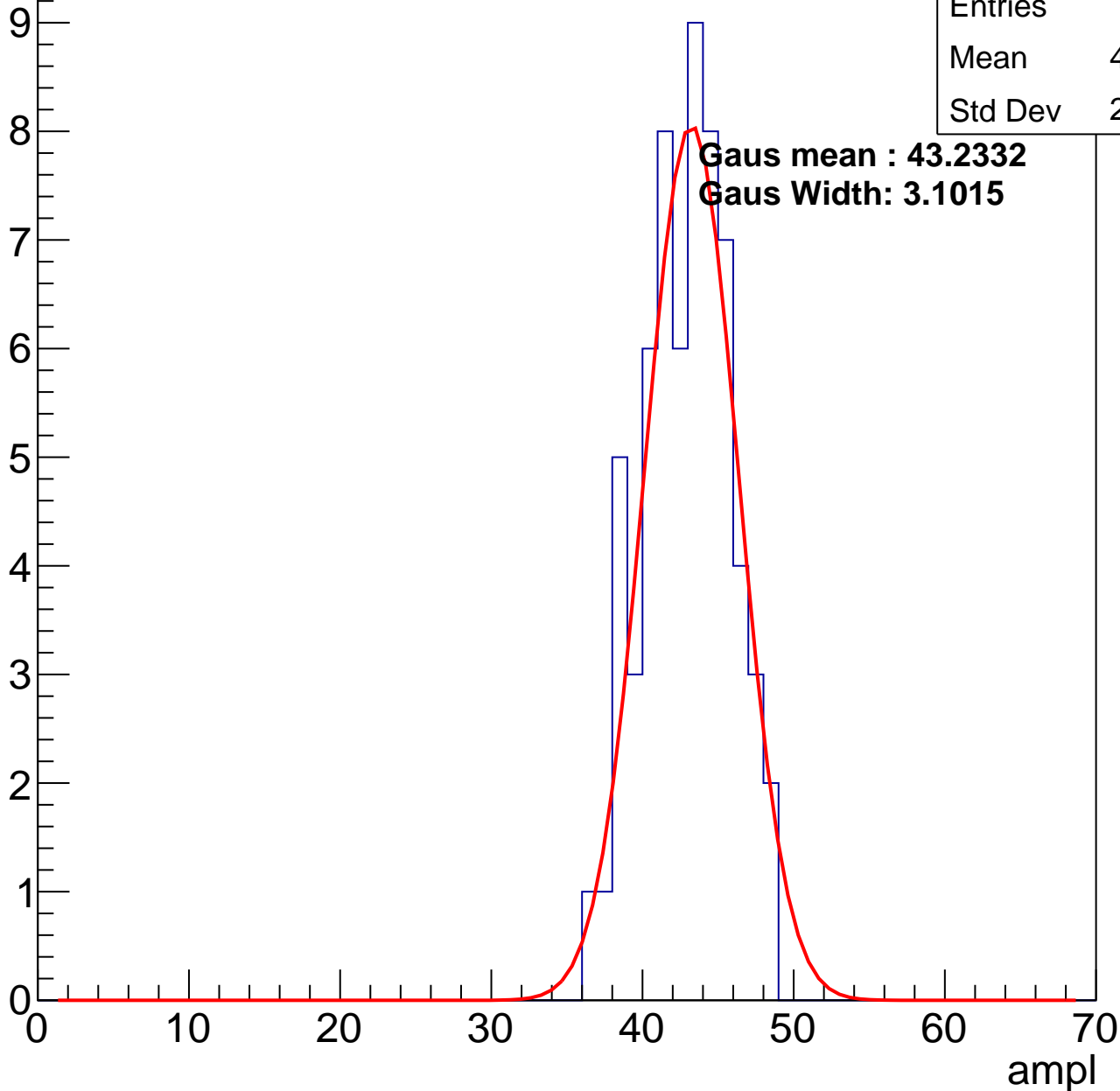
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.46
Std Dev	2.822

Gaus mean : 43.2332

Gaus Width: 3.1015

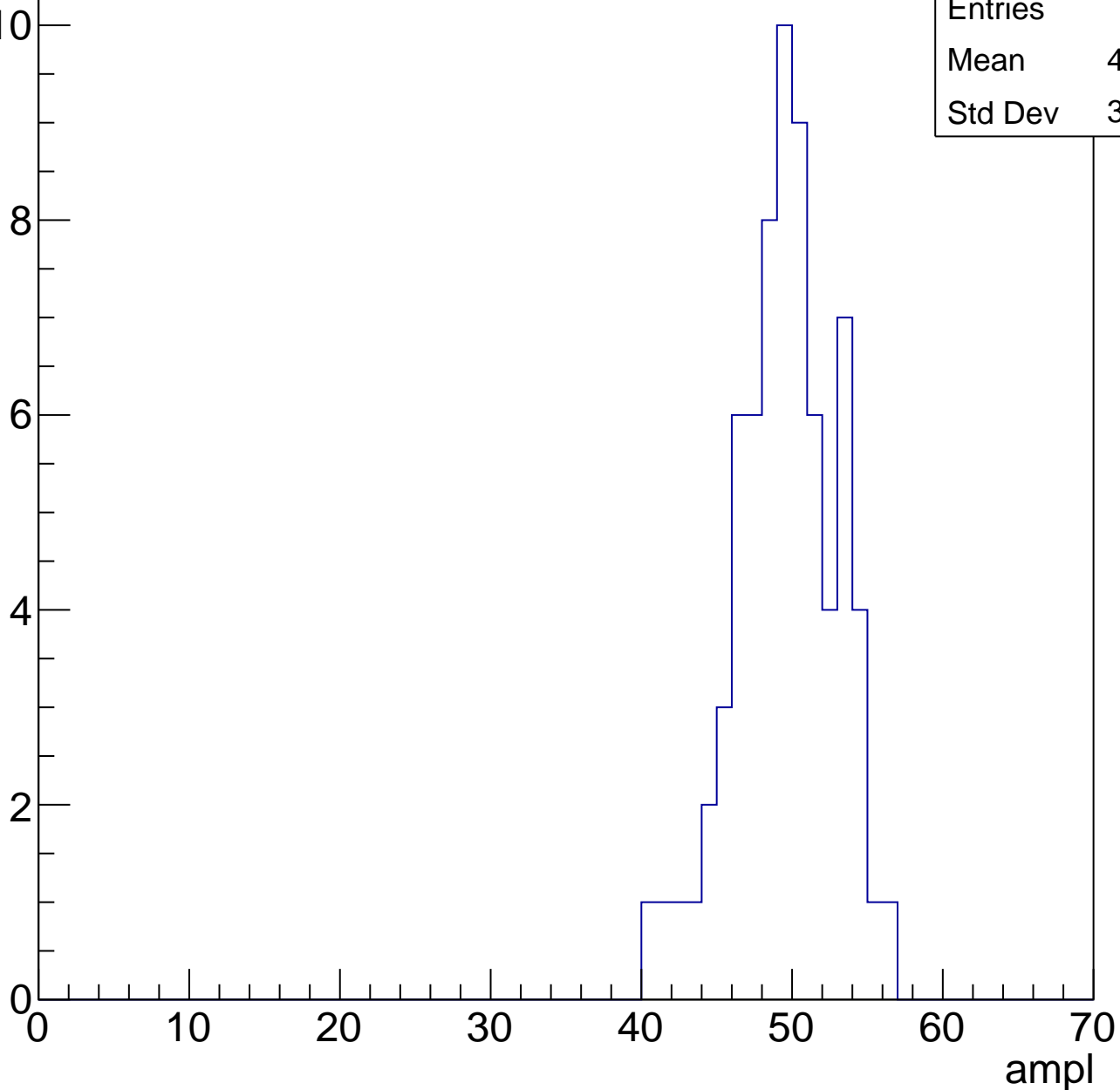


B1L100S, U19-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	49.06
Std Dev	3.314

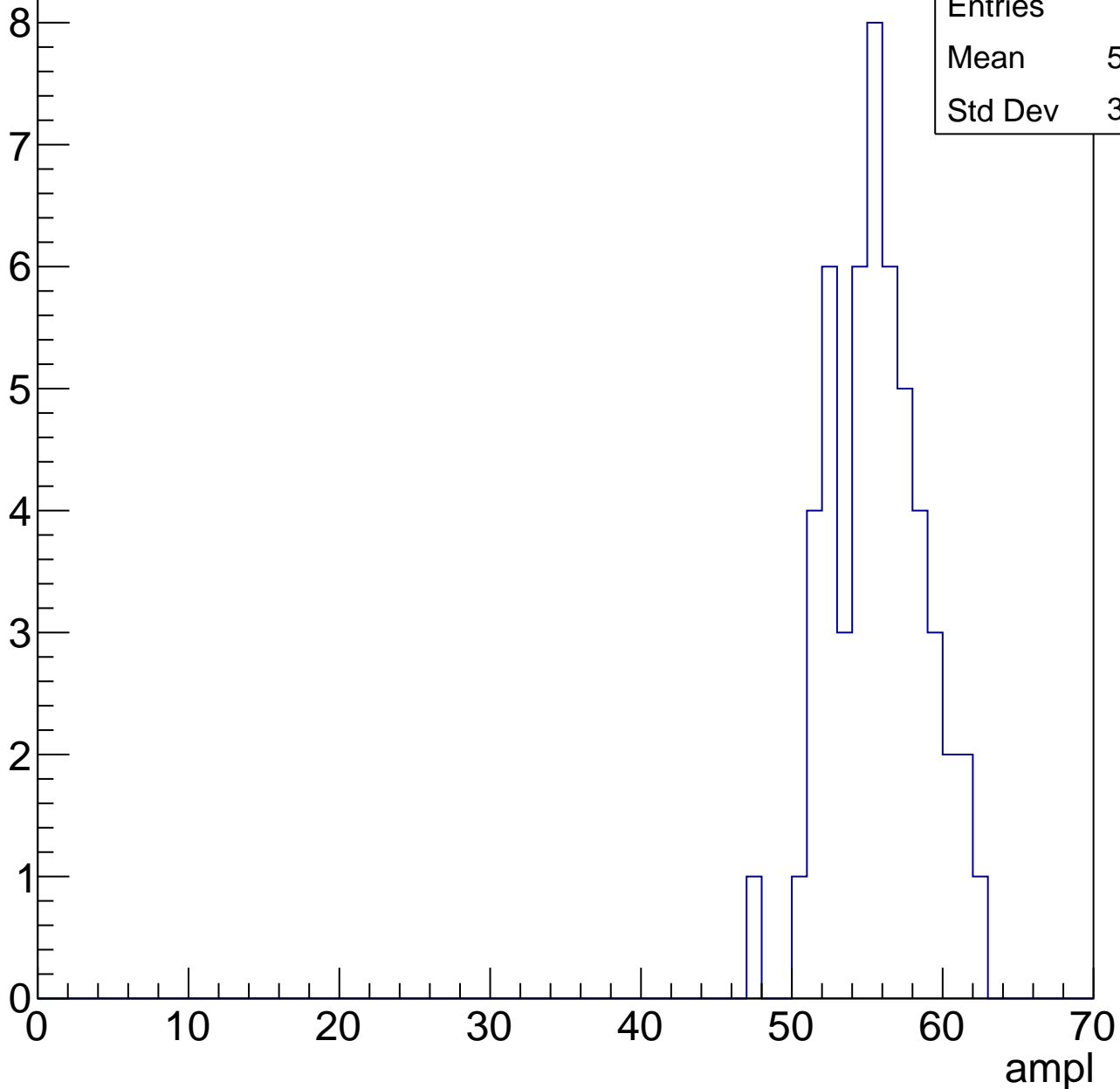


B1L100S, U19-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.19
Std Dev	3.113

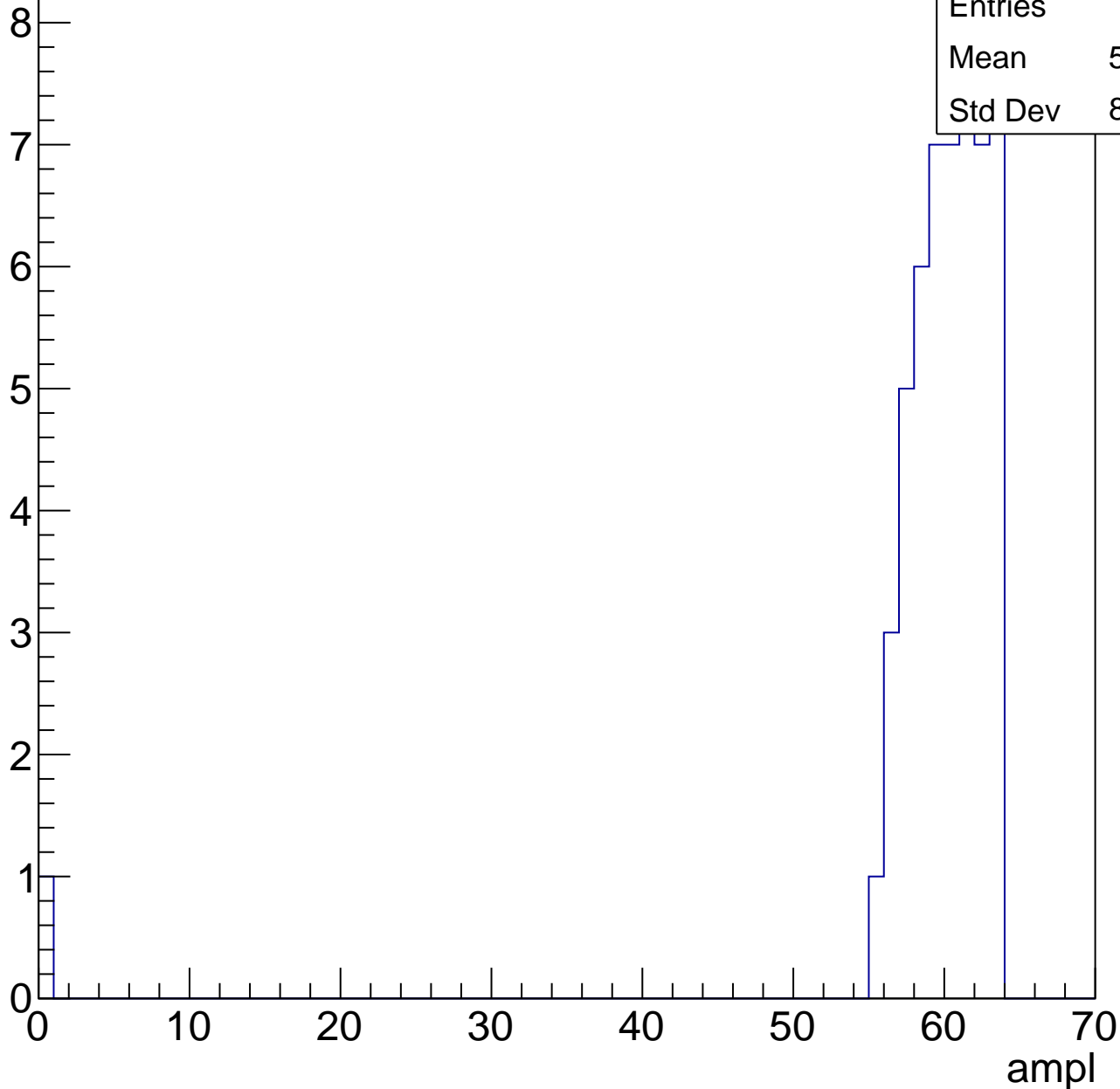


B1L100S, U19-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.77
Std Dev	8.442



B1L100S, U19-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch66, adc0

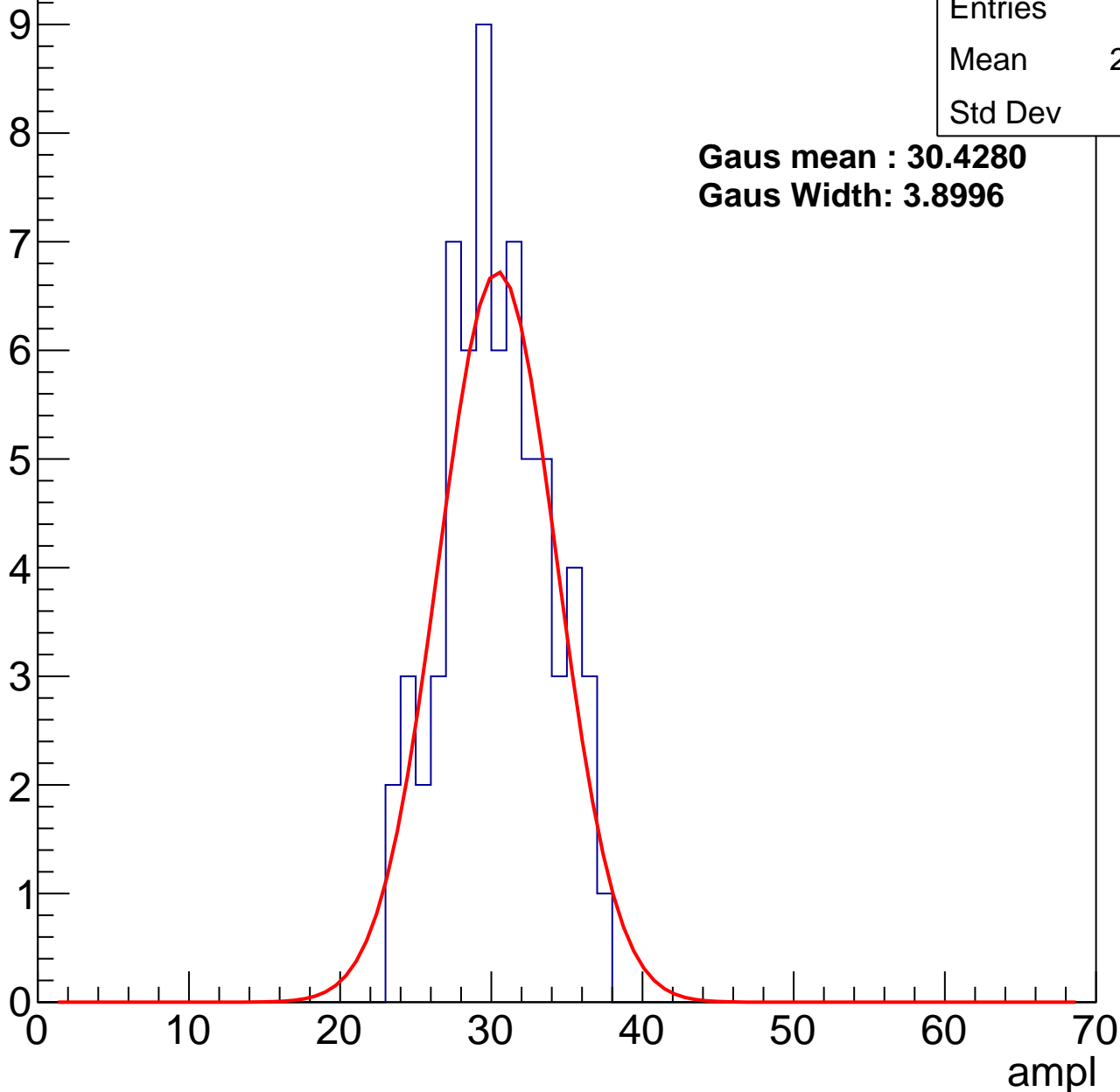
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.89
Std Dev	3.43

Gaus mean : 30.4280

Gaus Width: 3.8996



B1L100S, U19-ch66, adc1

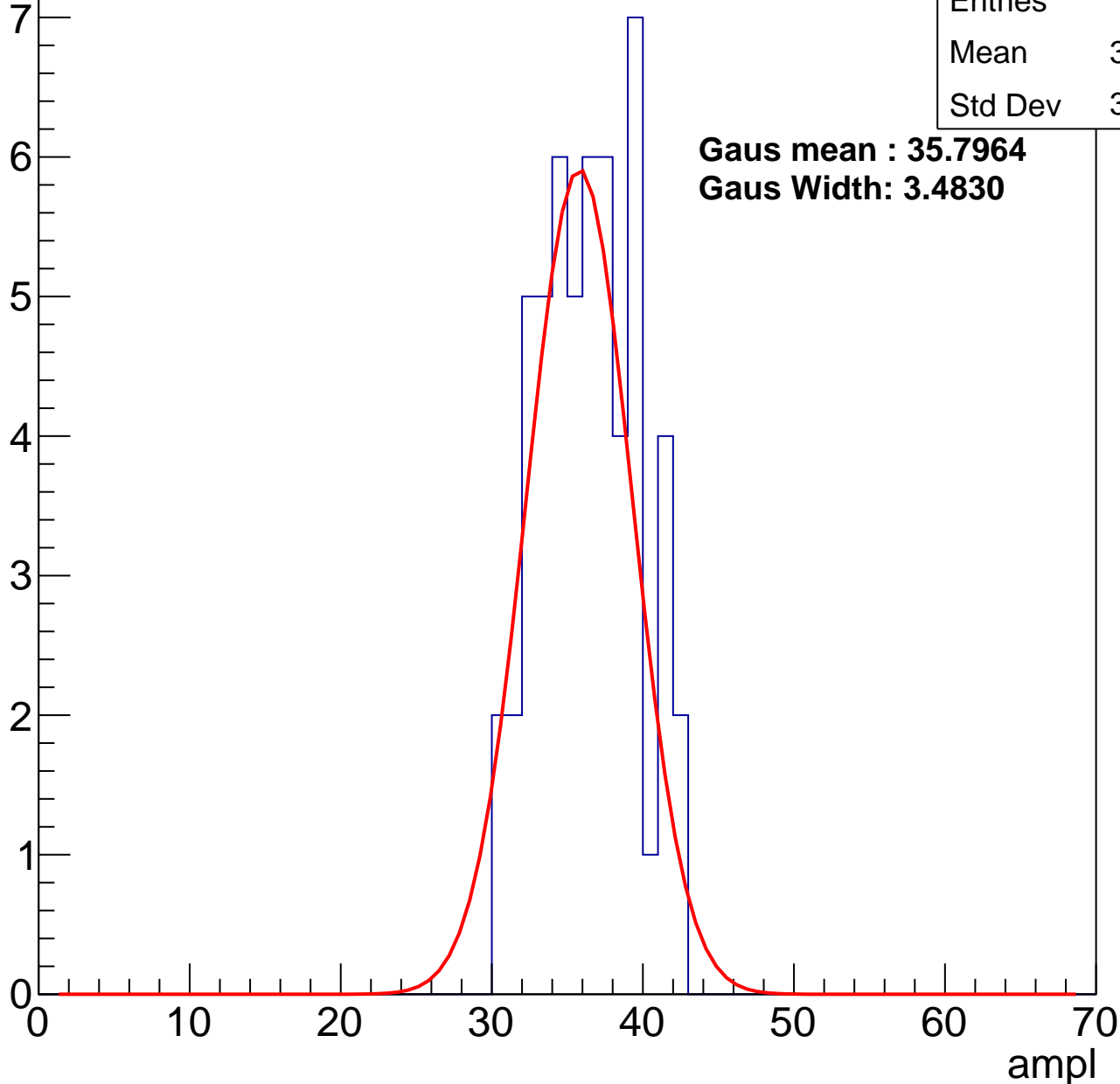
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.95
Std Dev	3.159

Gaus mean : 35.7964

Gaus Width: 3.4830



B1L100S, U19-ch66, adc2

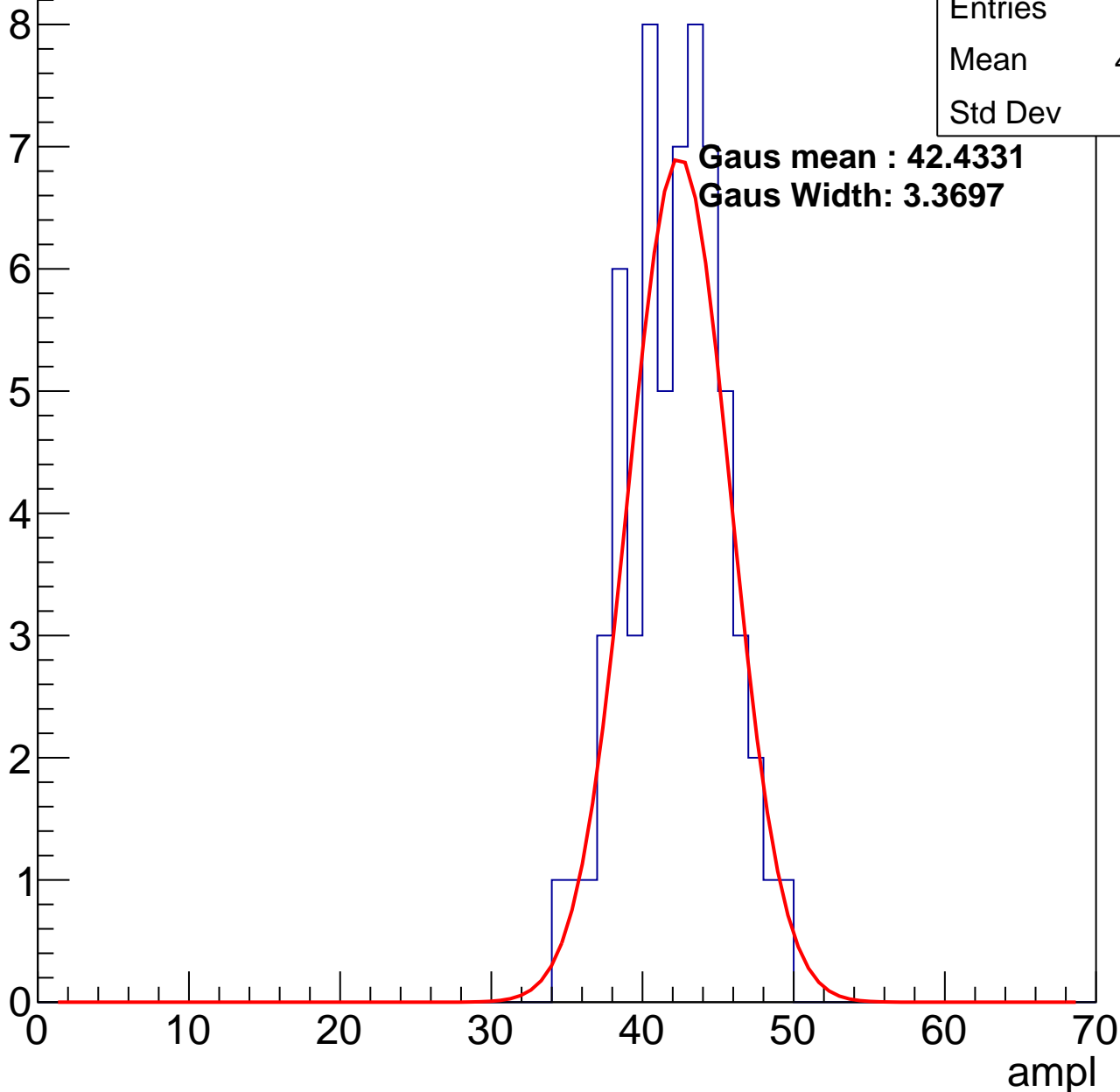
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.71
Std Dev	3.22

Gaus mean : 42.4331

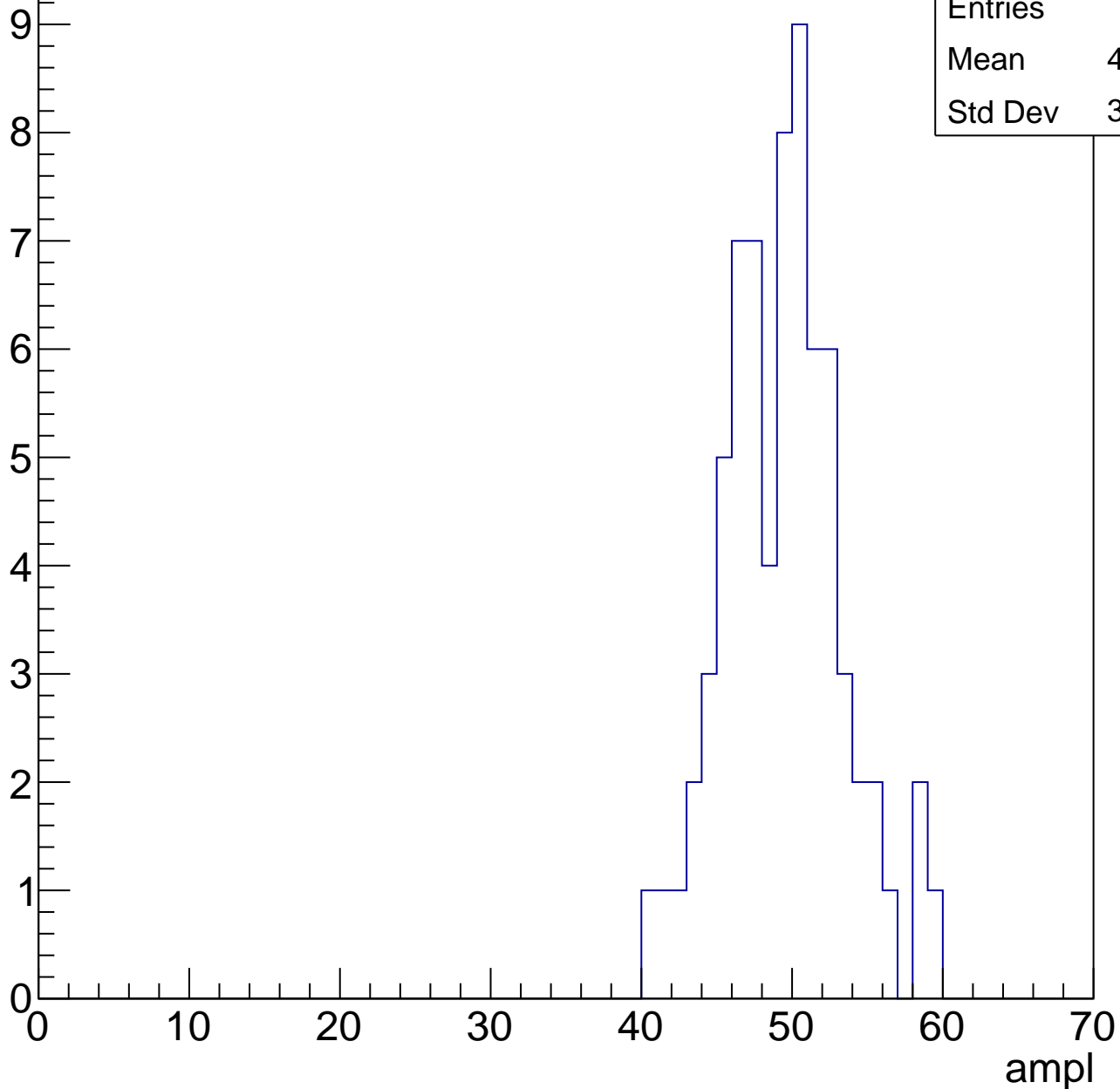
Gaus Width: 3.3697



B1L100S, U19-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

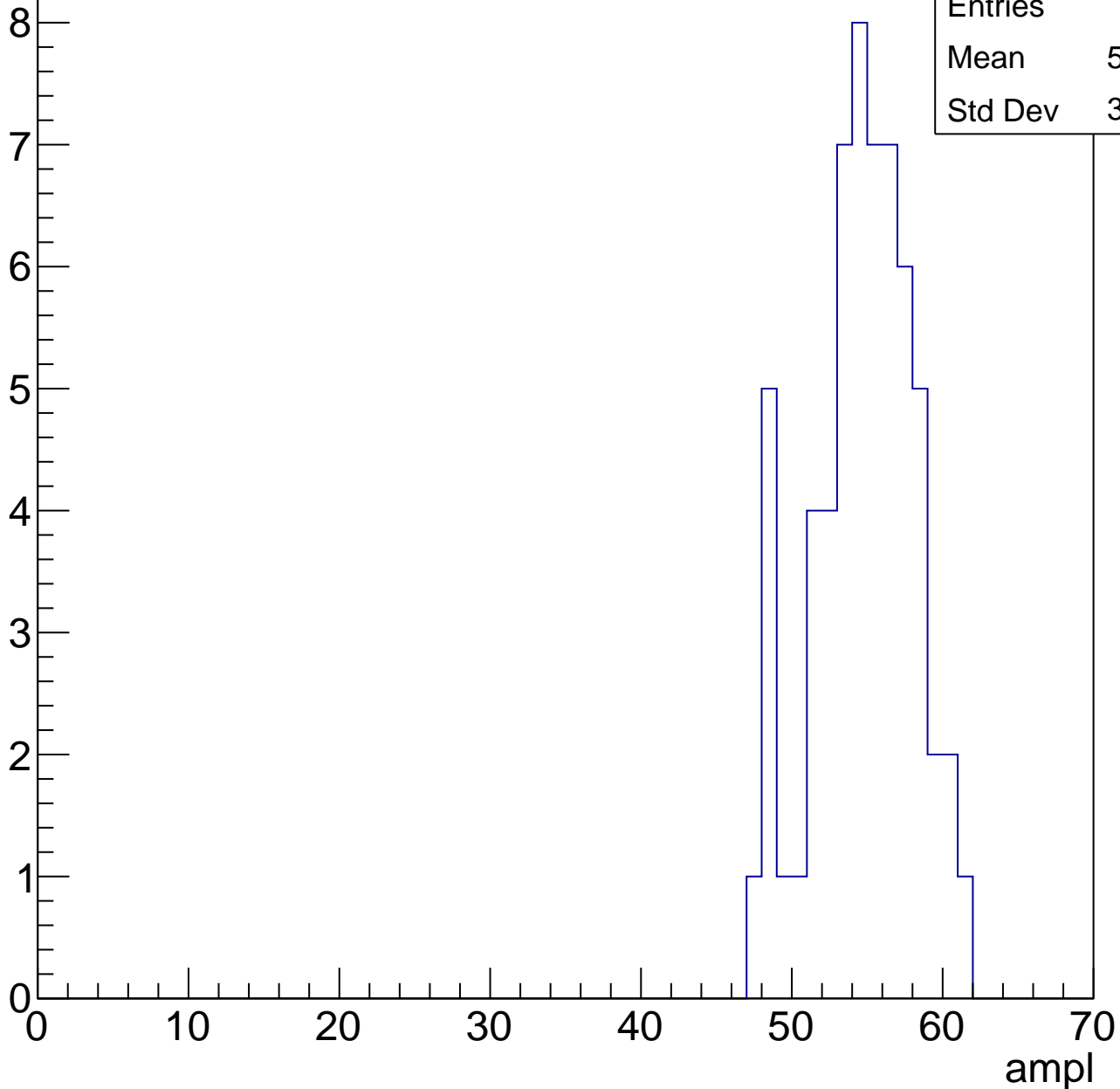


B1L100S, U19-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	54.25
Std Dev	3.303

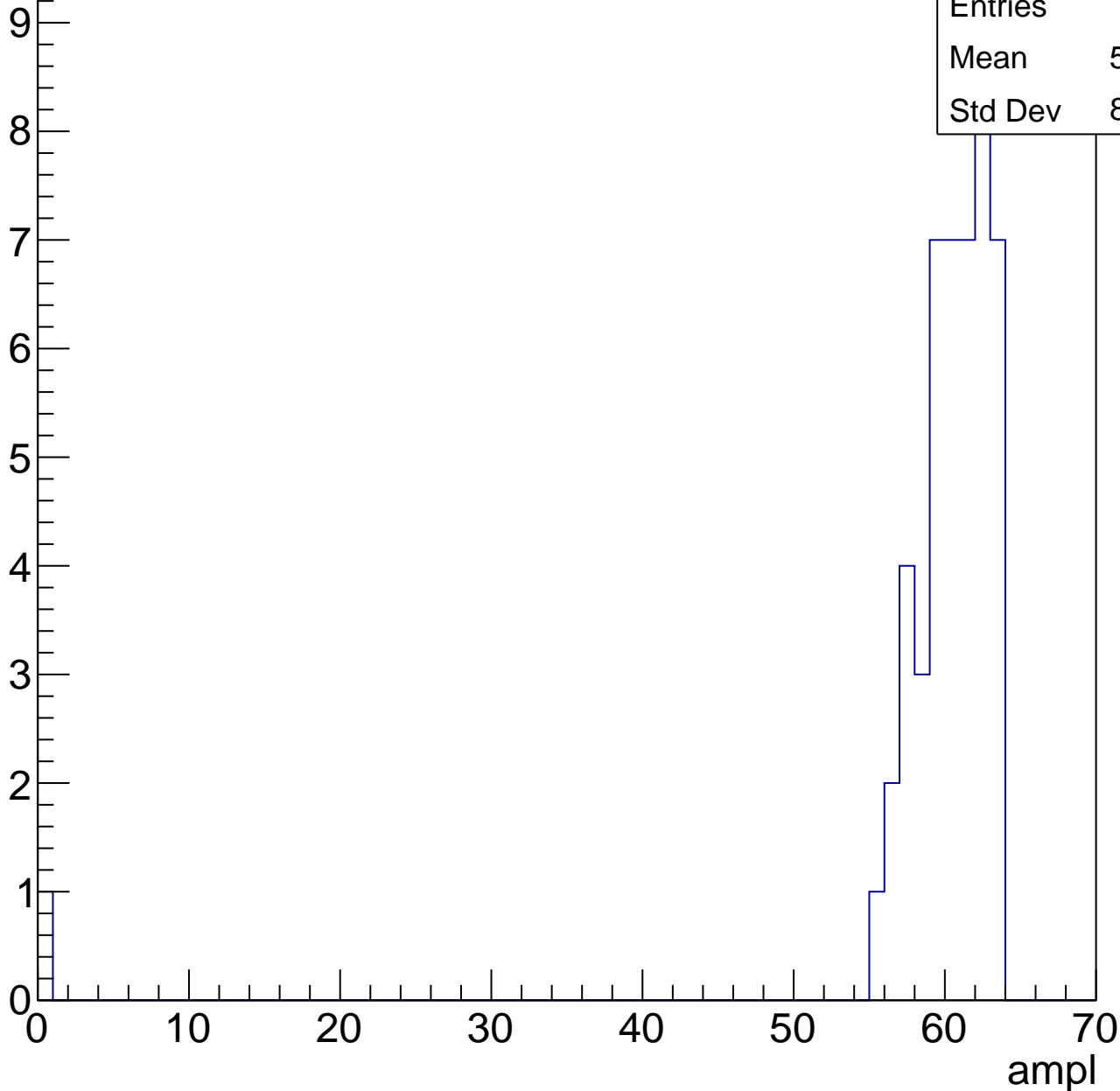


B1L100S, U19-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.92
Std Dev	8.853



B1L100S, U19-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	7
Mean	61.86
Std Dev	1.125

B1L100S, U19-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U19-ch67, adc0

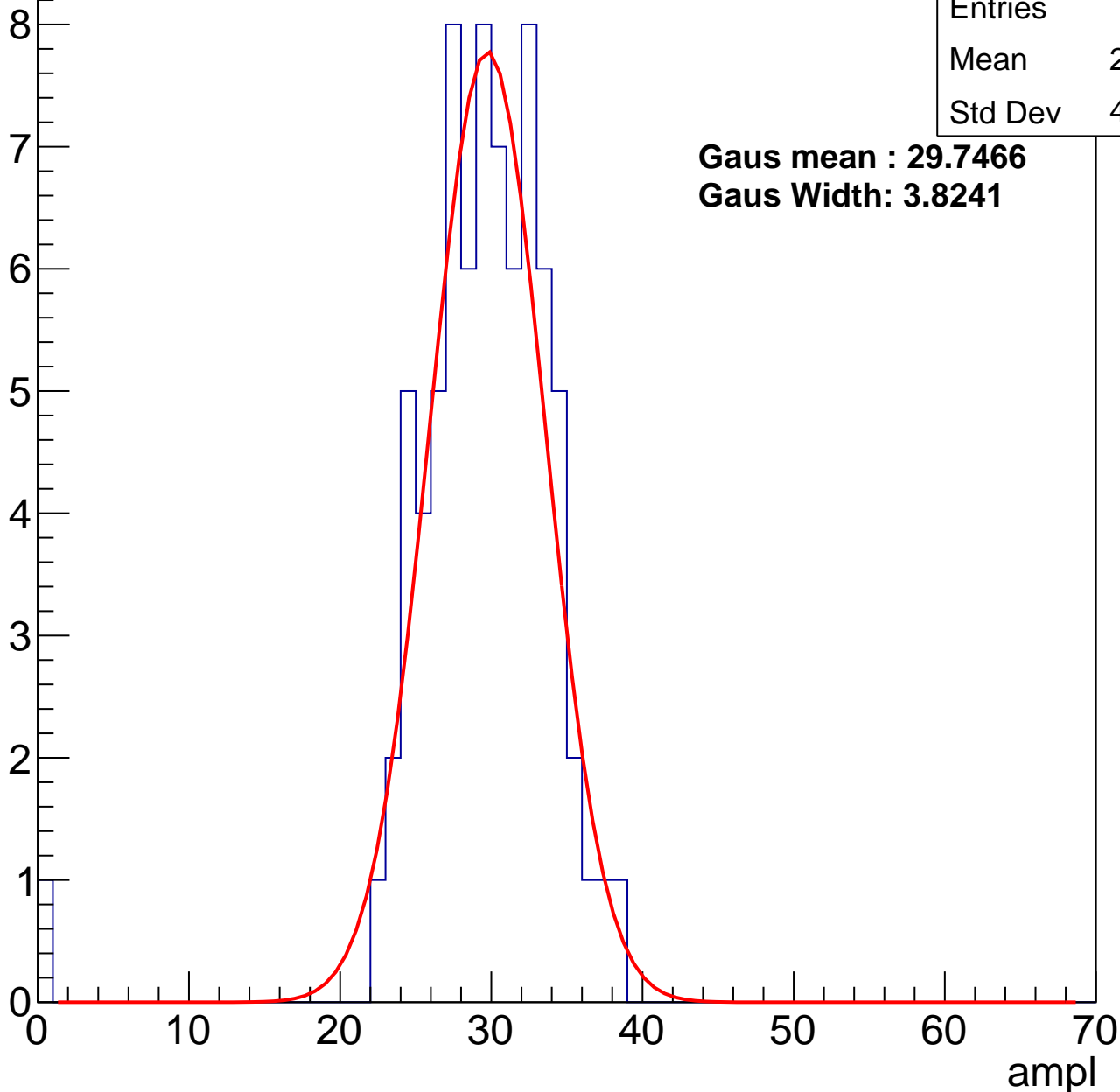
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.03
Std Dev	4.867

Gaus mean : 29.7466

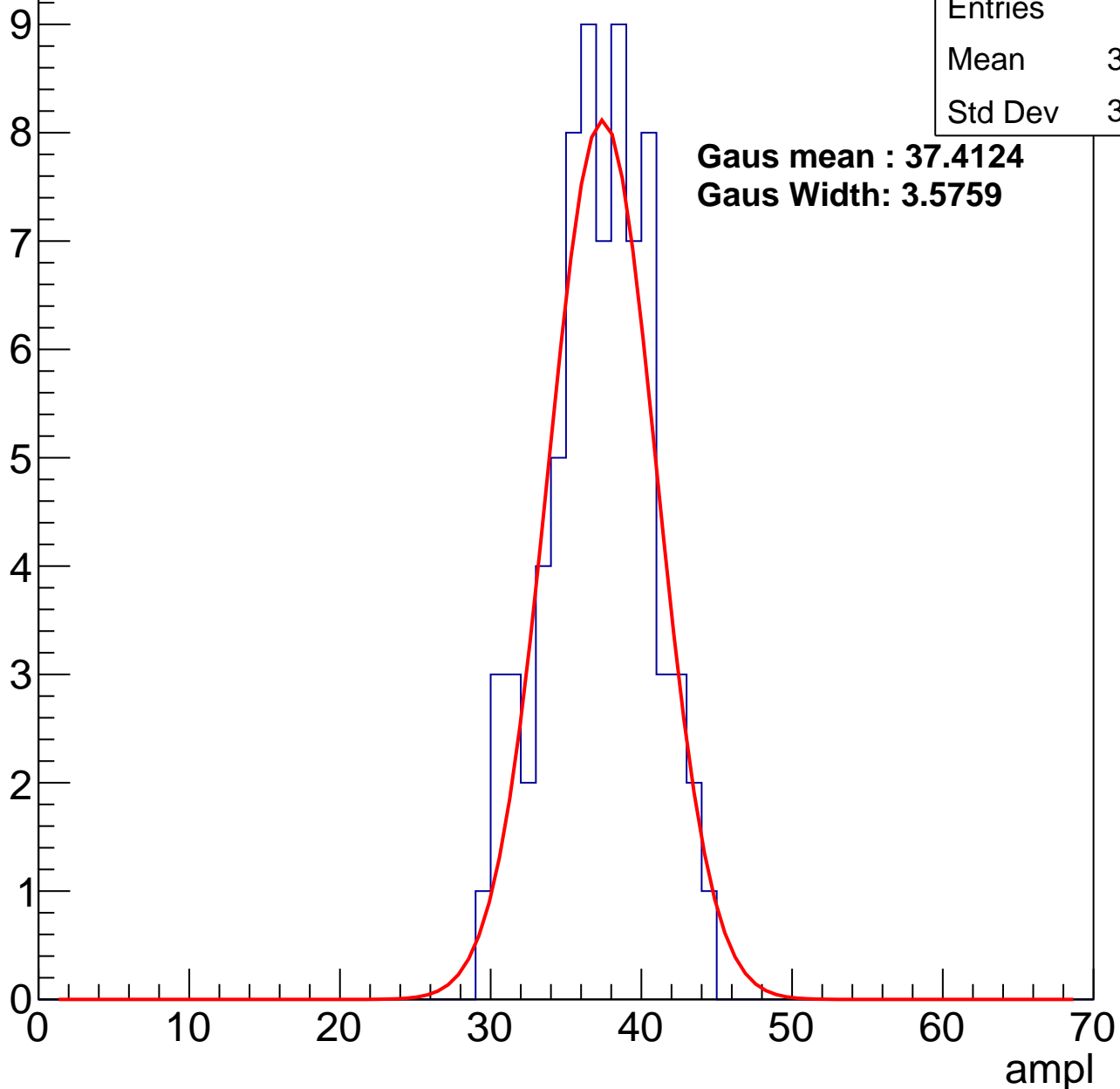
Gaus Width: 3.8241



B1L100S, U19-ch67, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	75
Mean	36.73
Std Dev	3.399

B1L100S, U19-ch67, adc2

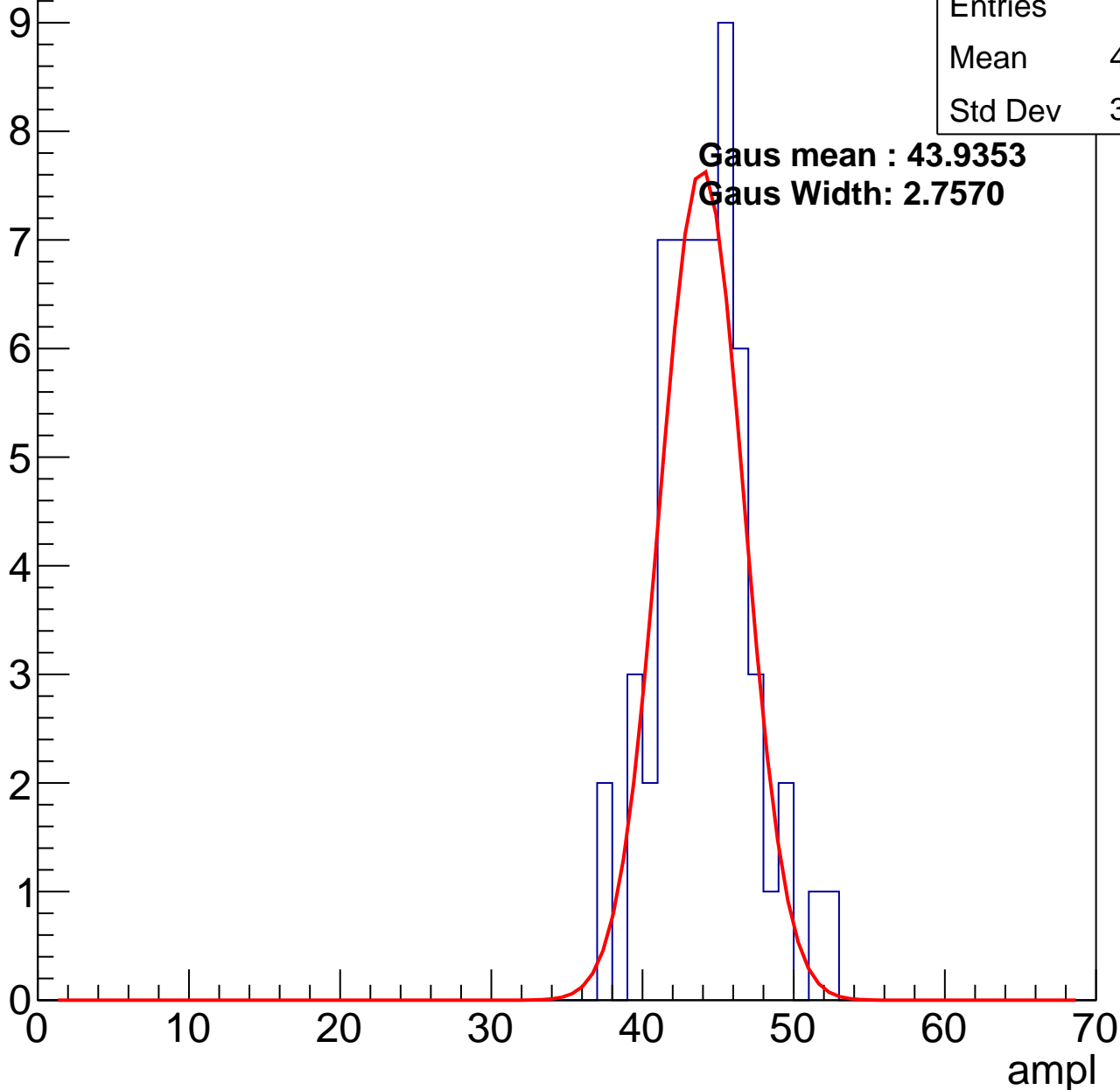
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.66
Std Dev	3.037

Gaus mean : 43.9353

Gaus Width: 2.7570

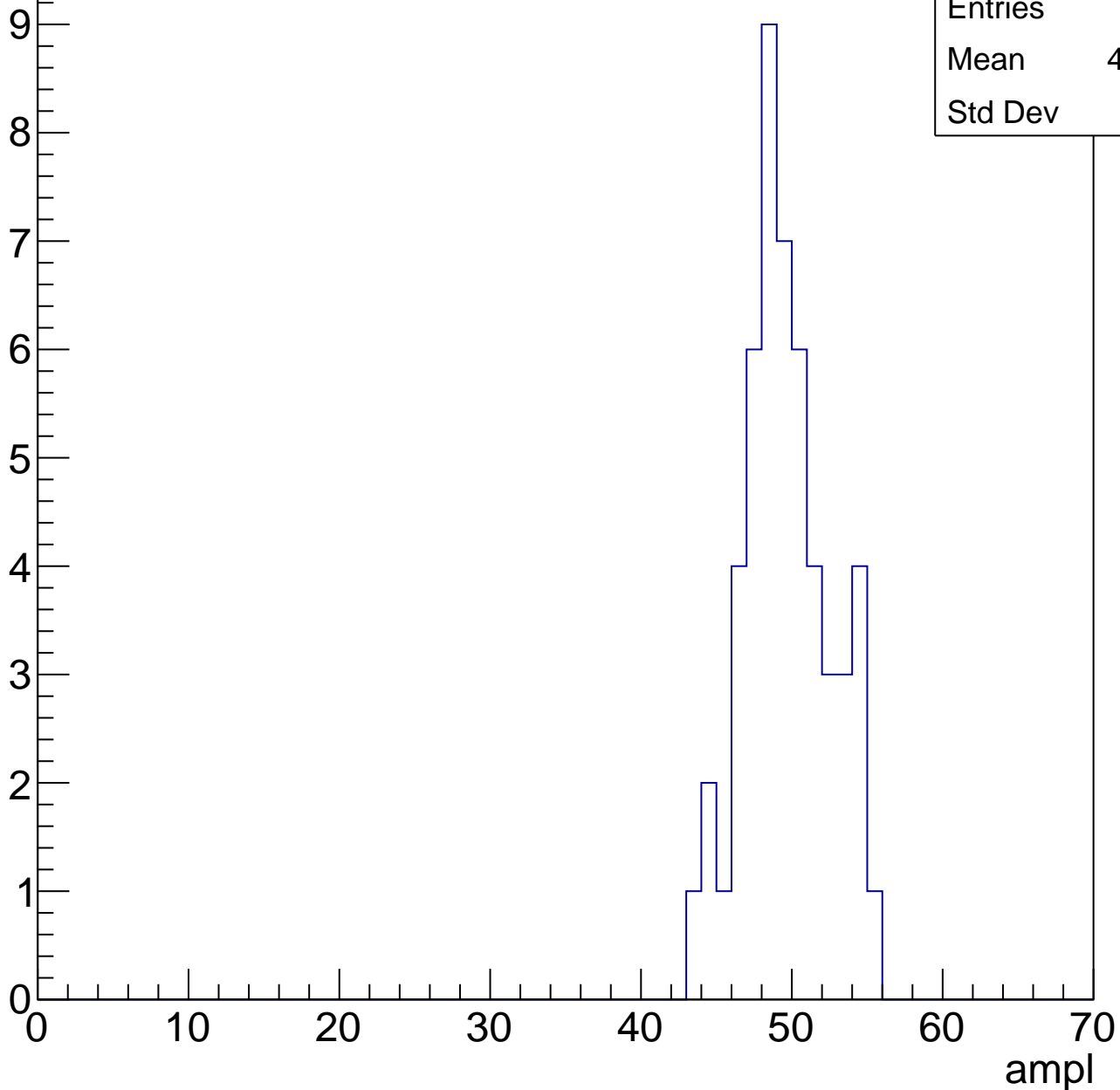


B1L100S, U19-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	49.16
Std Dev	2.81

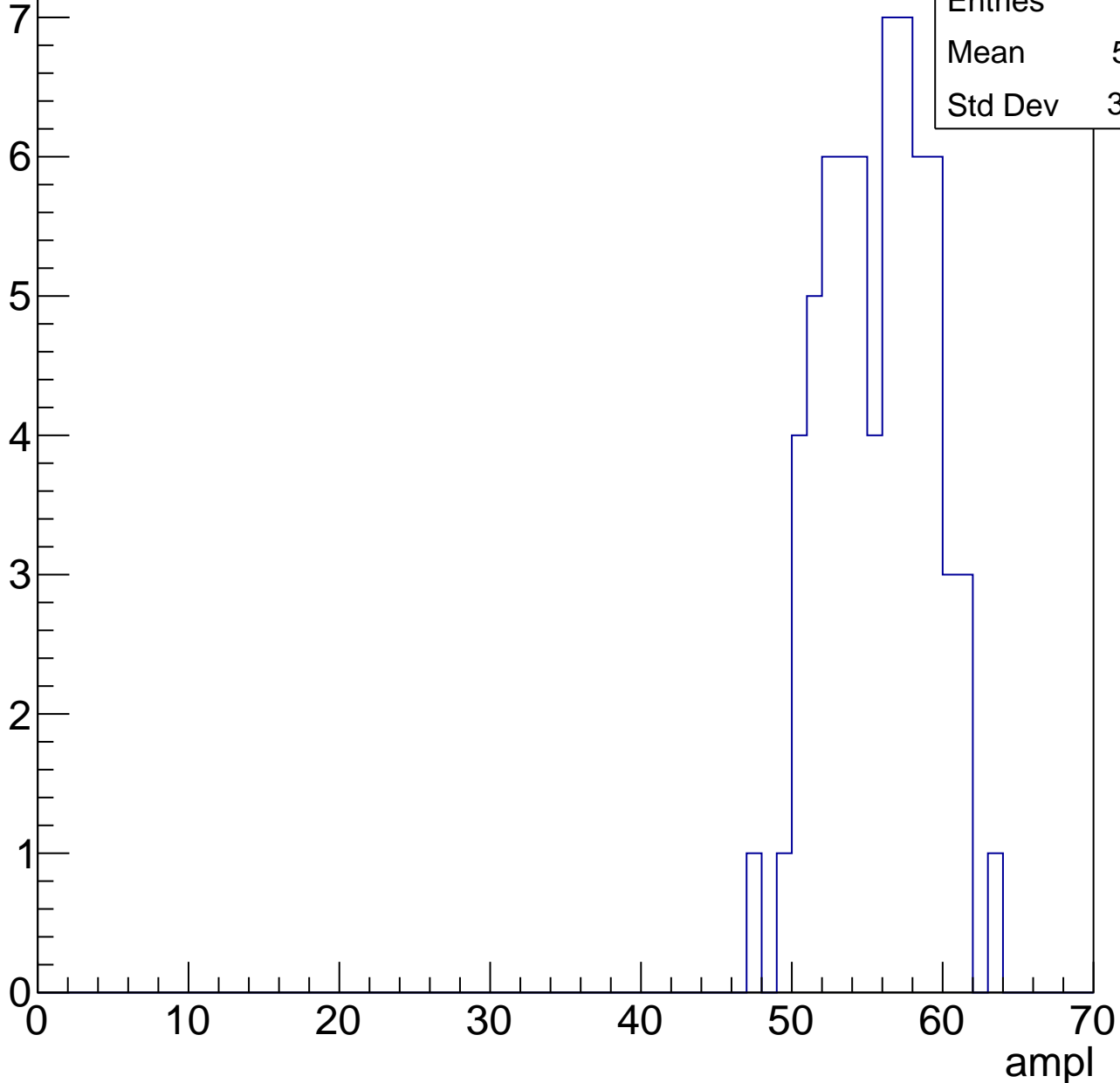


B1L100S, U19-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

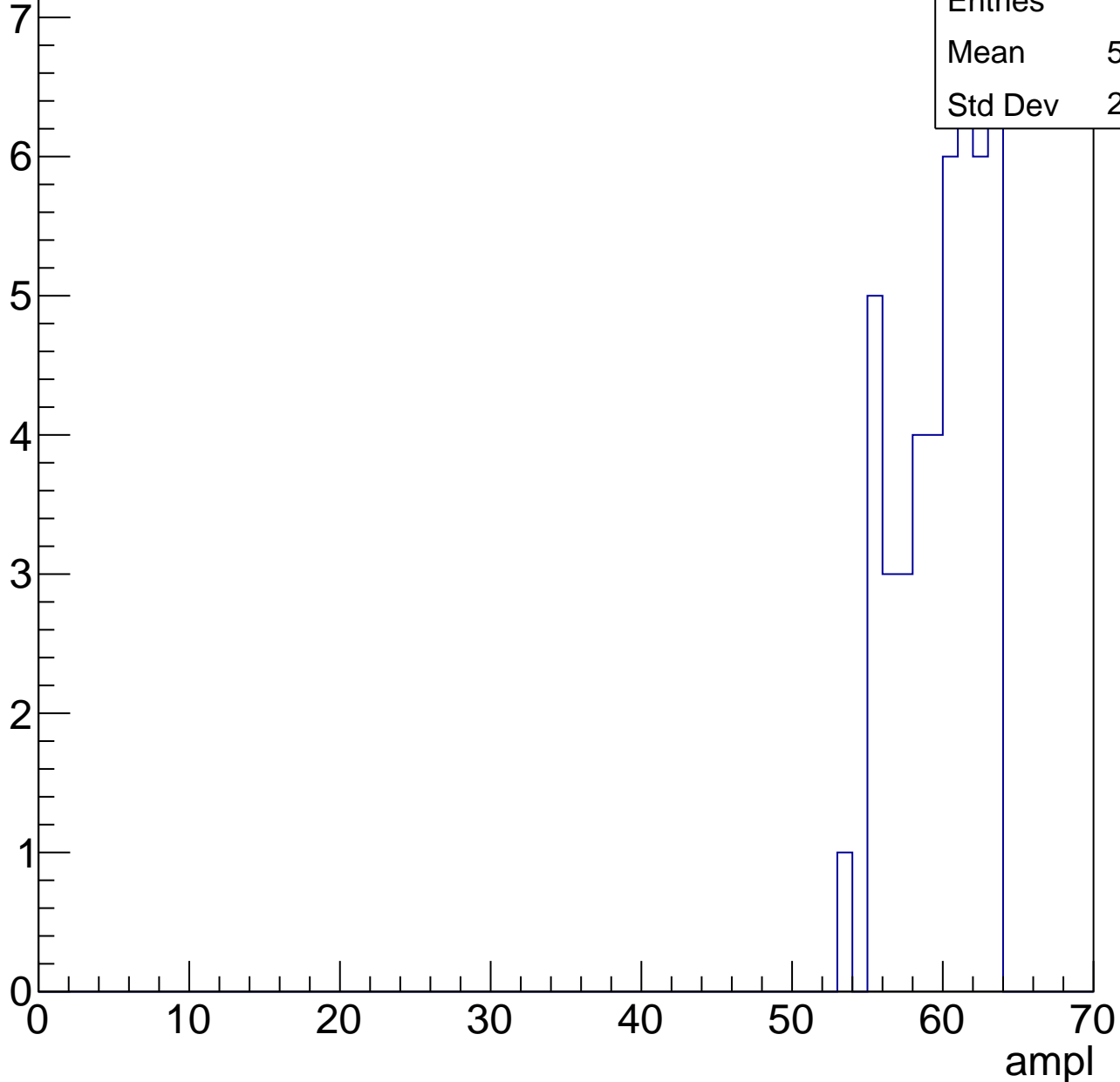
Entries	66
Mean	55.21
Std Dev	3.466



B1L100S, U19-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

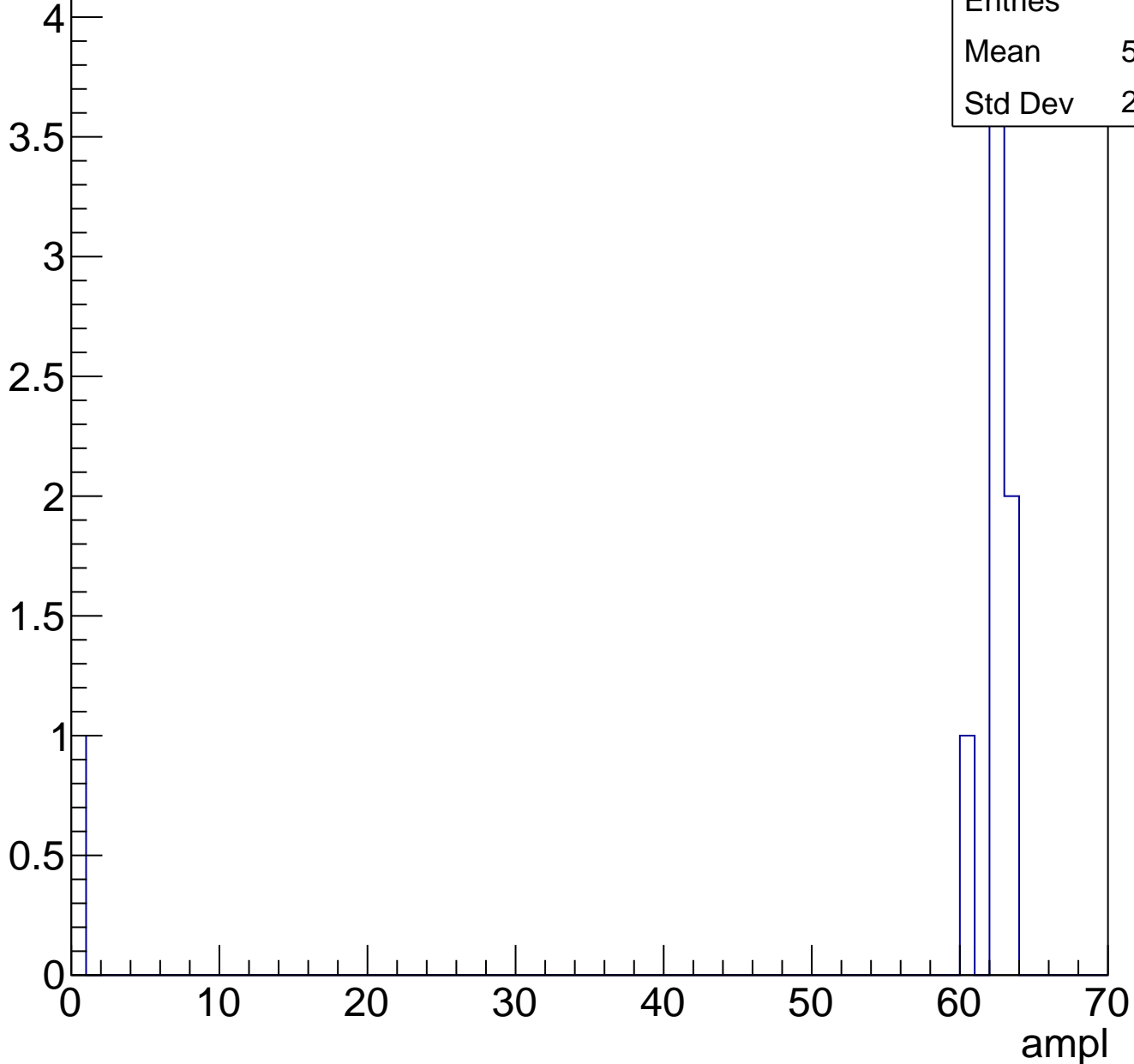
Entry



B1L100S, U19-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch68, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	28.83
Std Dev	6.704

Gaus mean : 30.8150

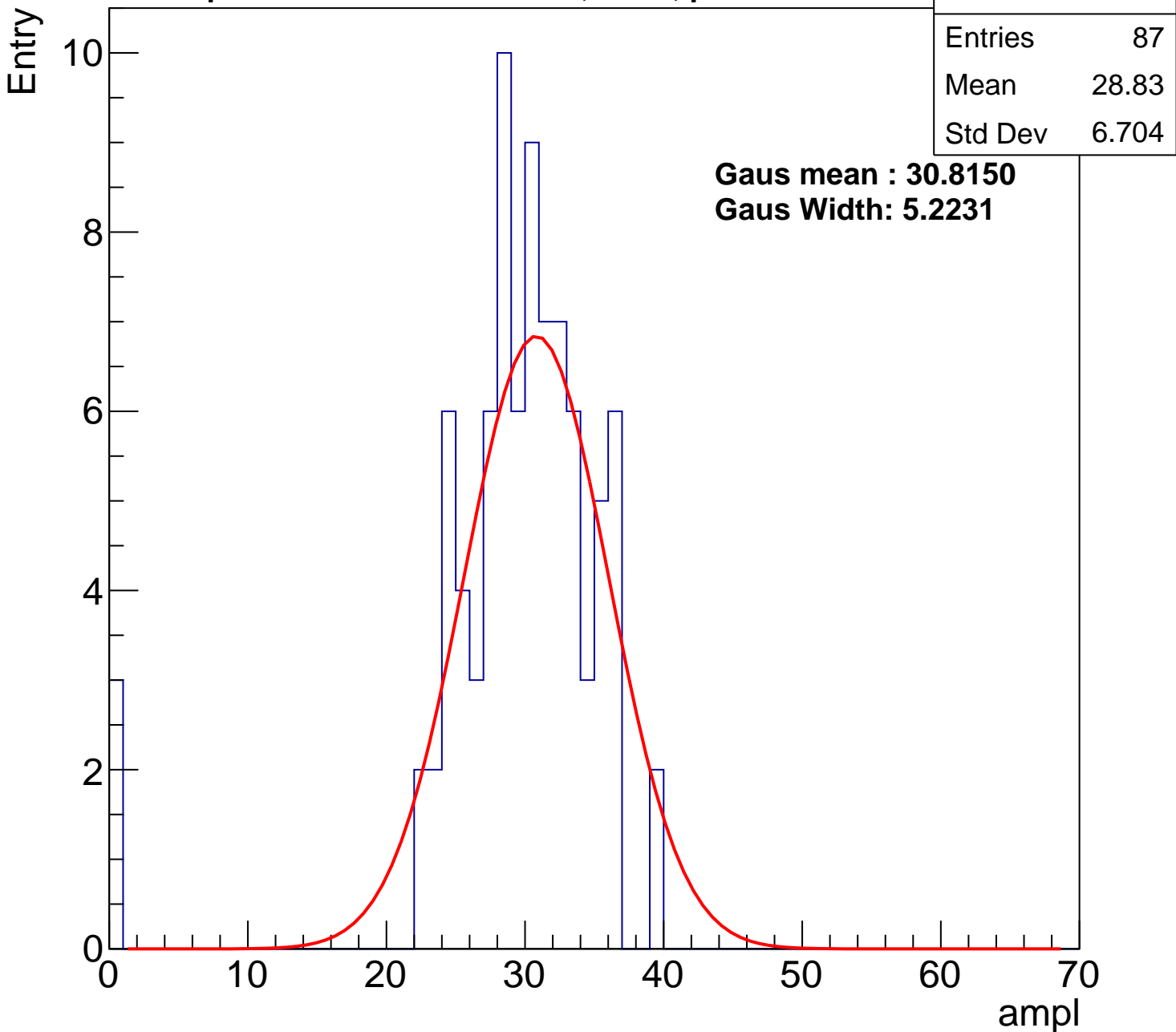
Gaus Width: 5.2231

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch68, adc1

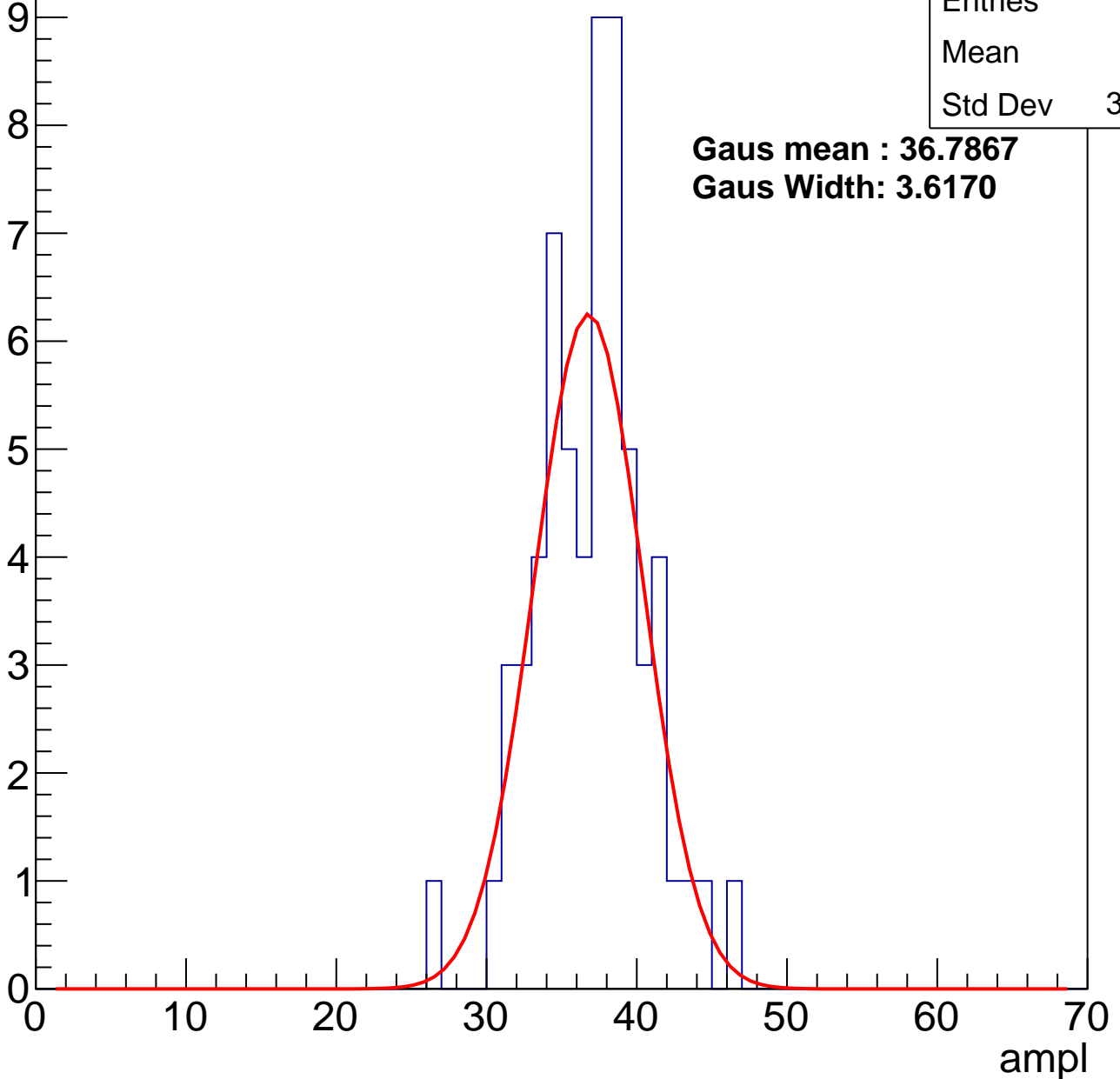
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.5
Std Dev	3.591

Gaus mean : 36.7867

Gaus Width: 3.6170



B1L100S, U19-ch68, adc2

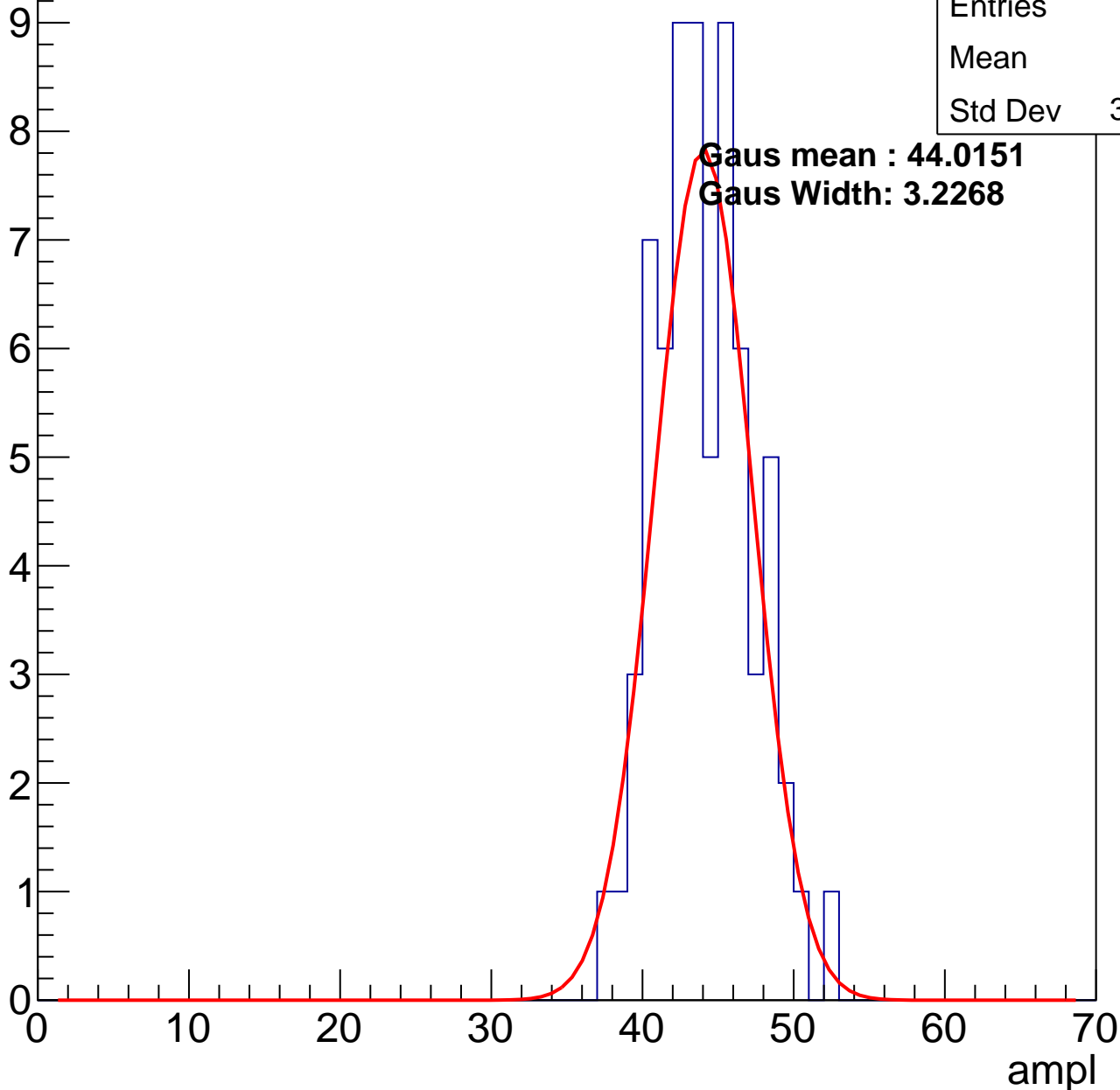
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.6
Std Dev	3.092

Gaus mean : 44.0151

Gaus Width: 3.2268

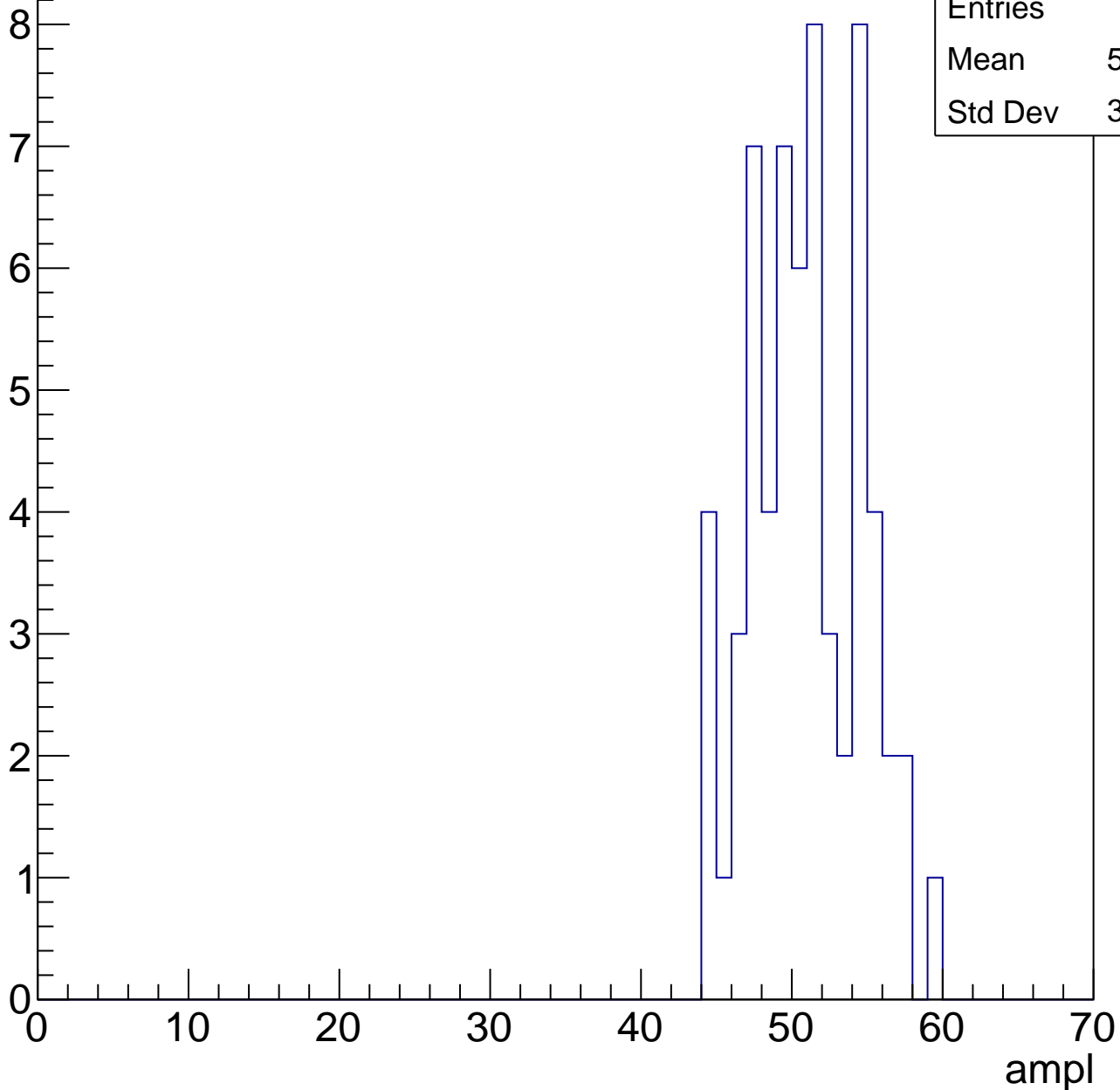


B1L100S, U19-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.48
Std Dev	3.604

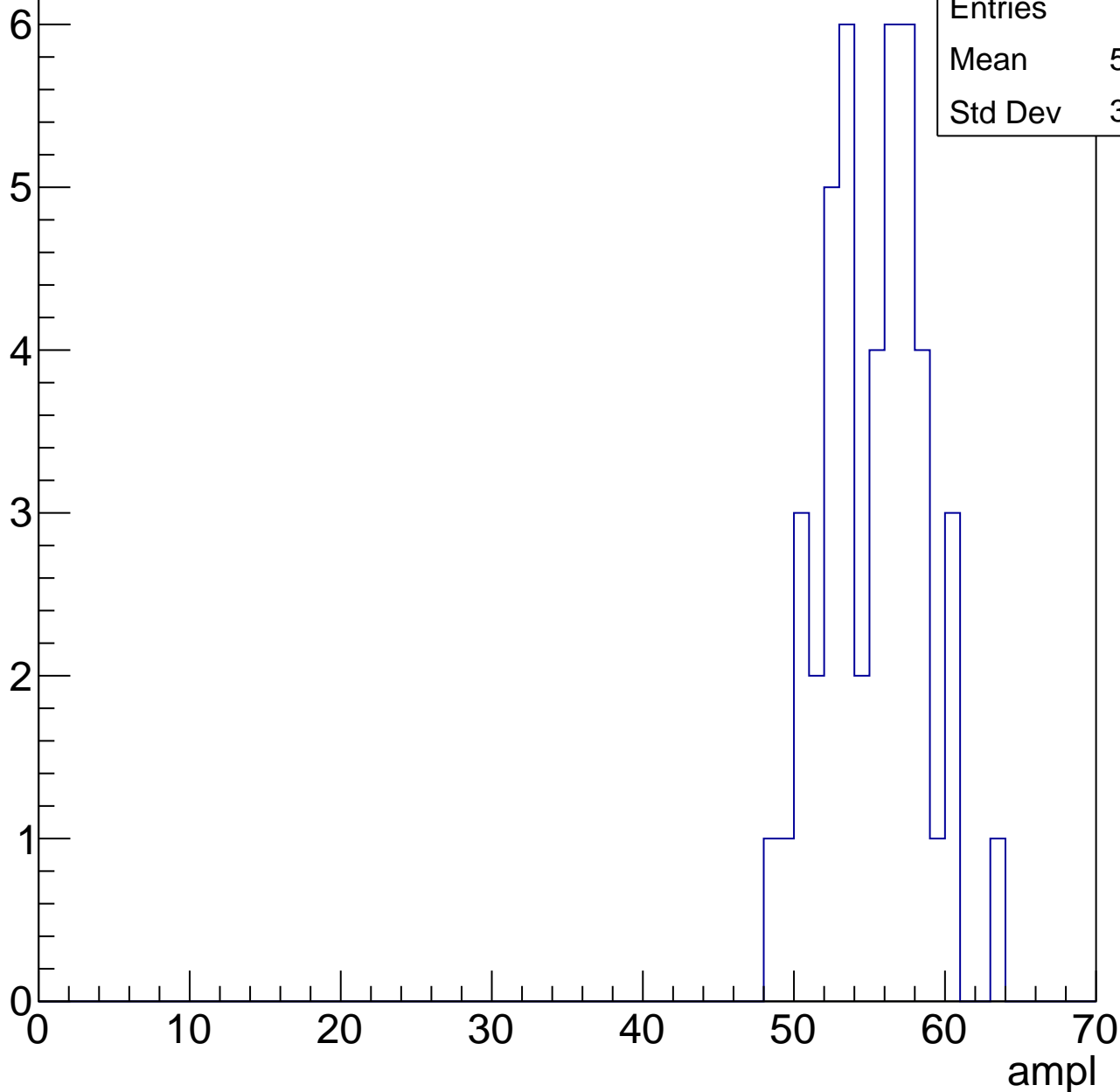


B1L100S, U19-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	54.82
Std Dev	3.275

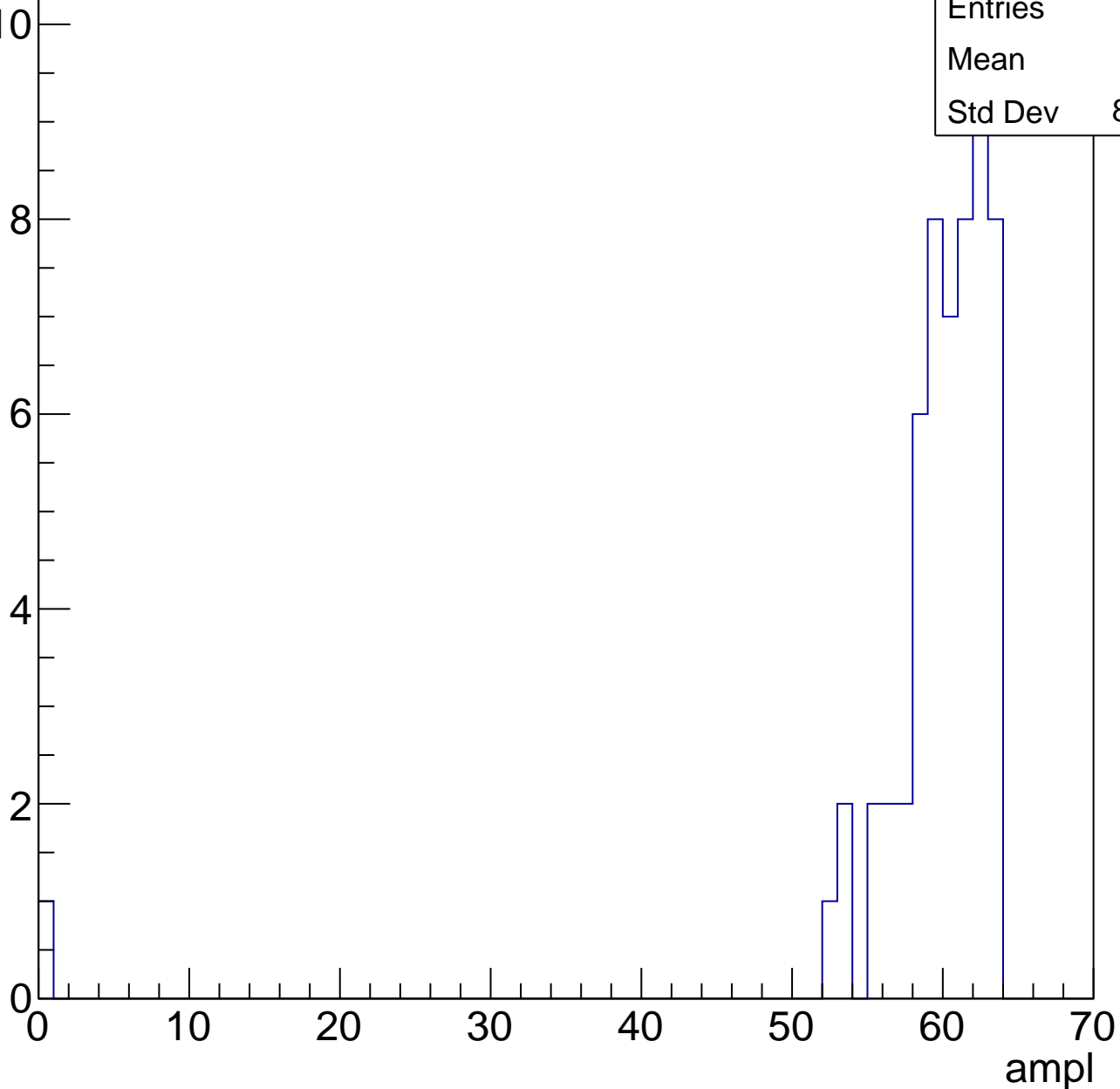


B1L100S, U19-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

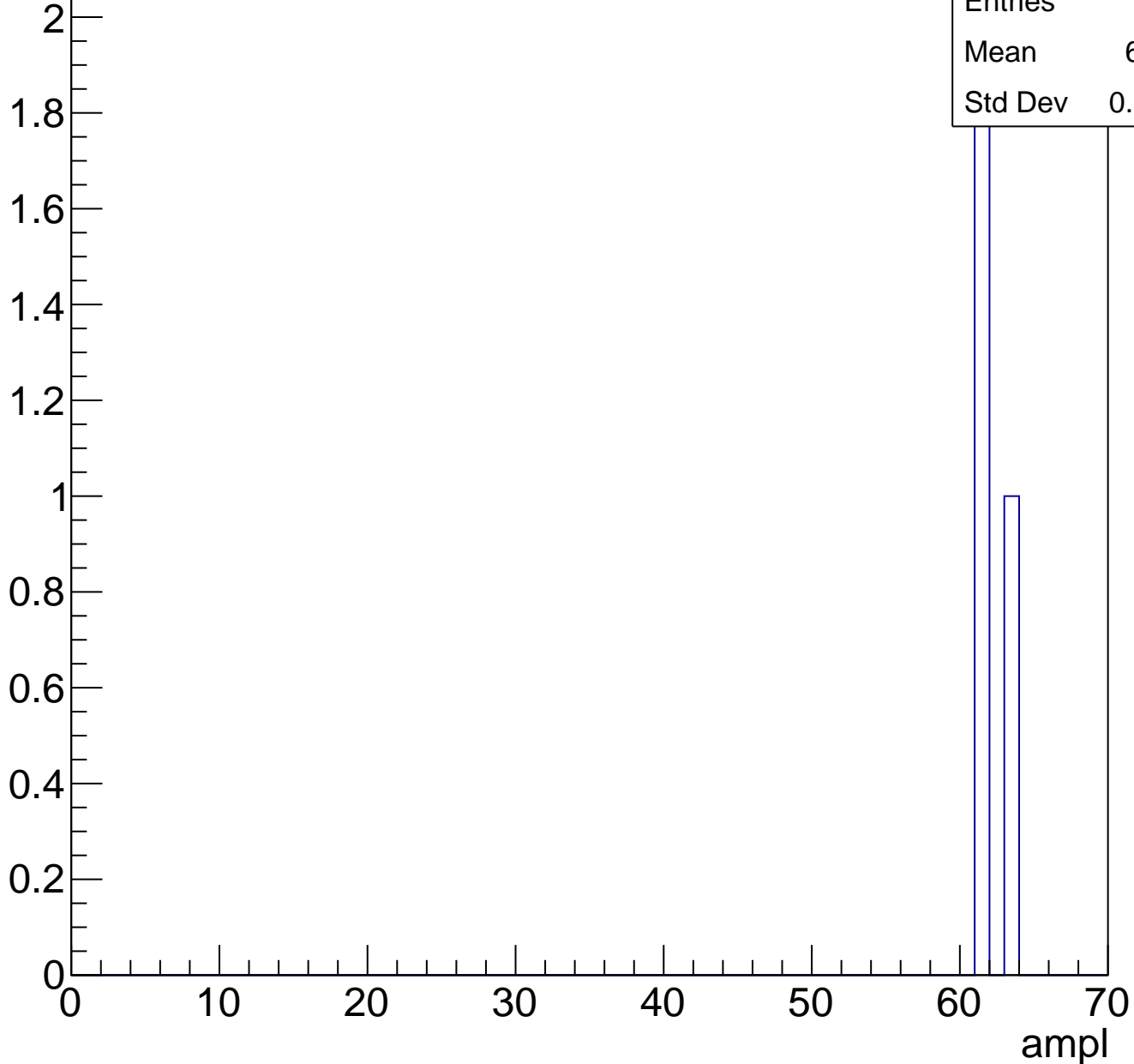
Entries	57
Mean	58.7
Std Dev	8.291



B1L100S, U19-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch69, adc0

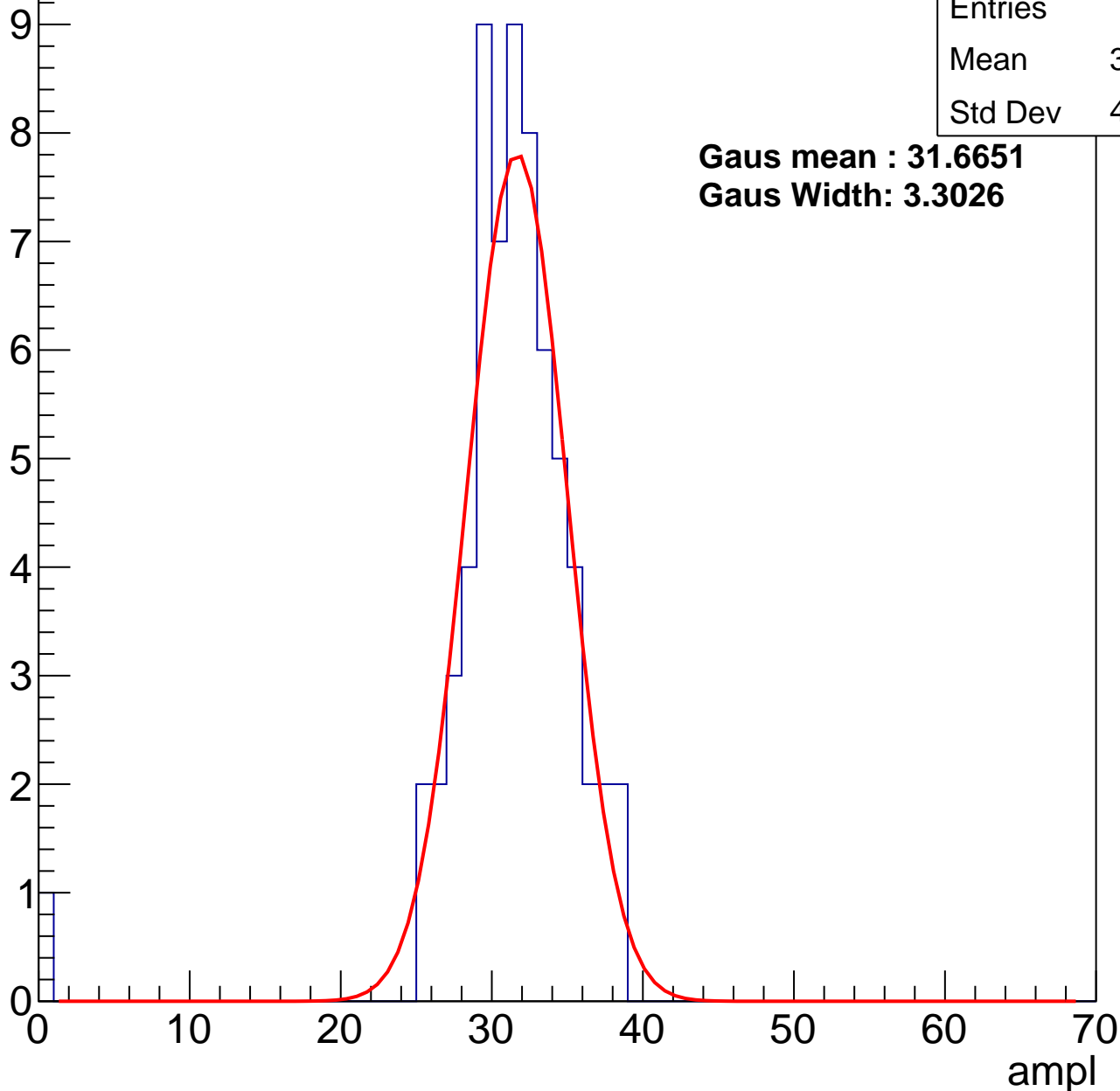
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.77
Std Dev	4.874

Gaus mean : 31.6651

Gaus Width: 3.3026



B1L100S, U19-ch69, adc1

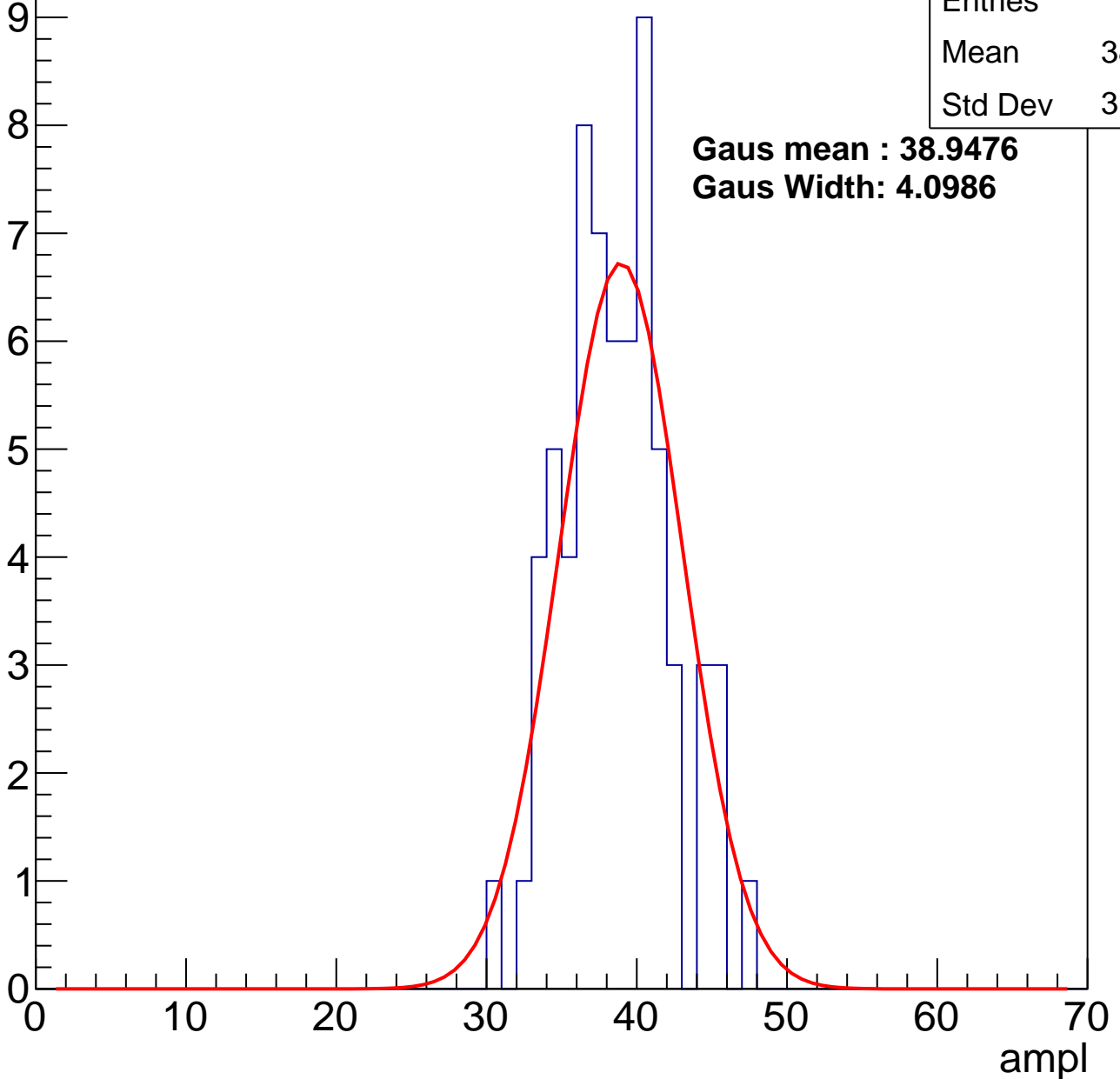
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	38.15
Std Dev	3.534

Gaus mean : 38.9476

Gaus Width: 4.0986



B1L100S, U19-ch69, adc2

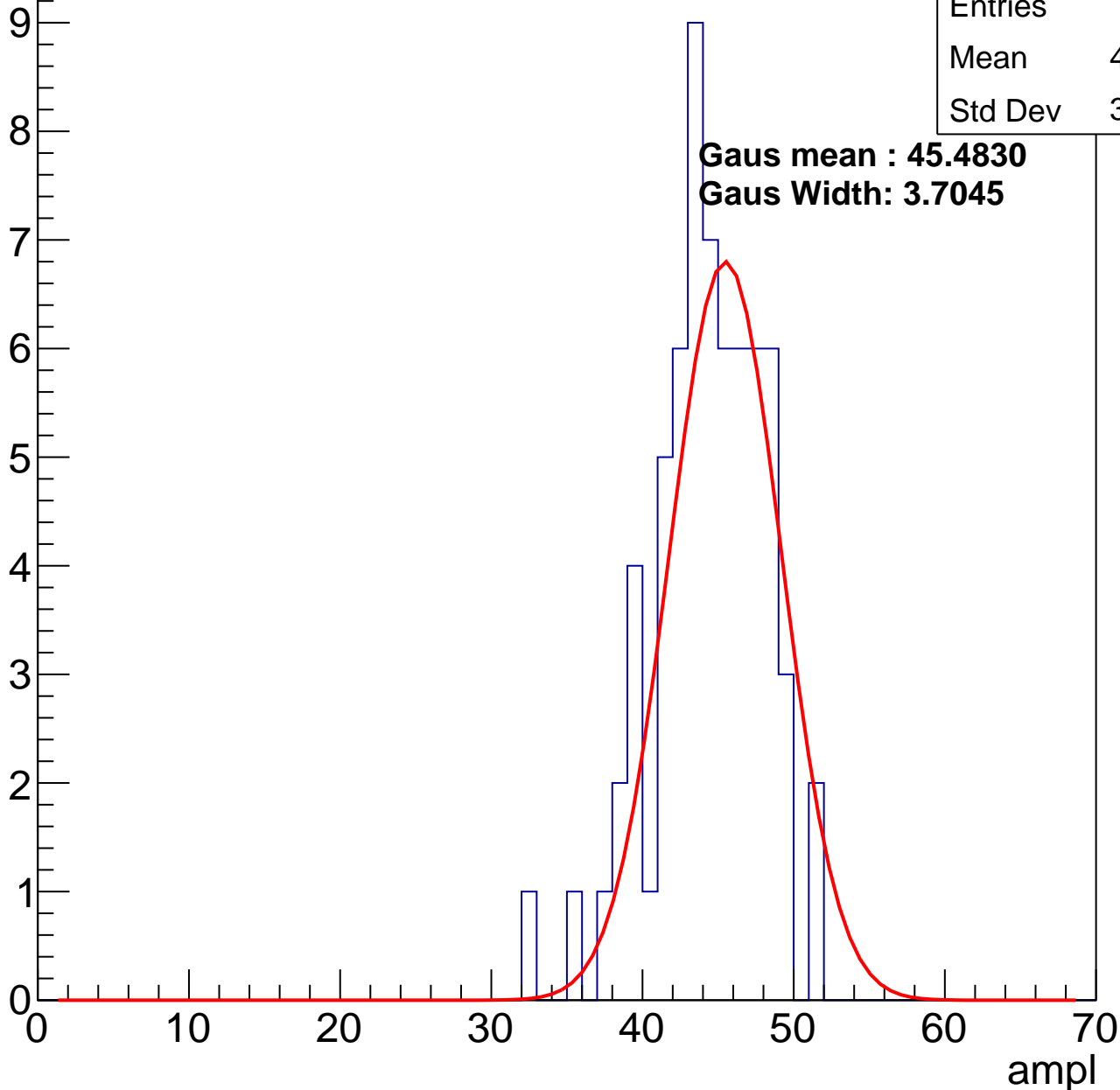
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.83
Std Dev	3.675

Gaus mean : 45.4830

Gaus Width: 3.7045

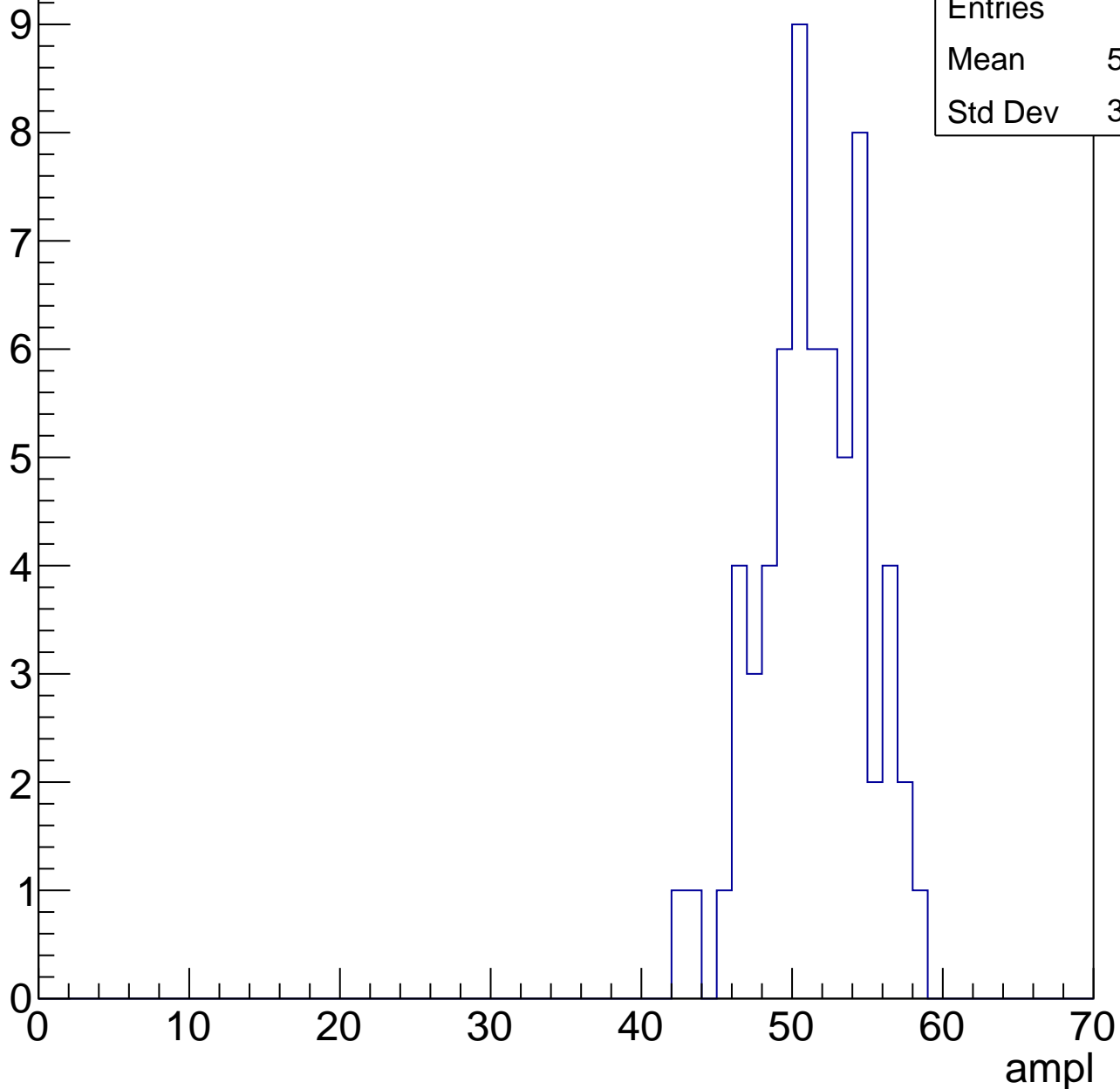


B1L100S, U19-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

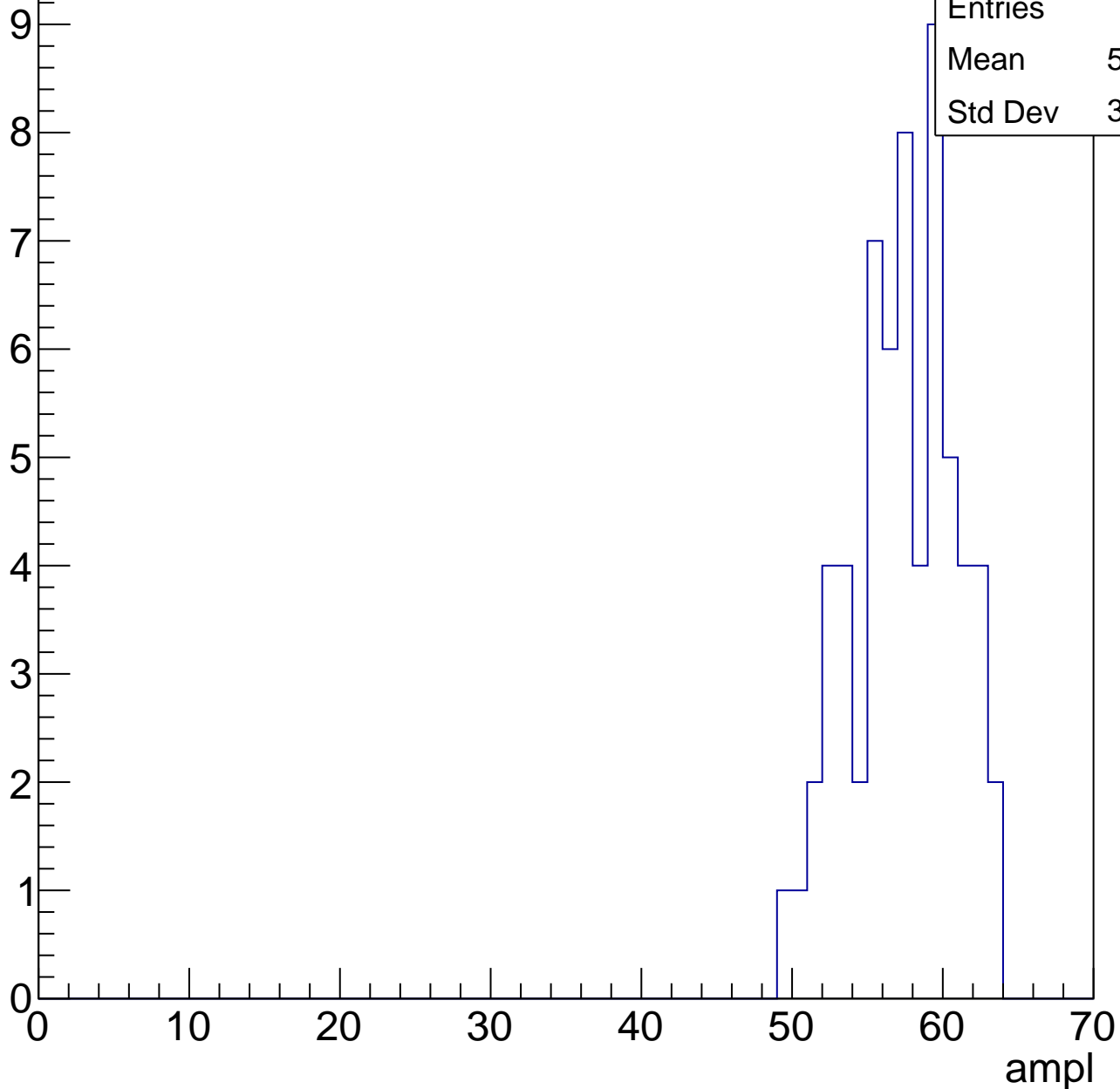
Entries	63
Mean	50.98
Std Dev	3.448



B1L100S, U19-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

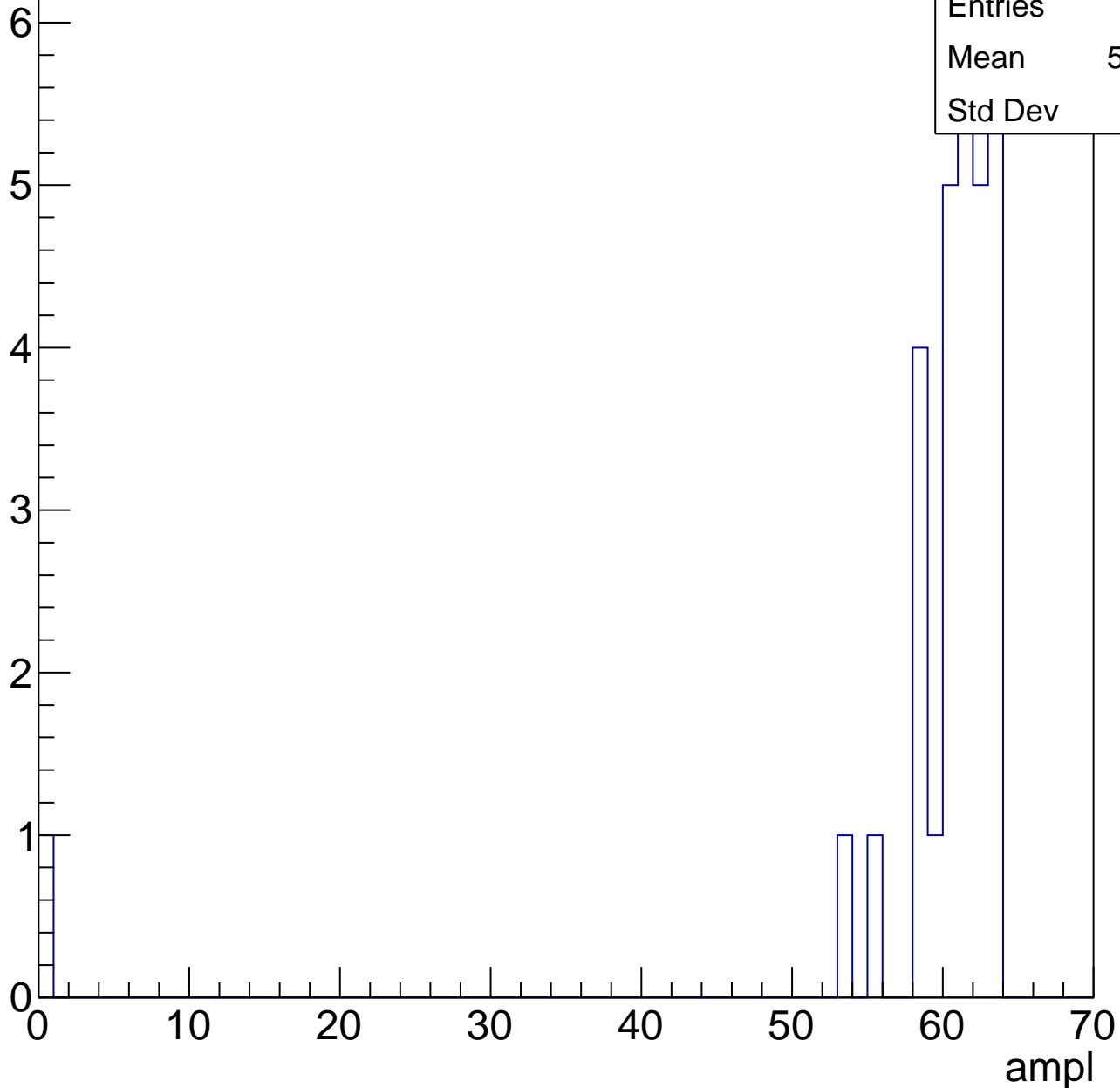


B1L100S, U19-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.43
Std Dev	11.1



B1L100S, U19-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch70, adc0

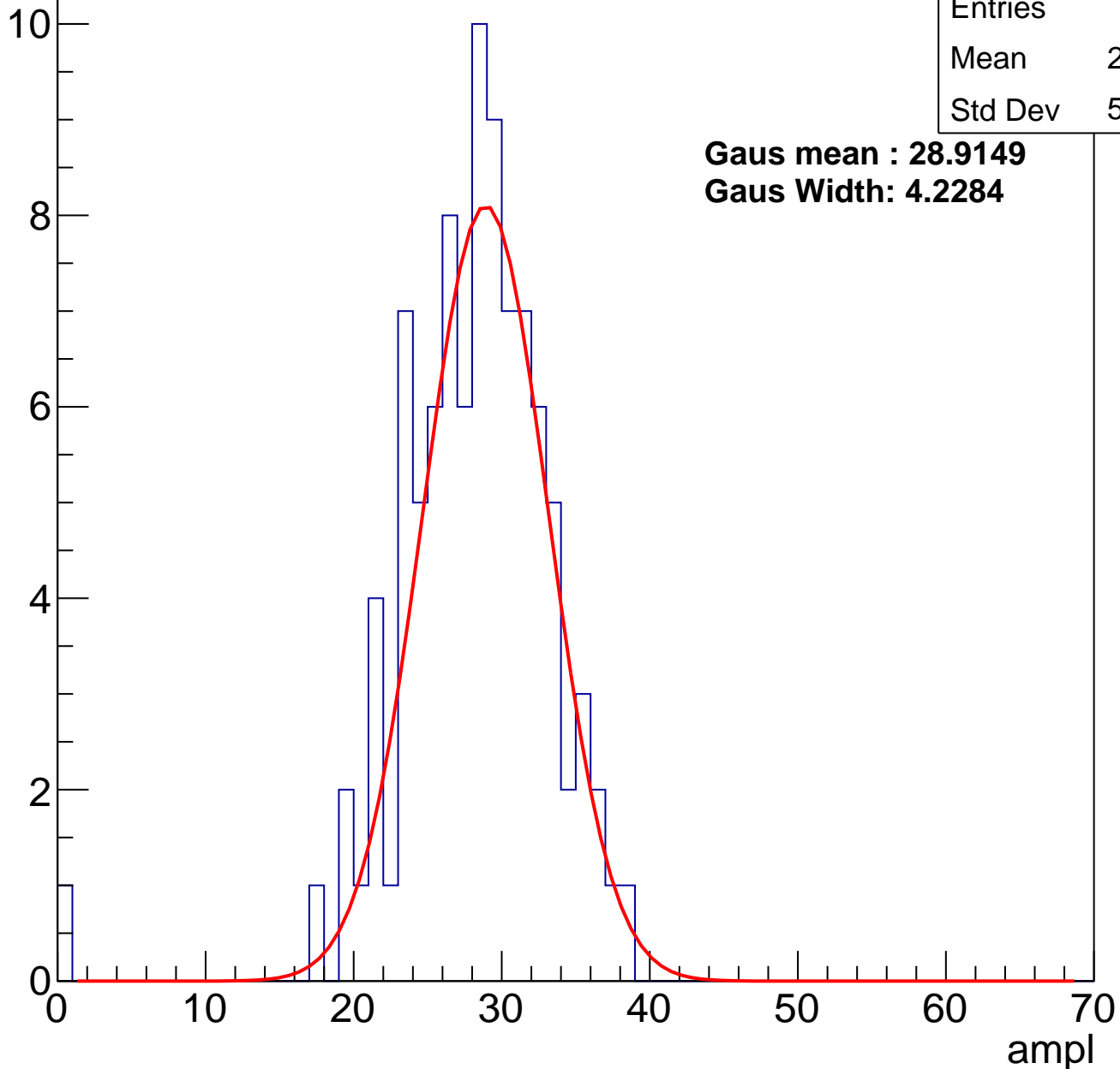
calib_packv5_042523_0143.root, FC#4, port A2

Entries	95
Mean	27.65
Std Dev	5.178

Gaus mean : 28.9149

Gaus Width: 4.2284

Entry



B1L100S, U19-ch70, adc1

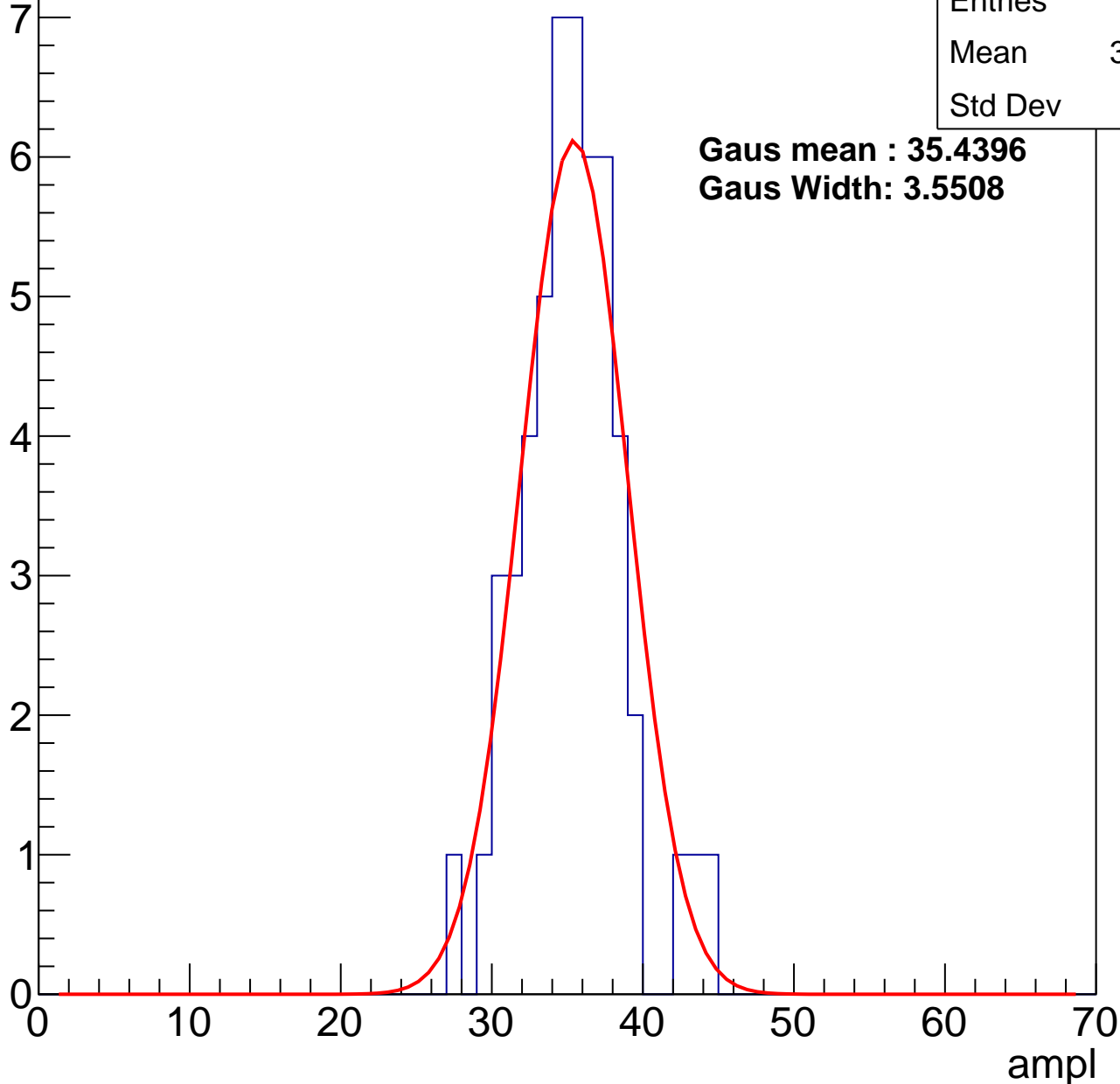
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	34.85
Std Dev	3.33

Gaus mean : 35.4396

Gaus Width: 3.5508



B1L100S, U19-ch70, adc2

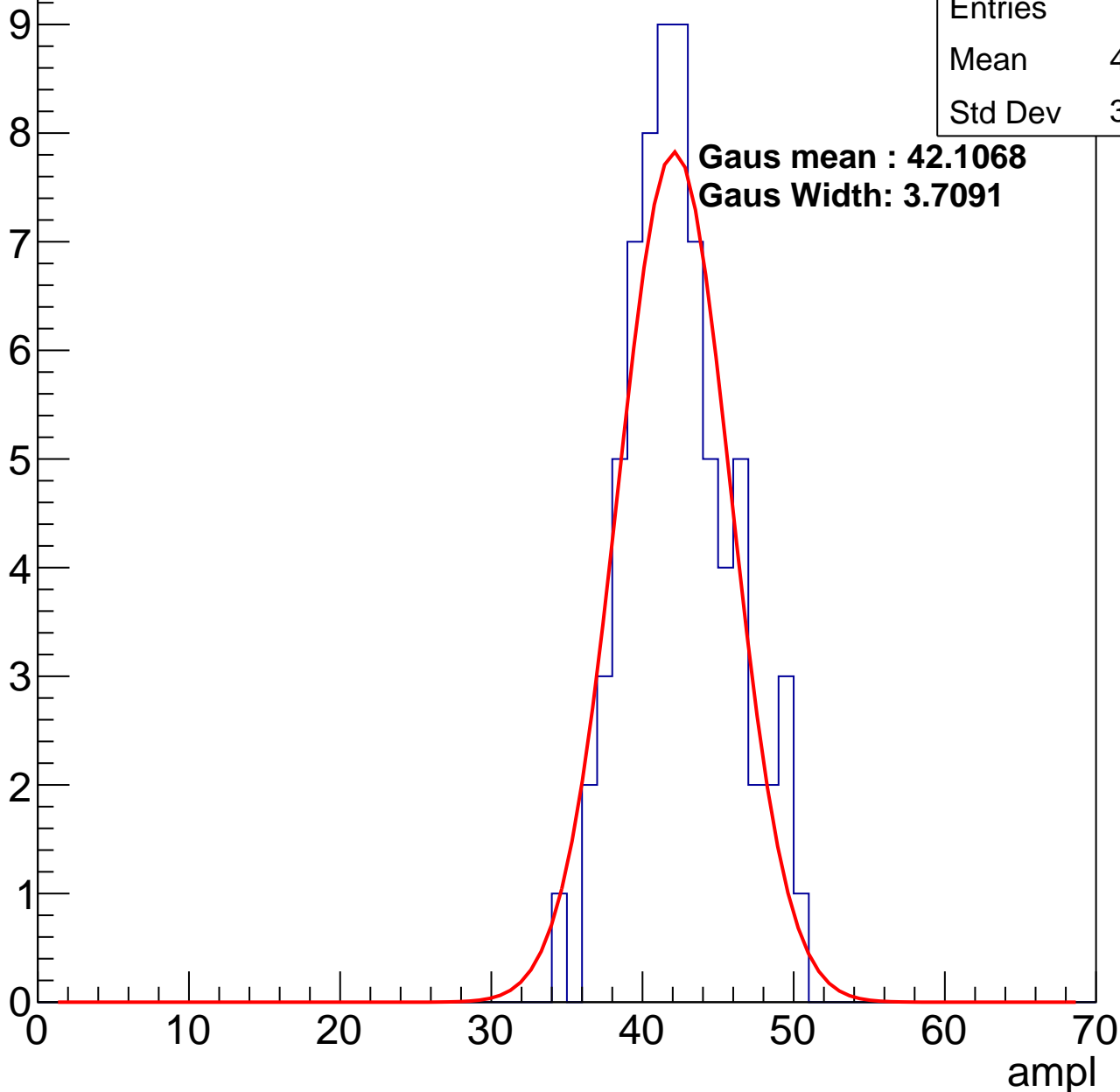
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	41.99
Std Dev	3.454

Gaus mean : 42.1068

Gaus Width: 3.7091

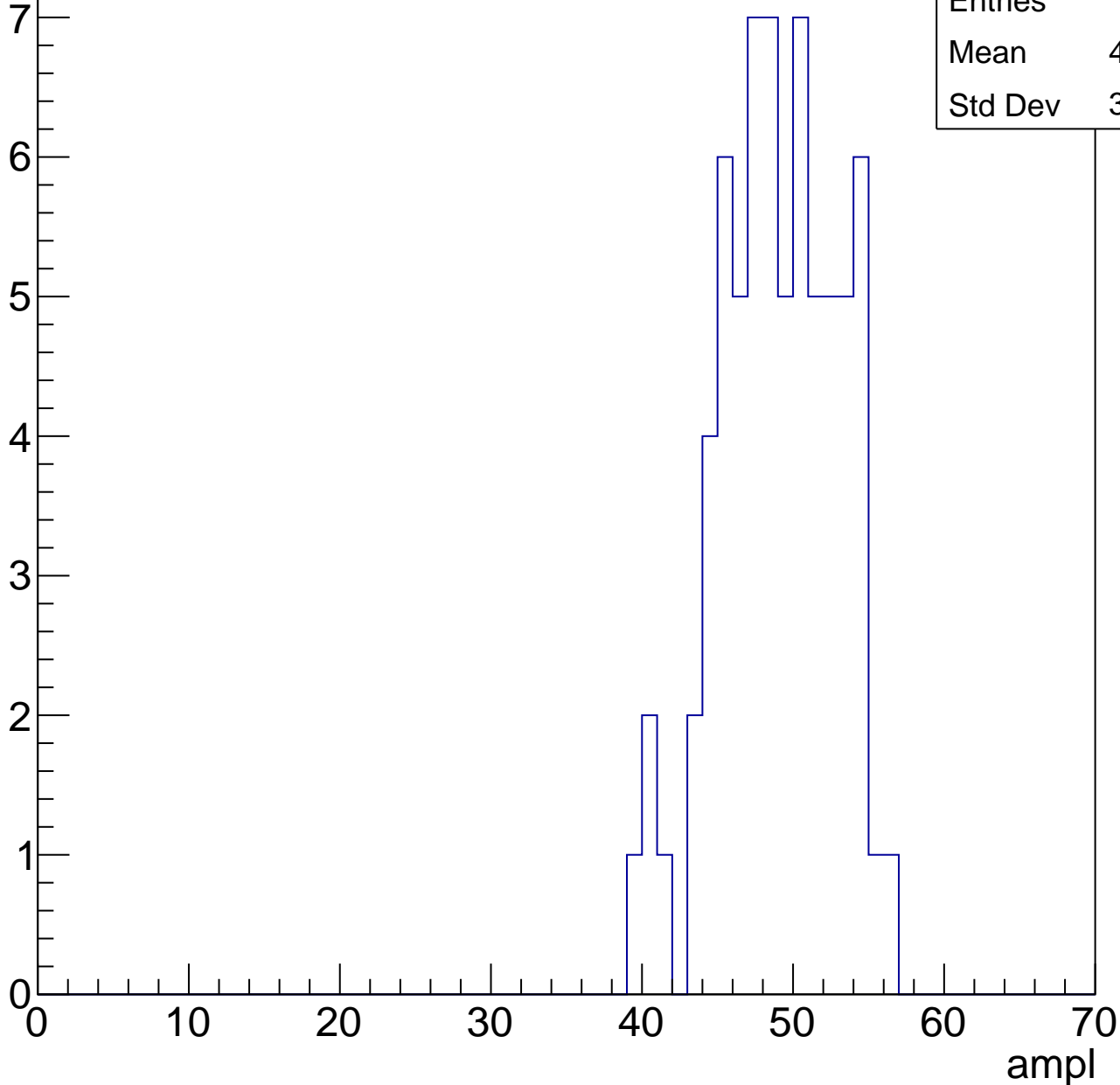


B1L100S, U19-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	48.53
Std Dev	3.865

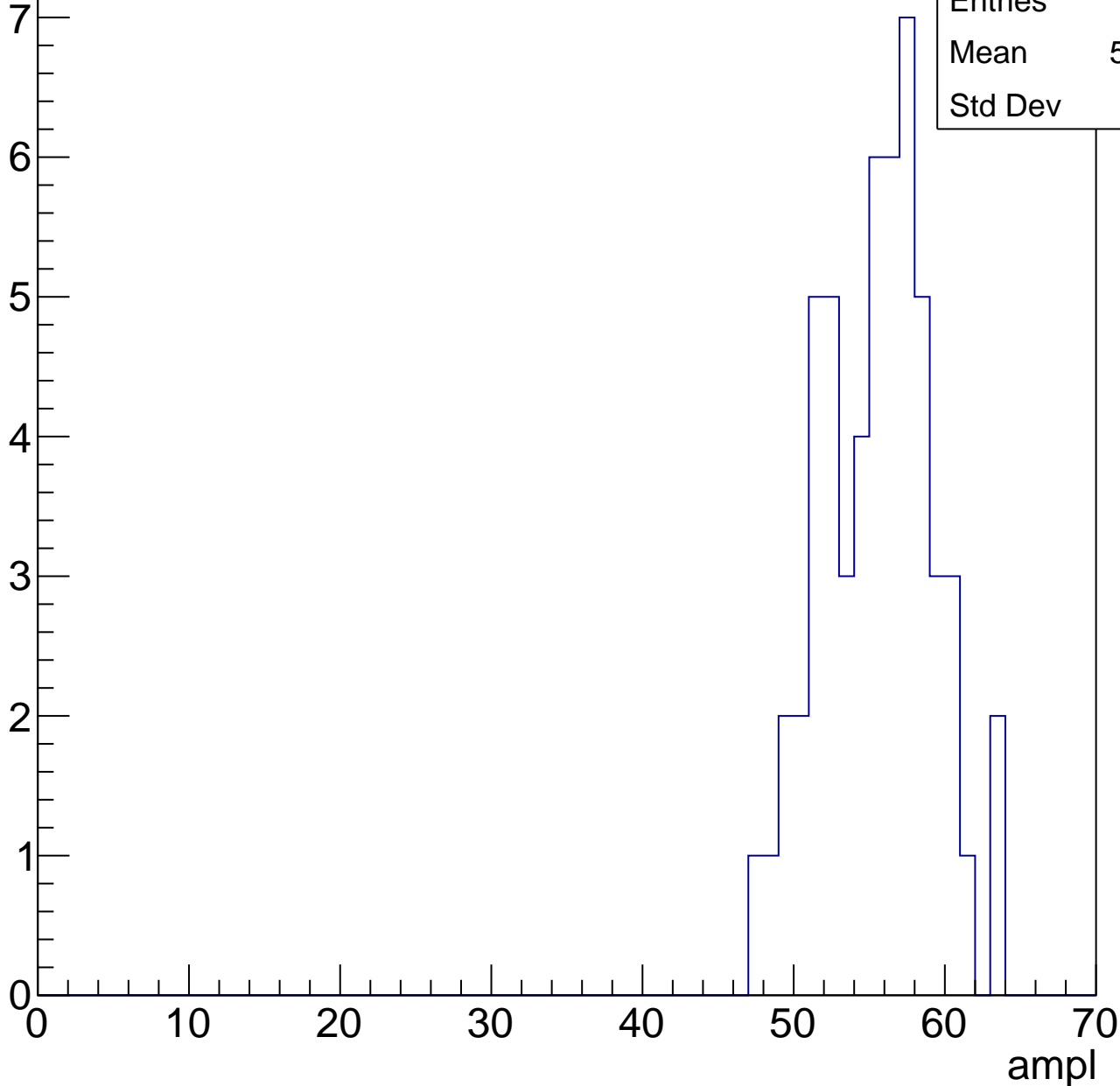


B1L100S, U19-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	55.04
Std Dev	3.64



B1L100S, U19-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	48
Mean	58.06
Std Dev	8.863

ampl

0

10

20

30

40

50

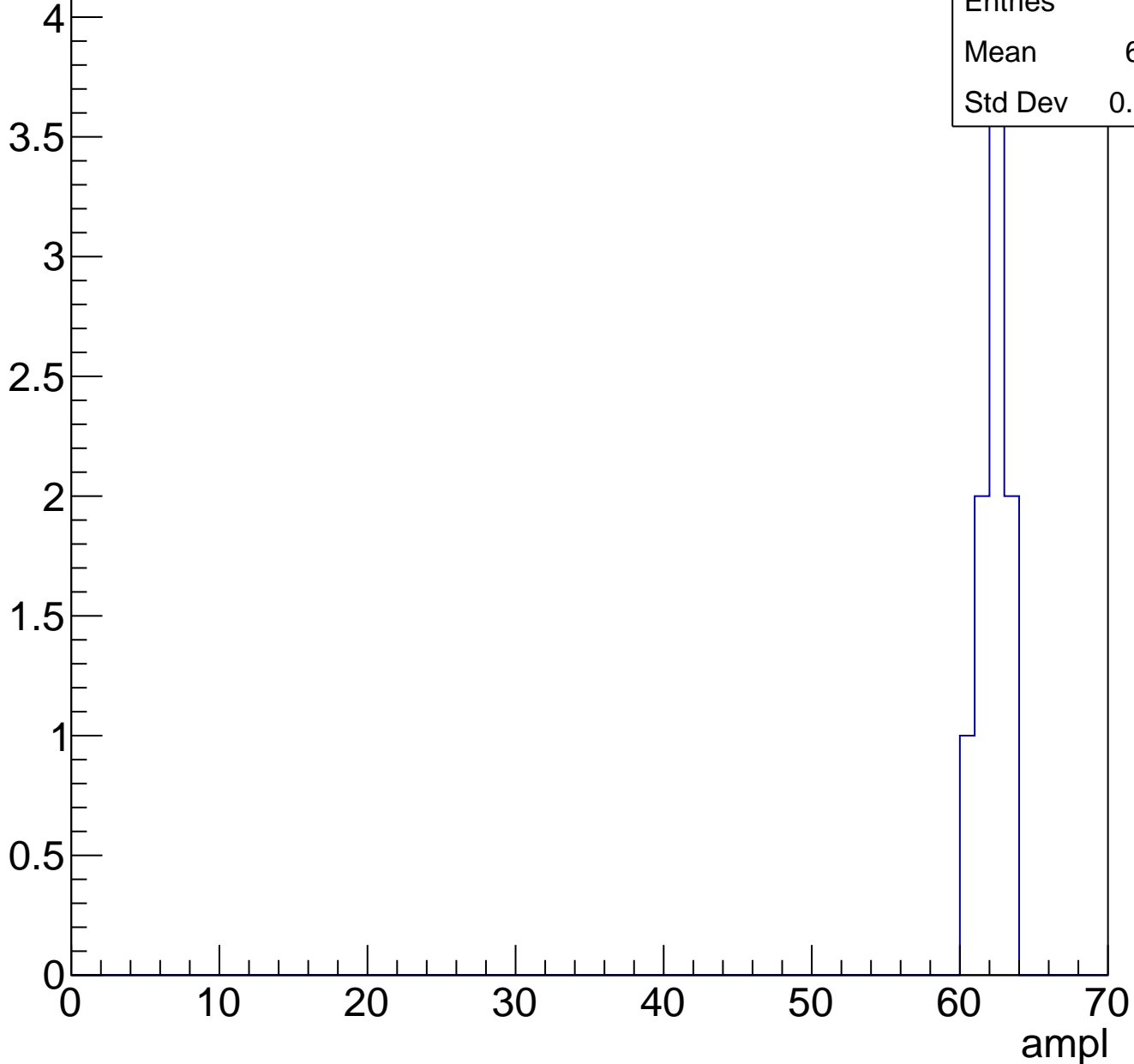
60

70

B1L100S, U19-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	9
Mean	61.78
Std Dev	0.9162

B1L100S, U19-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U19-ch71, adc0

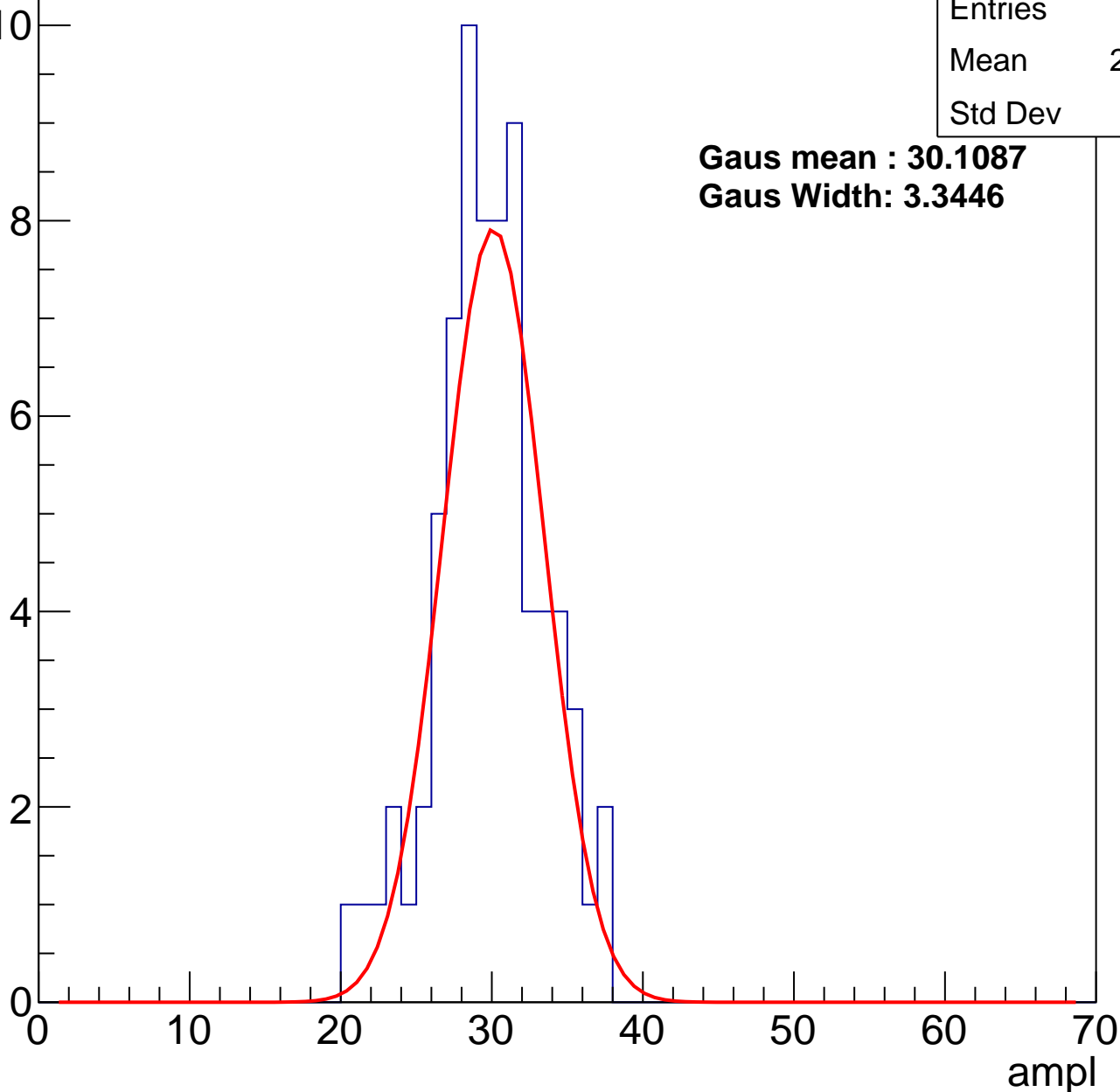
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.37
Std Dev	3.56

Gaus mean : 30.1087

Gaus Width: 3.3446



B1L100S, U19-ch71, adc1

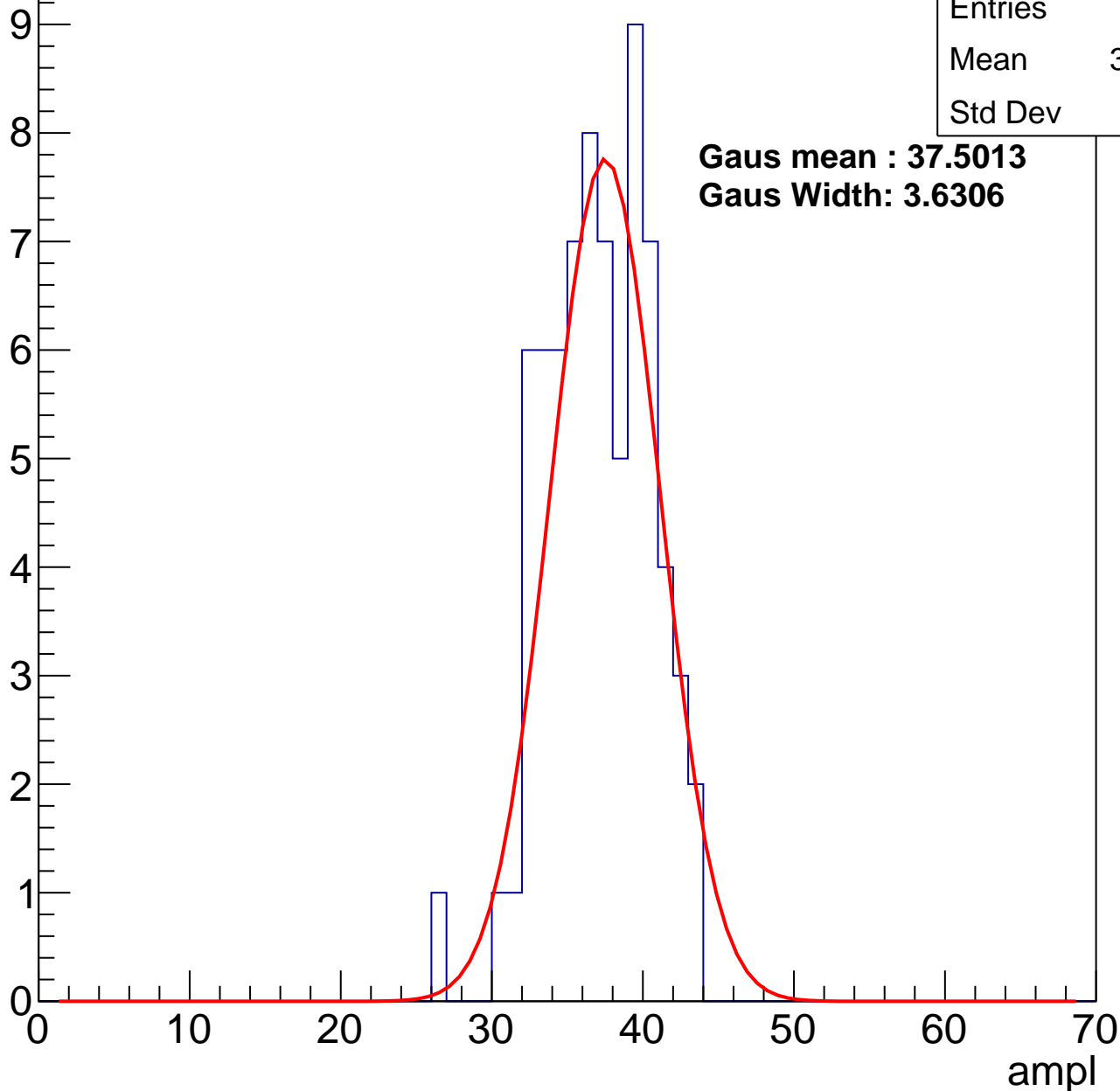
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	36.58
Std Dev	3.4

Gaus mean : 37.5013

Gaus Width: 3.6306



B1L100S, U19-ch71, adc2

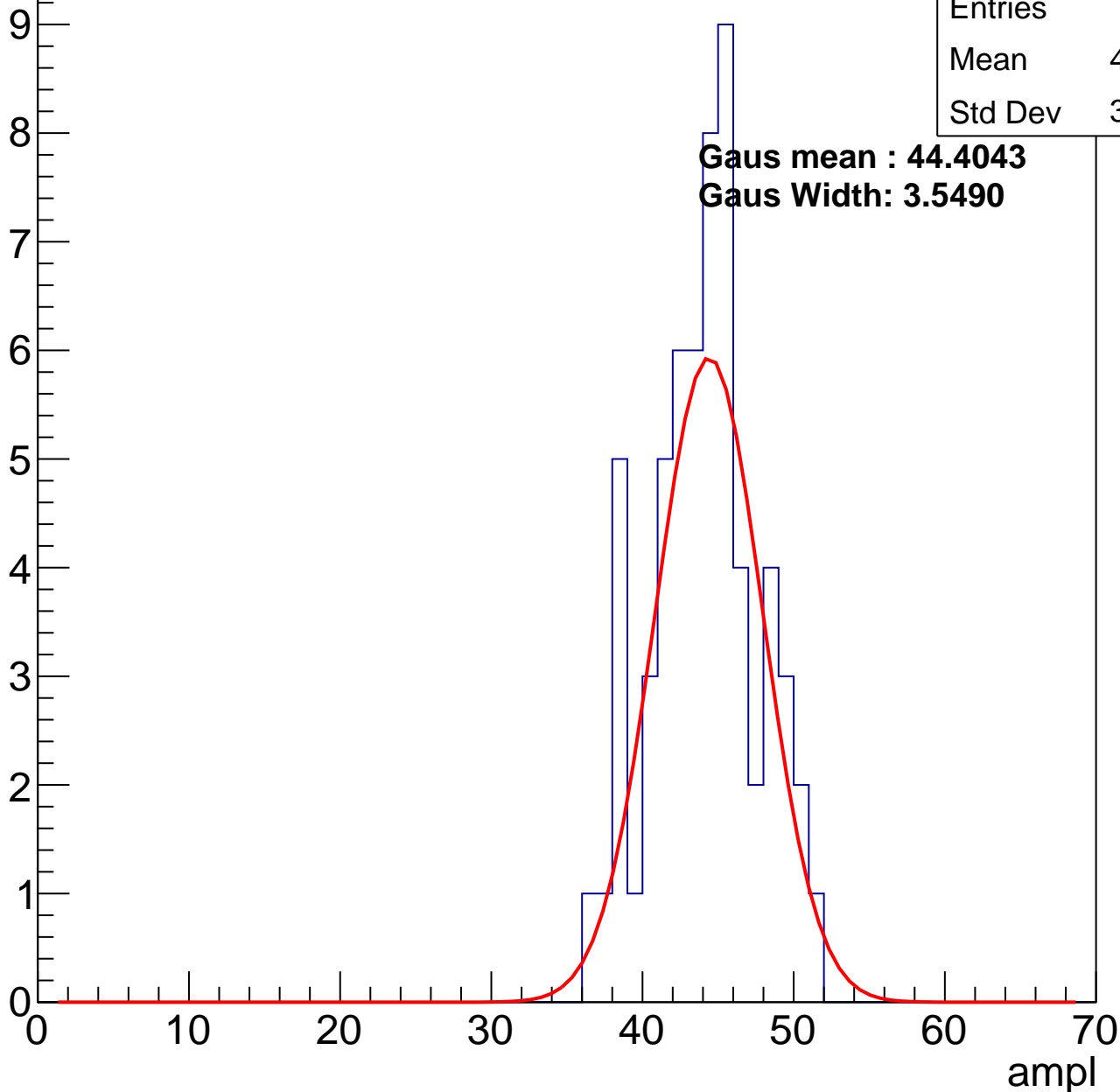
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.64
Std Dev	3.474

Gaus mean : 44.4043

Gaus Width: 3.5490

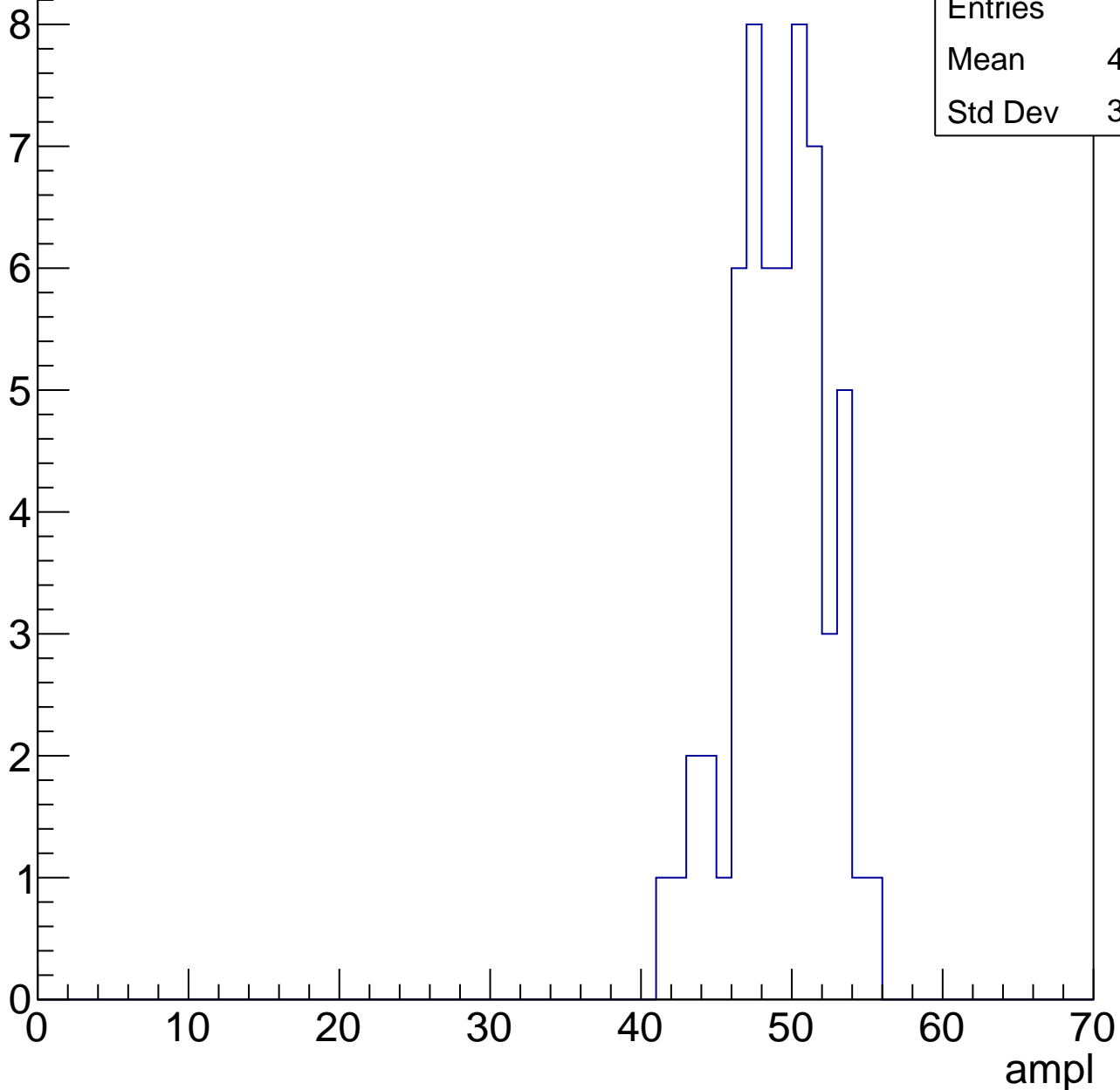


B1L100S, U19-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

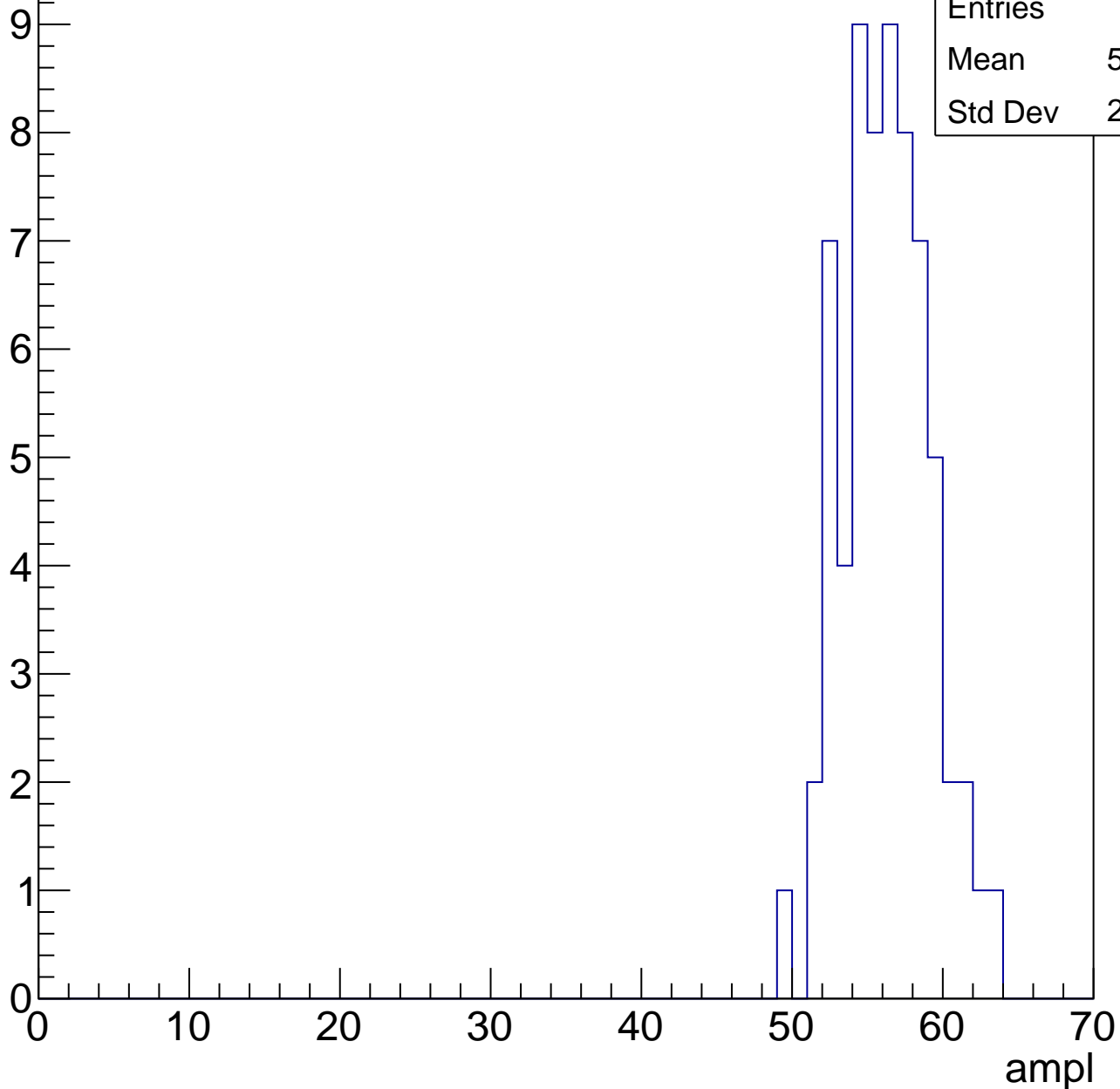
Entries	58
Mean	48.67
Std Dev	3.053



B1L100S, U19-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

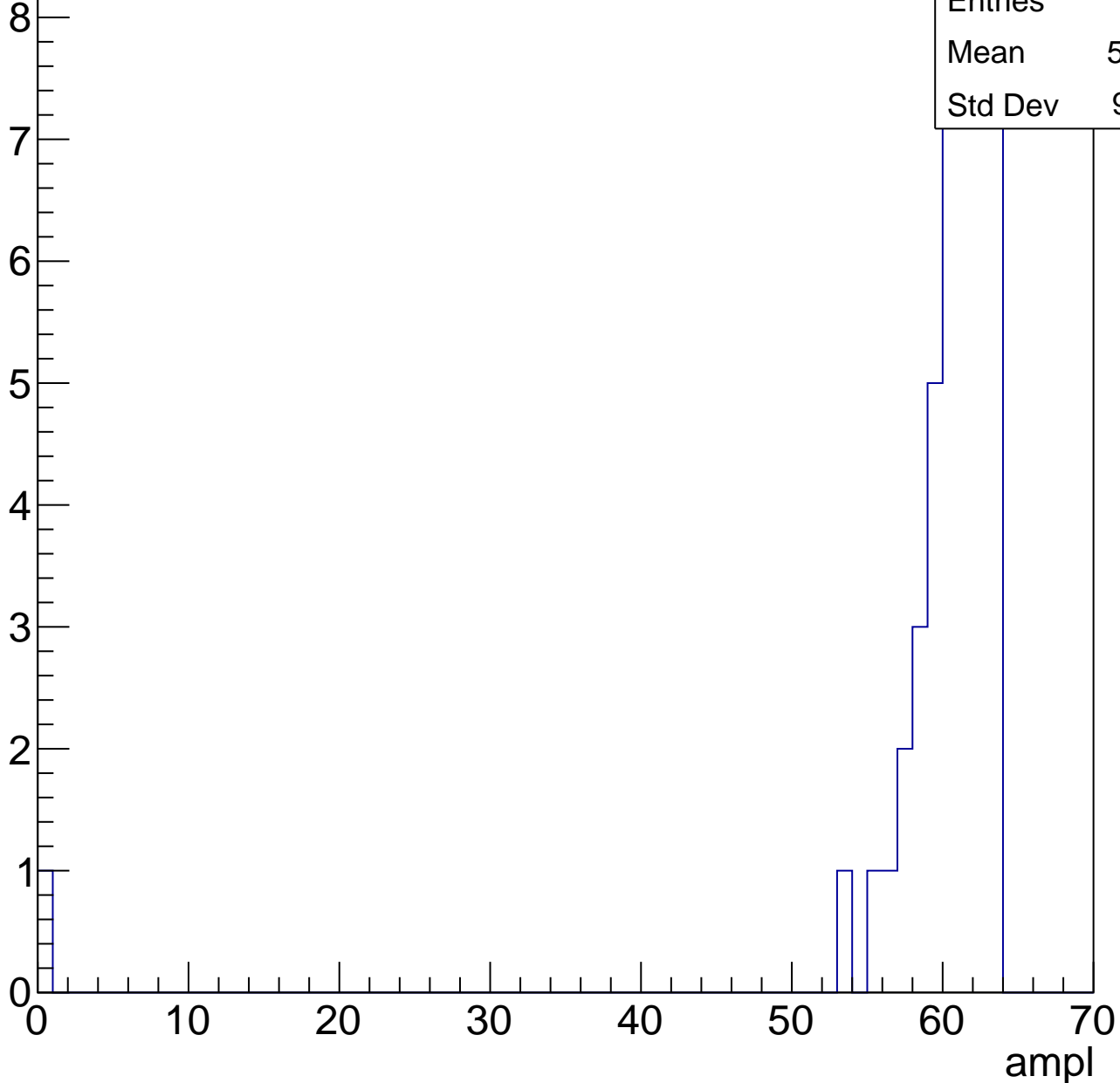


B1L100S, U19-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.02
Std Dev	9.081



B1L100S, U19-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch72, adc0

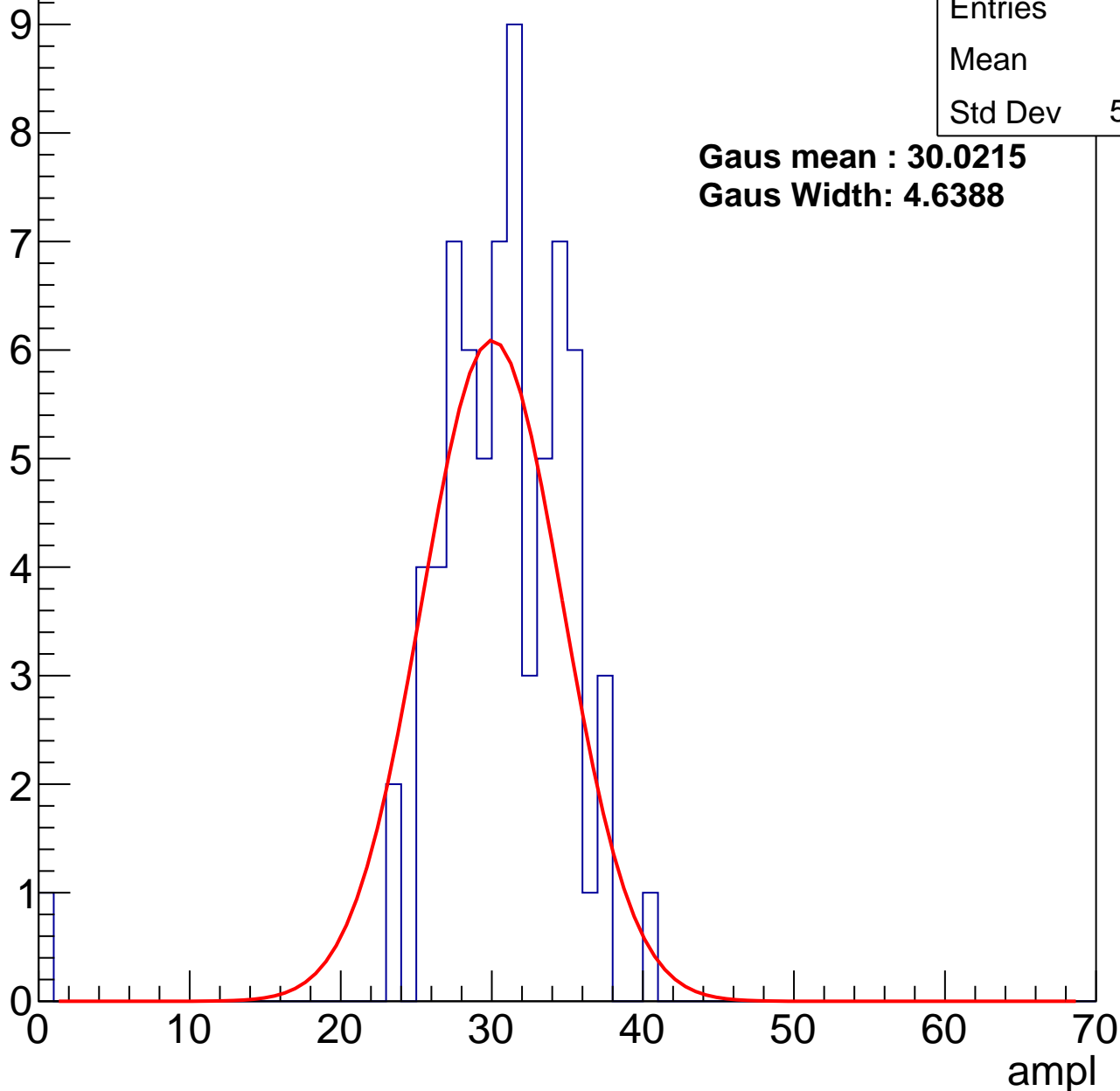
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.1
Std Dev	5.124

Gaus mean : 30.0215

Gaus Width: 4.6388



B1L100S, U19-ch72, adc1

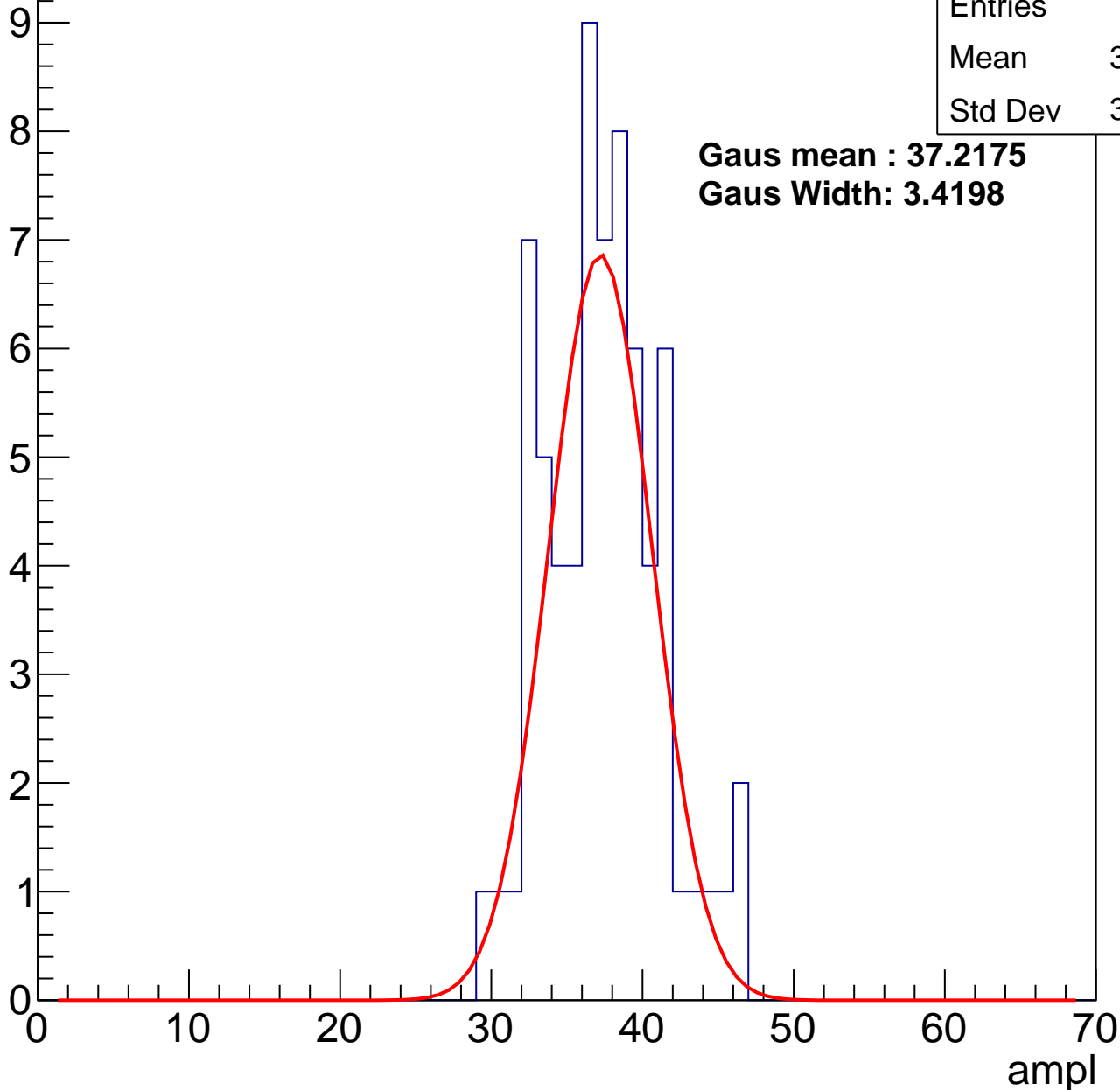
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.93
Std Dev	3.739

Gaus mean : 37.2175

Gaus Width: 3.4198



B1L100S, U19-ch72, adc2

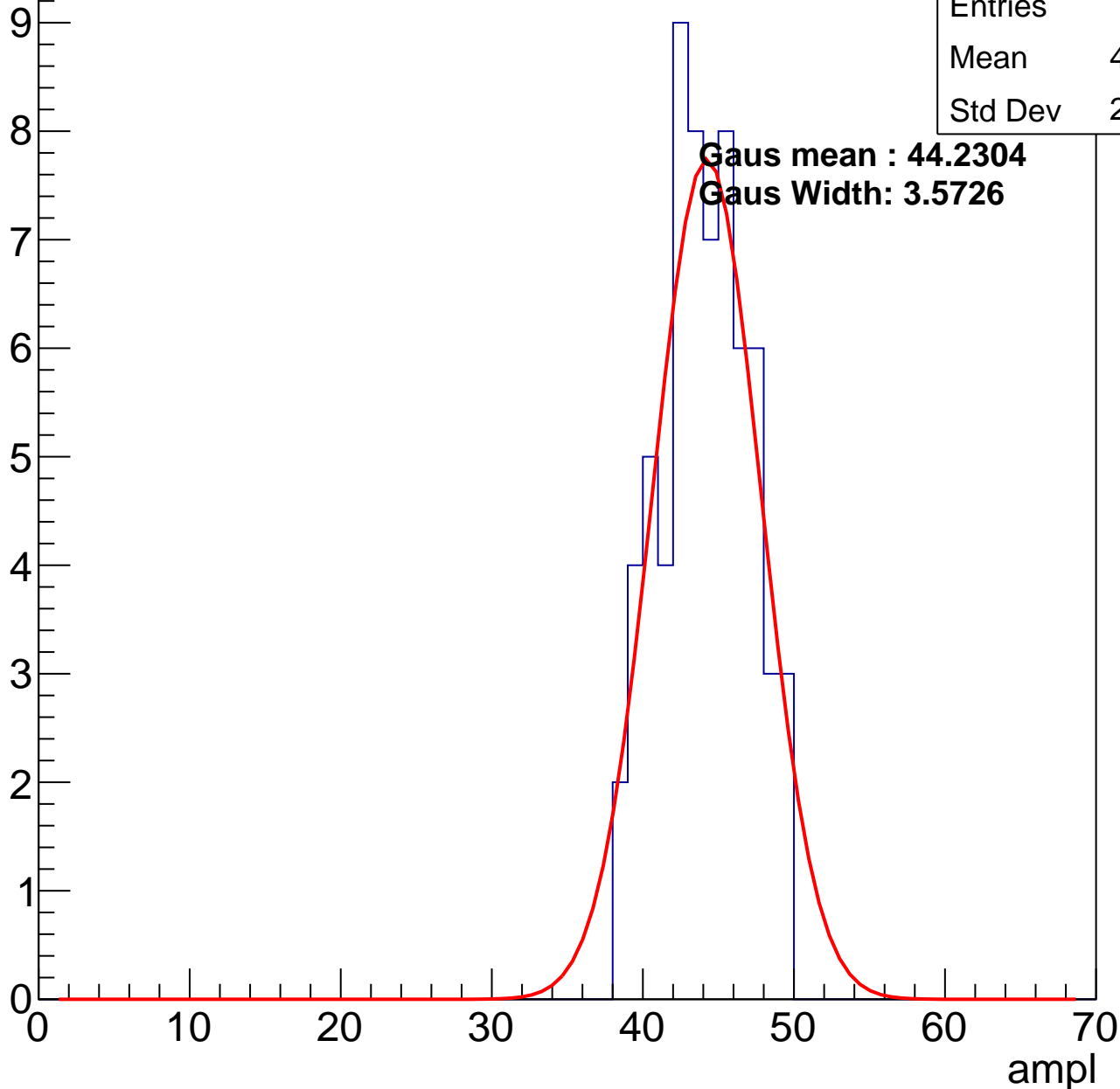
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.62
Std Dev	2.859

Gaus mean : 44.2304

Gaus Width: 3.5726

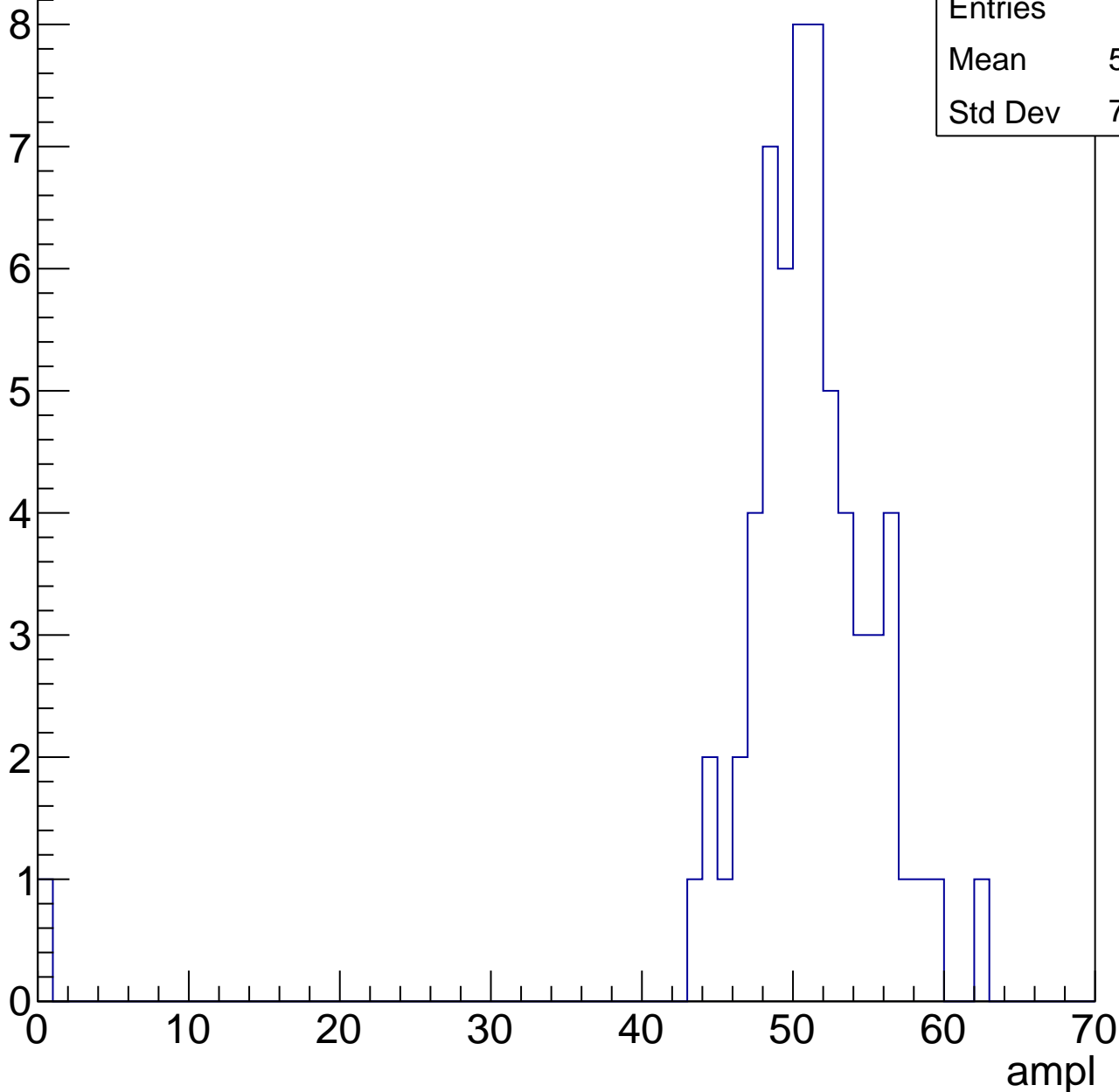


B1L100S, U19-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	50.05
Std Dev	7.364

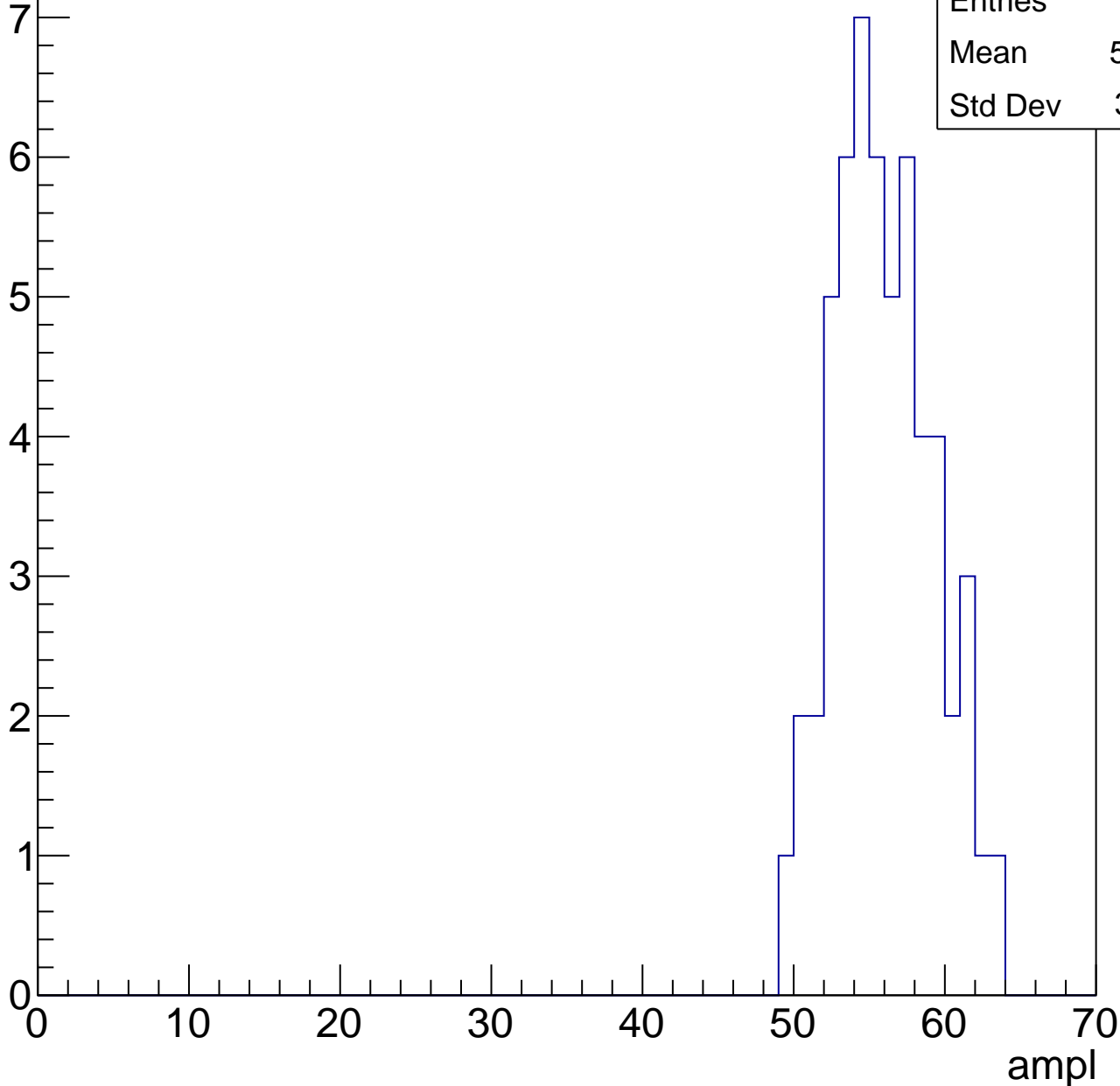


B1L100S, U19-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.55
Std Dev	3.241



B1L100S, U19-ch72, adc5

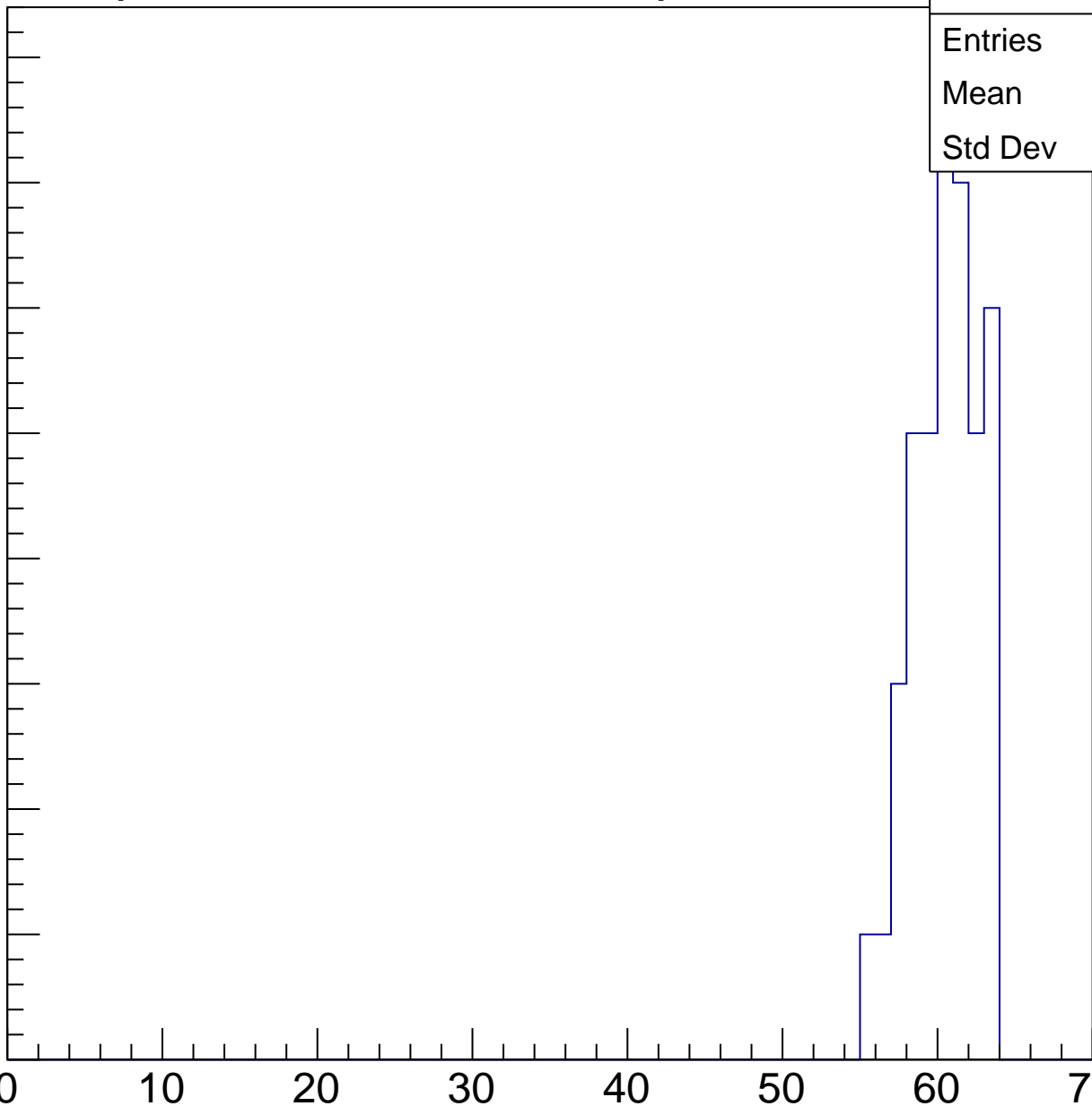
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.05
Std Dev	2.059

ampl



B1L100S, U19-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch73, adc0

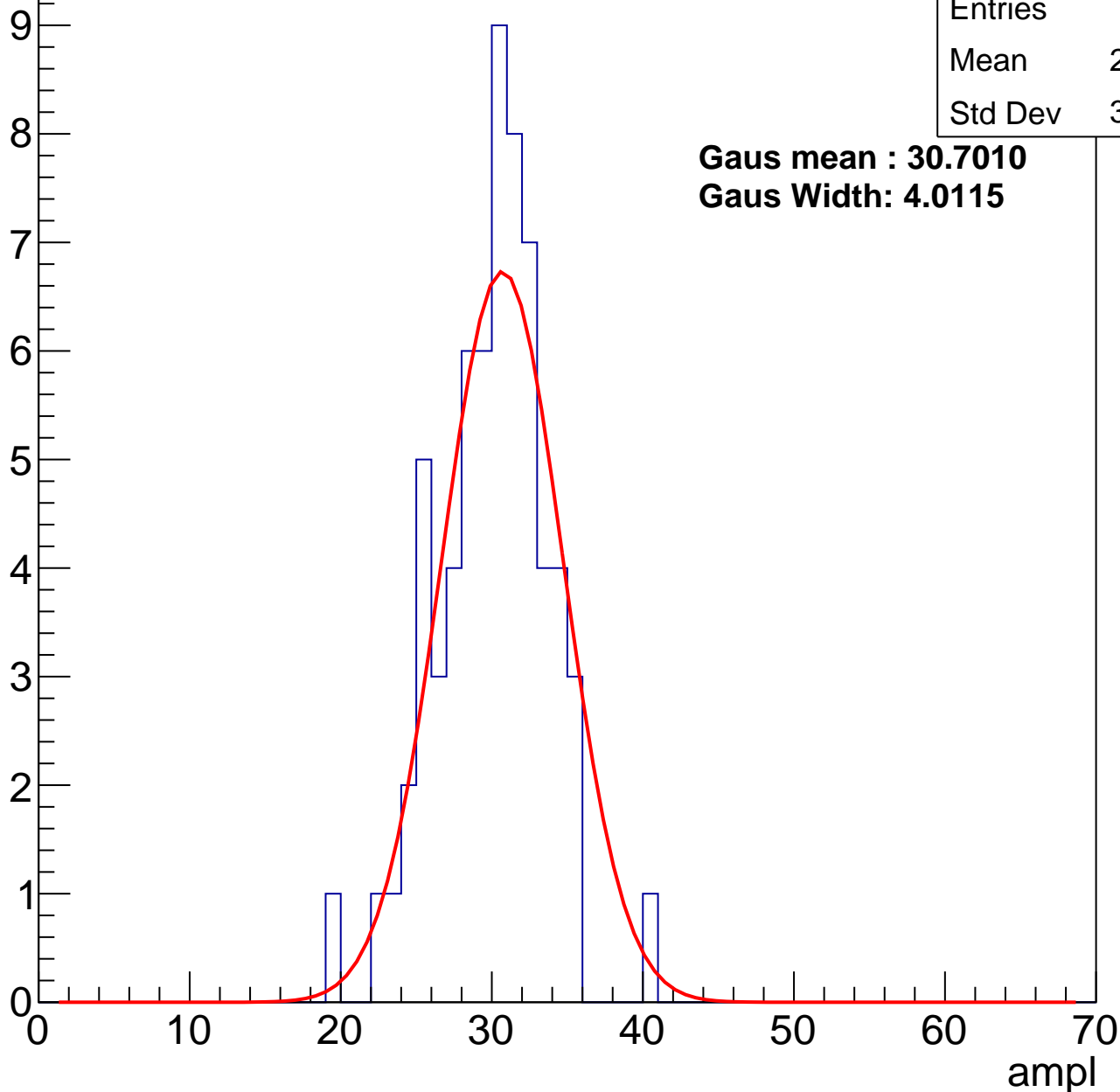
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.54
Std Dev	3.599

Gaus mean : 30.7010

Gaus Width: 4.0115



B1L100S, U19-ch73, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	36.89
Std Dev	3.386

Gaus mean : 37.3816

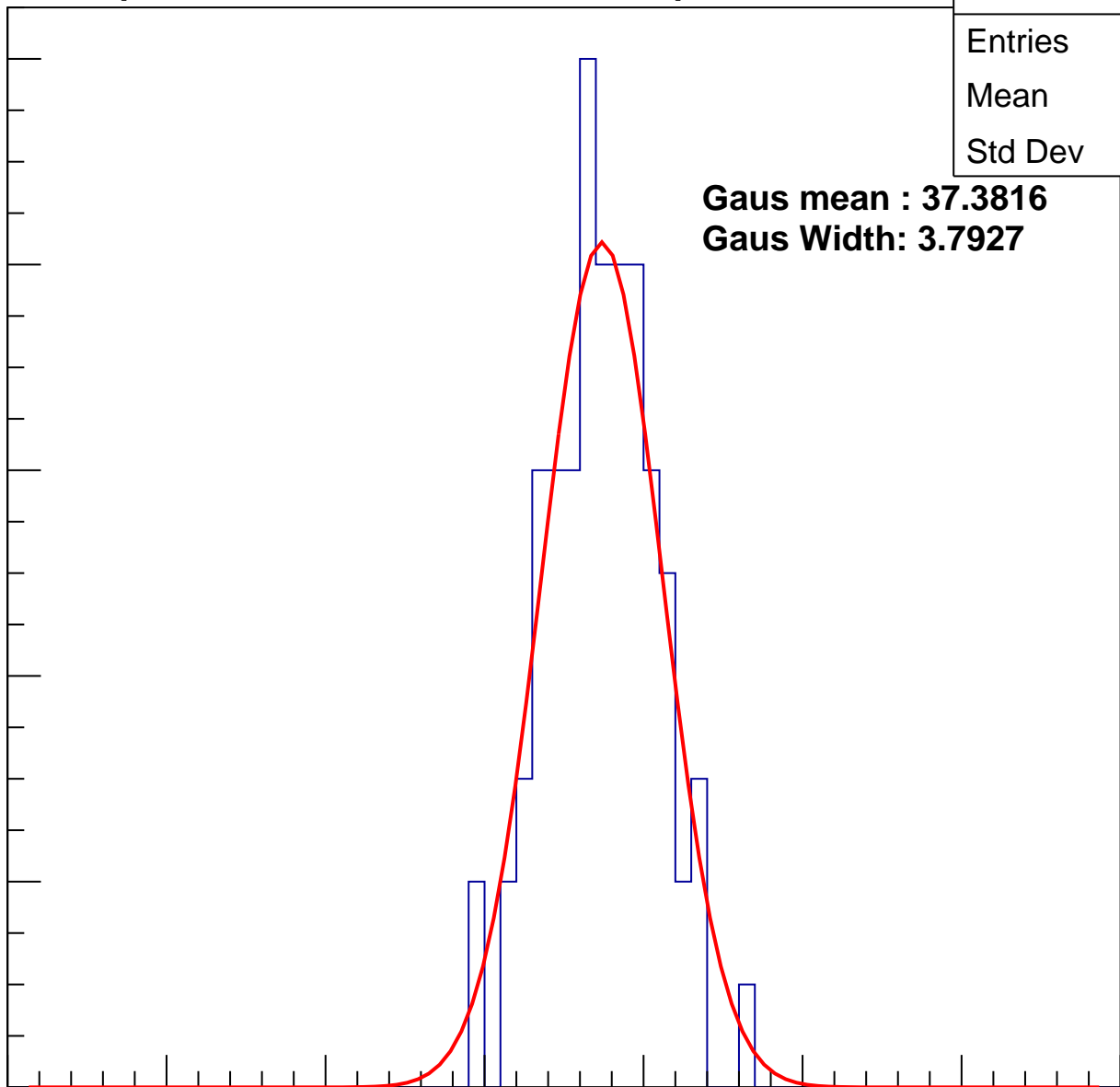
Gaus Width: 3.7927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch73, adc2

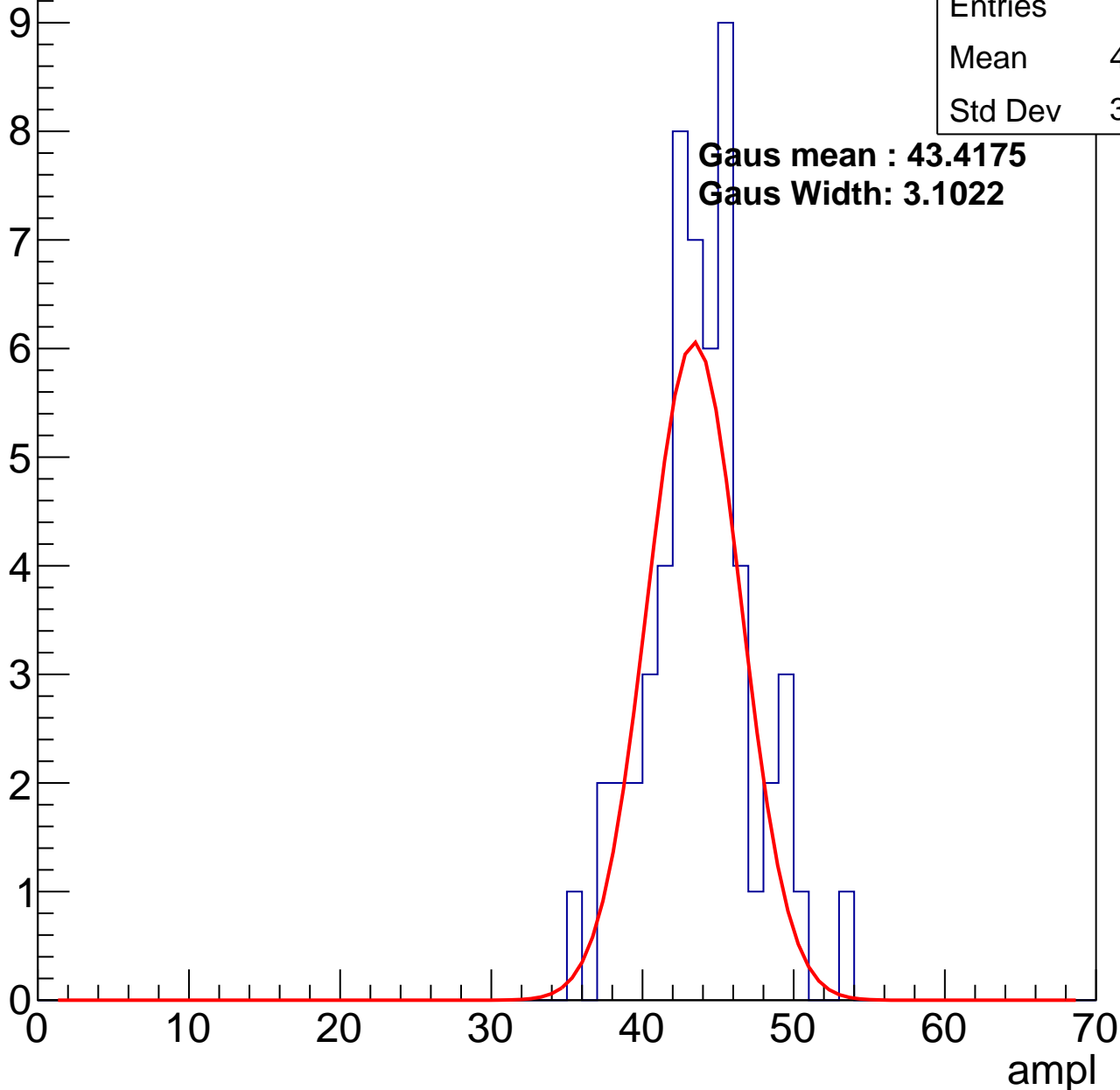
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.39
Std Dev	3.437

Gaus mean : 43.4175

Gaus Width: 3.1022

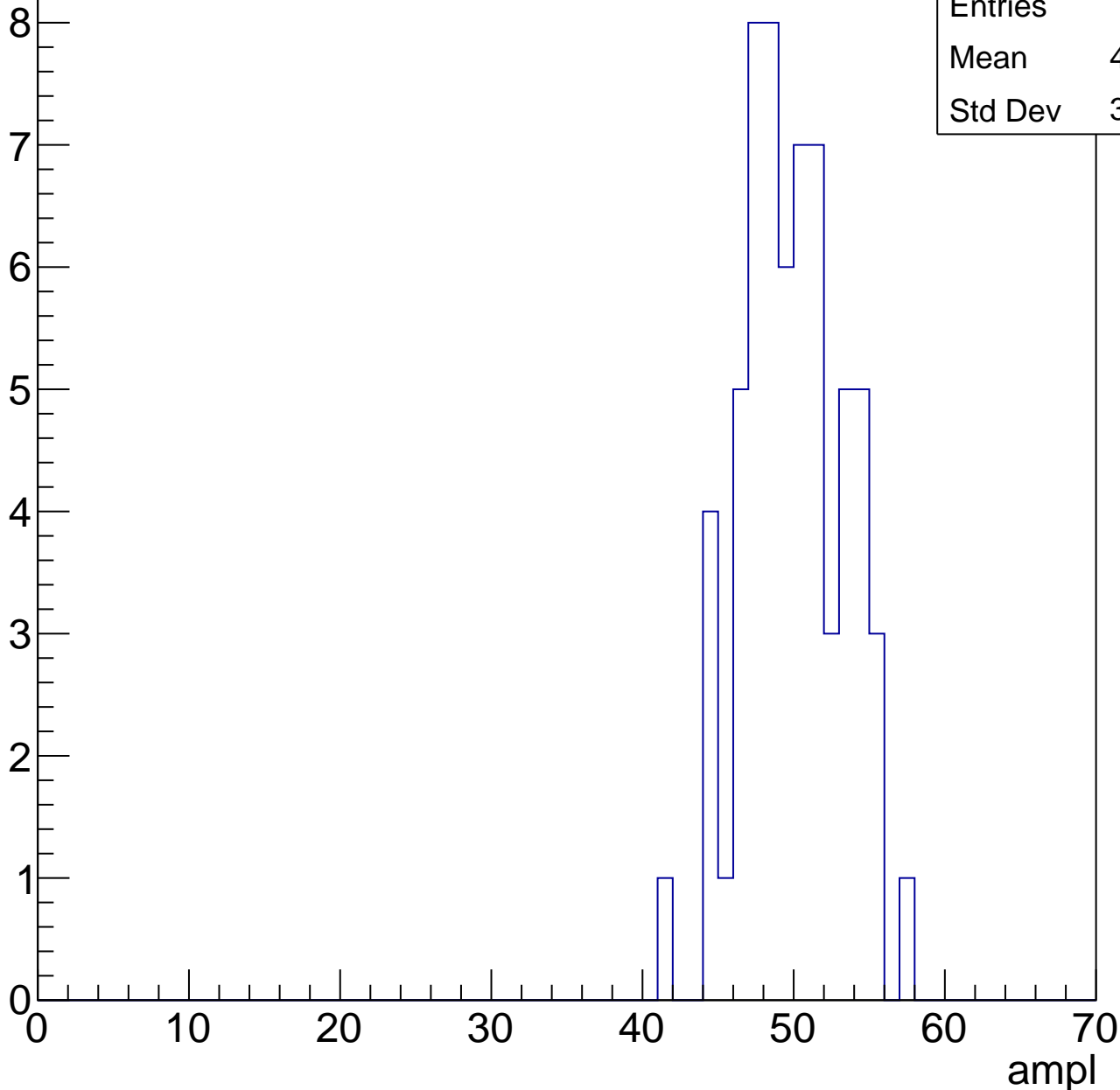


B1L100S, U19-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.47
Std Dev	3.283

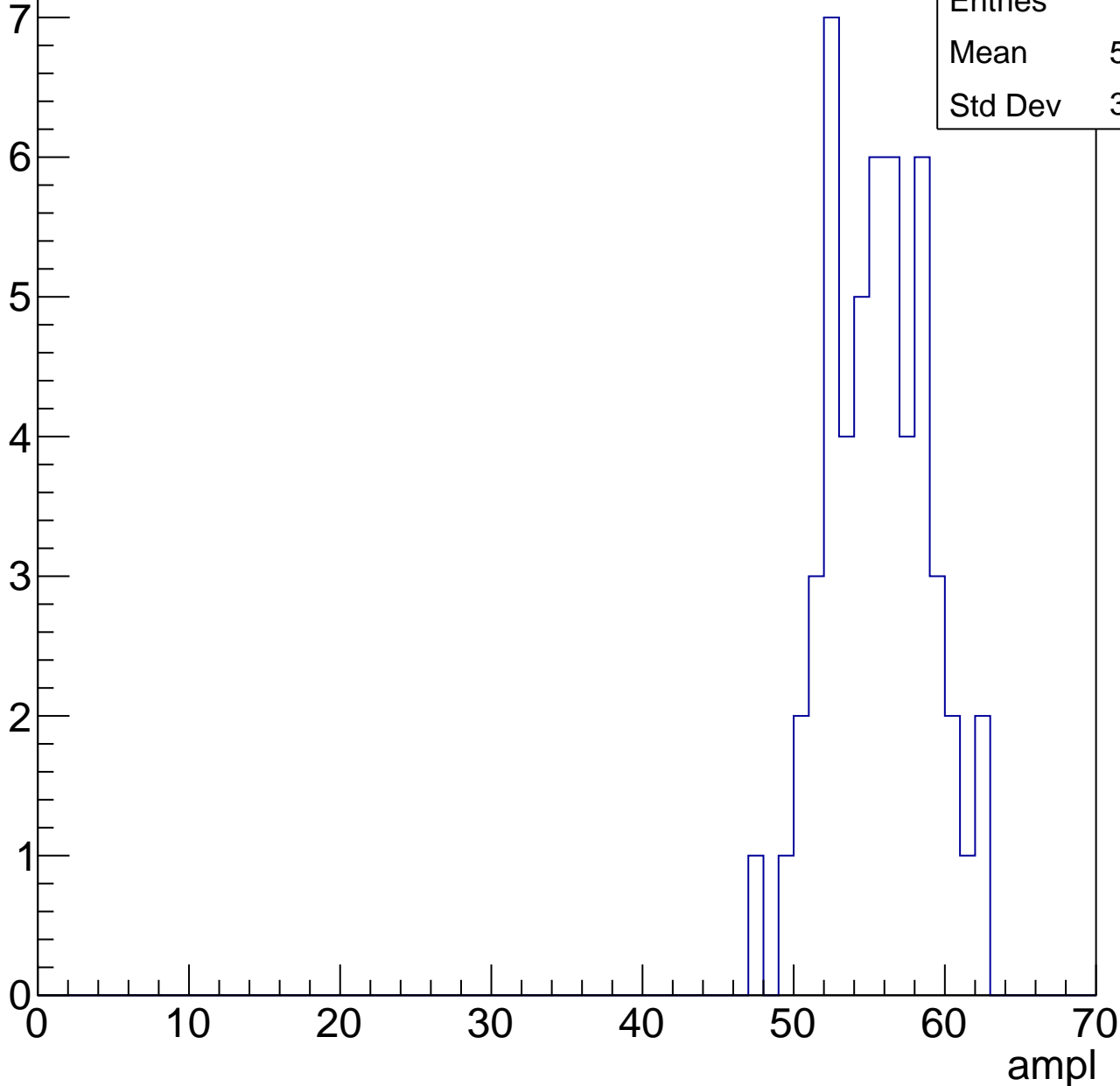


B1L100S, U19-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

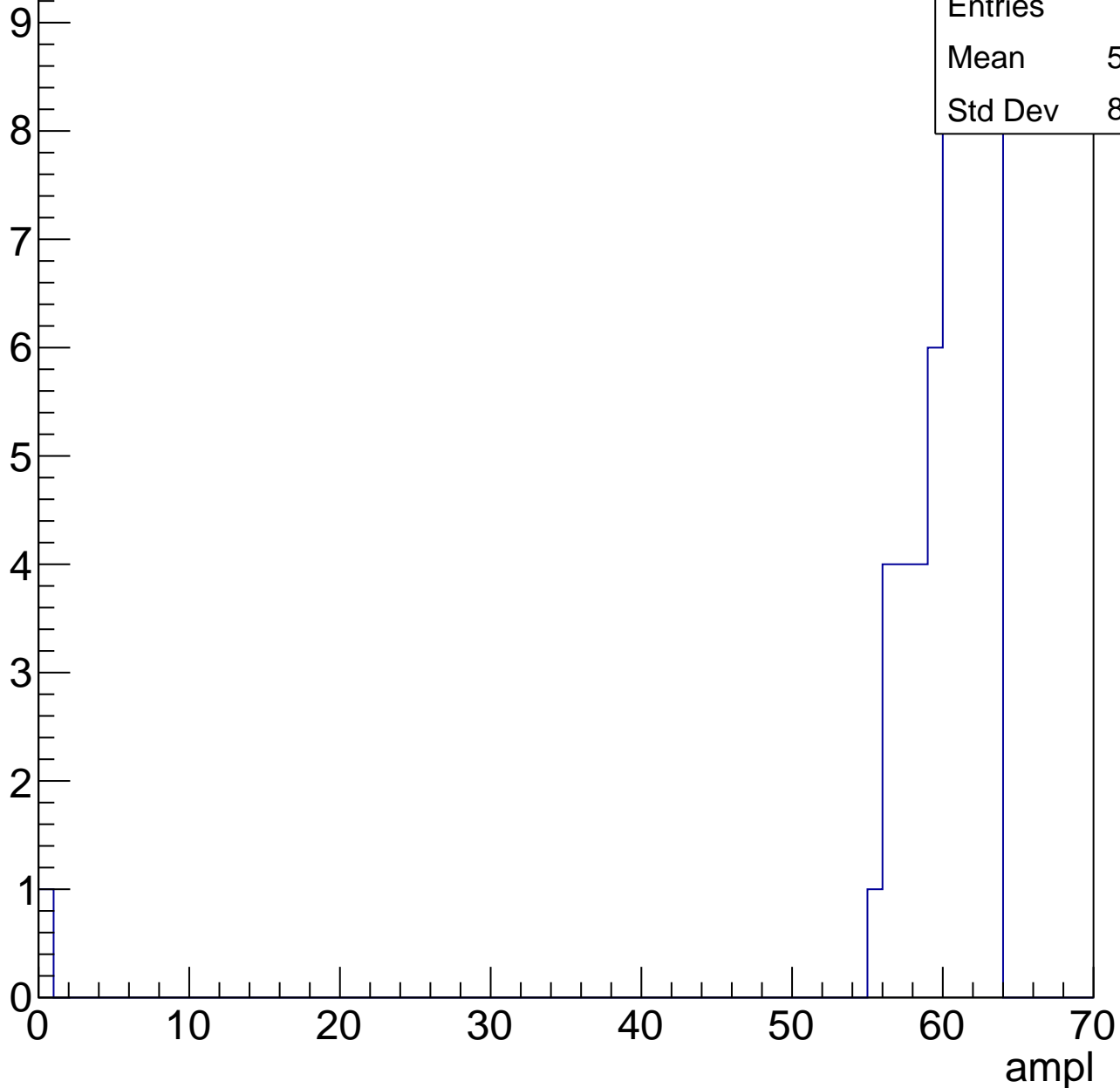
Entries	53
Mean	55.08
Std Dev	3.336



B1L100S, U19-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

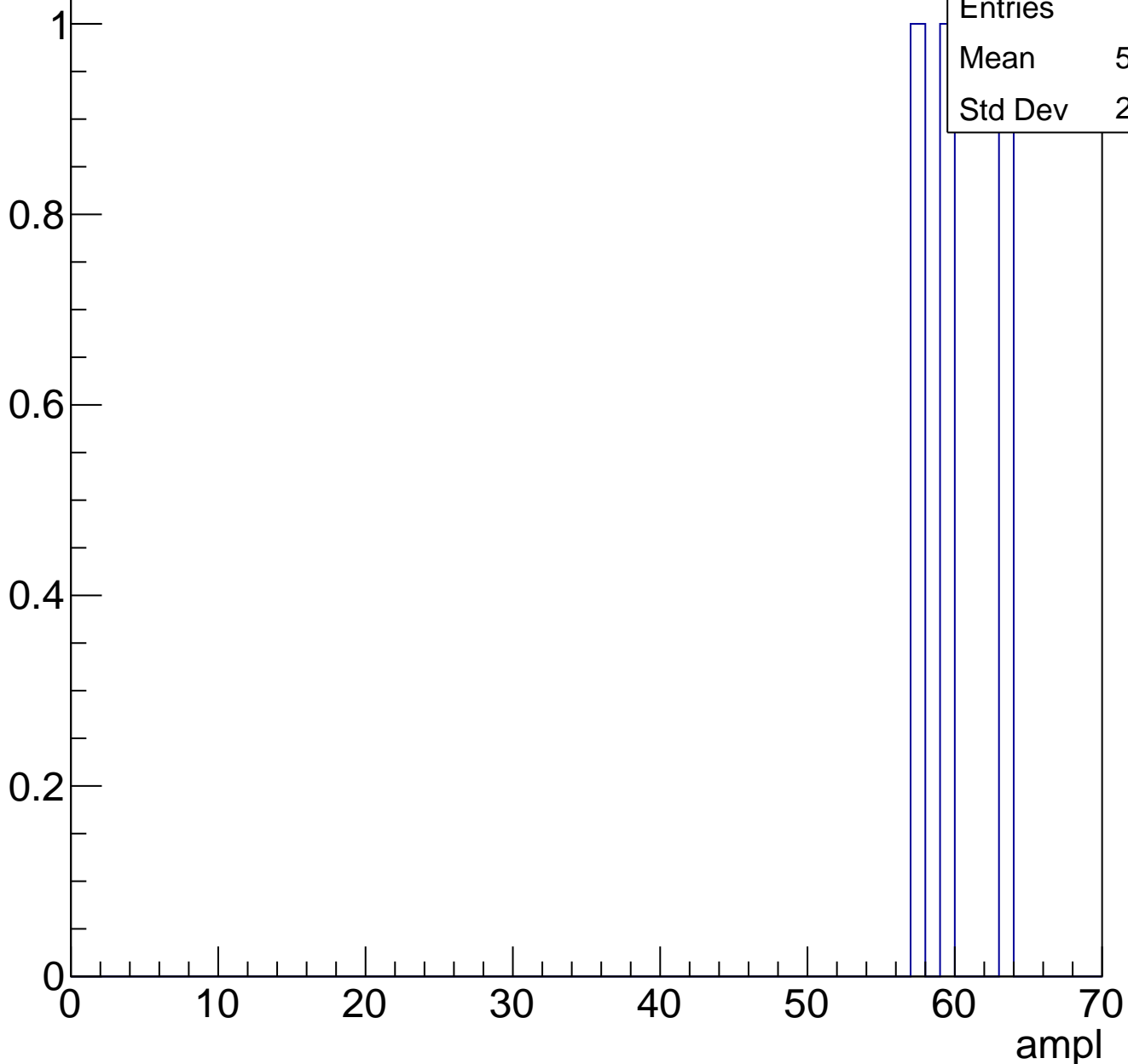
Entry



B1L100S, U19-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch74, adc0

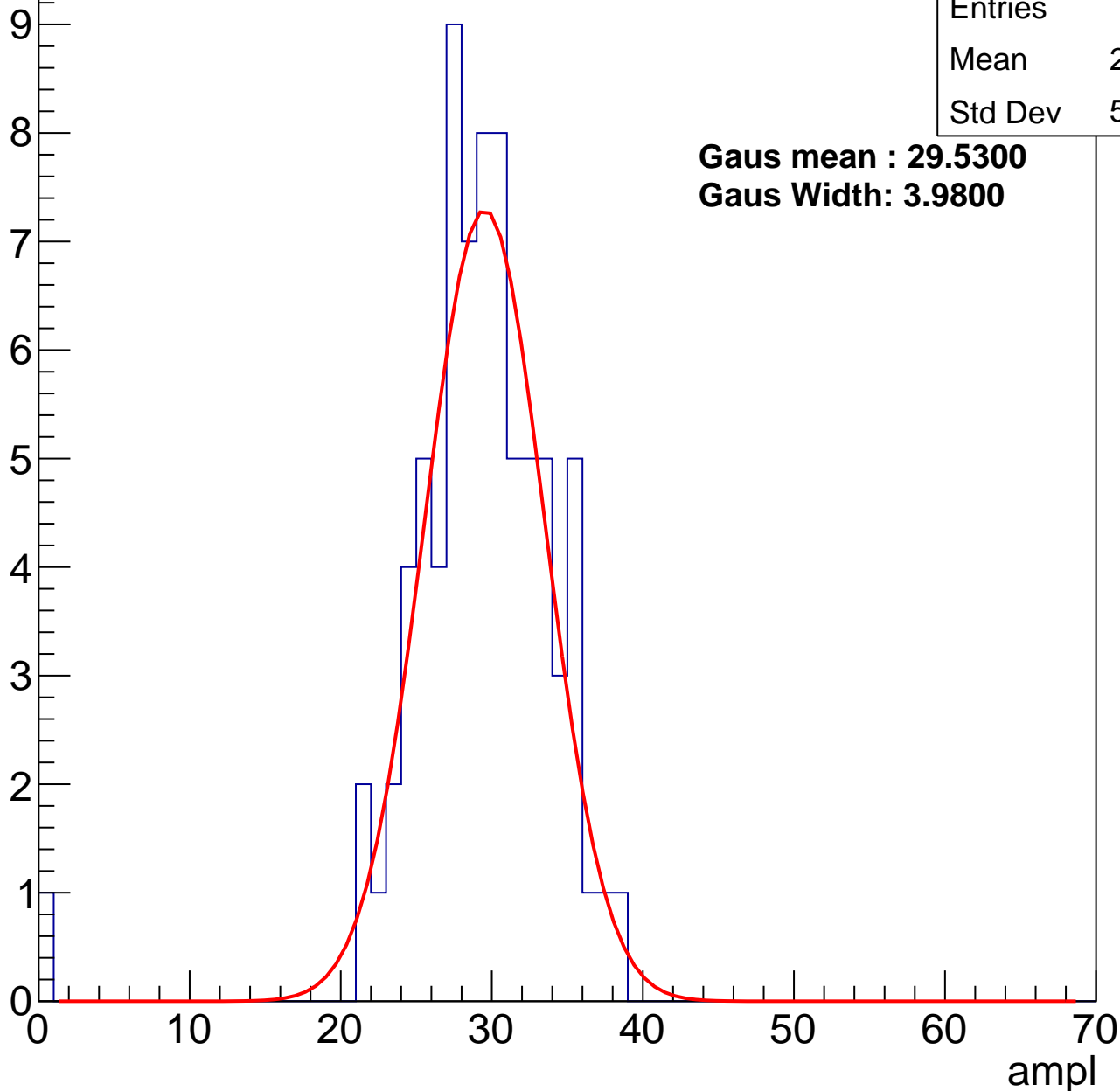
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	28.75
Std Dev	5.028

Gaus mean : 29.5300

Gaus Width: 3.9800



B1L100S, U19-ch74, adc1

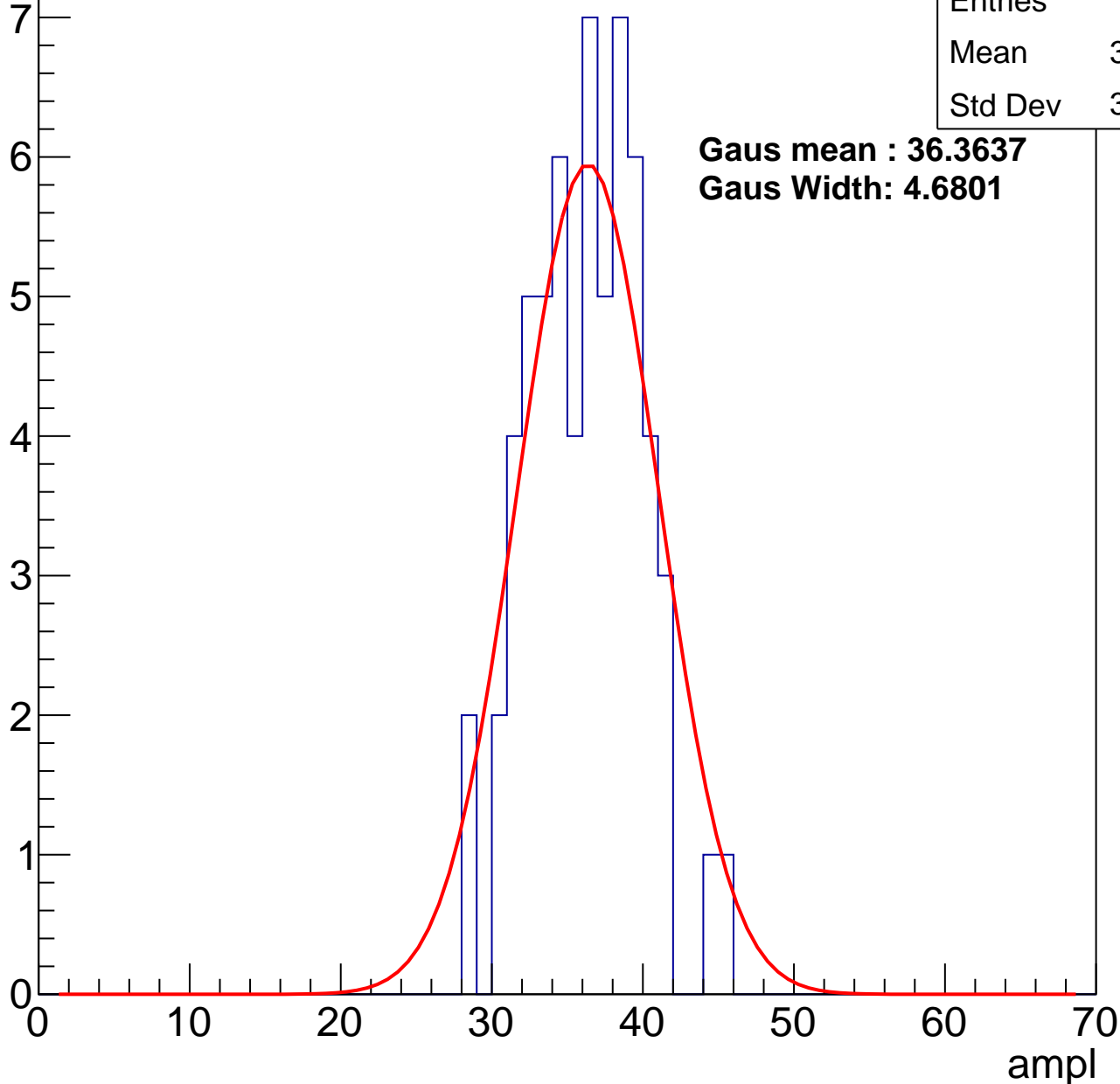
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.77
Std Dev	3.639

Gaus mean : 36.3637

Gaus Width: 4.6801



B1L100S, U19-ch74, adc2

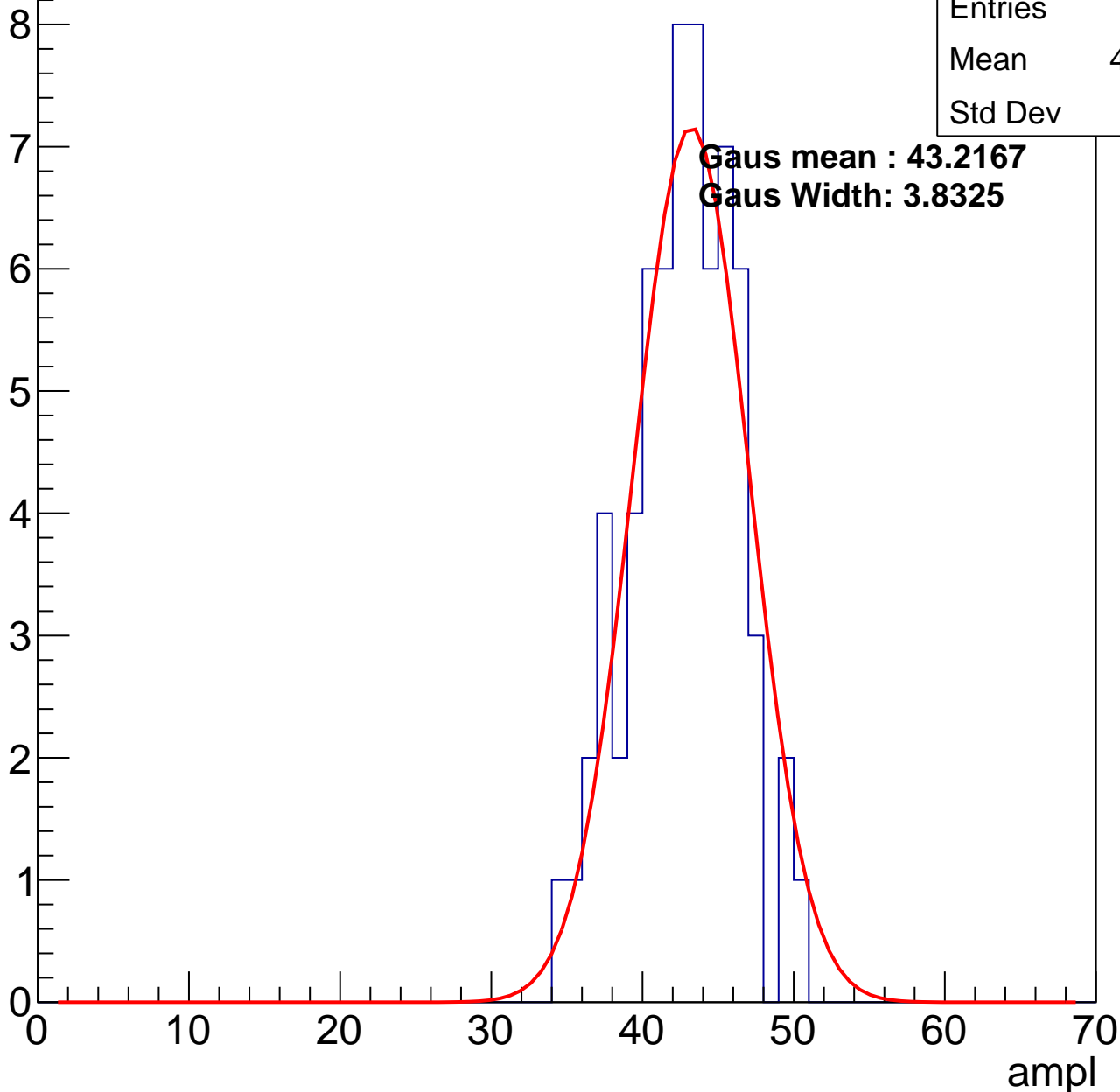
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.25
Std Dev	3.47

Gaus mean : 43.2167

Gaus Width: 3.8325

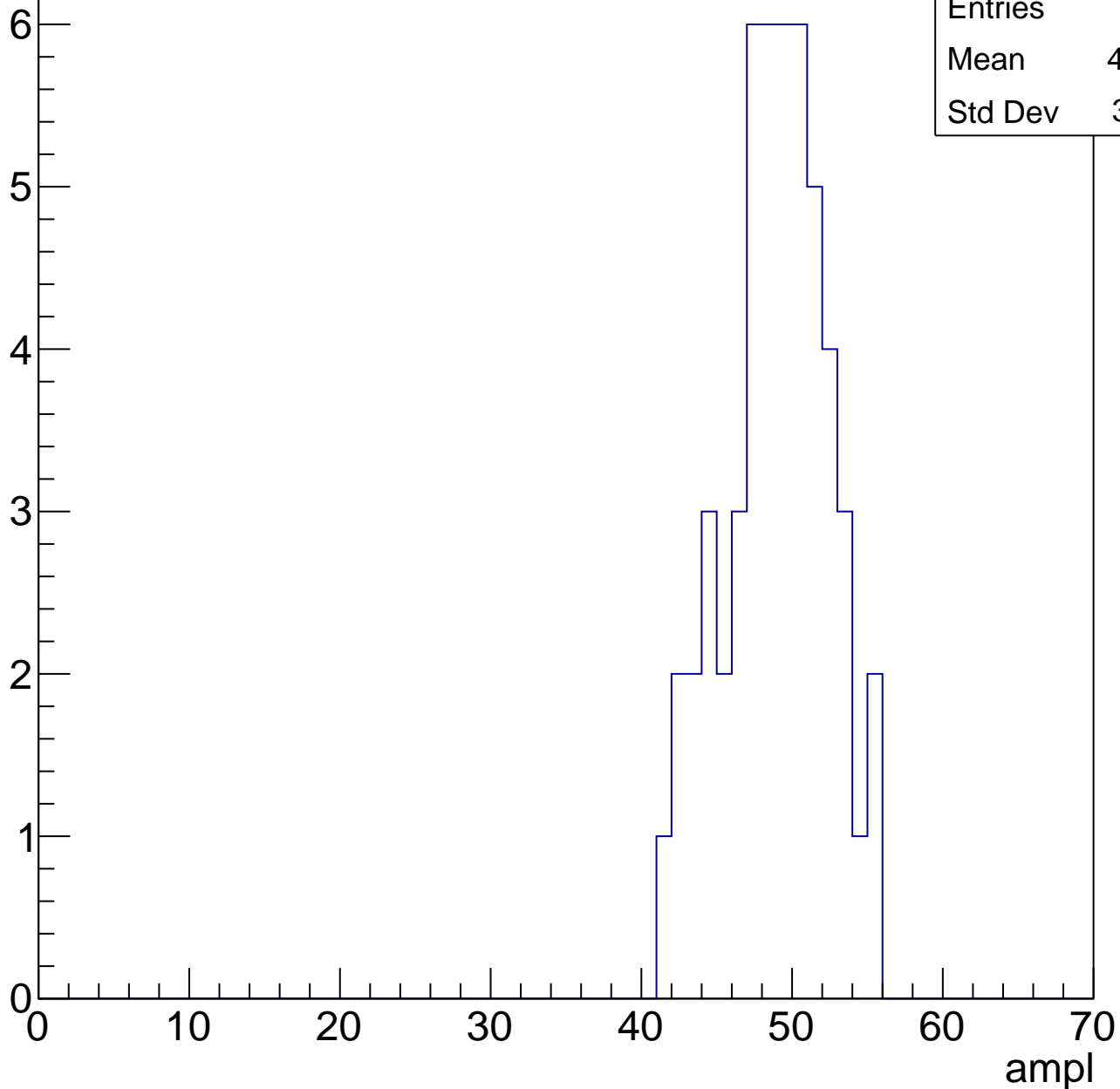


B1L100S, U19-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

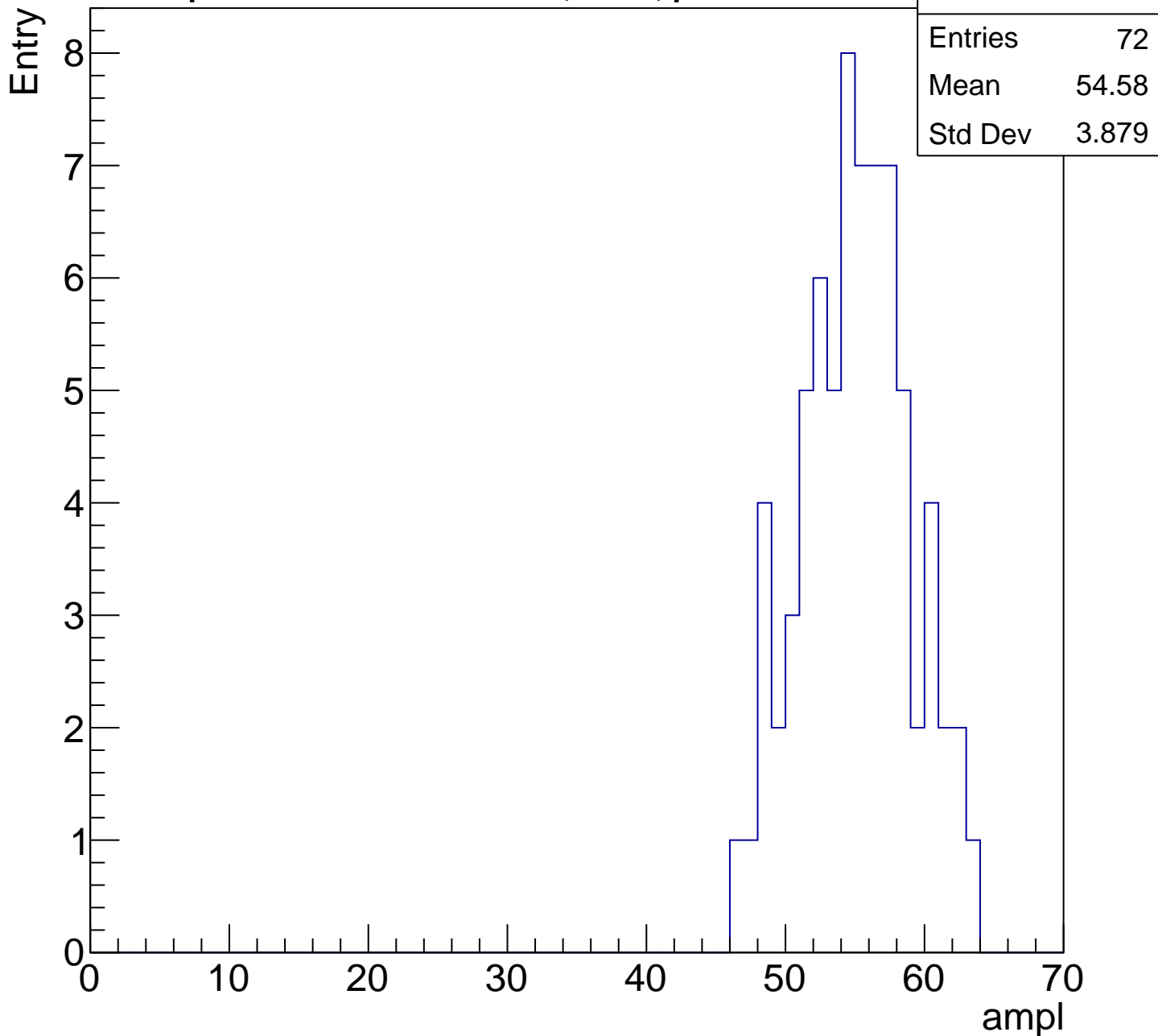
Entry

Entries	52
Mean	48.48
Std Dev	3.371



B1L100S, U19-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch74, adc5

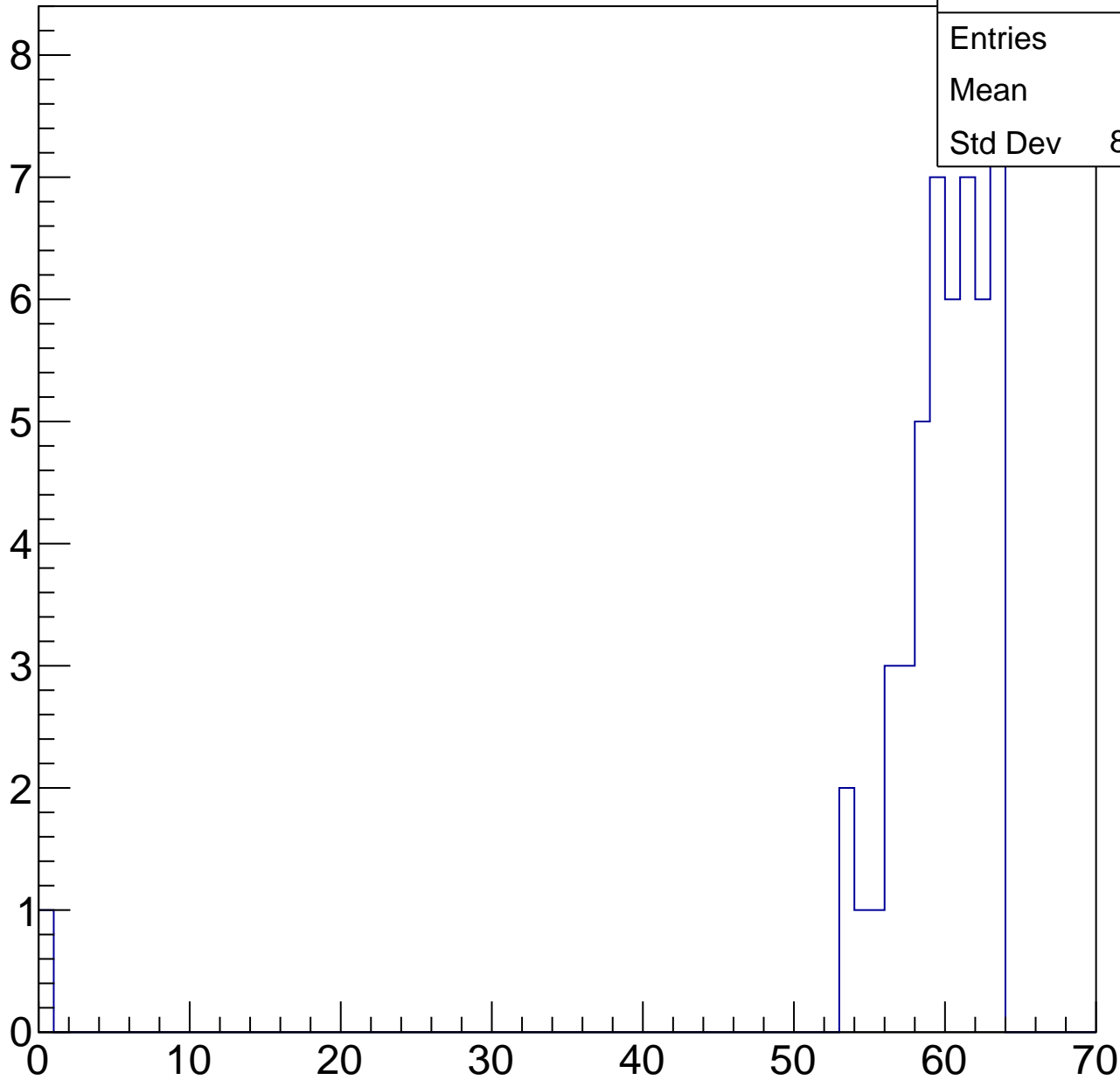
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.4
Std Dev	8.759

ampl



B1L100S, U19-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L100S, U19-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch75, adc0

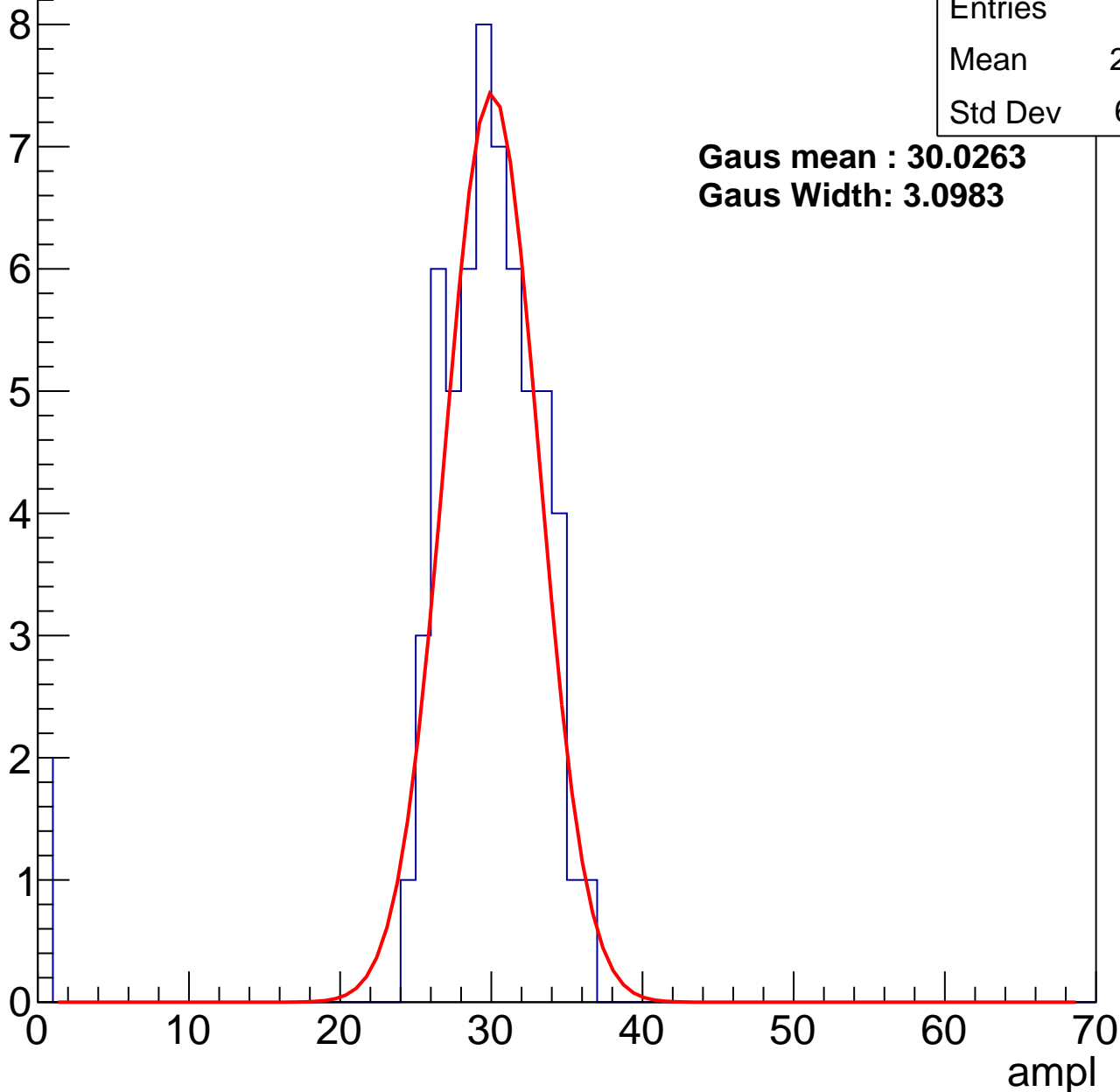
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	28.63
Std Dev	6.011

Gaus mean : 30.0263

Gaus Width: 3.0983



B1L100S, U19-ch75, adc1

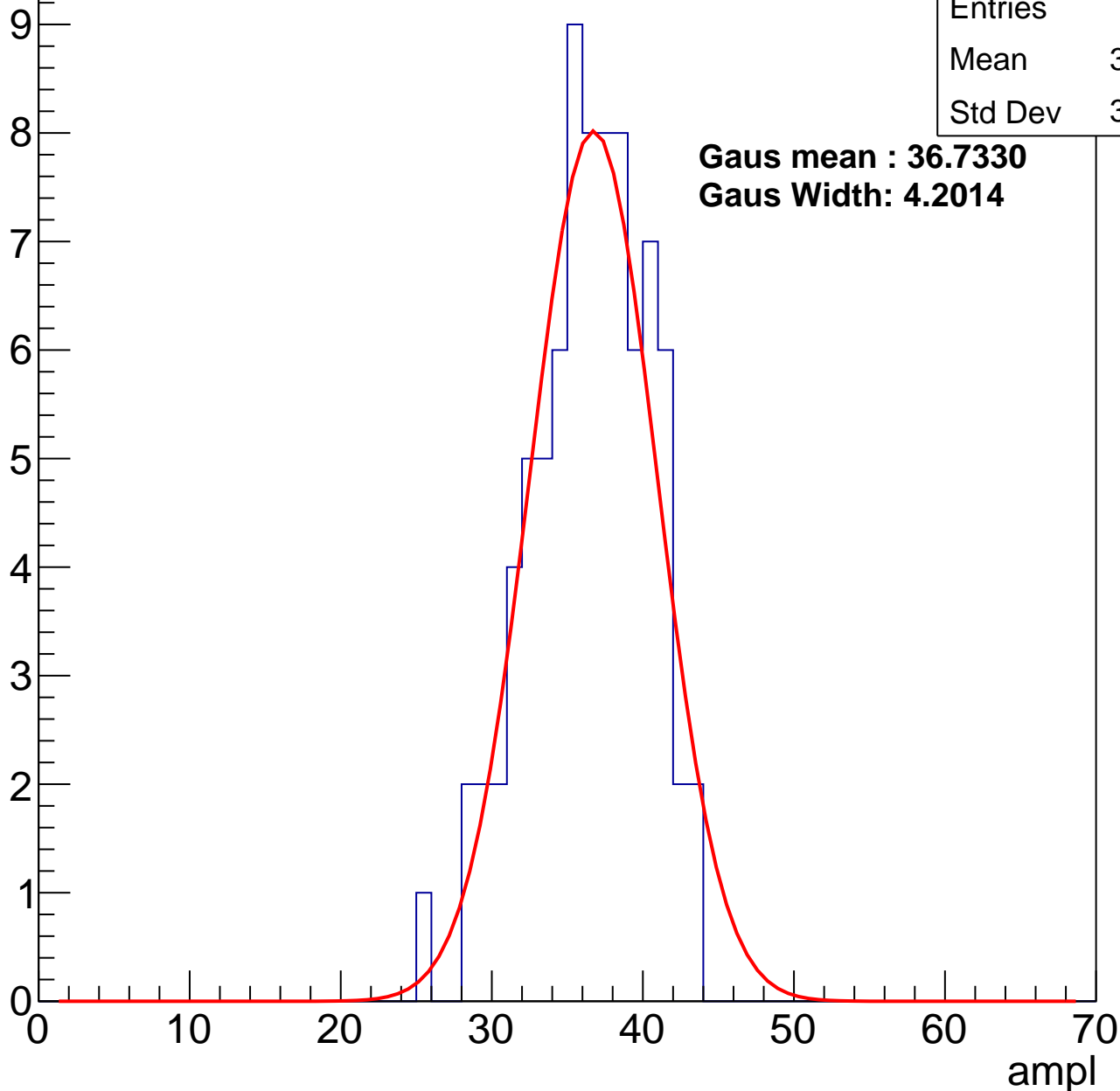
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	35.96
Std Dev	3.807

Gaus mean : 36.7330

Gaus Width: 4.2014



B1L100S, U19-ch75, adc2

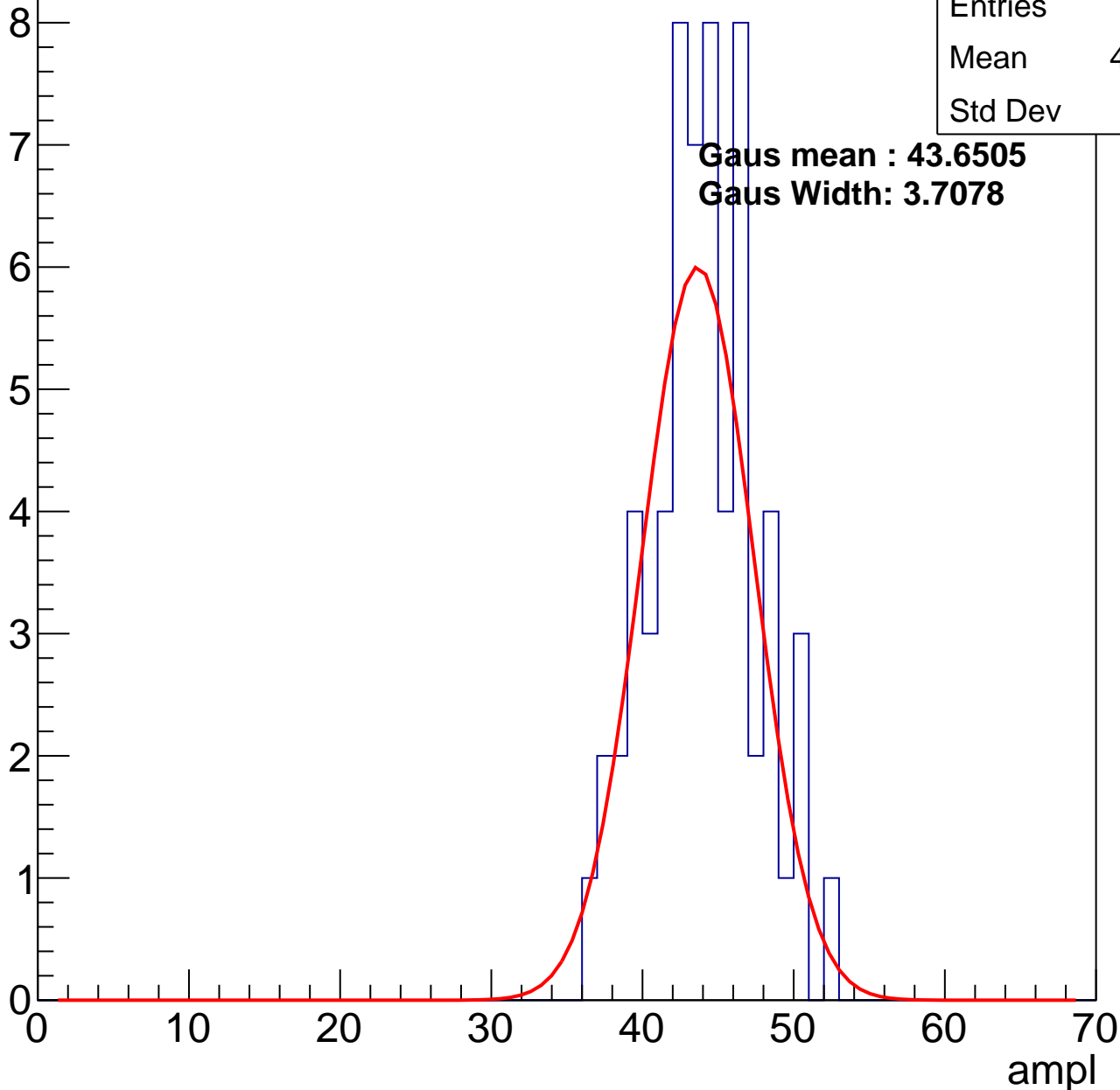
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.55
Std Dev	3.5

Gaus mean : 43.6505

Gaus Width: 3.7078

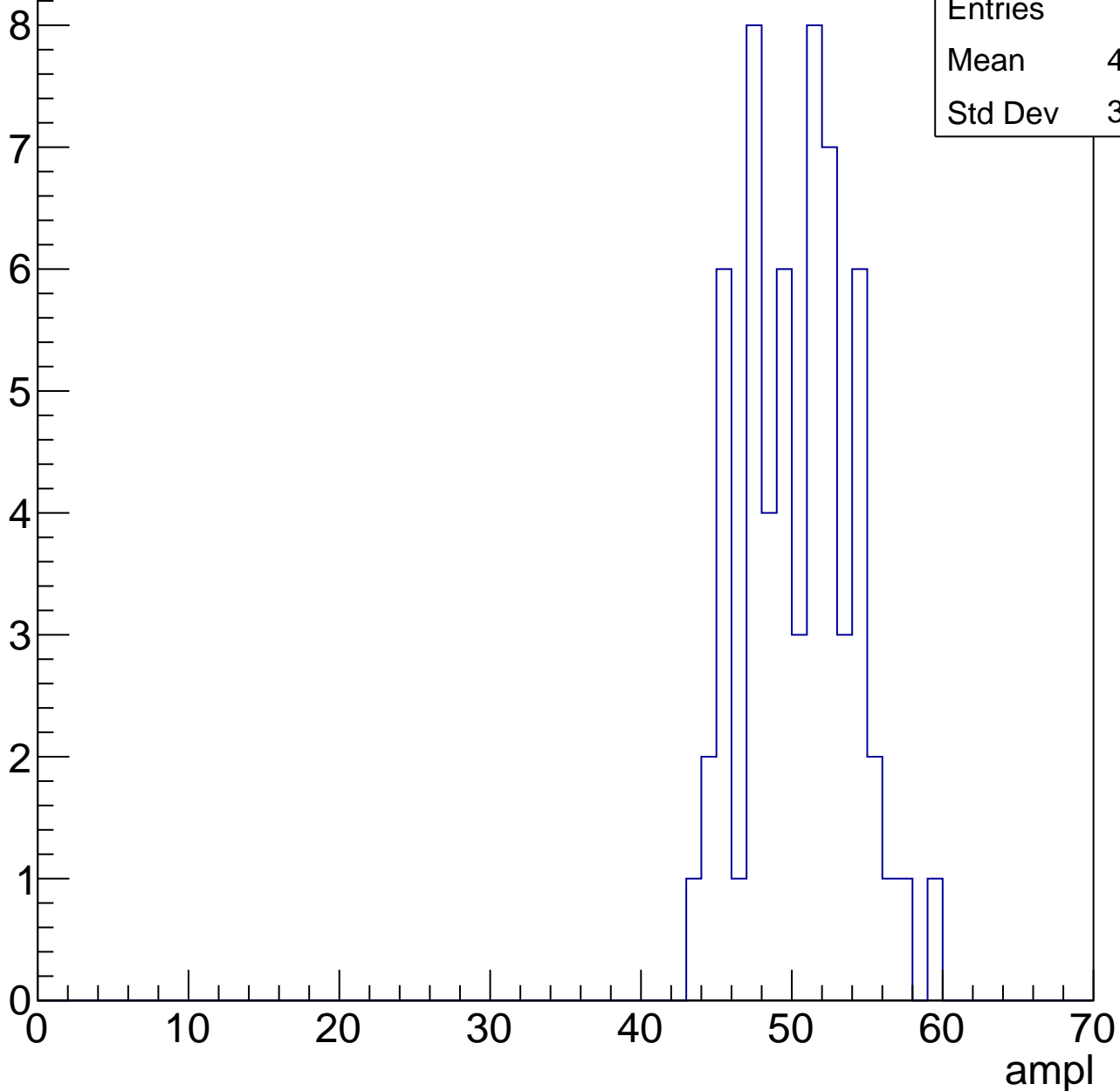


B1L100S, U19-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.93
Std Dev	3.549

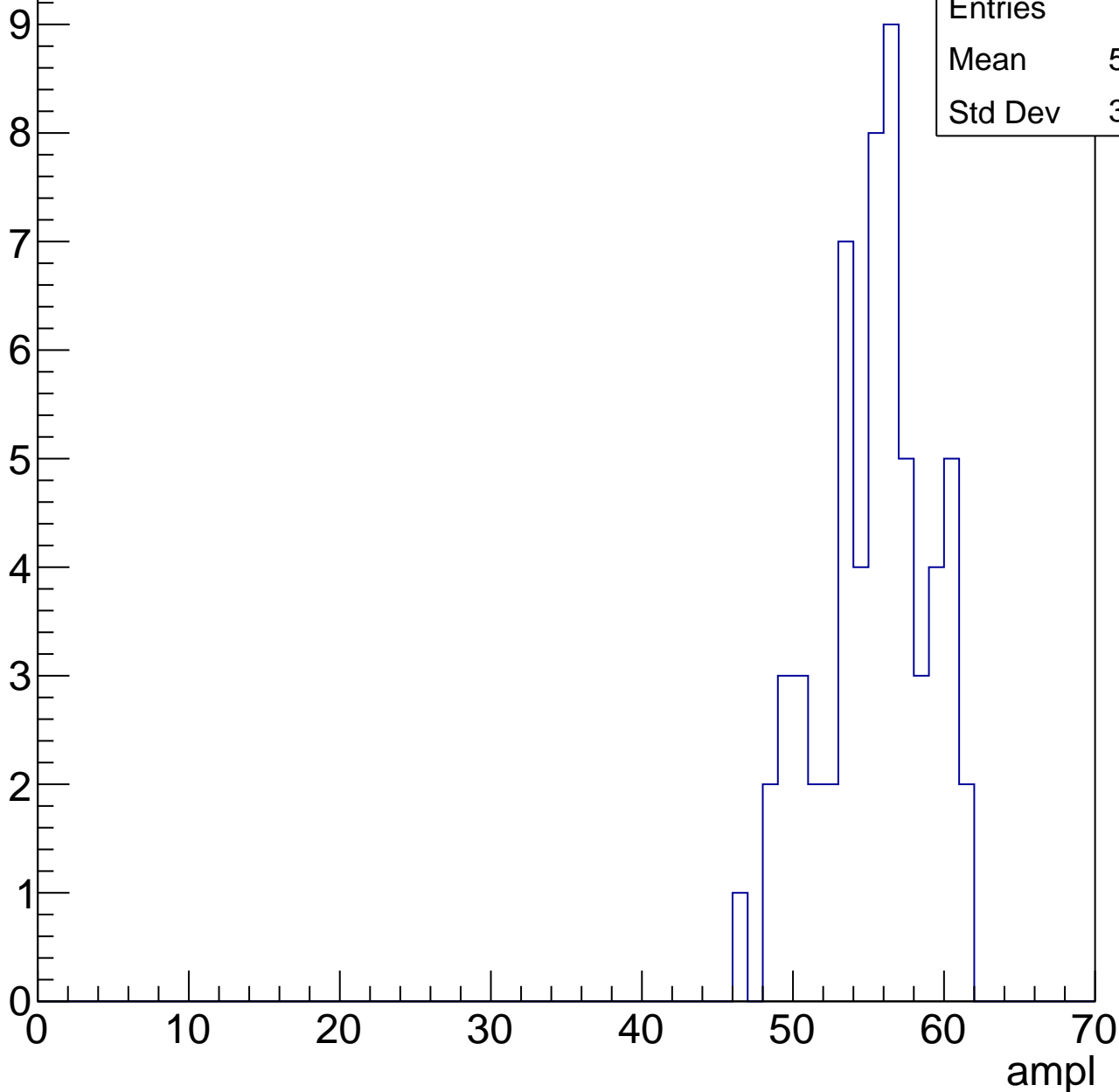


B1L100S, U19-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	54.88
Std Dev	3.559

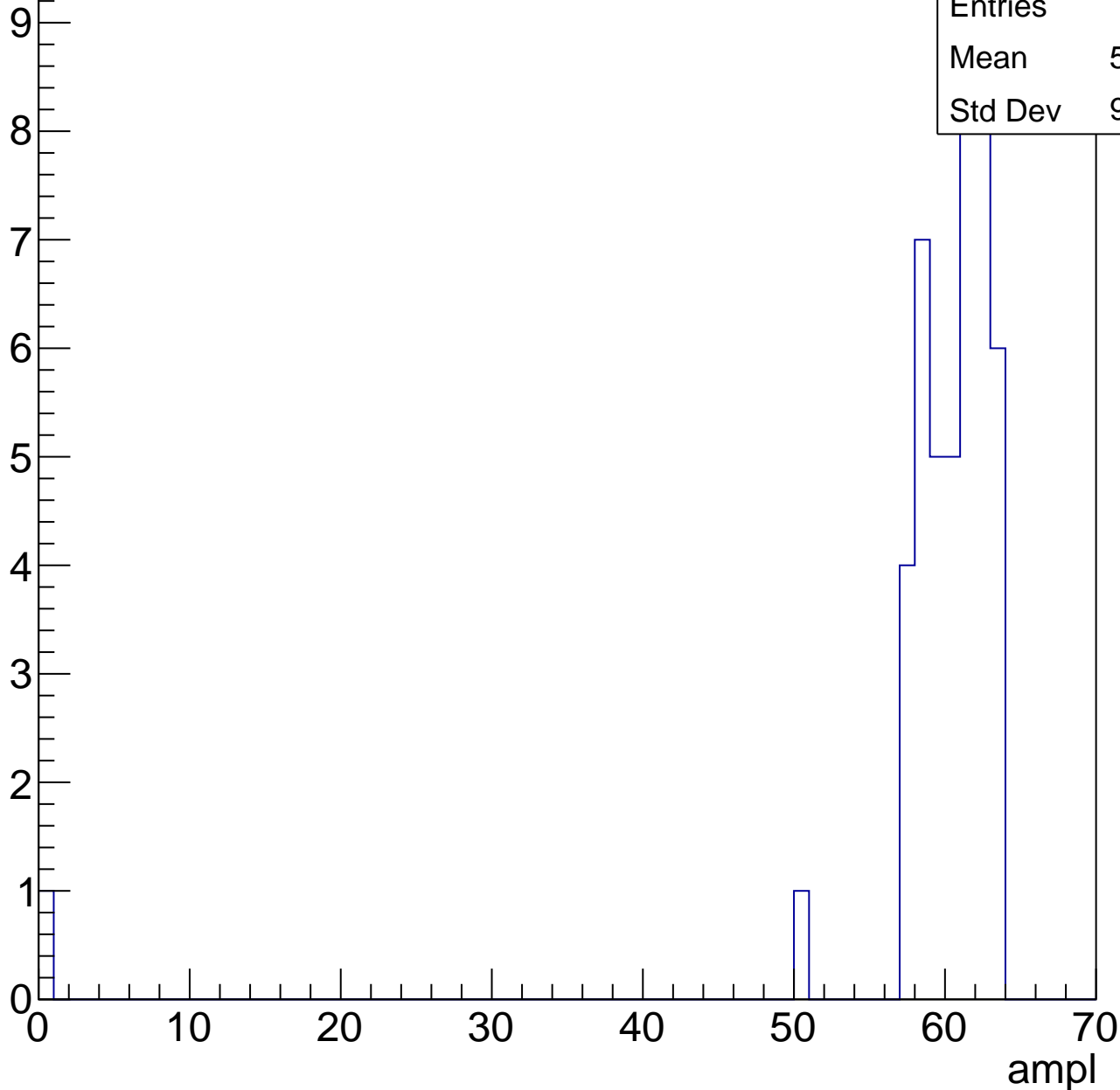


B1L100S, U19-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.76
Std Dev	9.085



B1L100S, U19-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch76, adc0

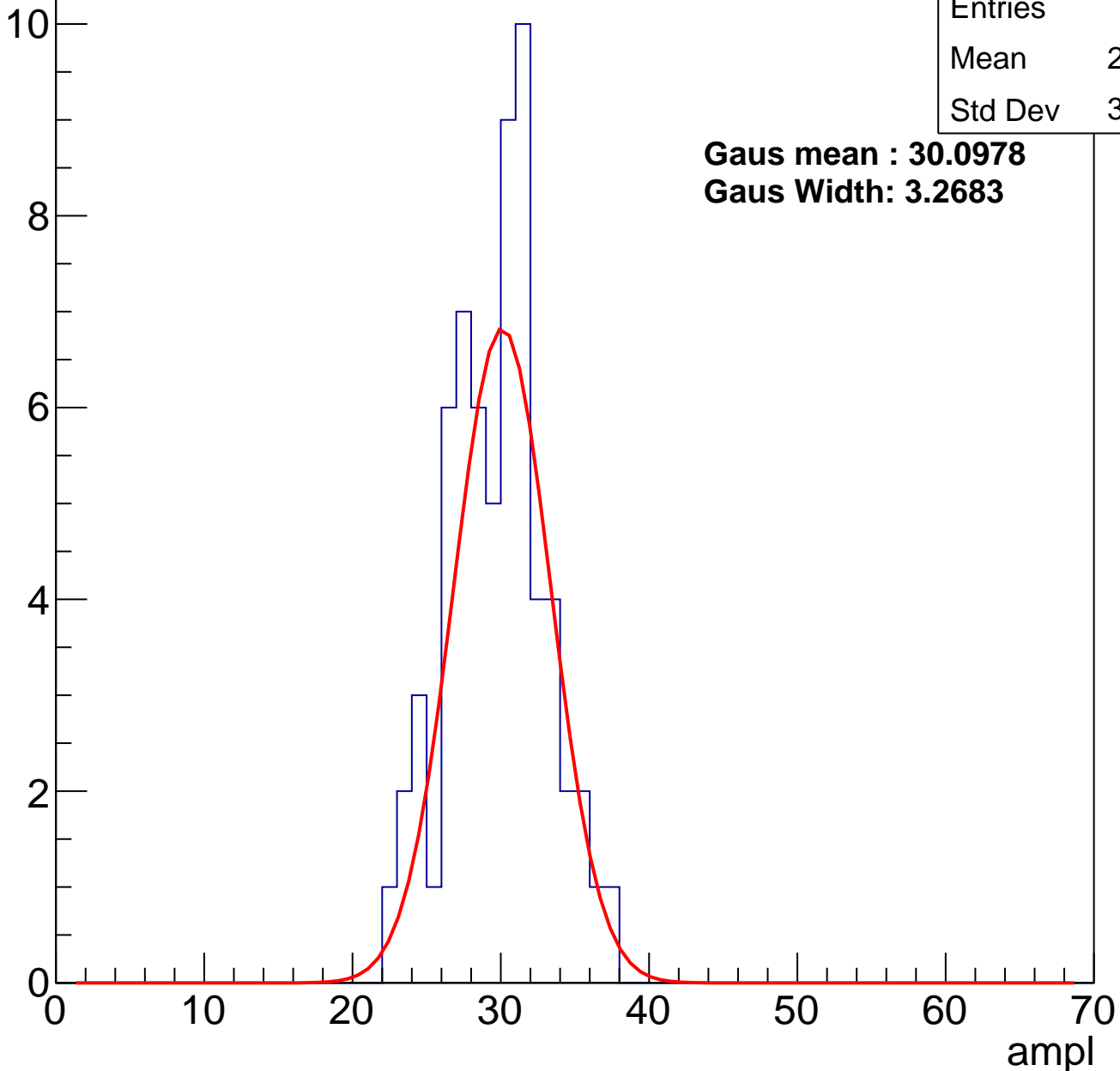
calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	29.28
Std Dev	3.257

Gaus mean : 30.0978

Gaus Width: 3.2683

Entry



B1L100S, U19-ch76, adc1

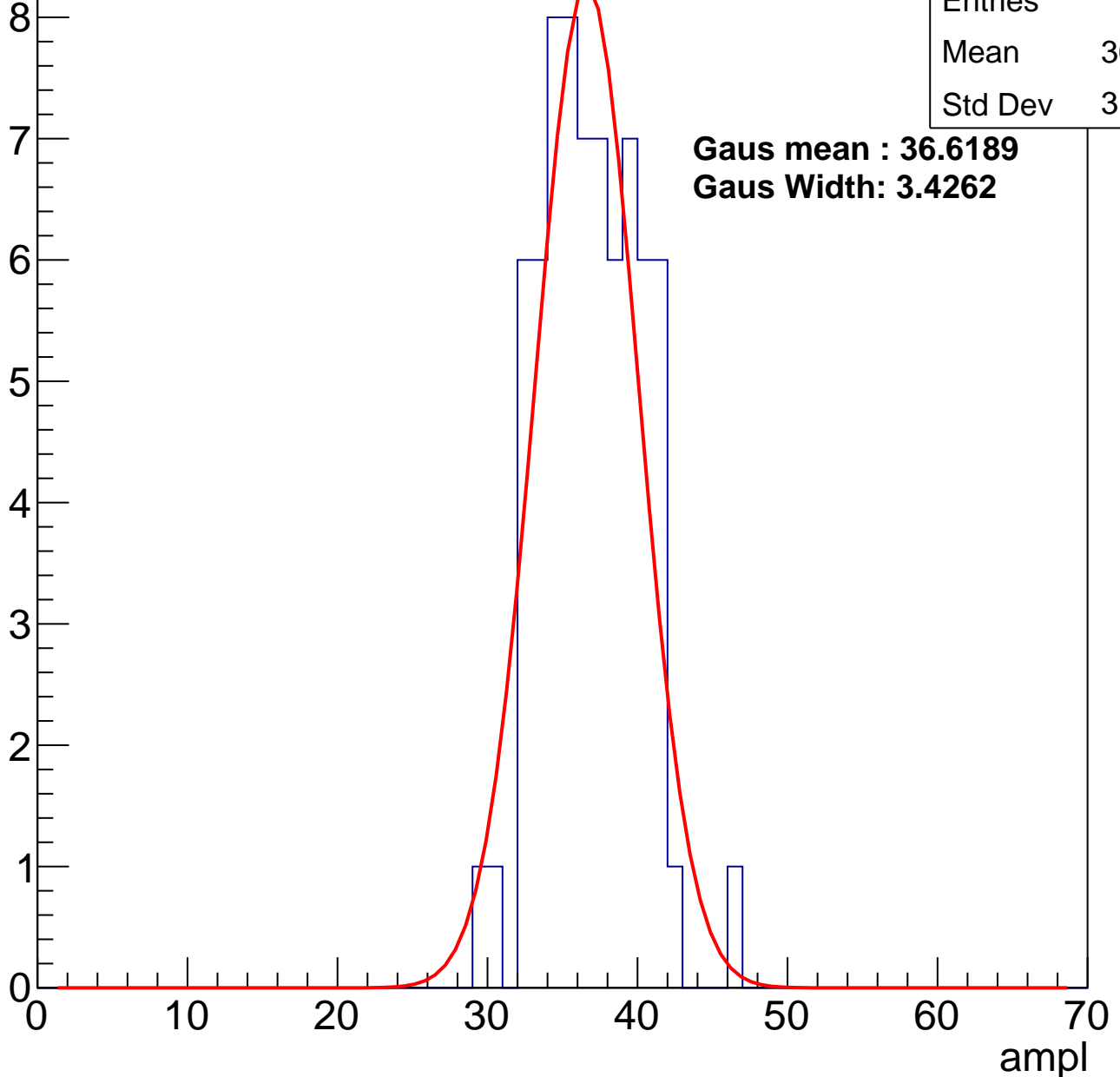
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.44
Std Dev	3.223

Gaus mean : 36.6189

Gaus Width: 3.4262



B1L100S, U19-ch76, adc2

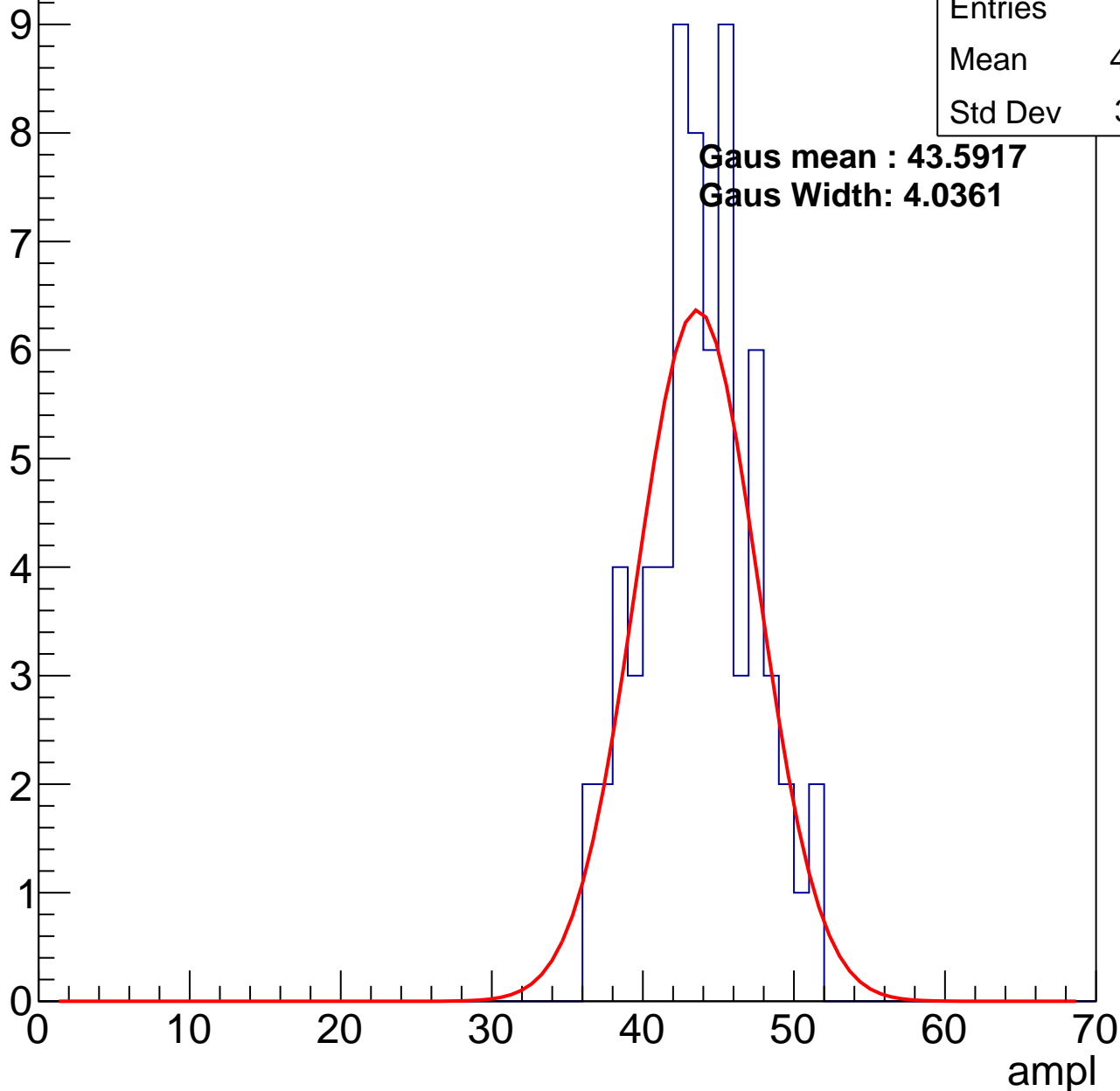
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.29
Std Dev	3.561

Gaus mean : 43.5917

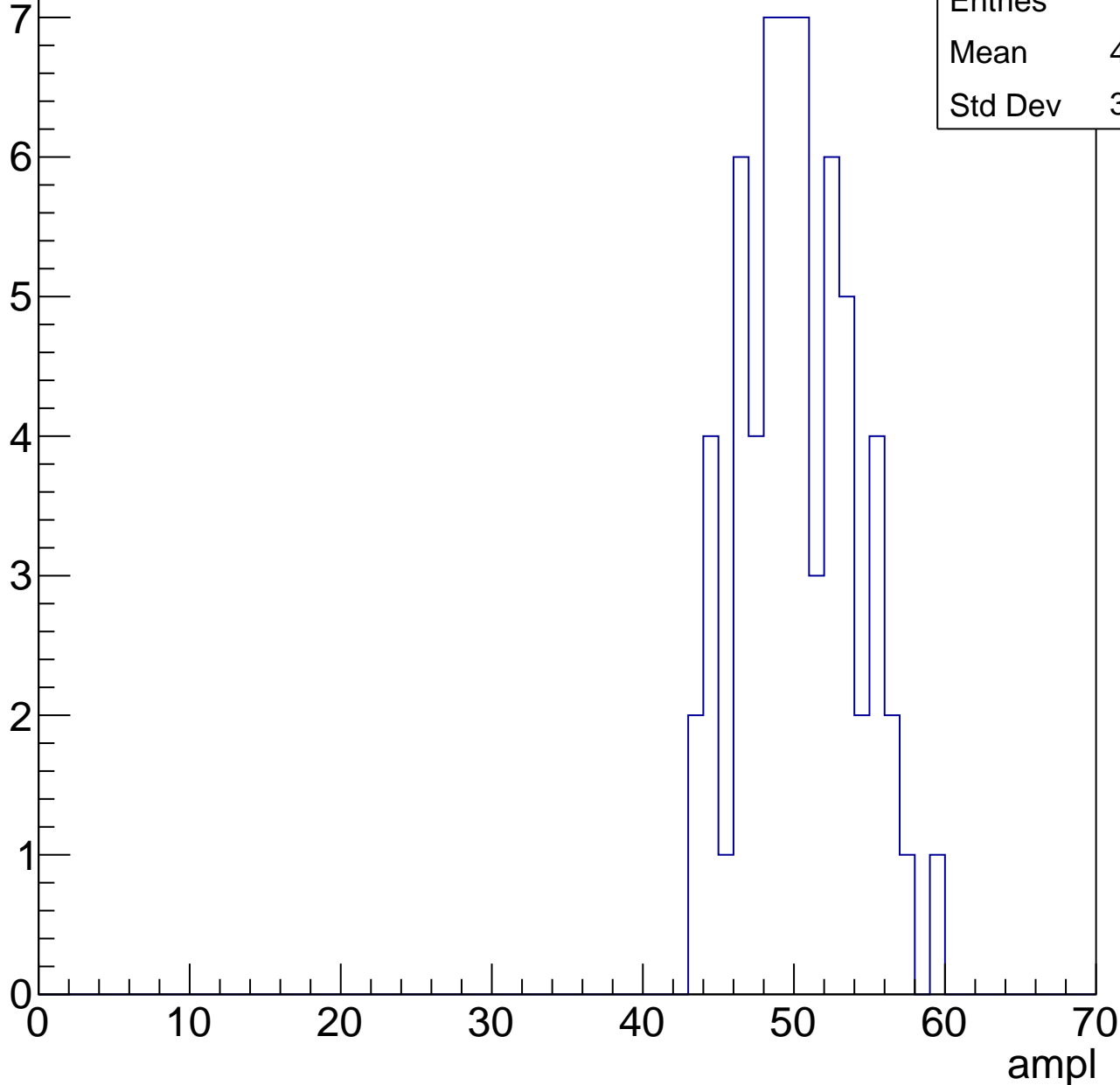
Gaus Width: 4.0361



B1L100S, U19-ch76, adc3

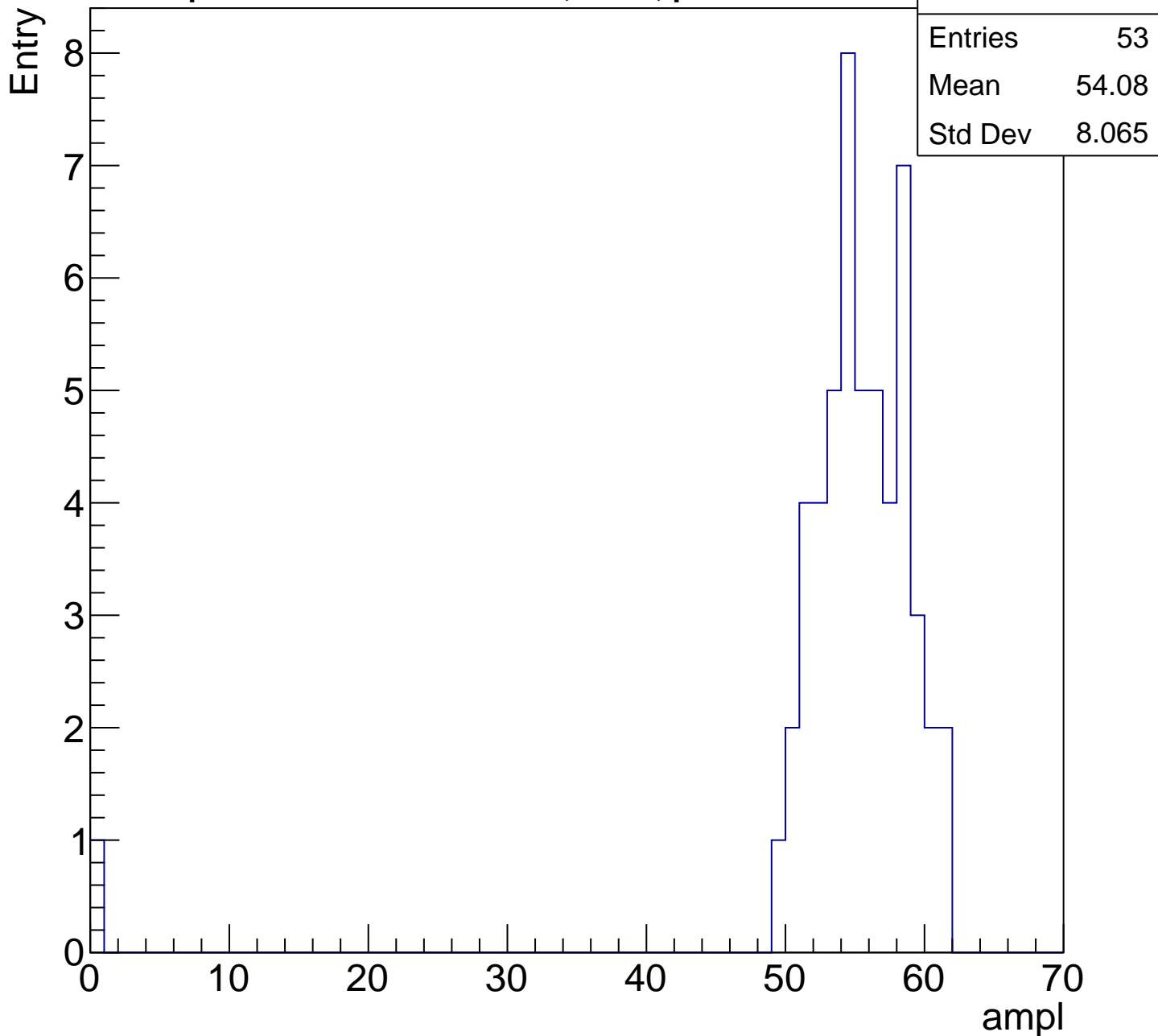
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

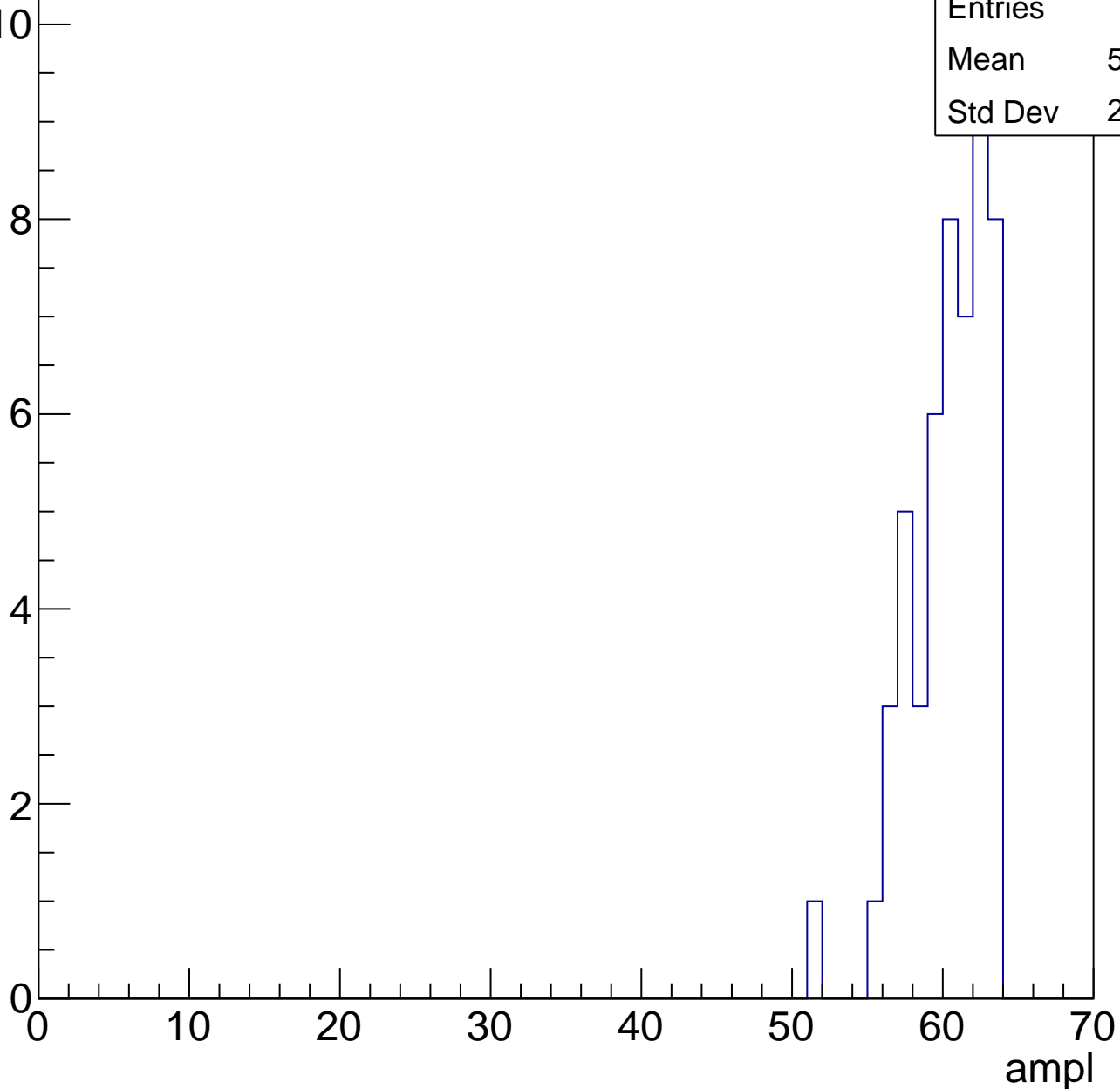


B1L100S, U19-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	59.96
Std Dev	2.542



B1L100S, U19-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch77, adc0

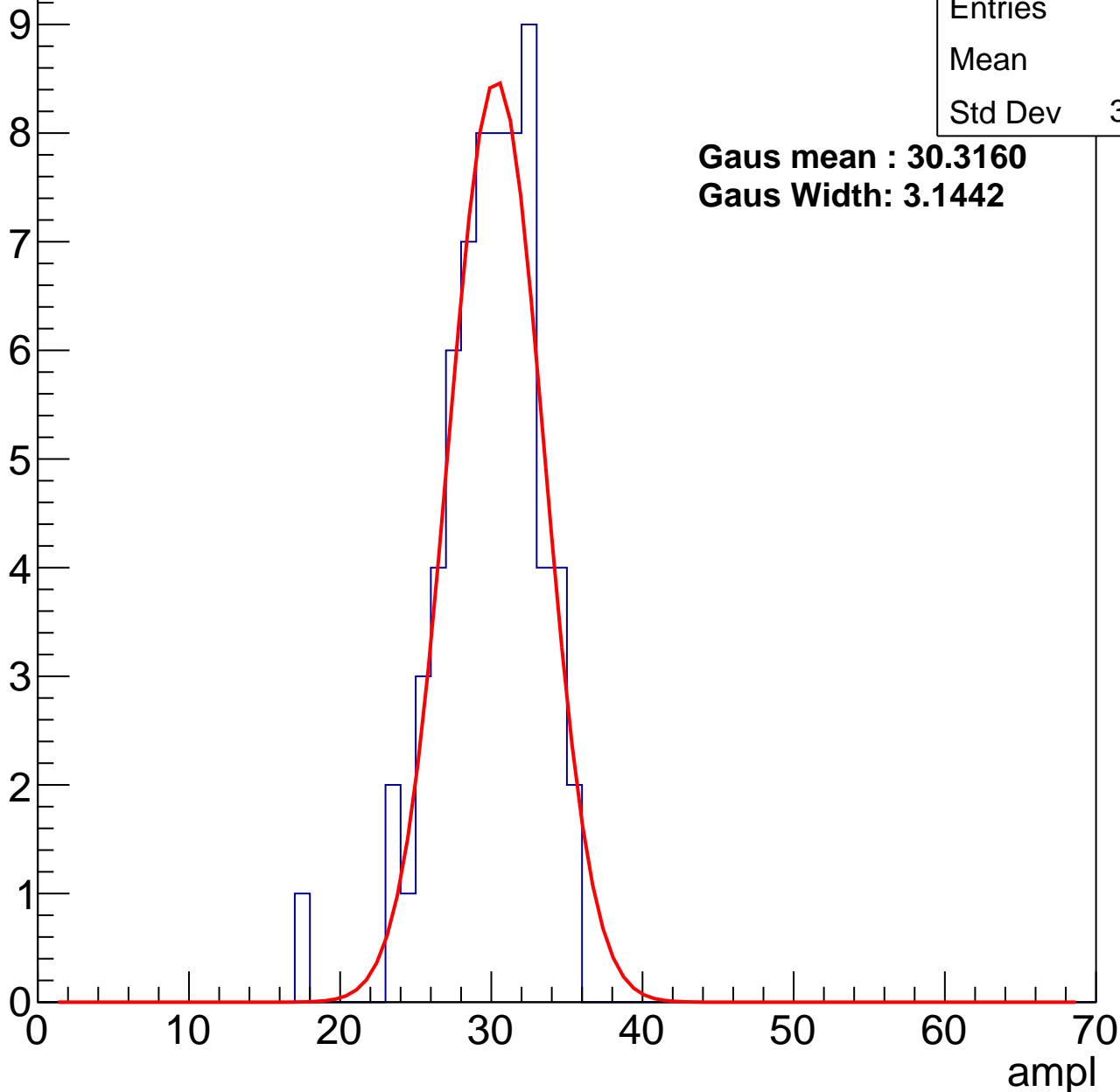
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.4
Std Dev	3.237

Gaus mean : 30.3160

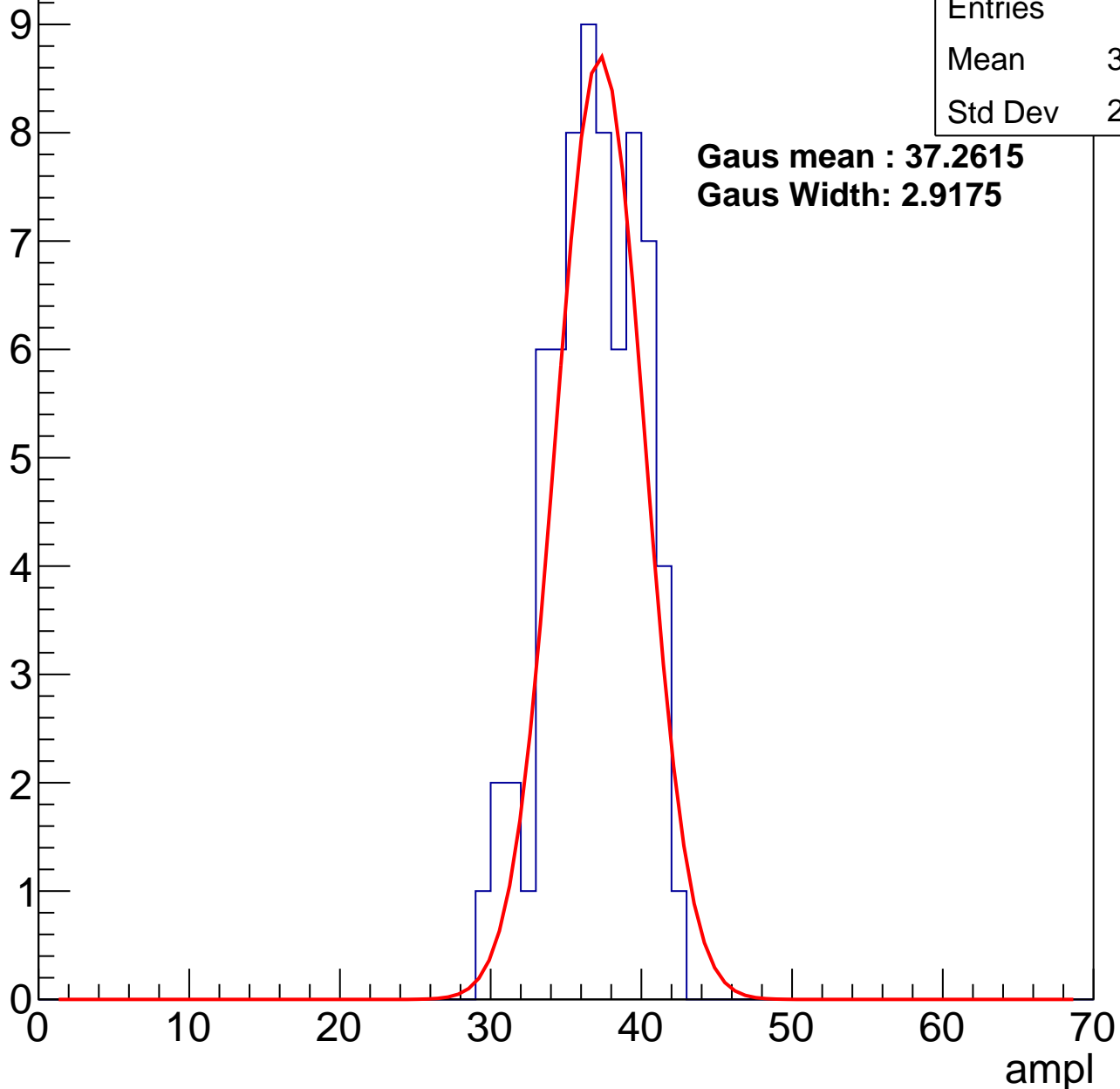
Gaus Width: 3.1442



B1L100S, U19-ch77, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch77, adc2

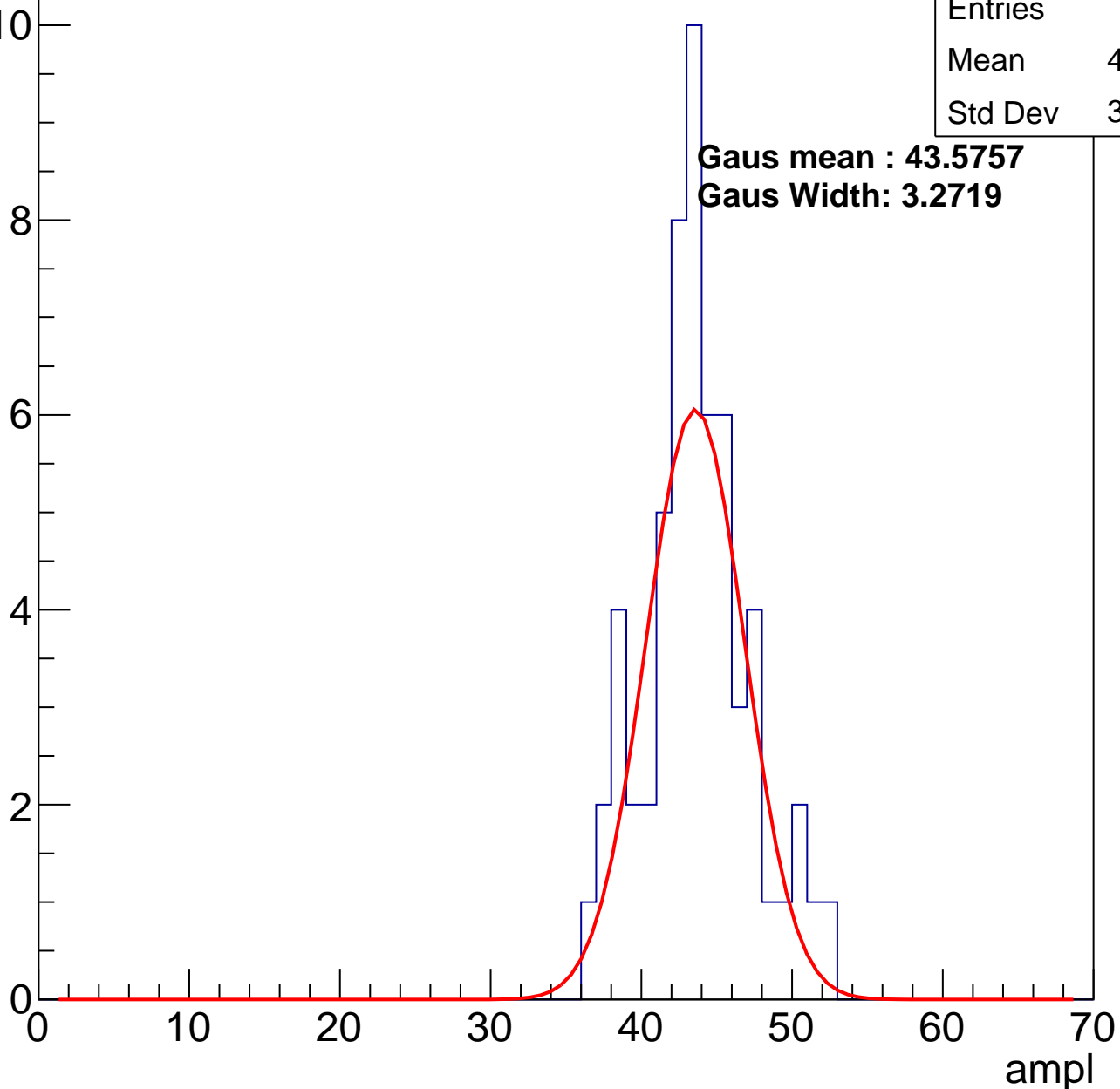
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.24
Std Dev	3.495

Gaus mean : 43.5757

Gaus Width: 3.2719

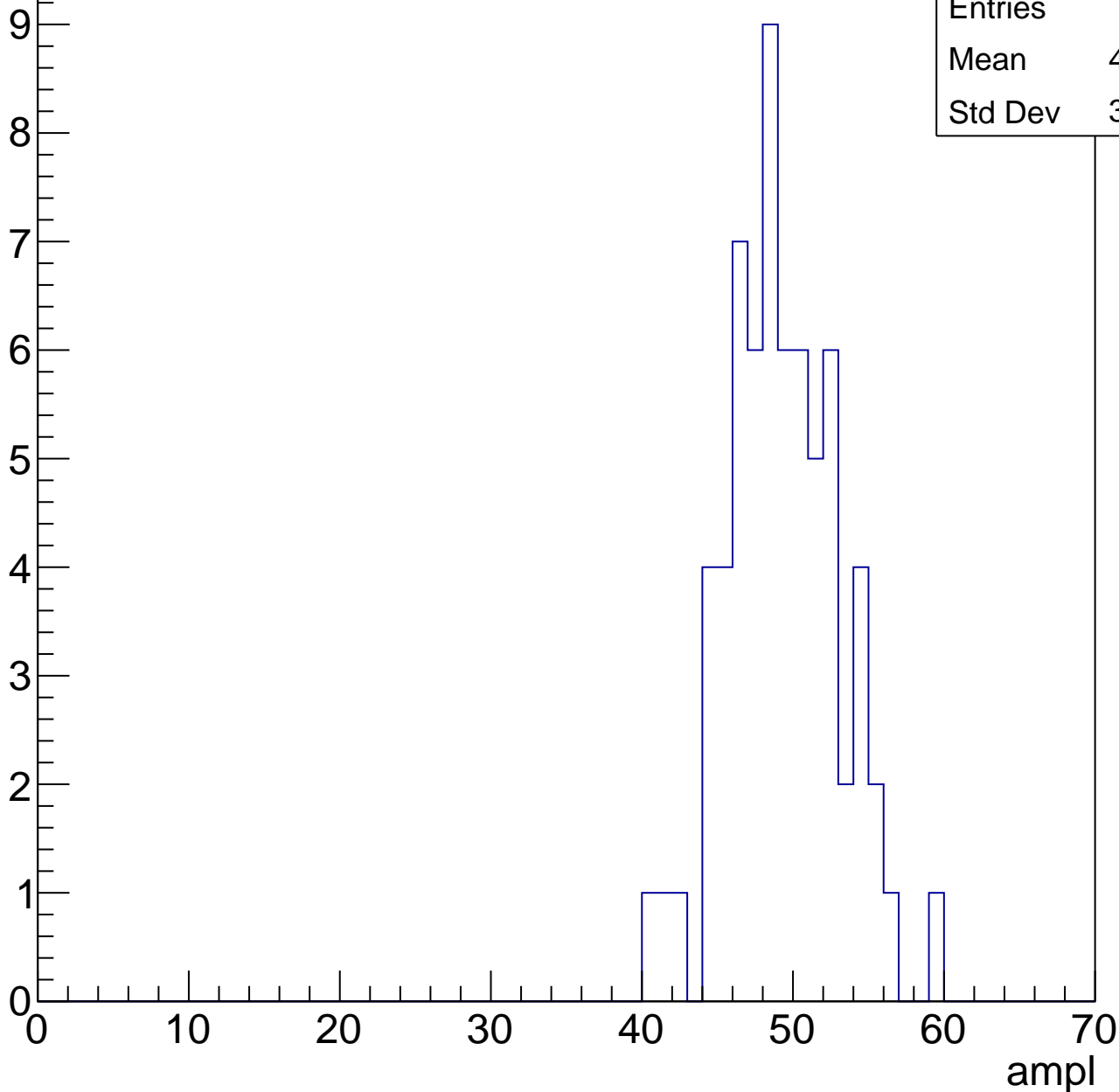


B1L100S, U19-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

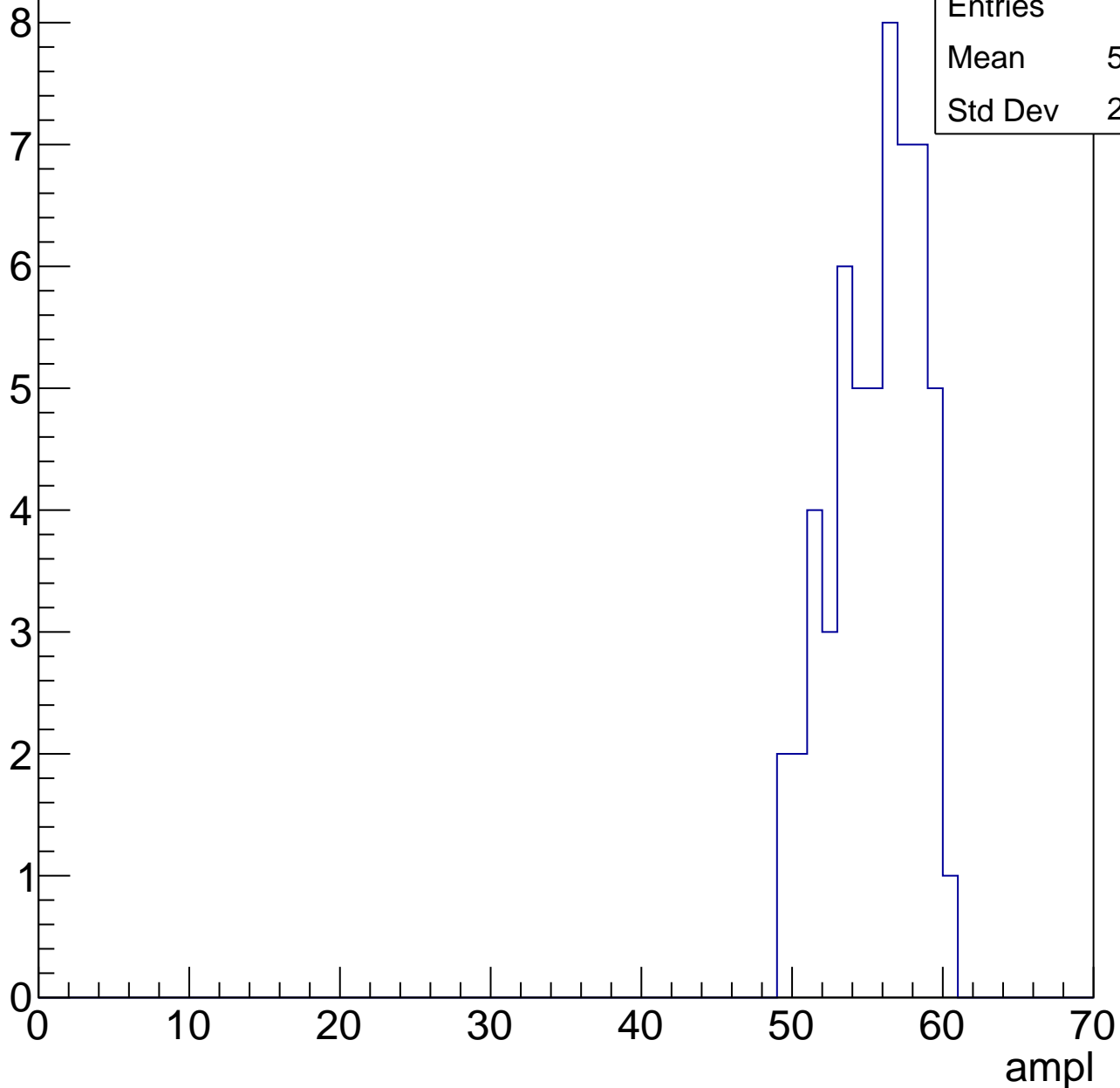
Entries	66
Mean	48.83
Std Dev	3.662



B1L100S, U19-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch77, adc5

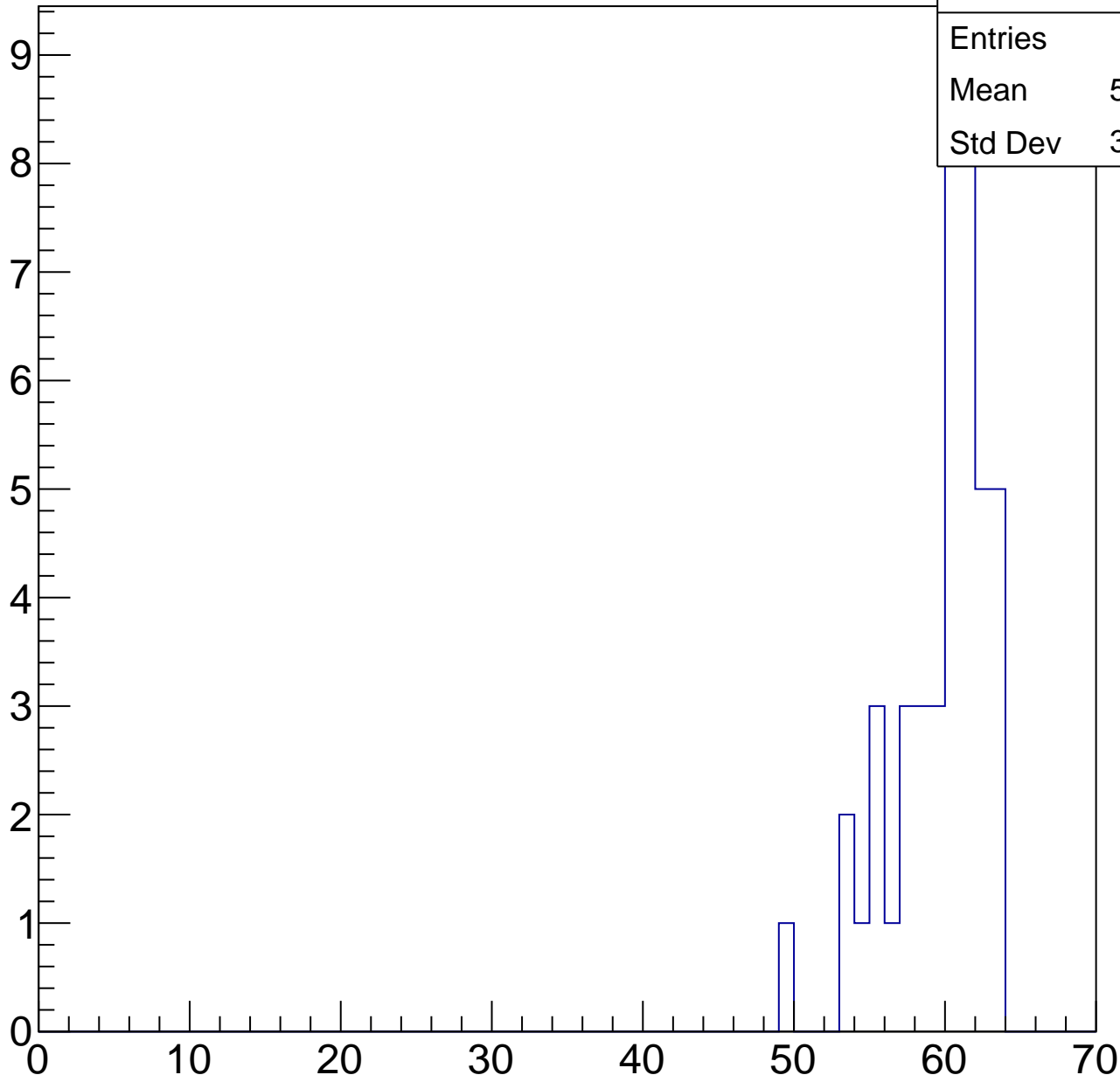
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.23
Std Dev	3.147

ampl

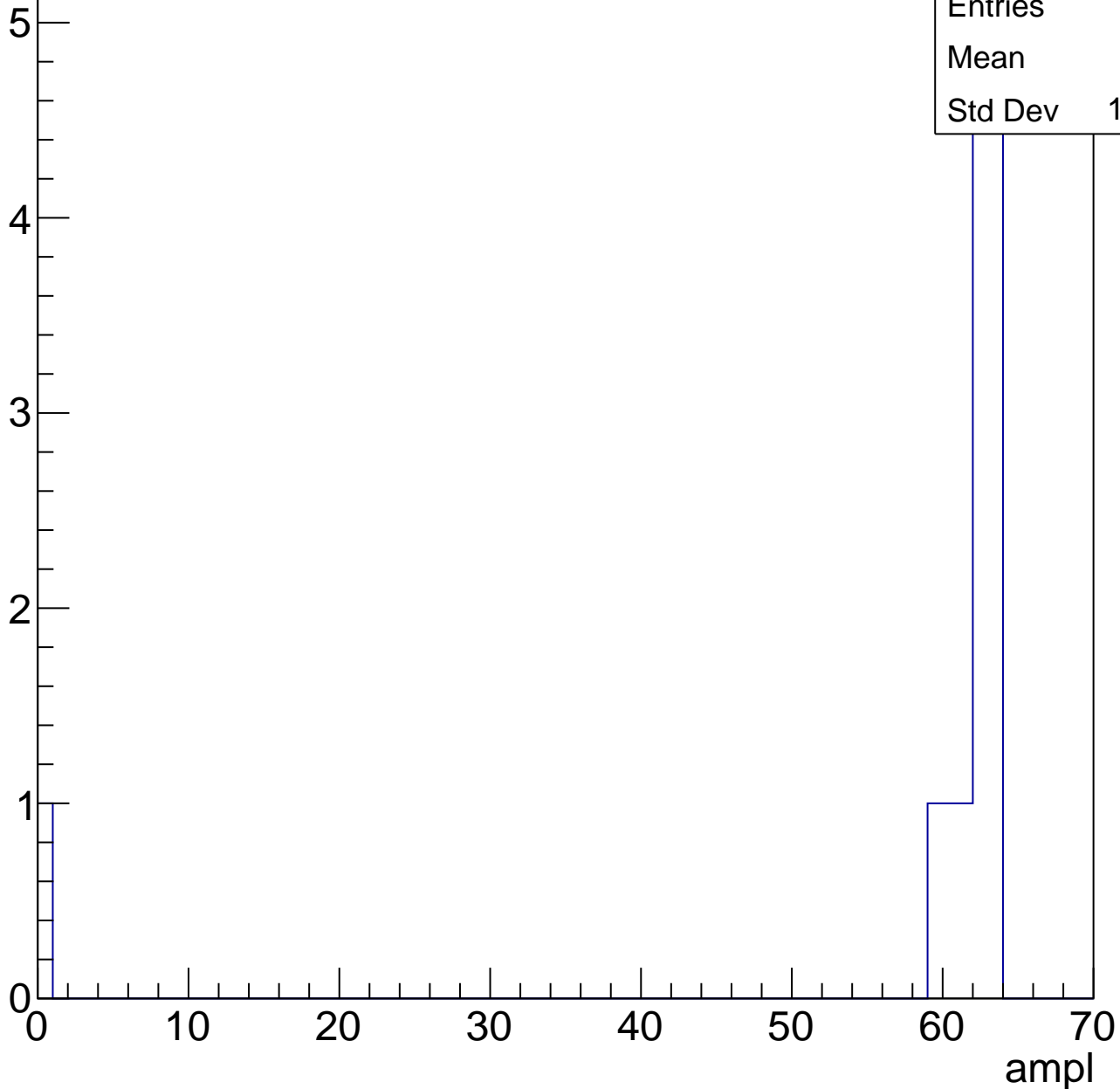


B1L100S, U19-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	57.5
Std Dev	15.99



B1L100S, U19-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch78, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	27.73
Std Dev	5.96

Gaus mean : 27.6947

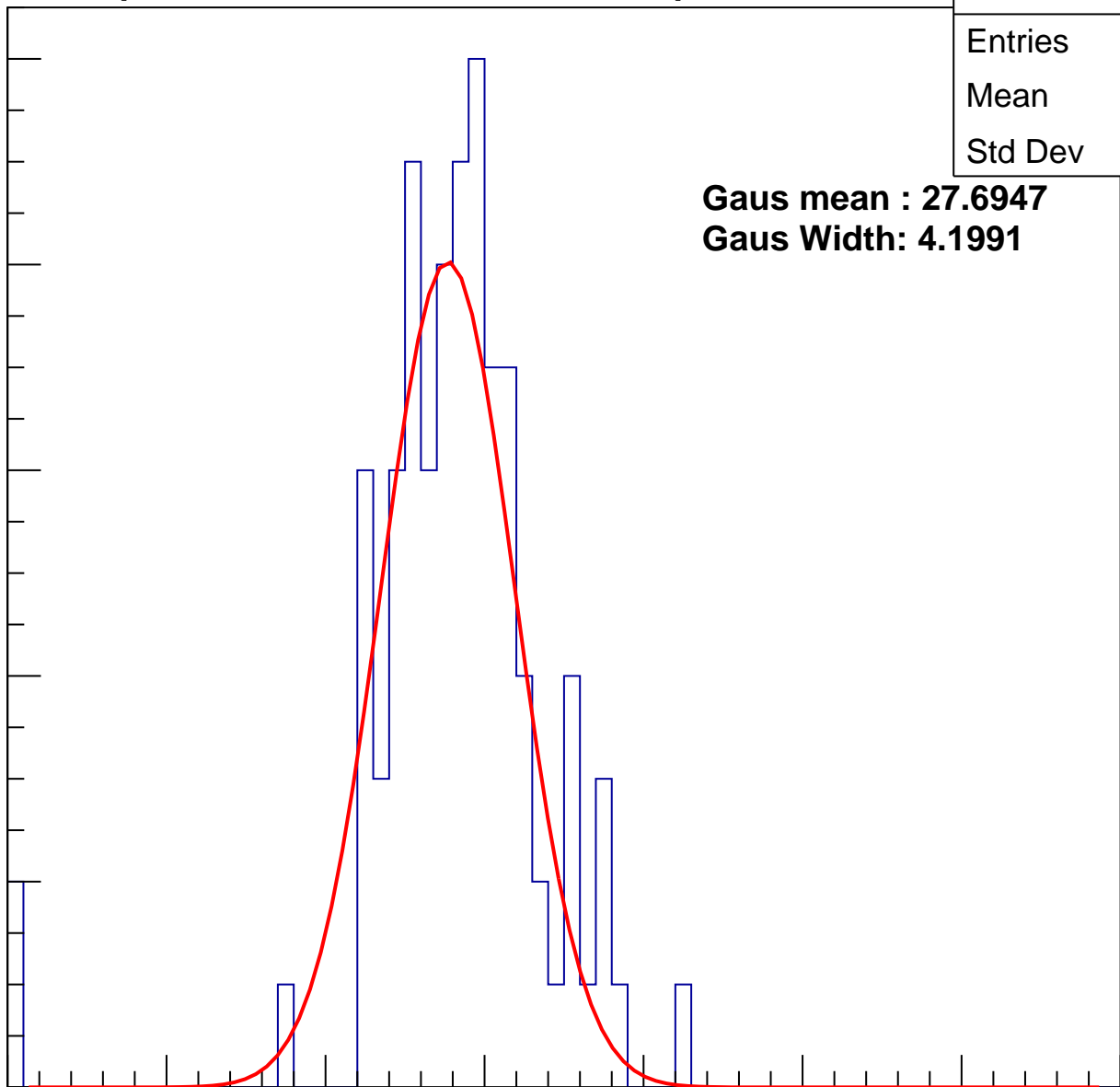
Gaus Width: 4.1991

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch78, adc1

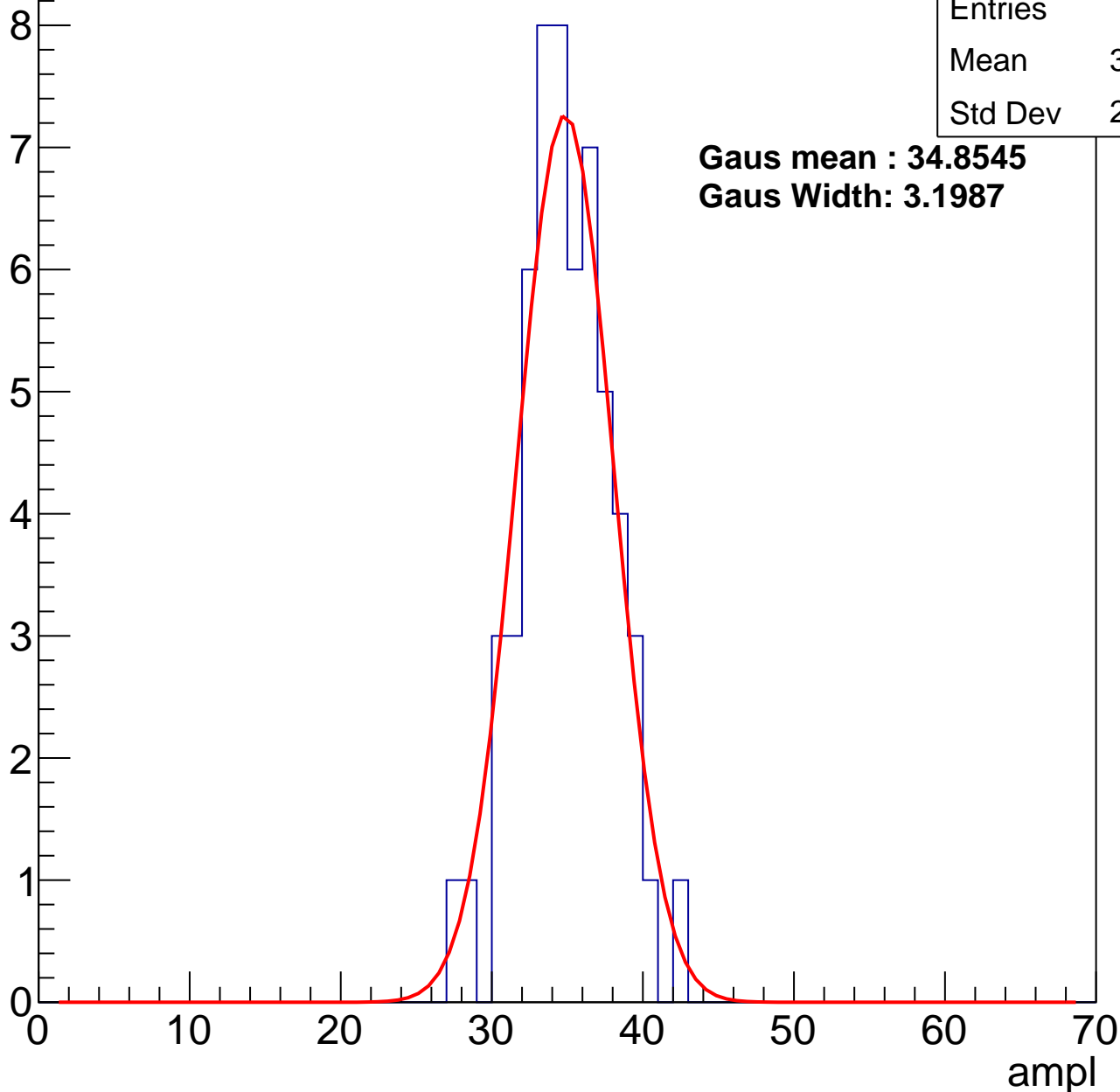
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.46
Std Dev	2.956

Gaus mean : 34.8545

Gaus Width: 3.1987



B1L100S, U19-ch78, adc2

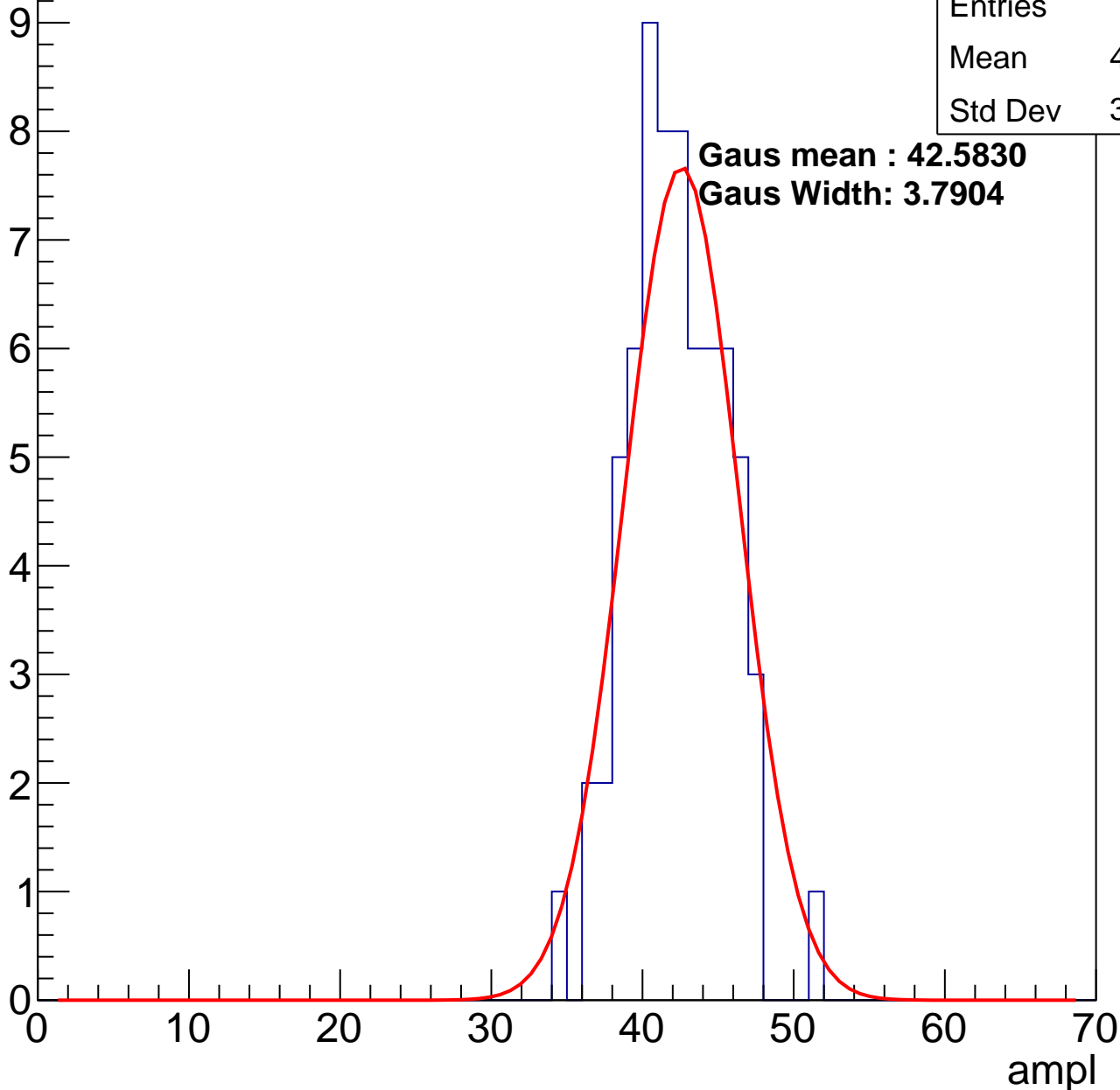
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	41.79
Std Dev	3.165

Gaus mean : 42.5830

Gaus Width: 3.7904

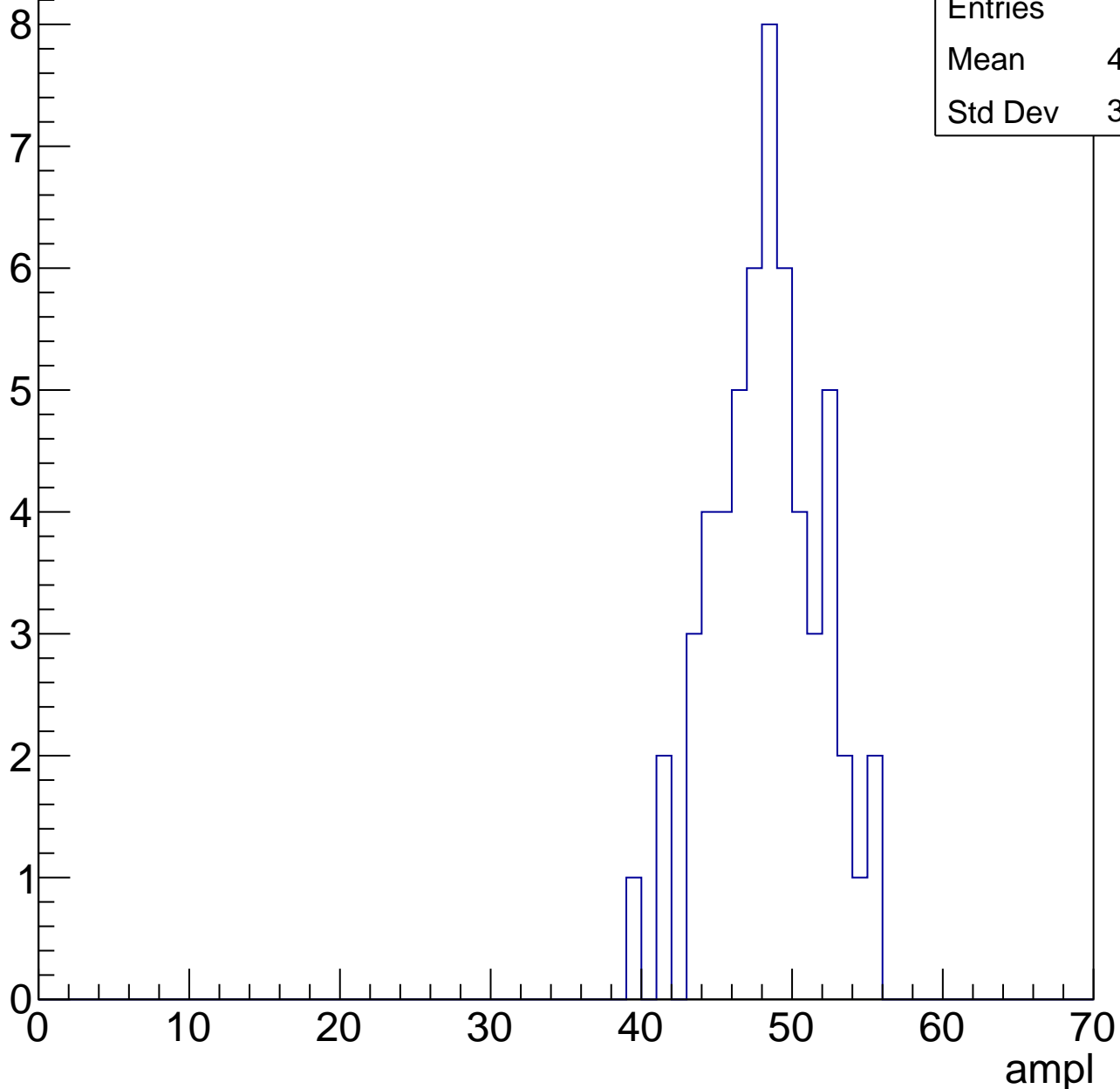


B1L100S, U19-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	47.84
Std Dev	3.514

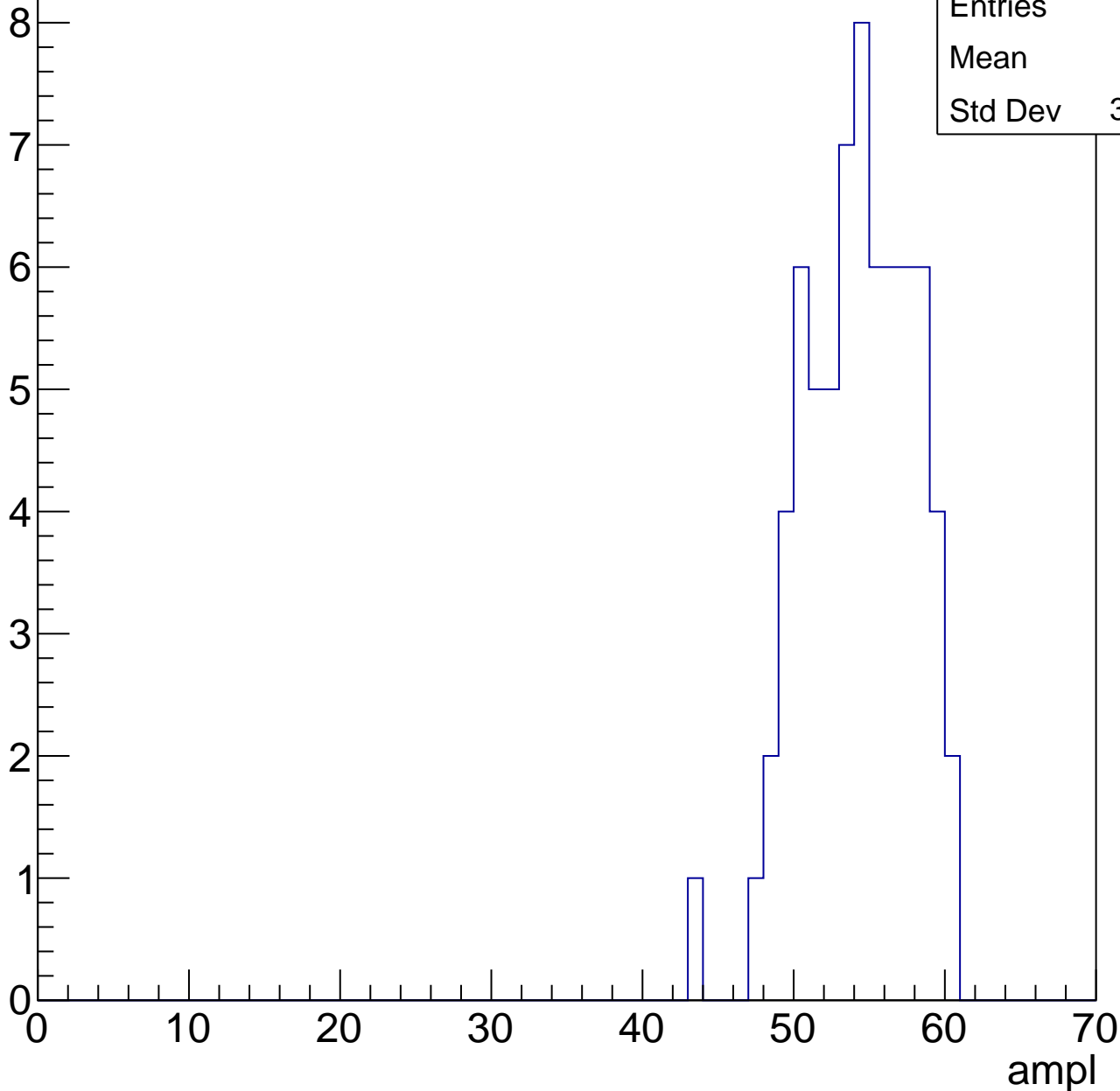


B1L100S, U19-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	53.8
Std Dev	3.529



B1L100S, U19-ch78, adc5

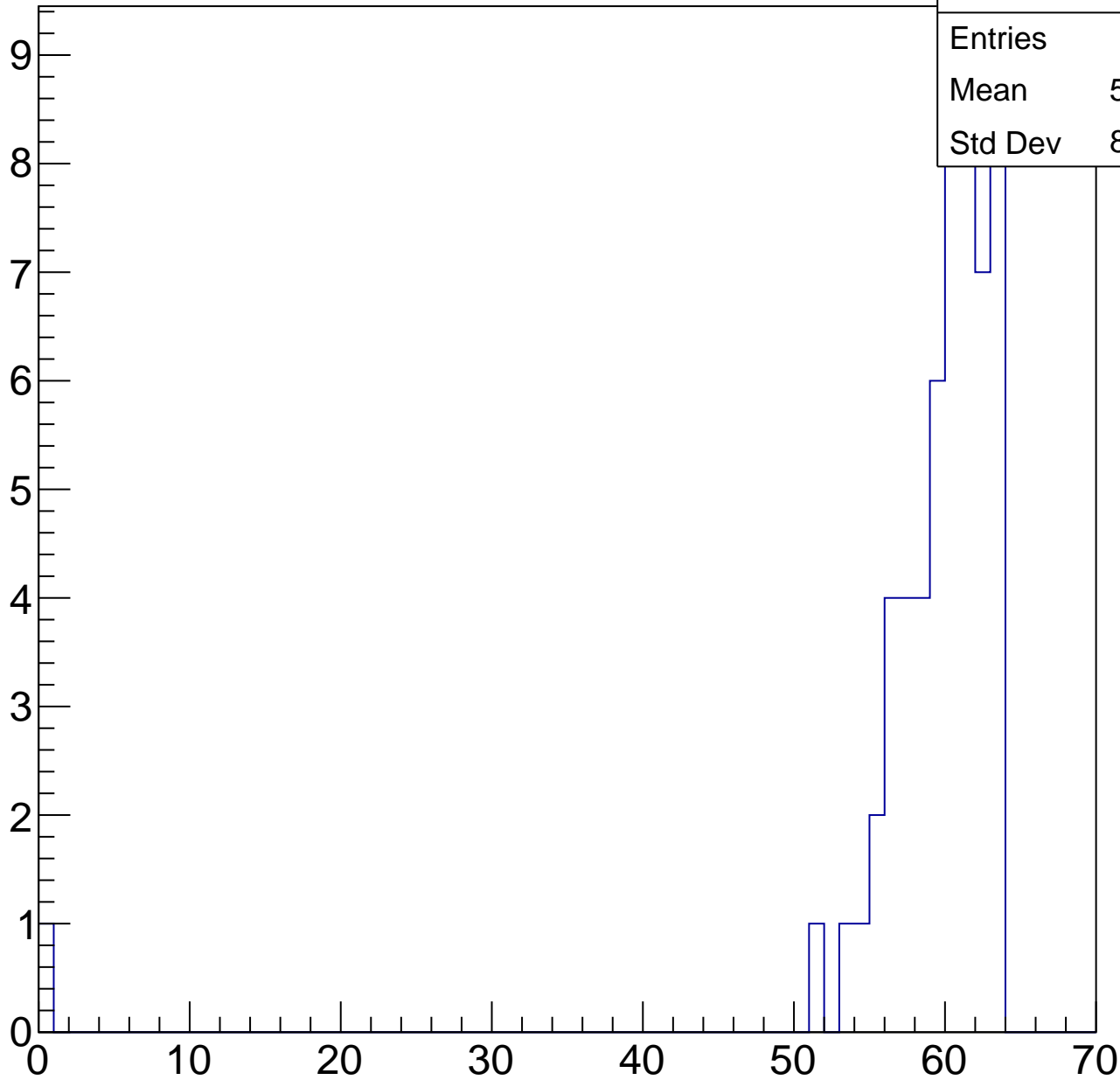
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.45
Std Dev	8.358

ampl



B1L100S, U19-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch79, adc0

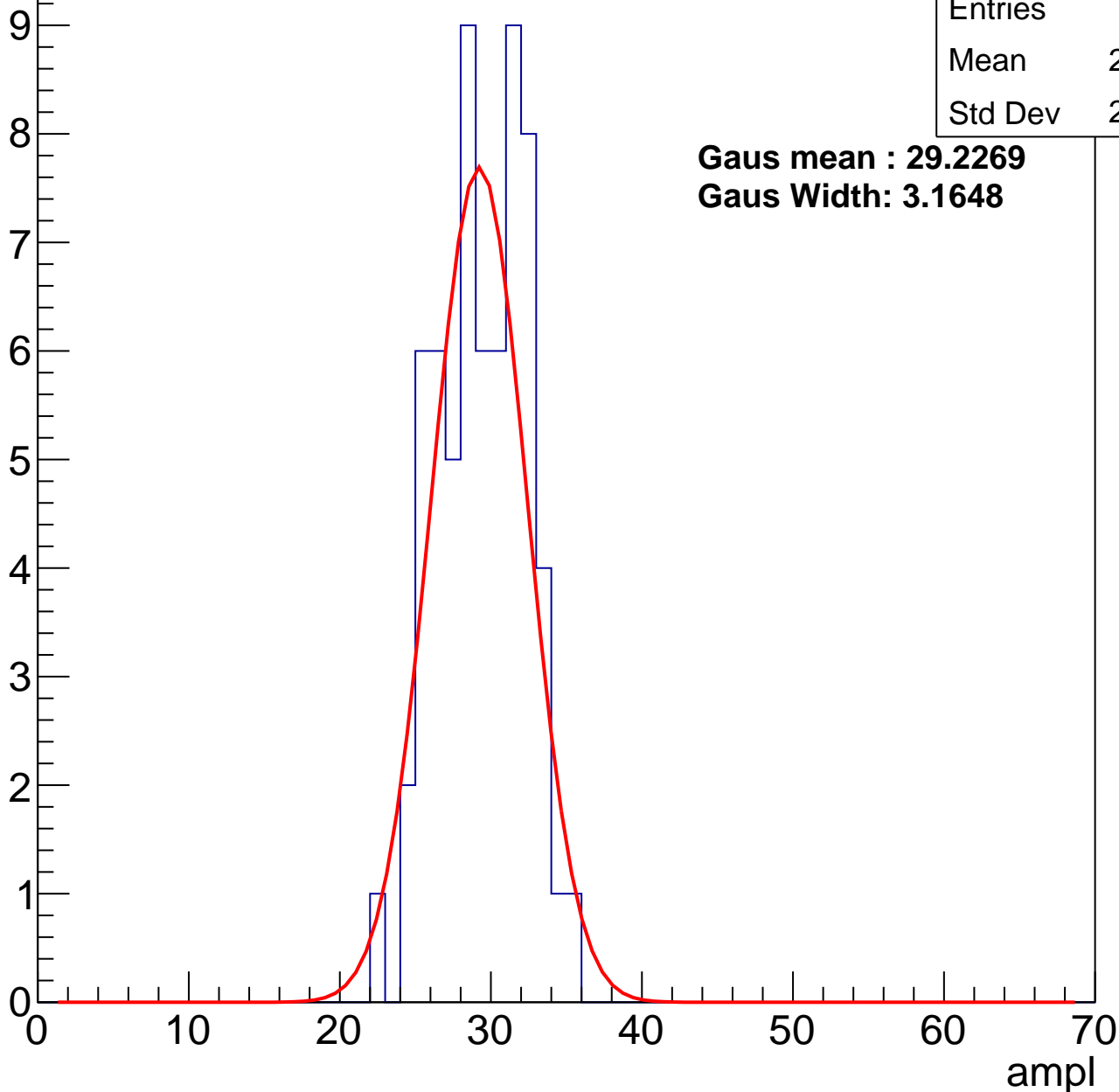
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.95
Std Dev	2.842

Gaus mean : 29.2269

Gaus Width: 3.1648

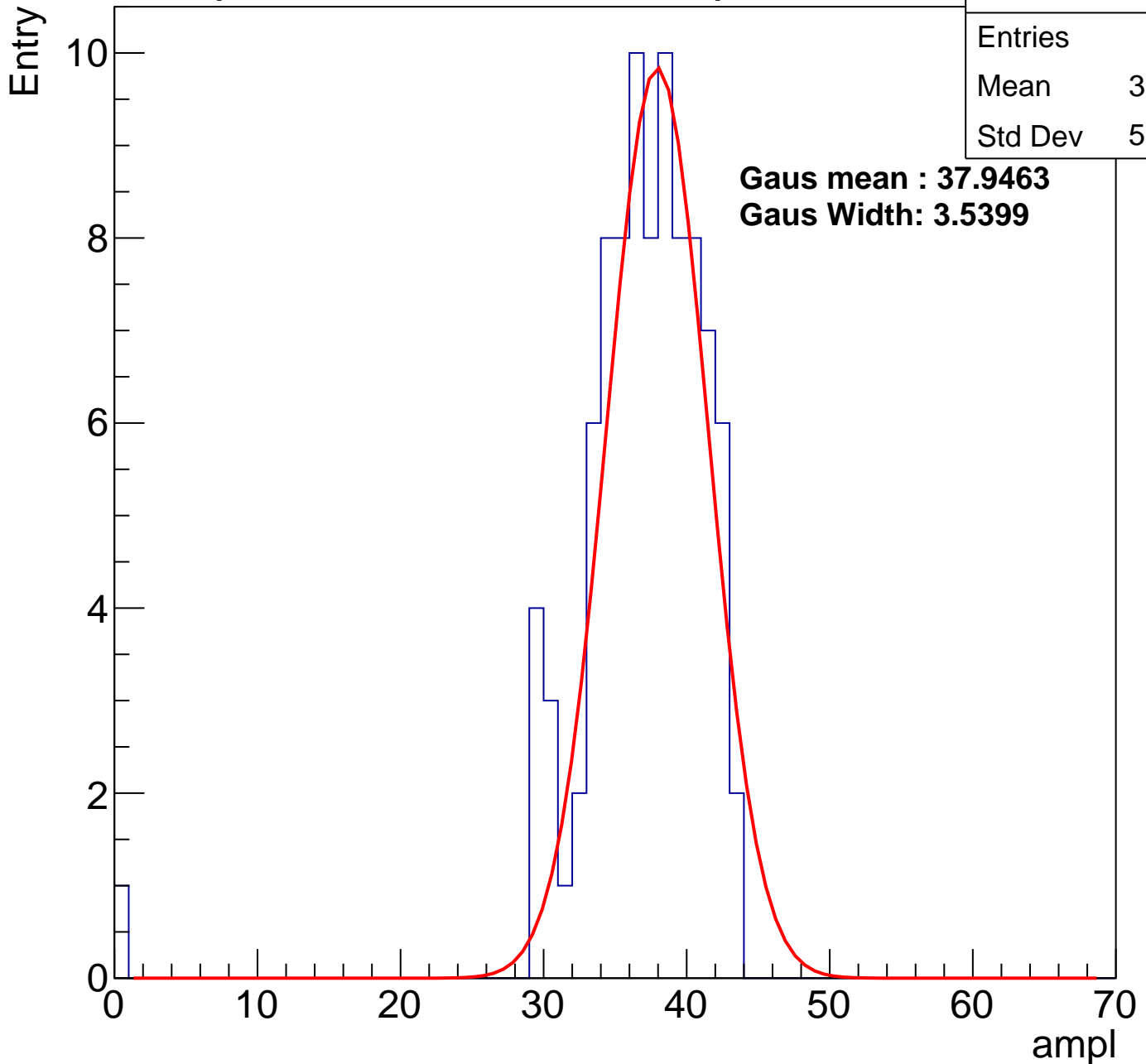


B1L100S, U19-ch79, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	36.35
Std Dev	5.189

Gaus mean : 37.9463
Gaus Width: 3.5399



B1L100S, U19-ch79, adc2

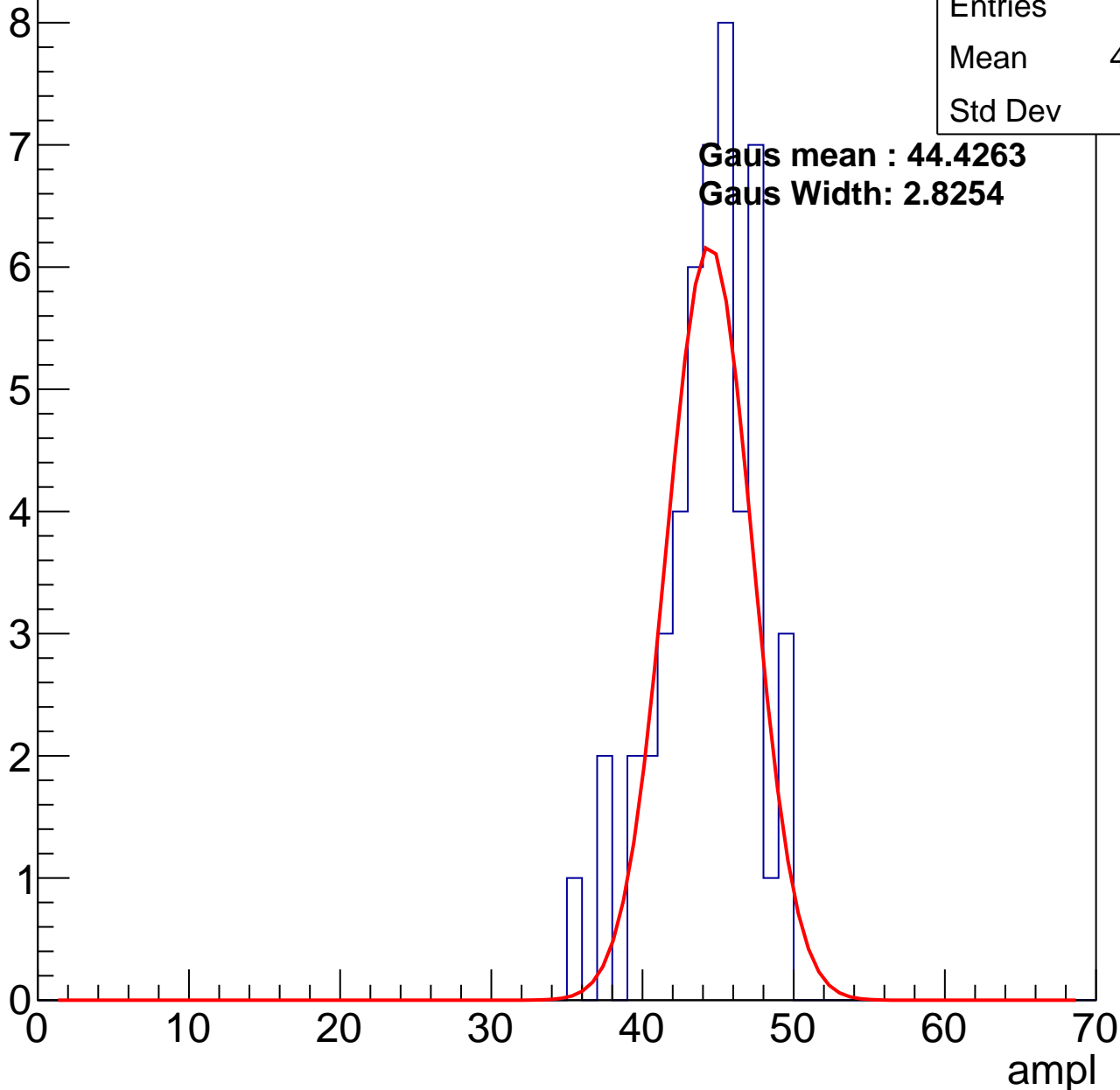
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	43.84
Std Dev	3.12

Gaus mean : 44.4263

Gaus Width: 2.8254



B1L100S, U19-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

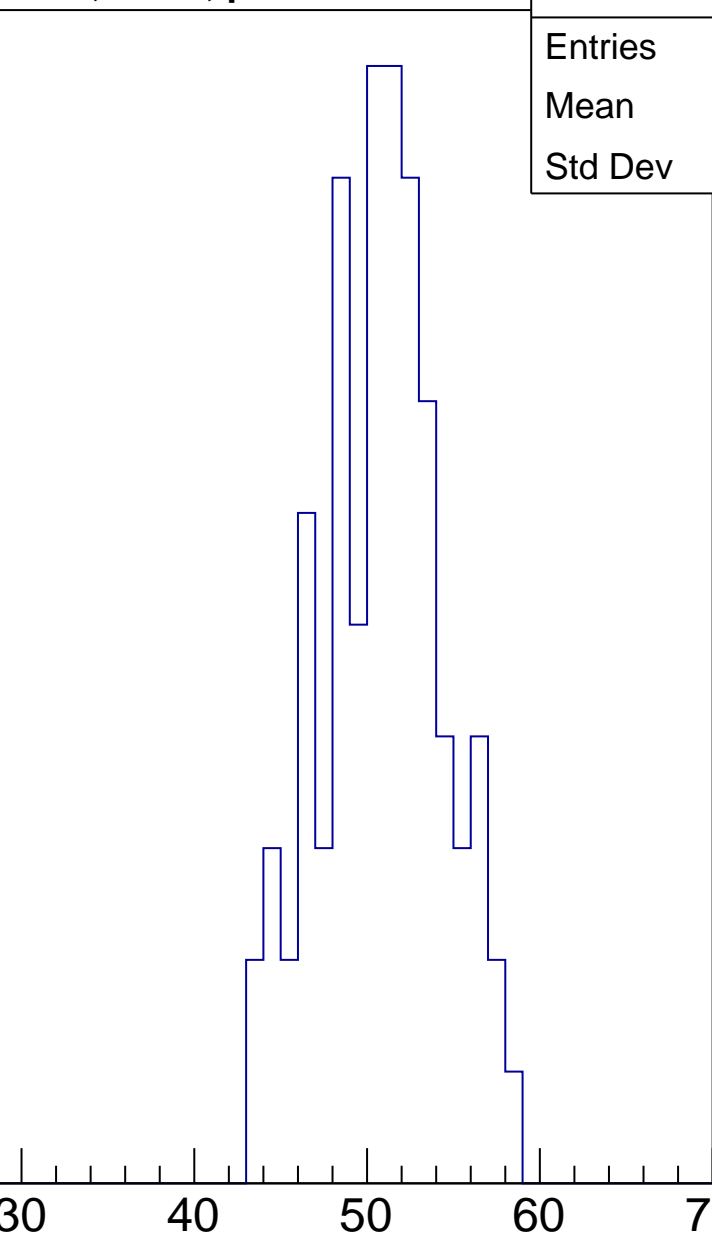
Entries	80
Mean	50.35
Std Dev	3.472

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

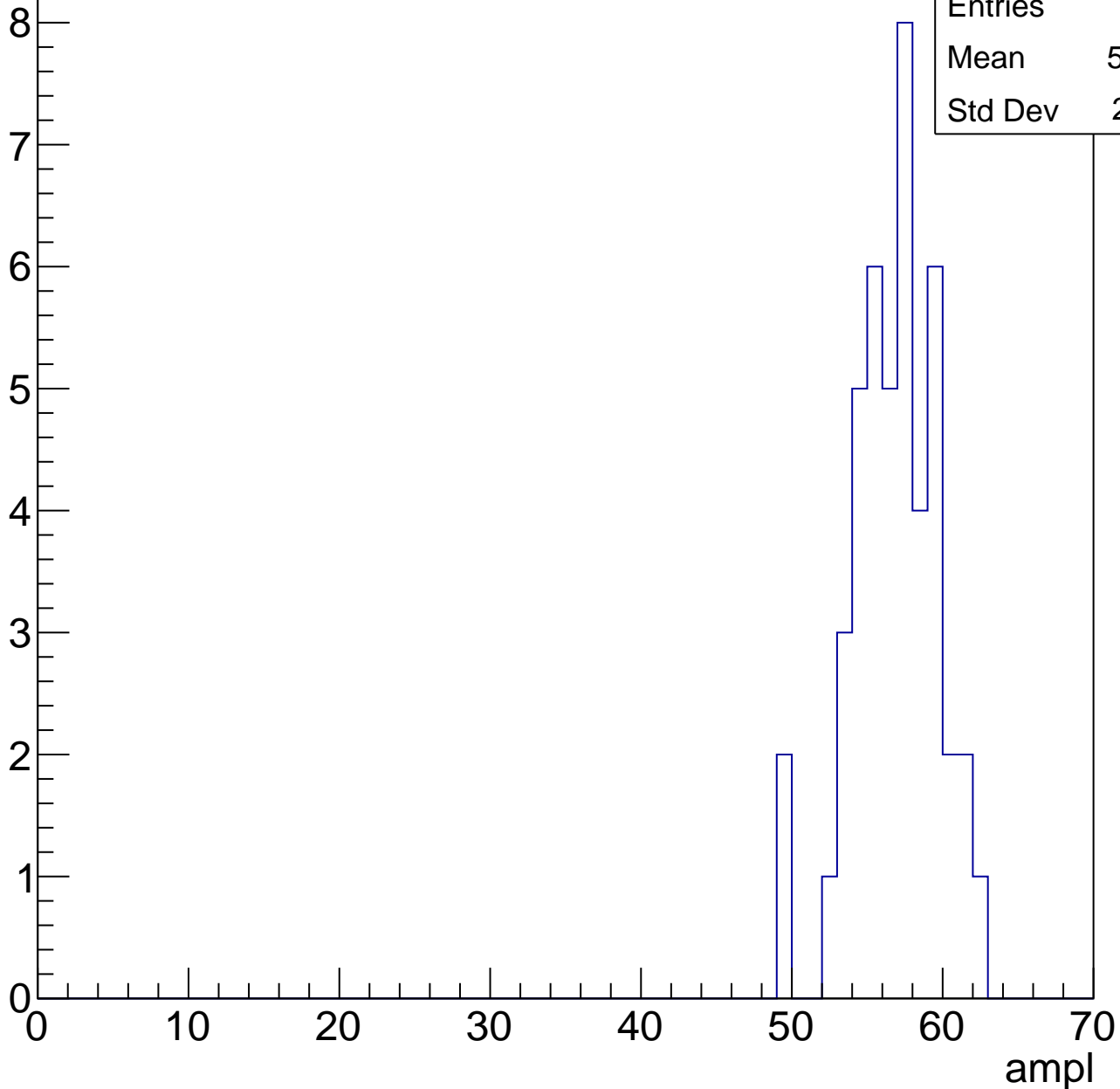


B1L100S, U19-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	56.33
Std Dev	2.821



B1L100S, U19-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.28
Std Dev	2.207

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

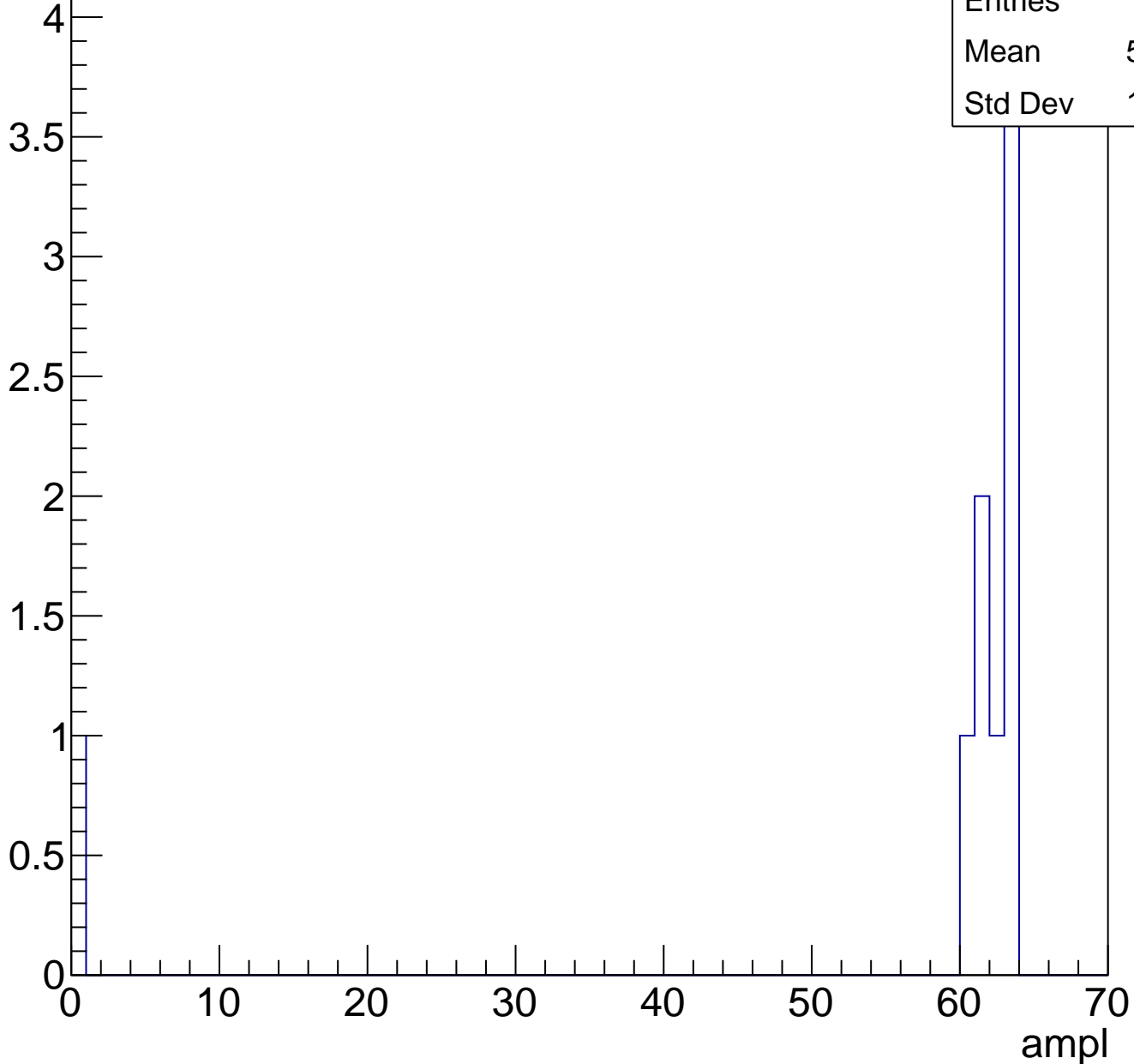
7

8

B1L100S, U19-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch80, adc0

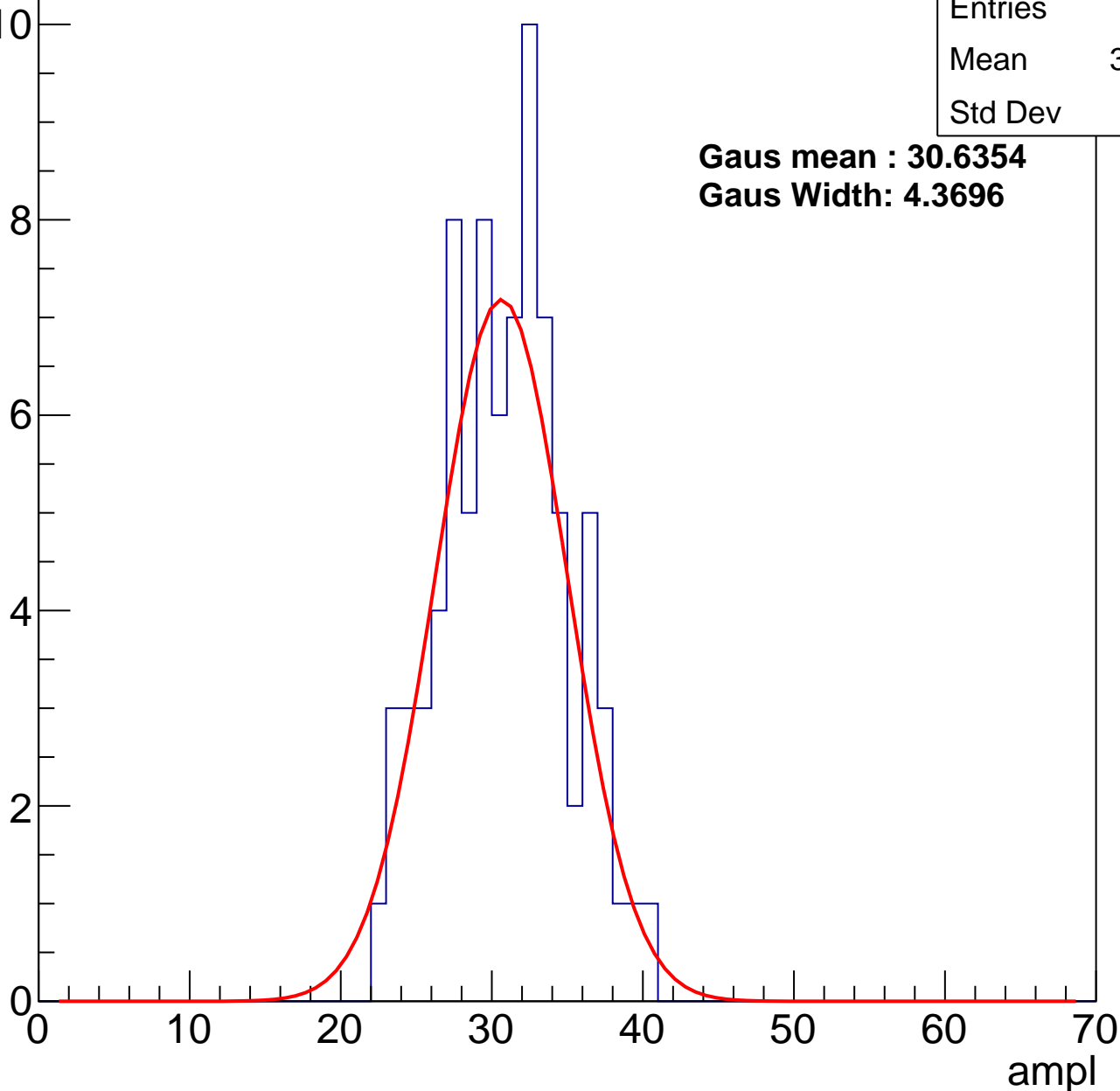
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	30.43
Std Dev	4.04

Gaus mean : 30.6354

Gaus Width: 4.3696



B1L100S, U19-ch80, adc1

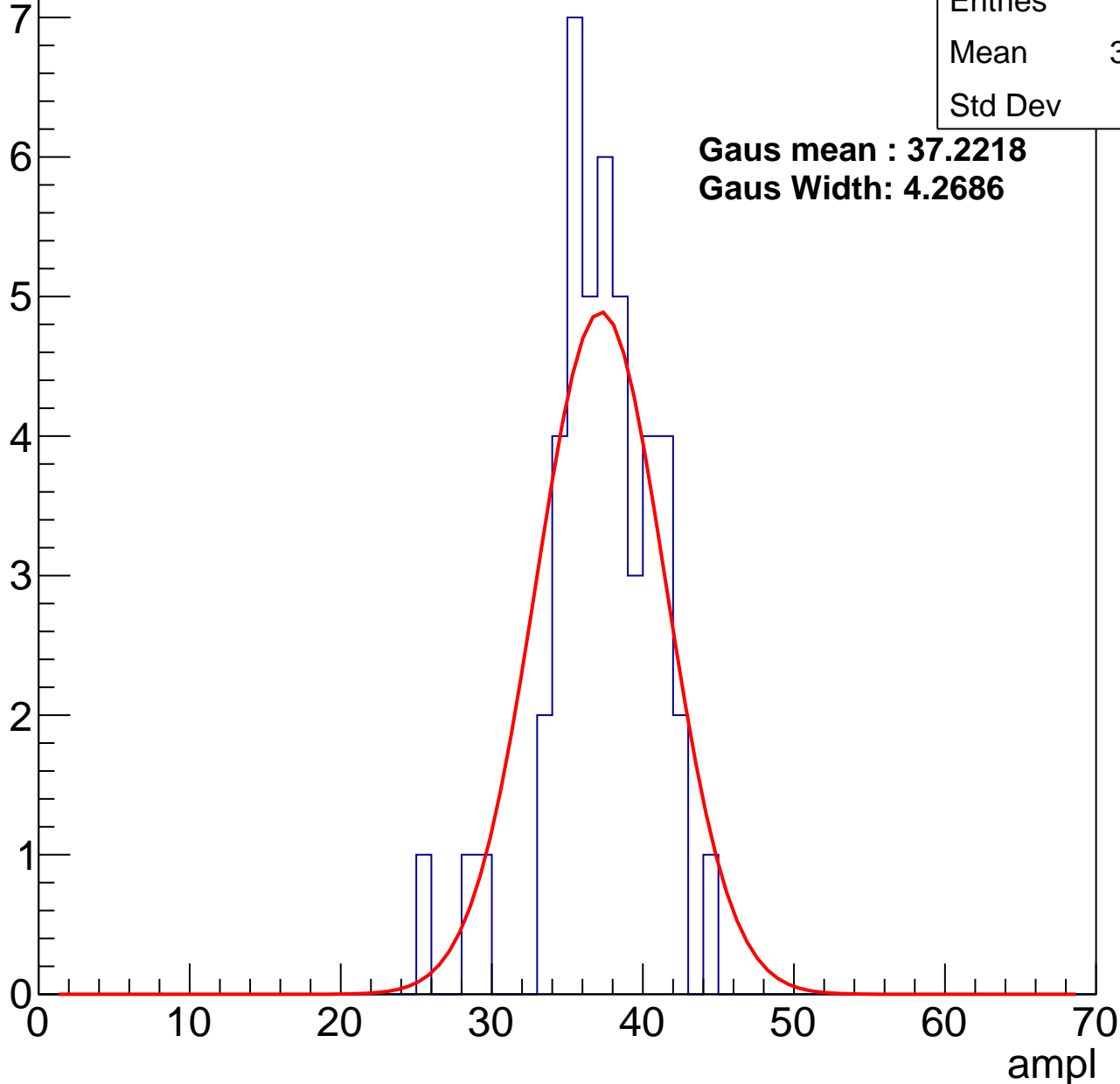
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	36.74
Std Dev	3.62

Gaus mean : 37.2218

Gaus Width: 4.2686



B1L100S, U19-ch80, adc2

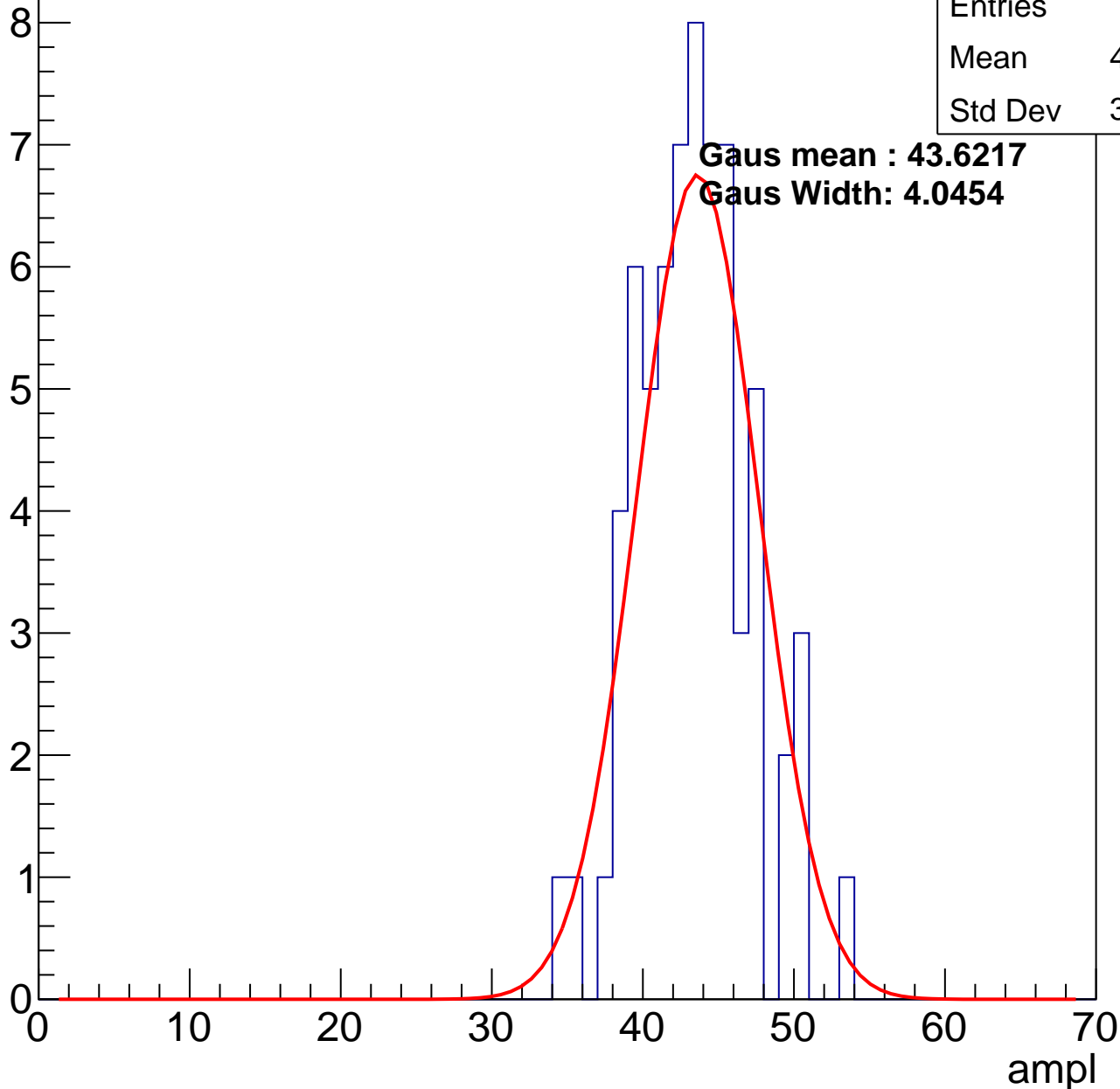
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.88
Std Dev	3.692

Gaus mean : 43.6217

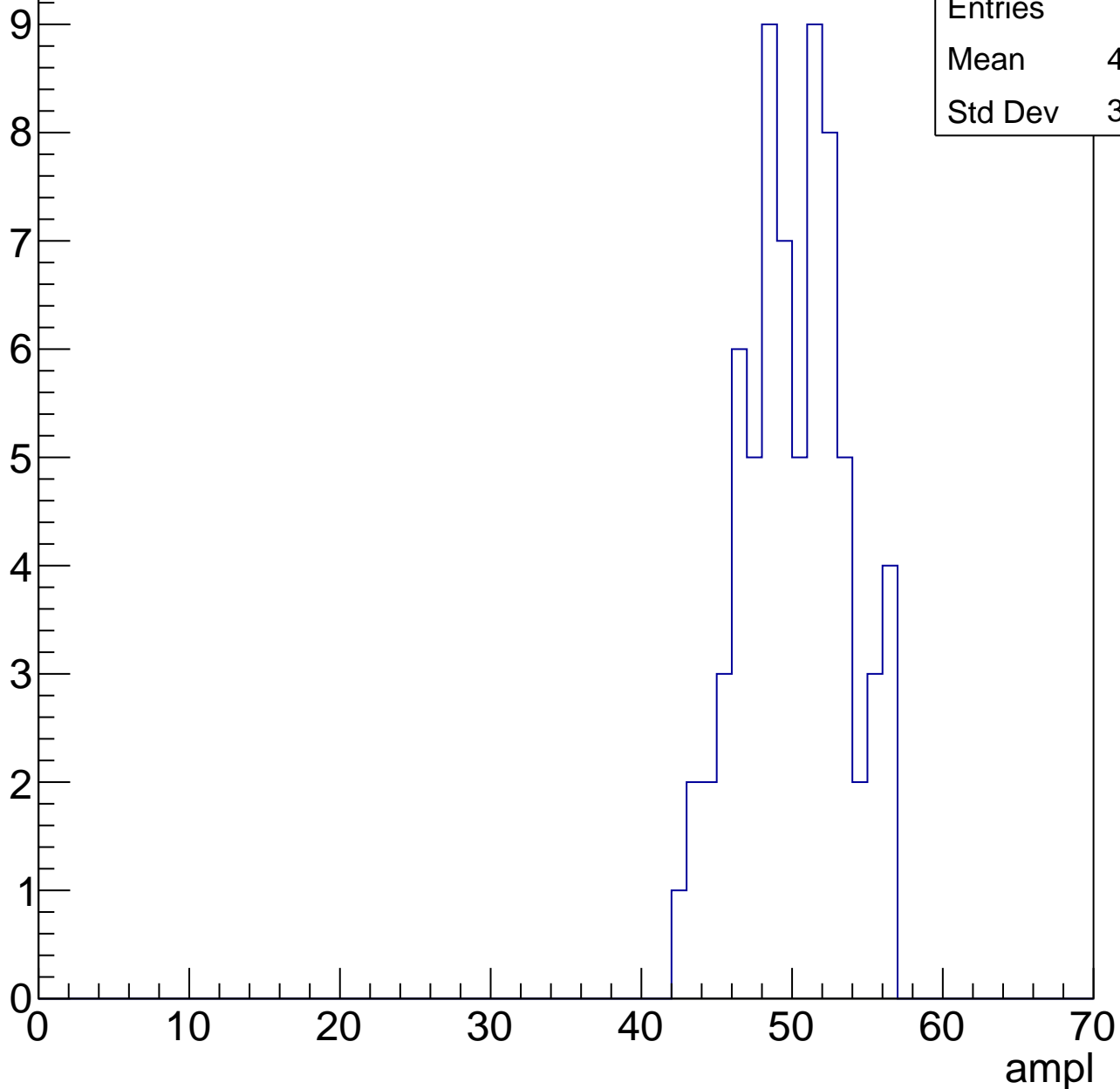
Gaus Width: 4.0454



B1L100S, U19-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



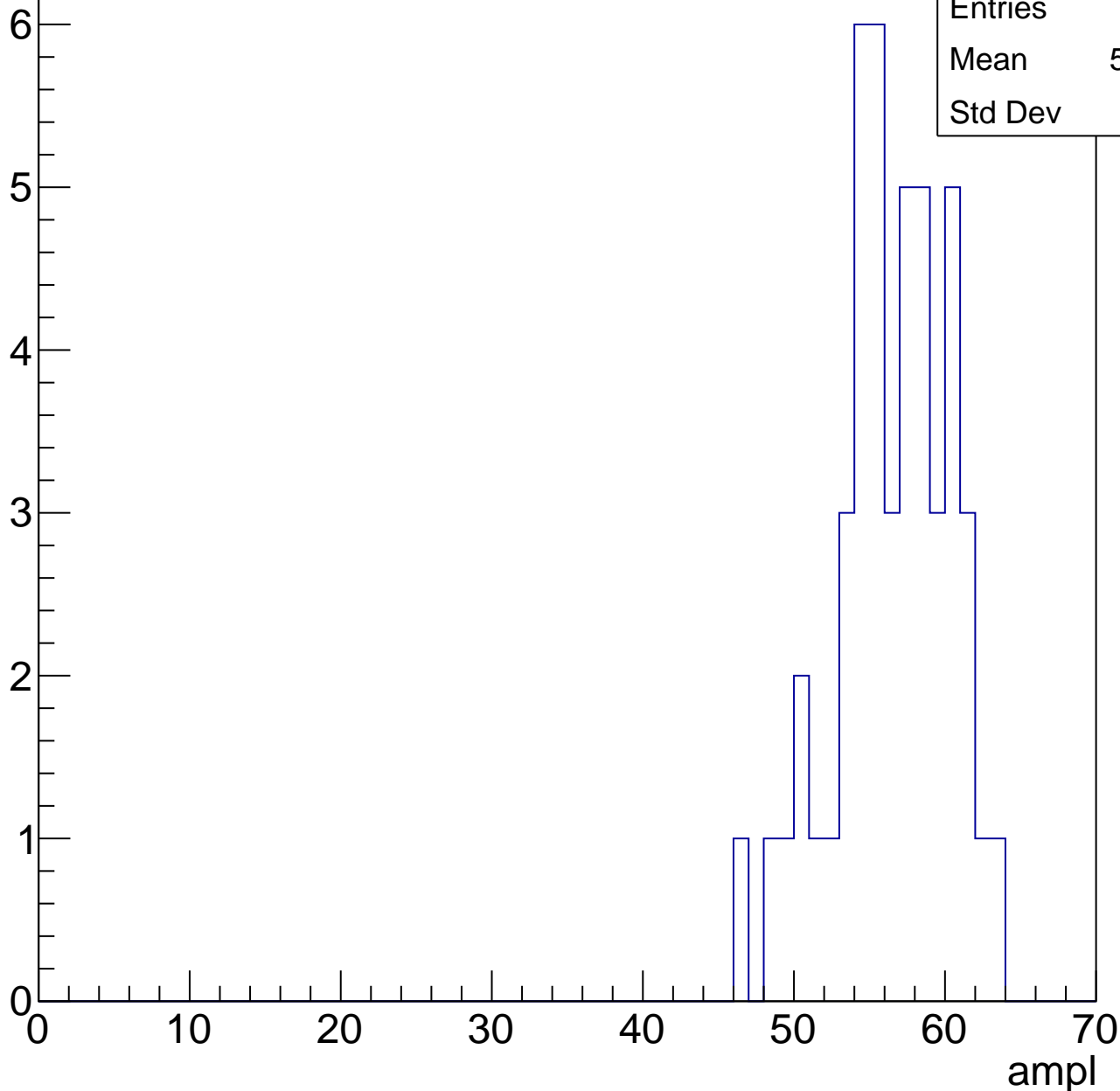
Entries	71
Mean	49.63
Std Dev	3.399

B1L100S, U19-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	55.98
Std Dev	3.75



B1L100S, U19-ch80, adc5

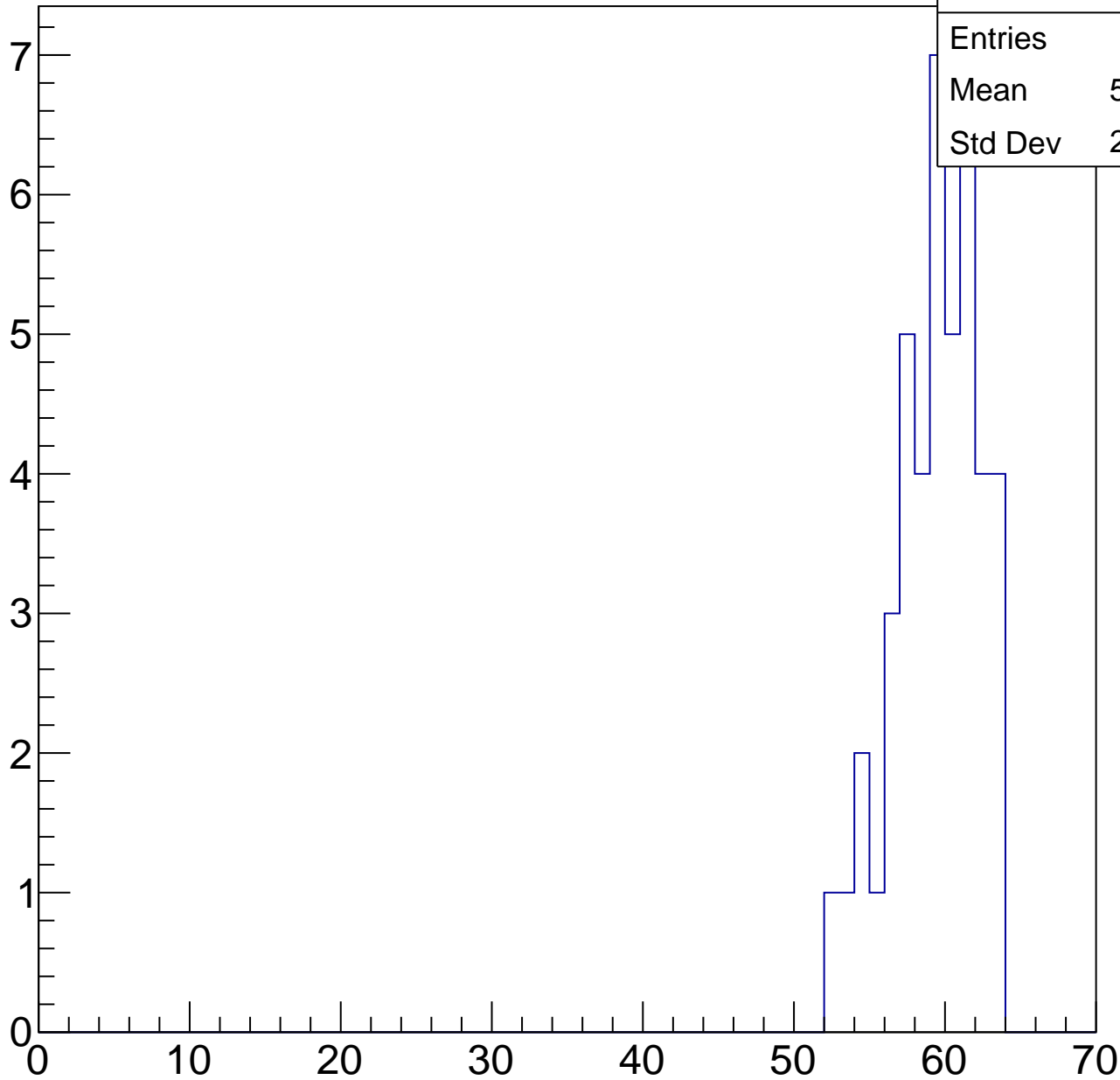
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	58.93
Std Dev	2.758

ampl

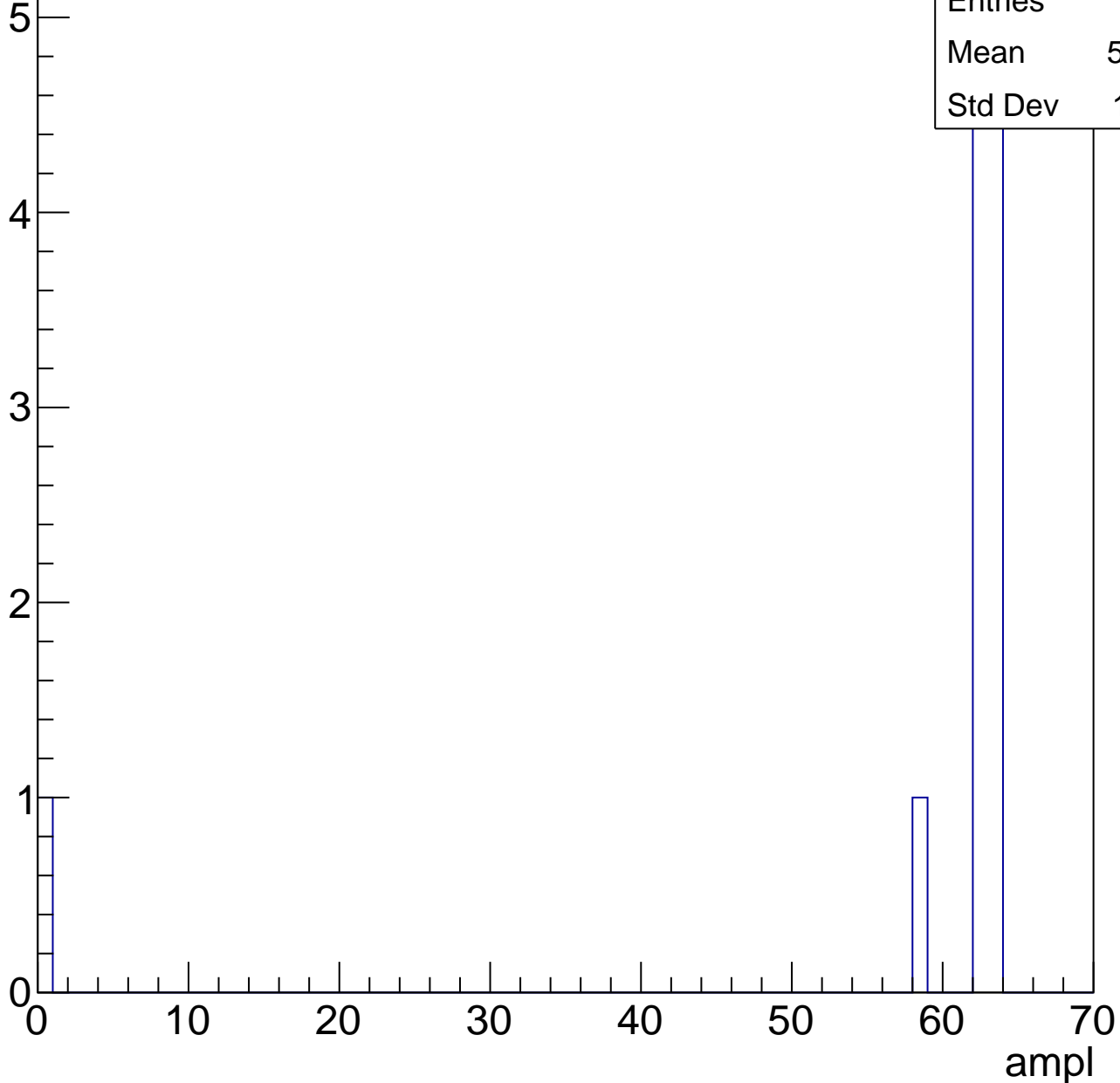


B1L100S, U19-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	56.92
Std Dev	17.21



B1L100S, U19-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch81, adc0

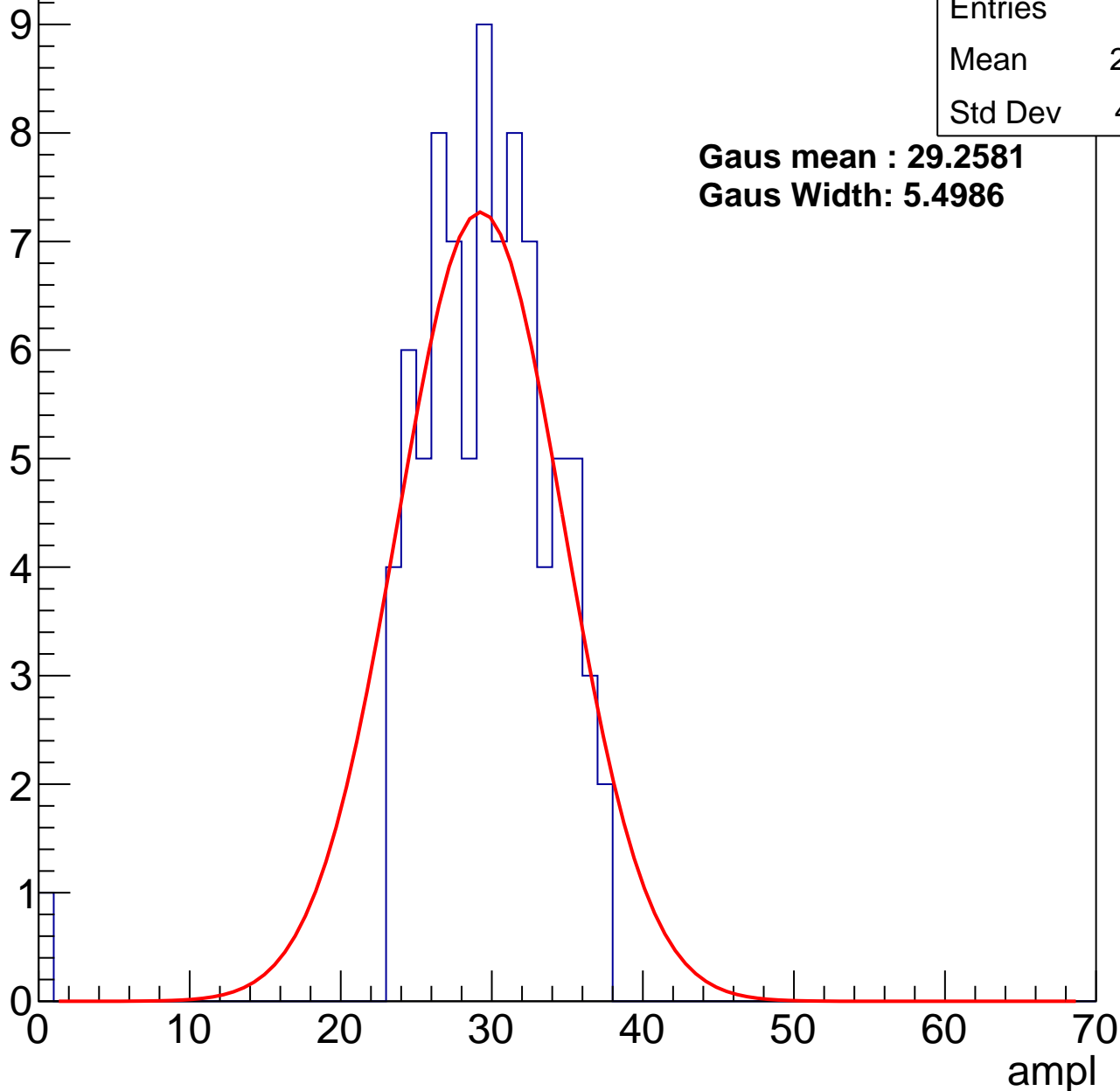
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.07
Std Dev	4.901

Gaus mean : 29.2581

Gaus Width: 5.4986



B1L100S, U19-ch81, adc1

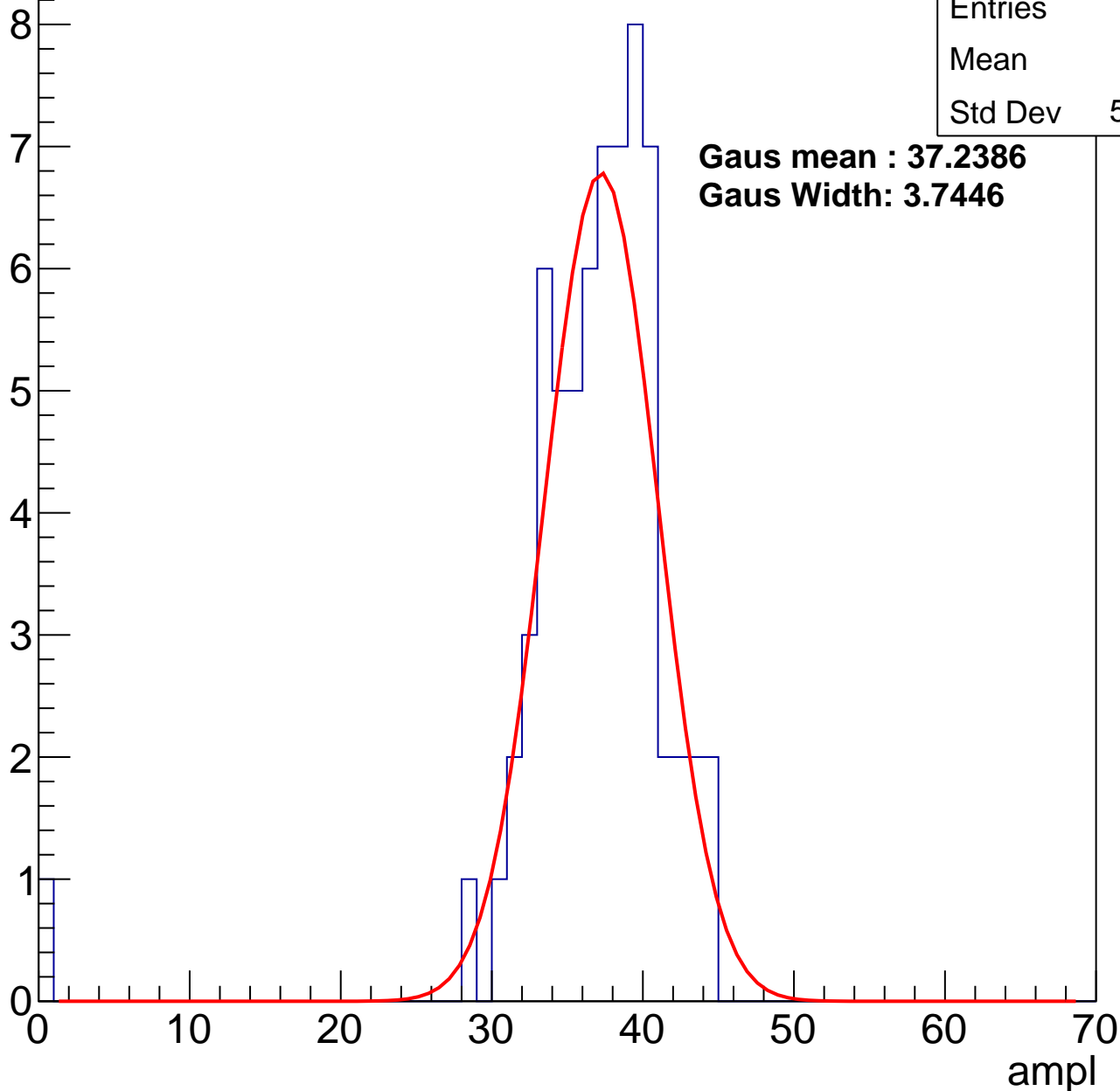
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.3
Std Dev	5.644

Gaus mean : 37.2386

Gaus Width: 3.7446

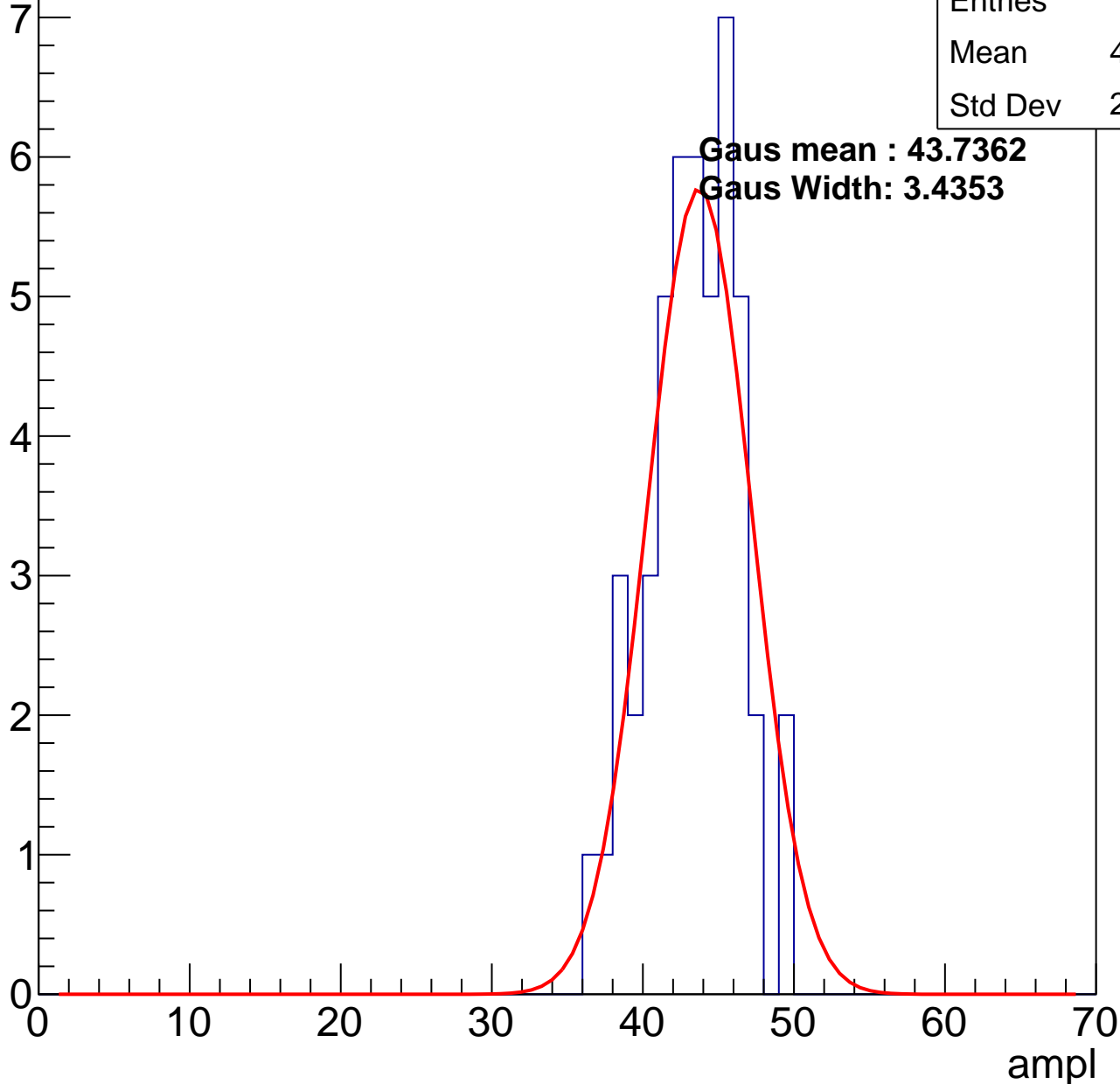


B1L100S, U19-ch81, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

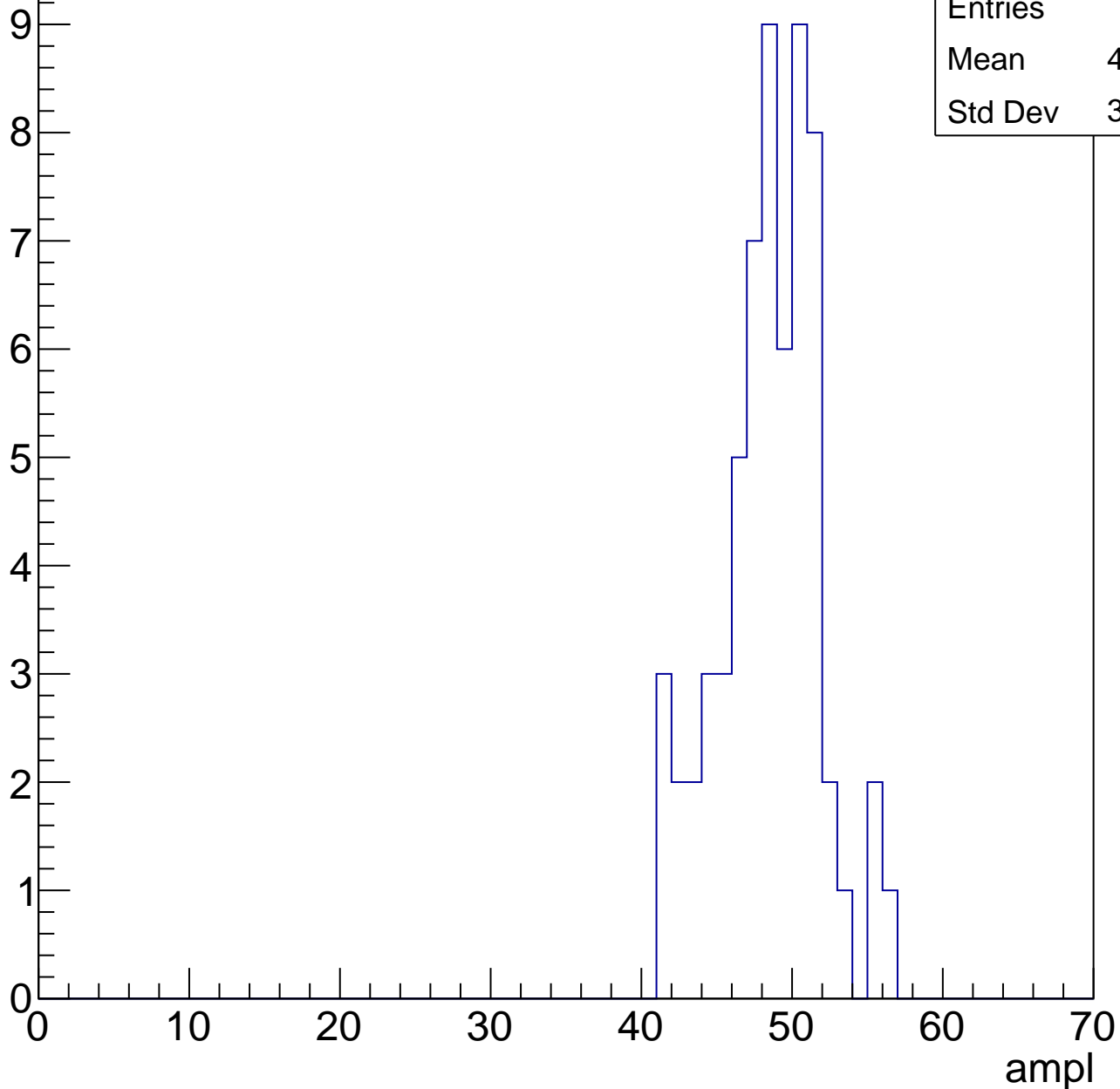
Entries	48
Mean	42.85
Std Dev	2.979



B1L100S, U19-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



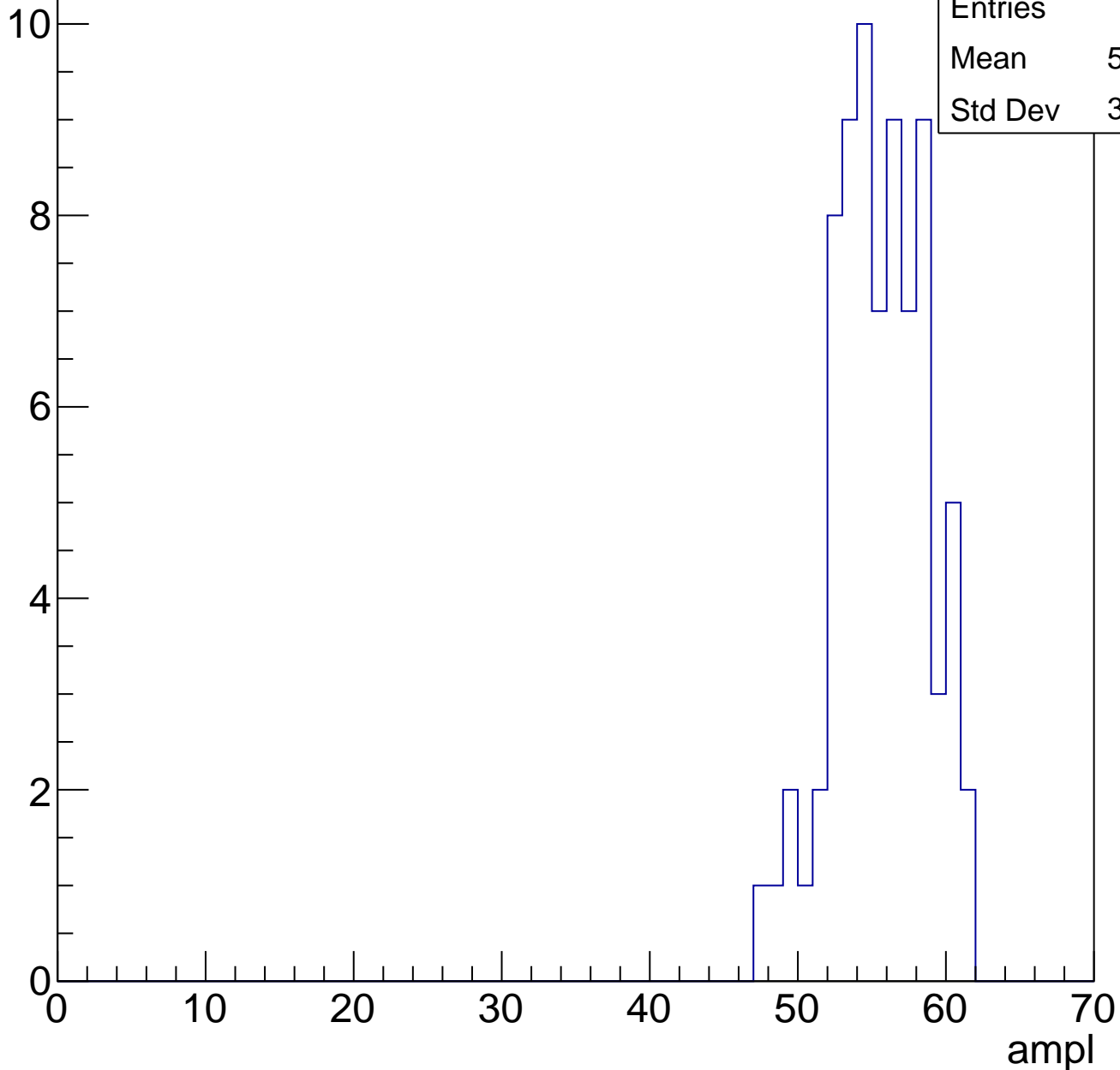
Entries	63
Mean	48.03
Std Dev	3.343

B1L100S, U19-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	55.09
Std Dev	3.083

Entry



B1L100S, U19-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	41
Mean	60.8
Std Dev	2.003

Entry

10

8

6

4

2

0

0

10

20

30

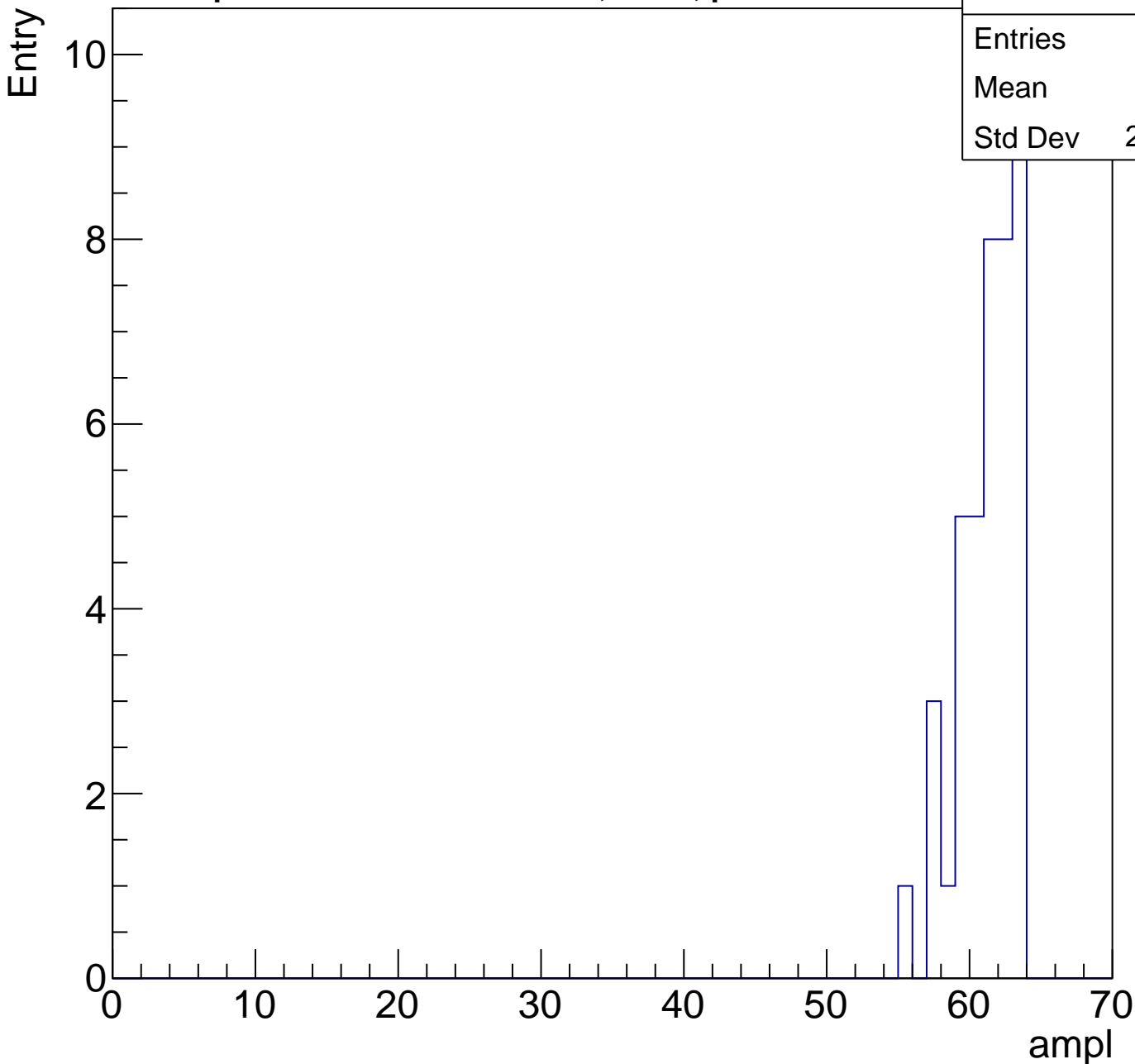
40

50

60

ampl

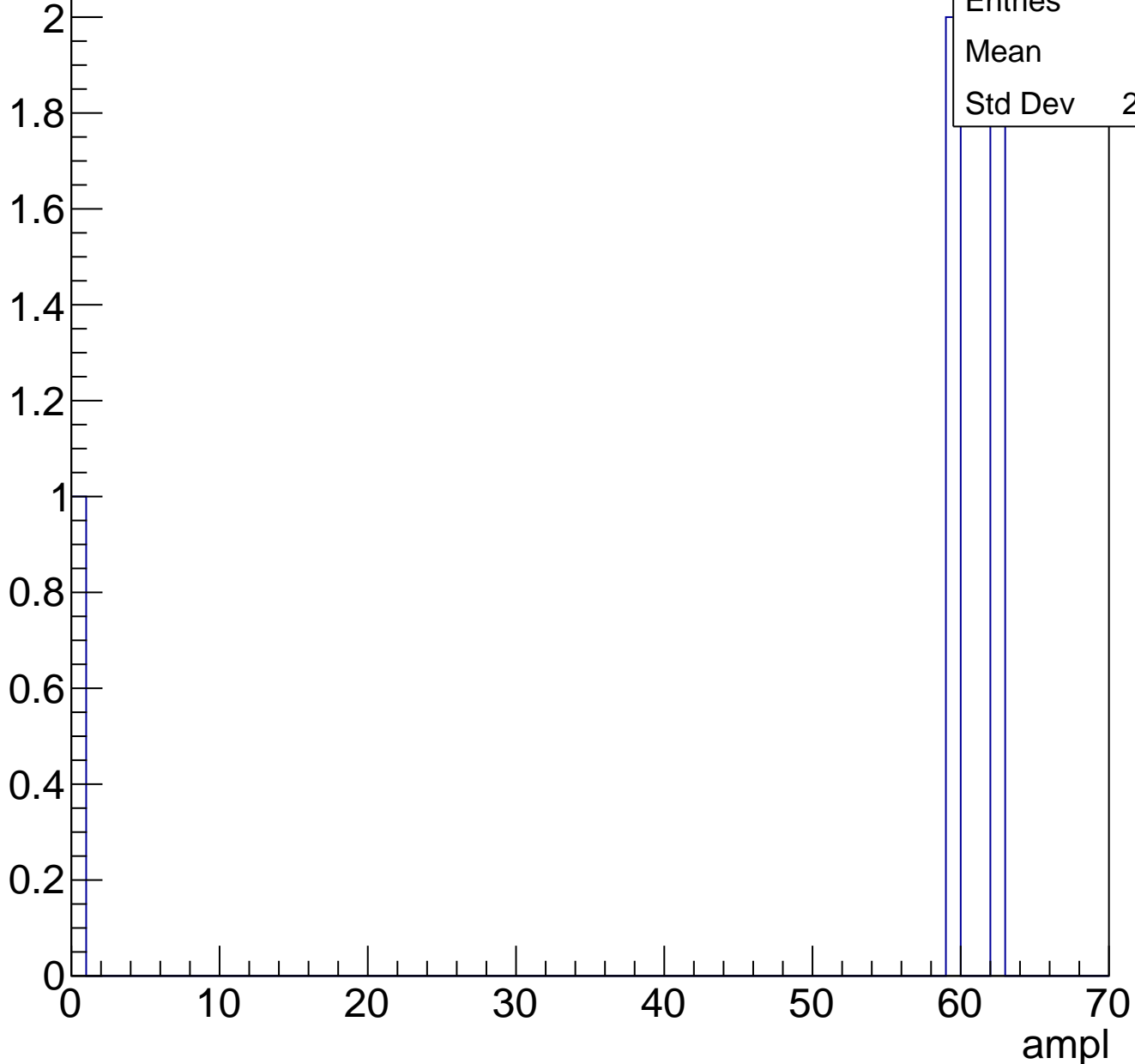
70



B1L100S, U19-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch82, adc0

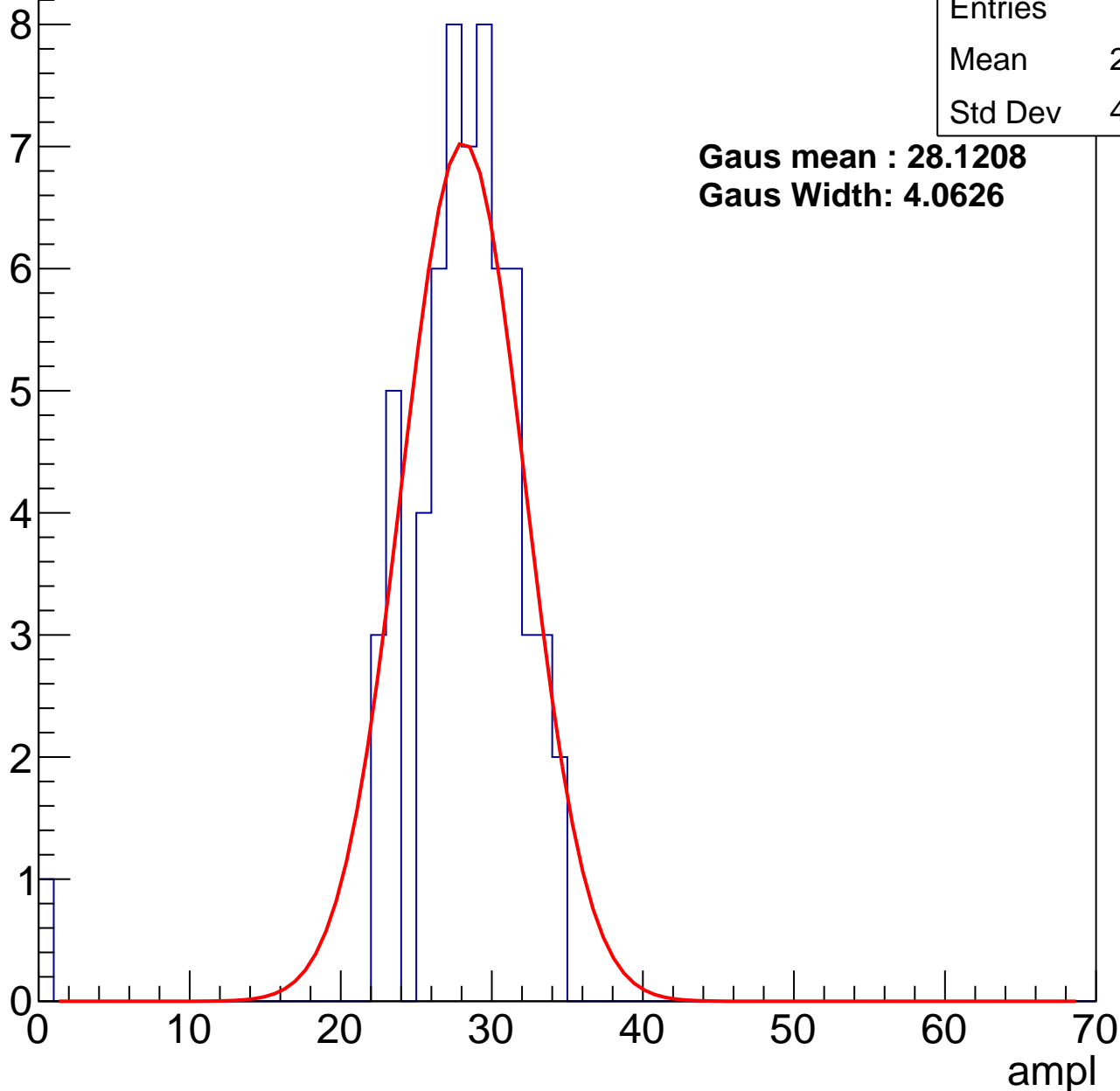
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.58
Std Dev	4.675

Gaus mean : 28.1208

Gaus Width: 4.0626



B1L100S, U19-ch82, adc1

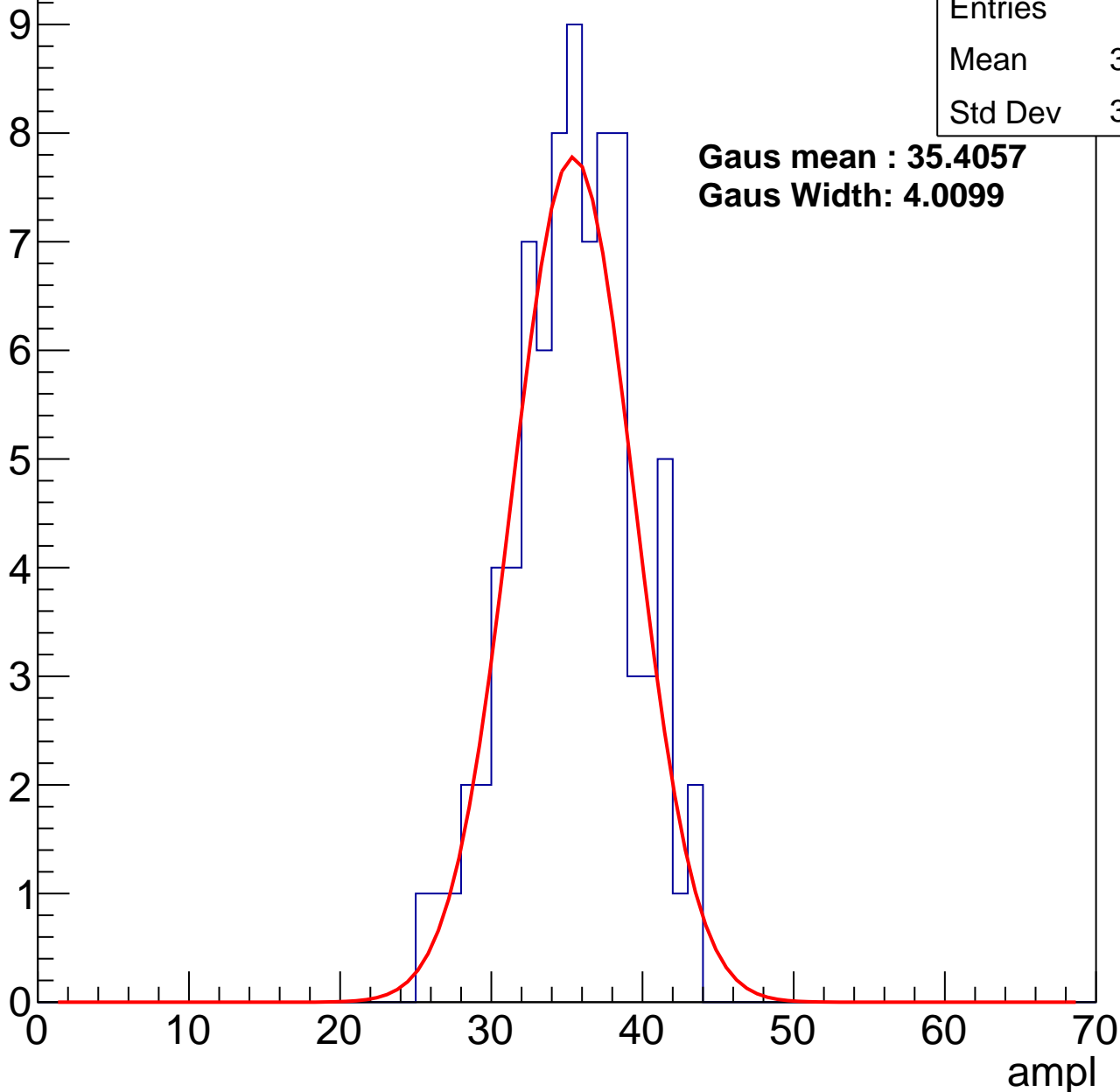
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	34.96
Std Dev	3.928

Gaus mean : 35.4057

Gaus Width: 4.0099



B1L100S, U19-ch82, adc2

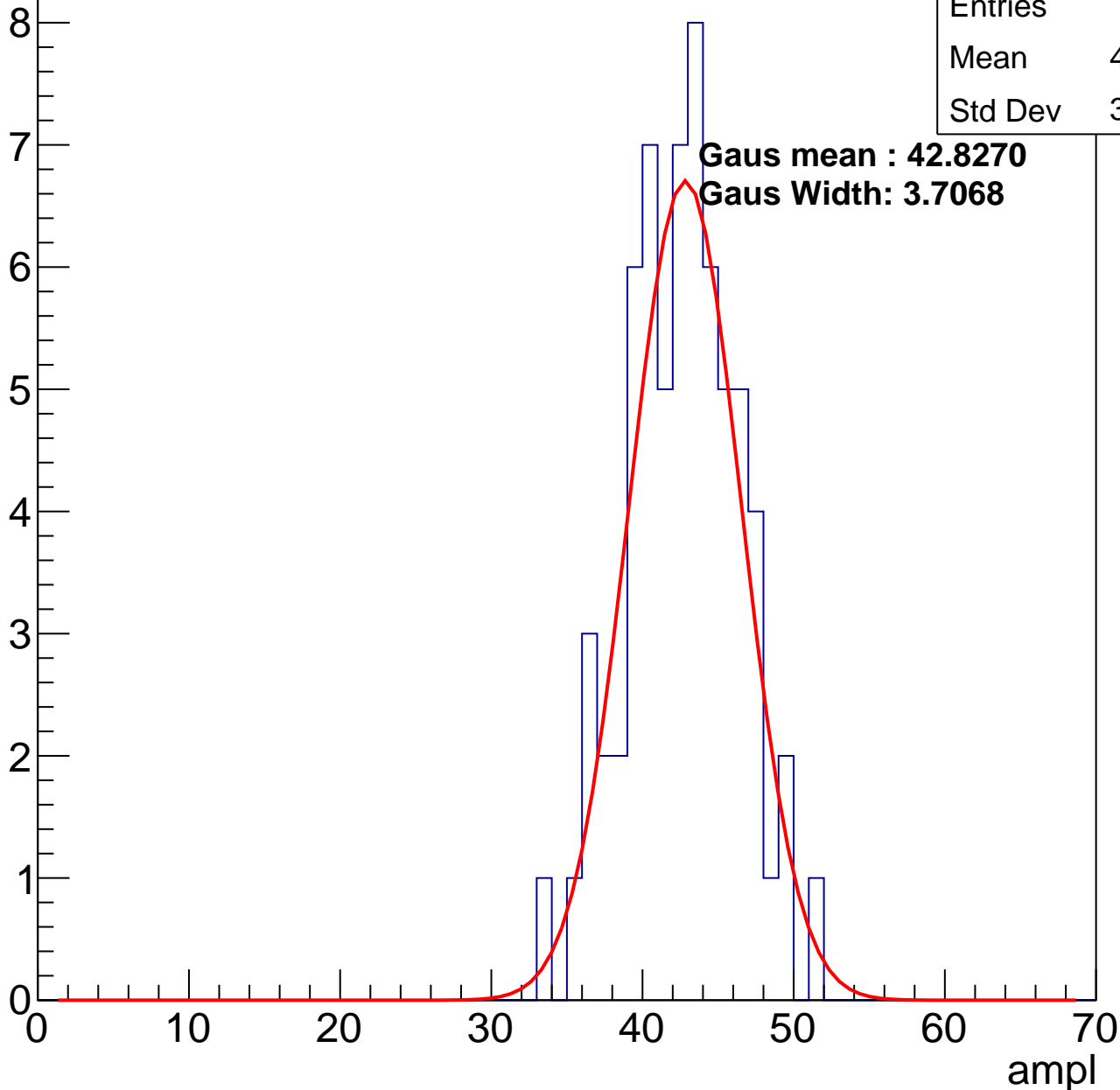
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.23
Std Dev	3.642

Gaus mean : 42.8270

Gaus Width: 3.7068

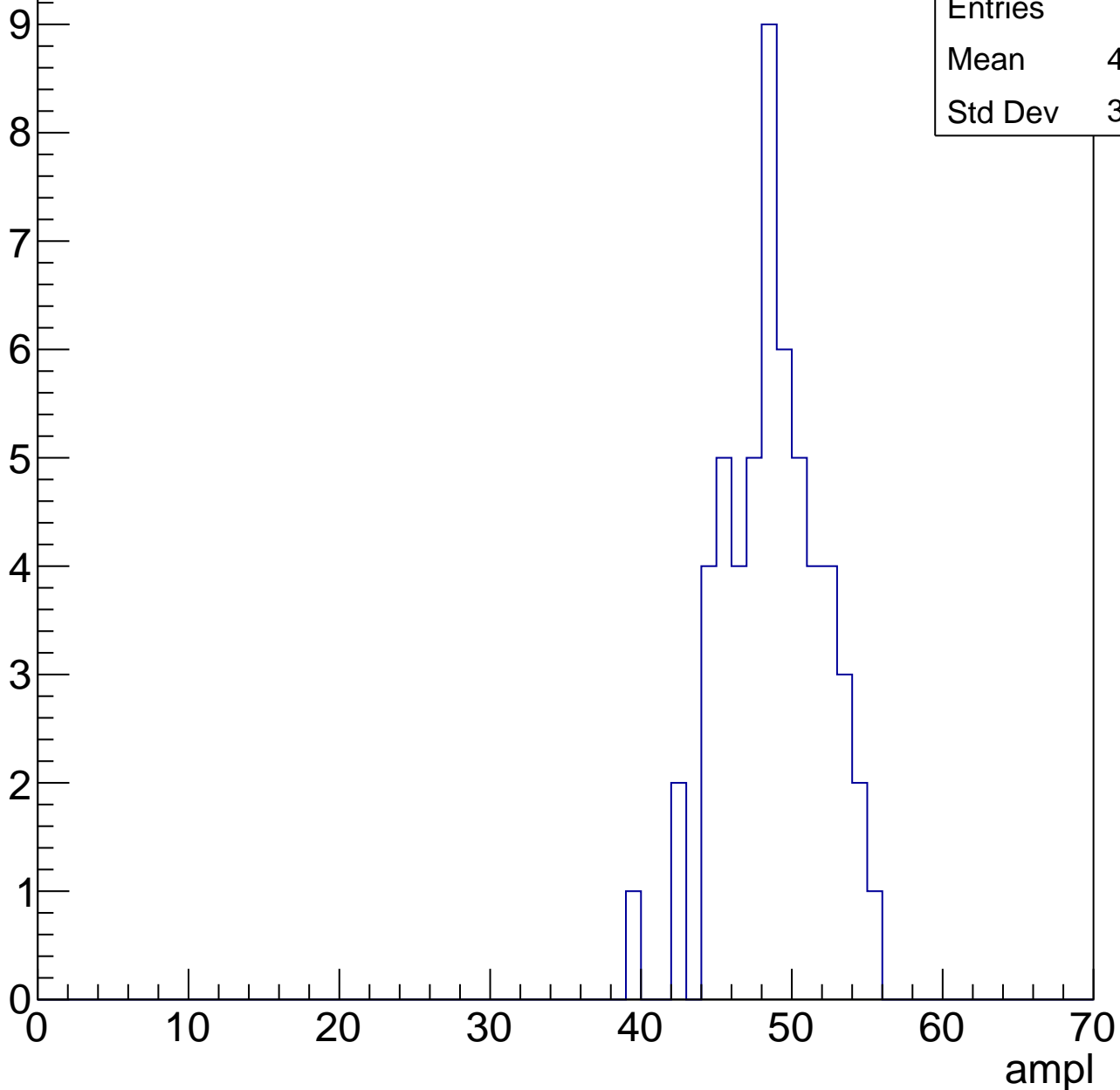


B1L100S, U19-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	48.24
Std Dev	3.308

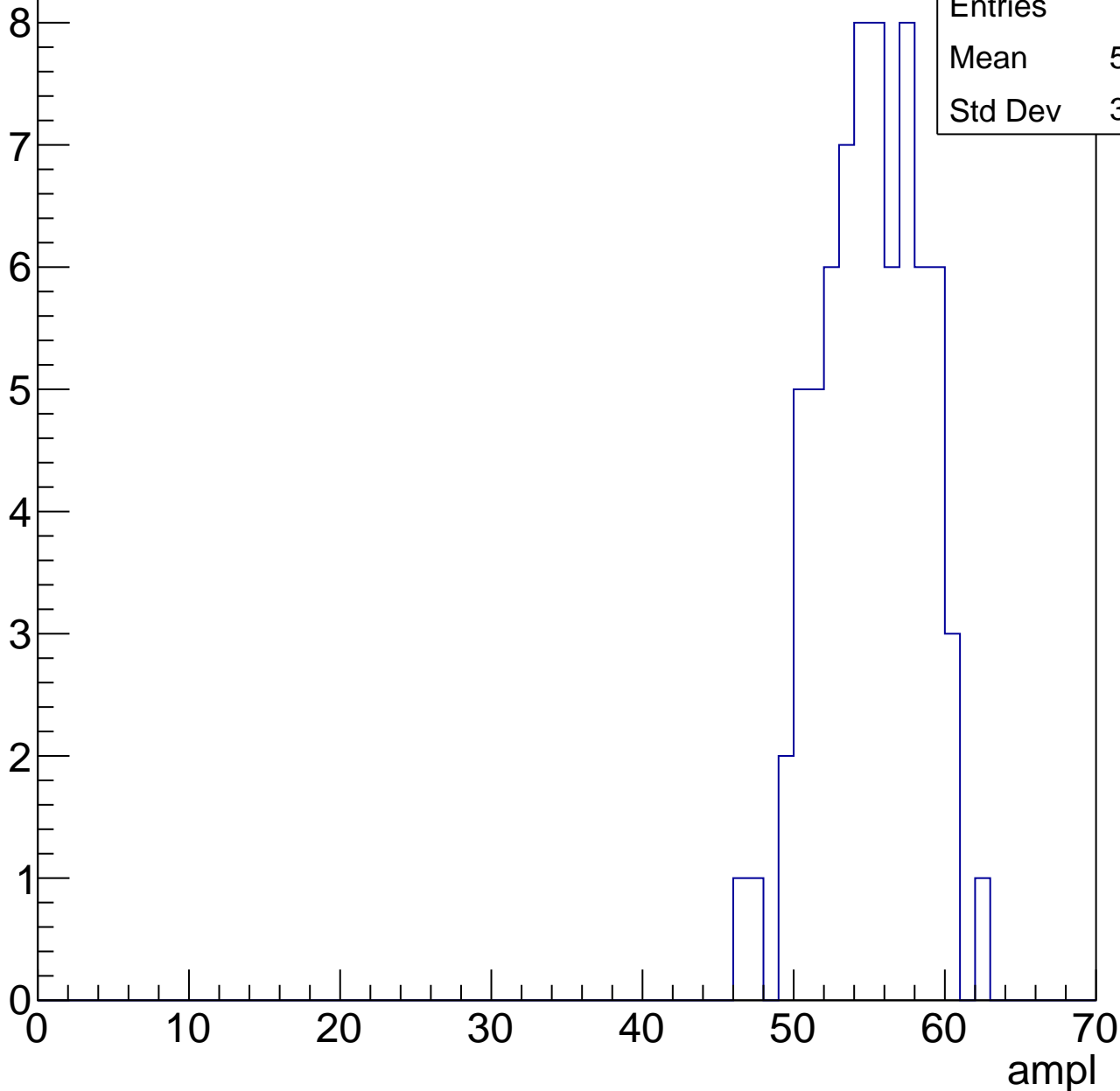


B1L100S, U19-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	54.62
Std Dev	3.338

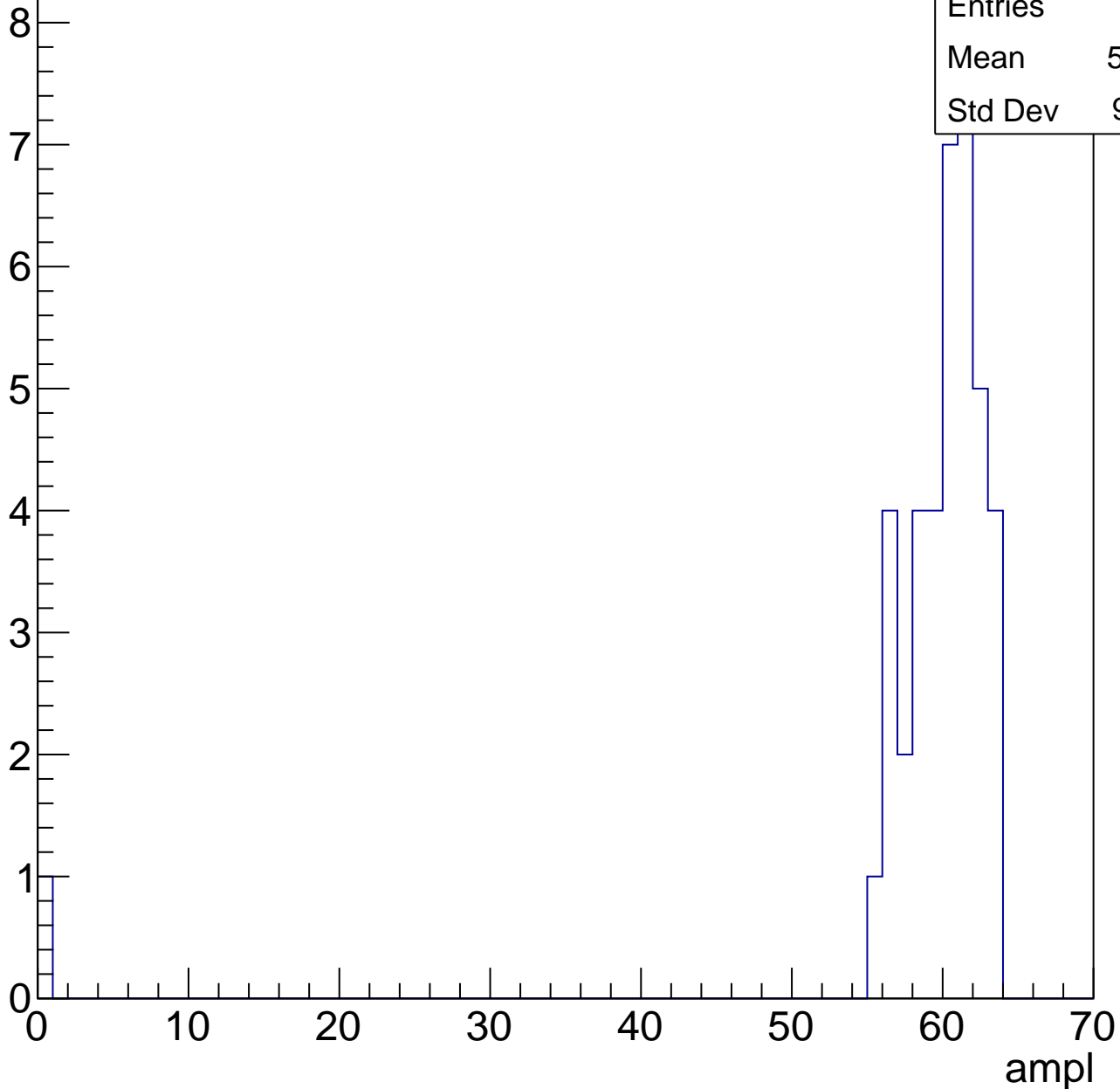


B1L100S, U19-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.27
Std Dev	9.581



B1L100S, U19-ch82, adc6

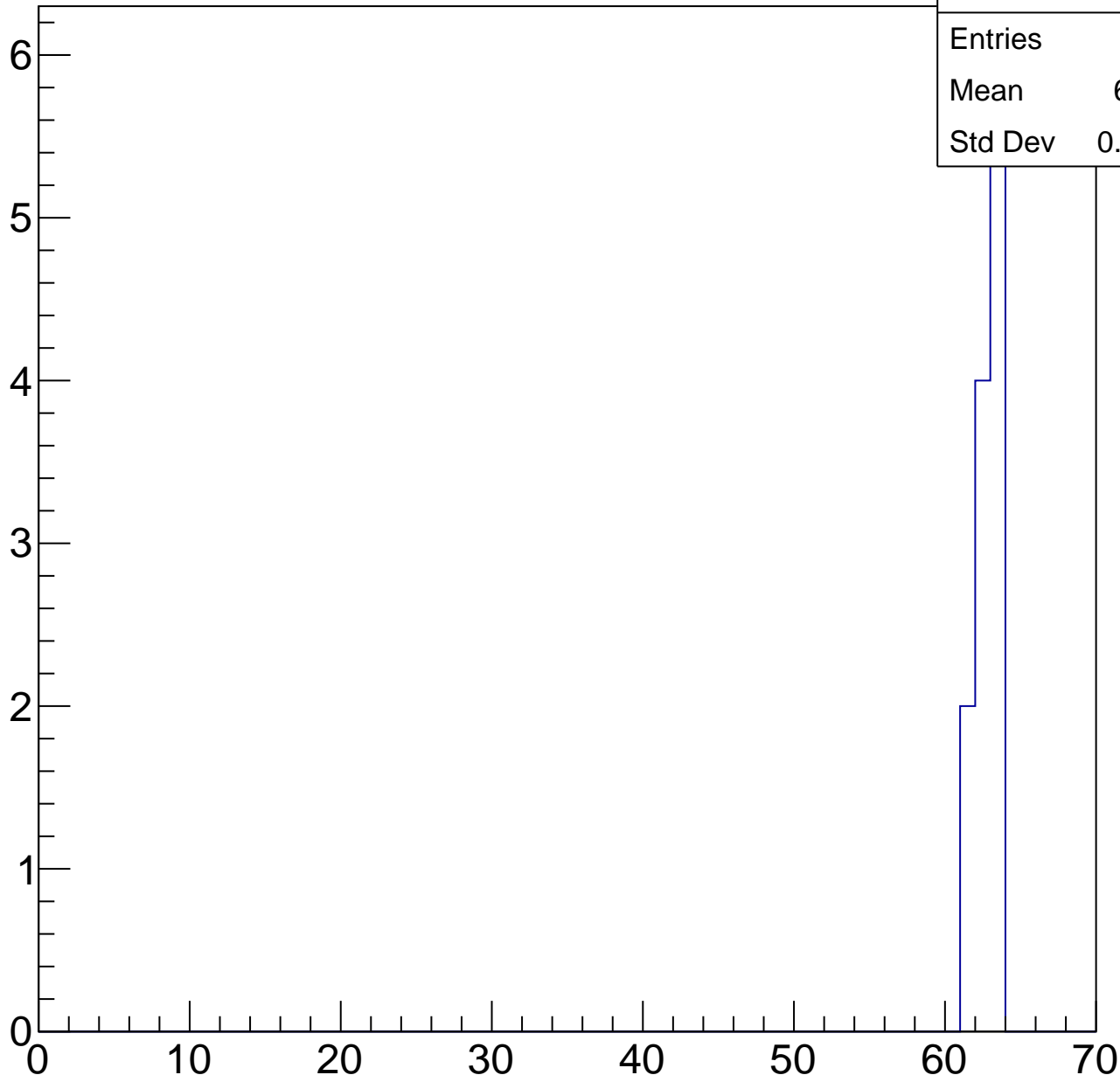
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	12
Mean	62.33
Std Dev	0.7454

ampl



B1L100S, U19-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch83, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	28.69
Std Dev	3.874

Gaus mean : 29.0239

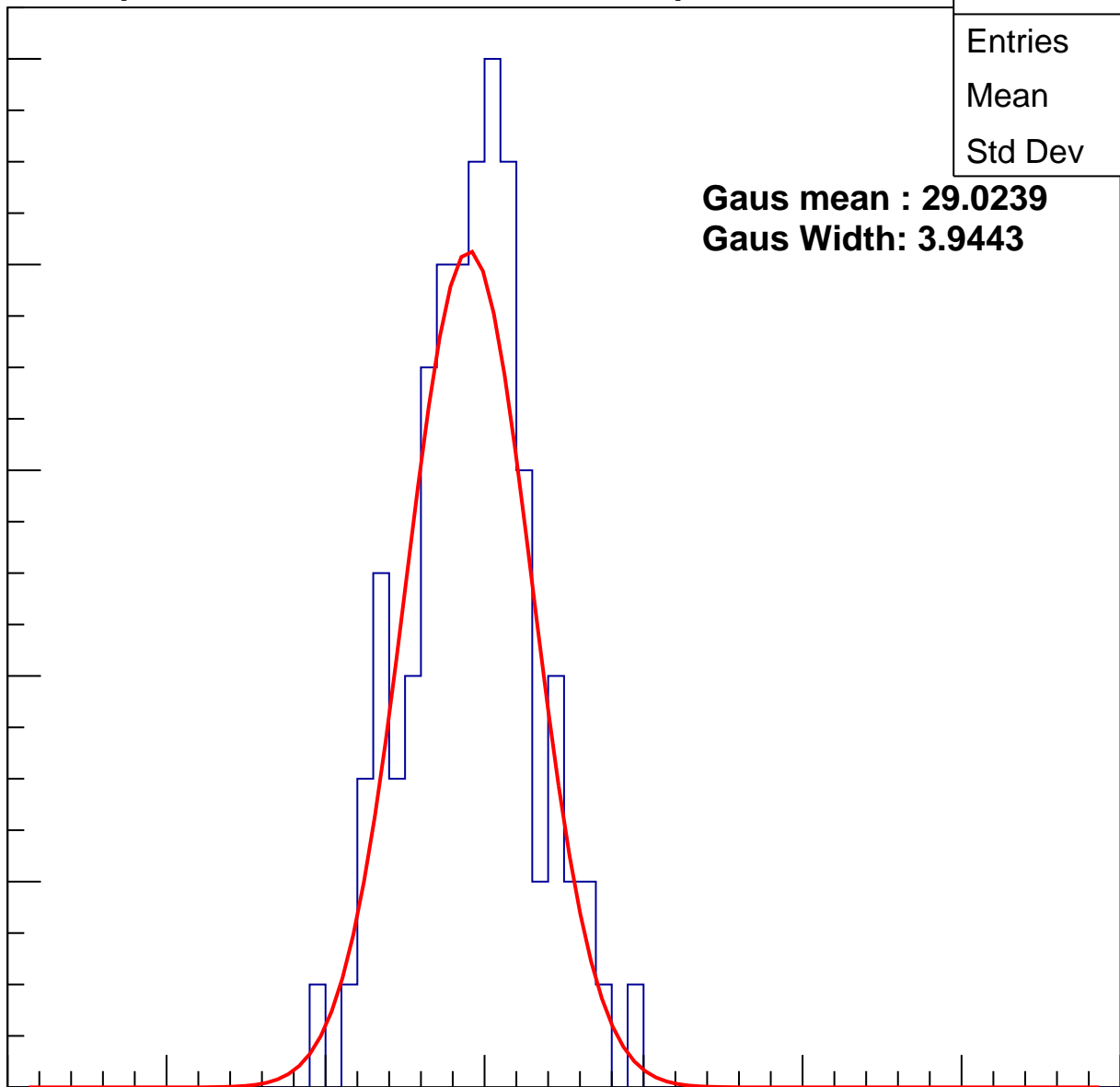
Gaus Width: 3.9443

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch83, adc1

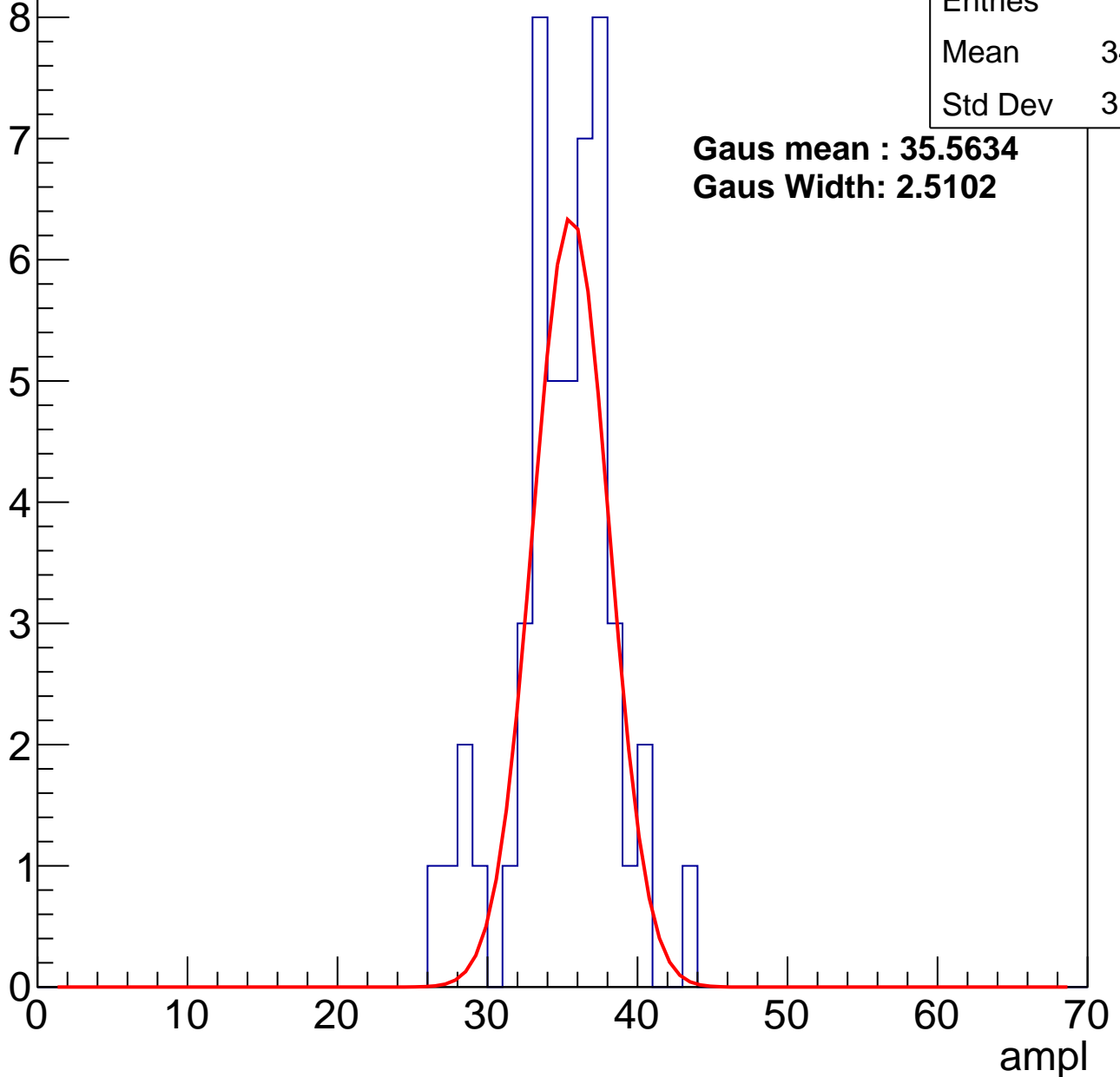
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	34.65
Std Dev	3.348

Gaus mean : 35.5634

Gaus Width: 2.5102



B1L100S, U19-ch83, adc2

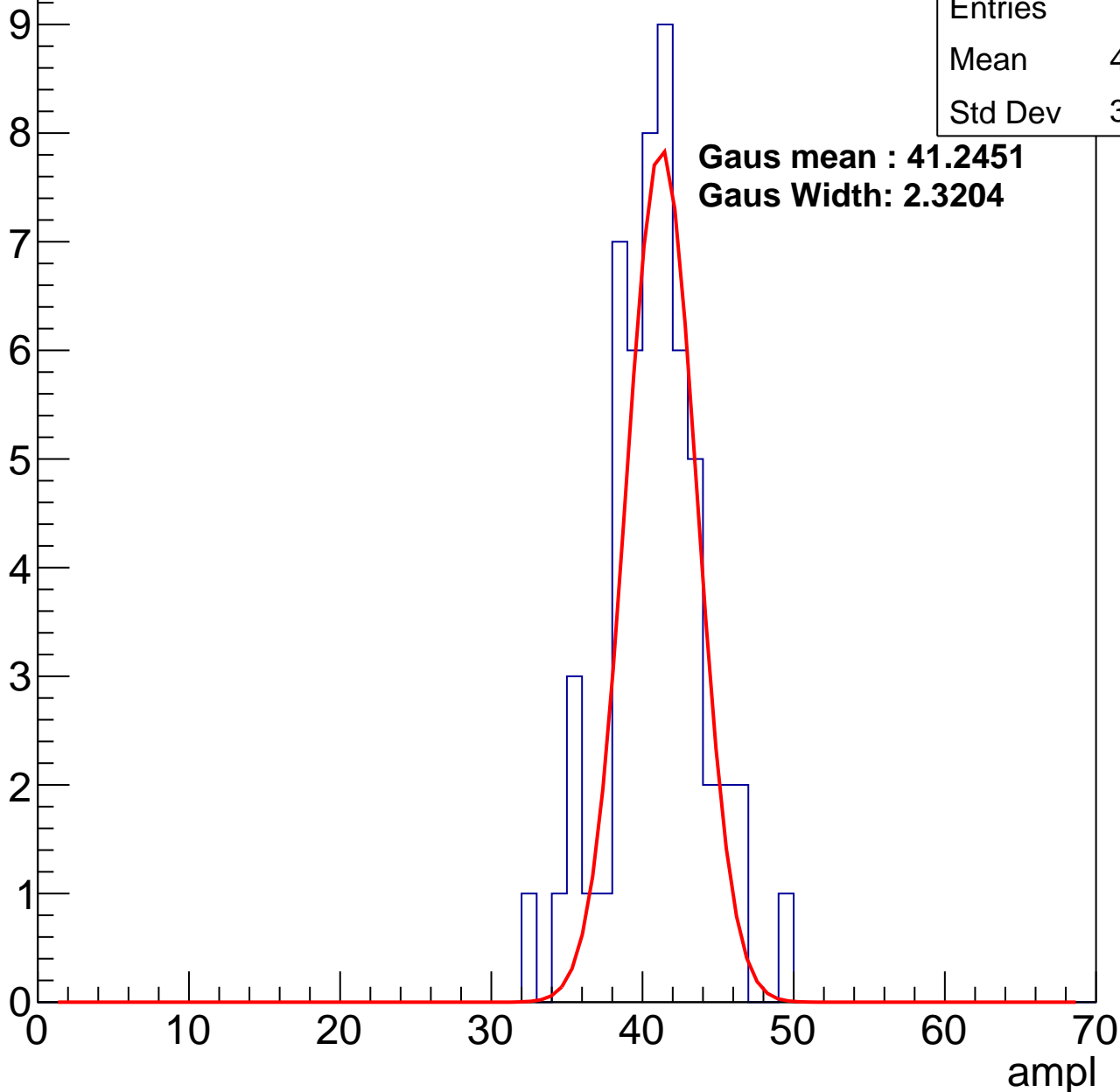
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	40.35
Std Dev	3.135

Gaus mean : 41.2451

Gaus Width: 2.3204



B1L100S, U19-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	47.17
Std Dev	3.523

Entry

10

8

6

4

2

0

0

10

20

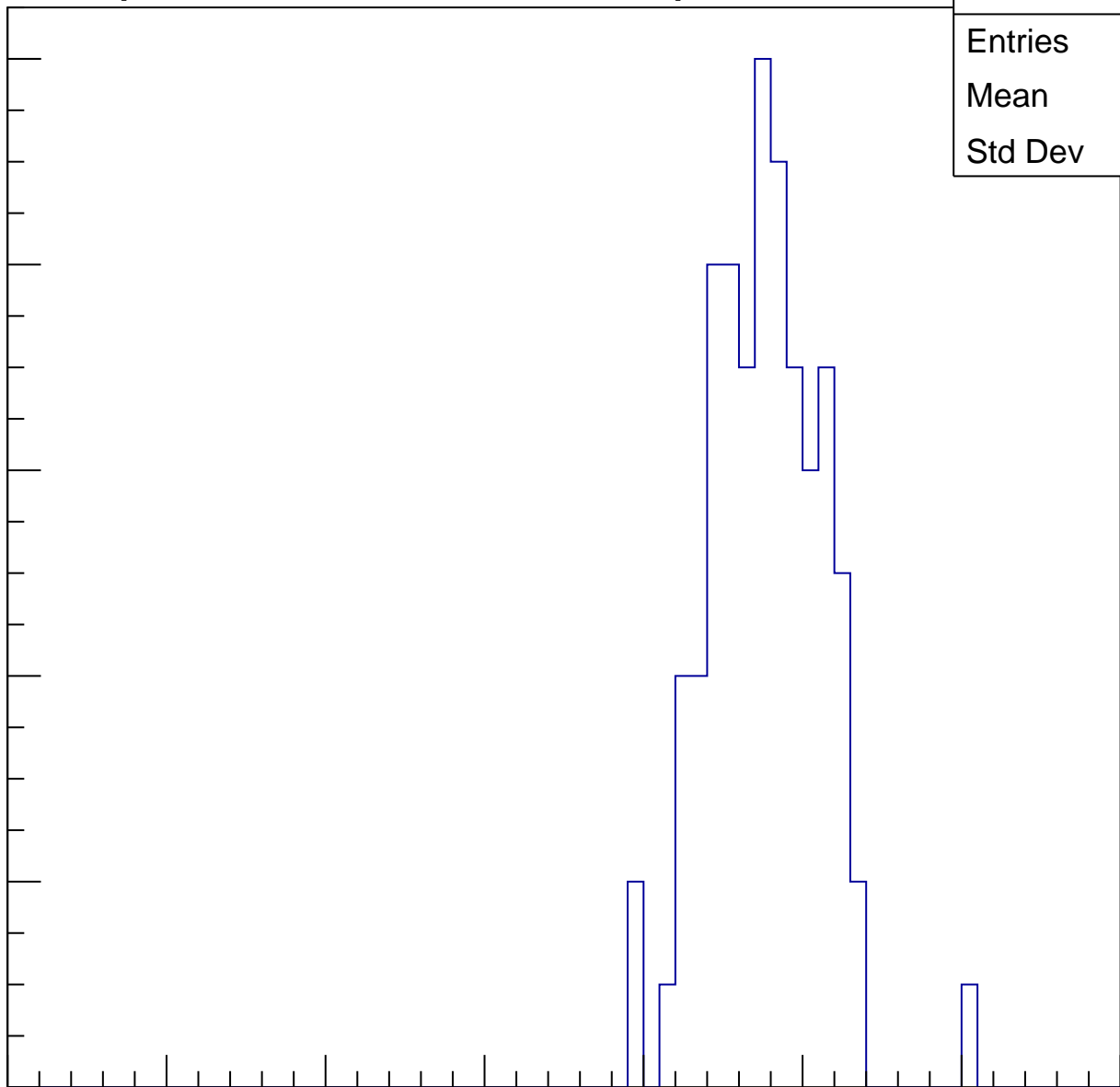
30

40

50

60

ampl



B1L100S, U19-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	54.54
Std Dev	3.164

Entry

10

8

6

4

2

0

0

10

20

30

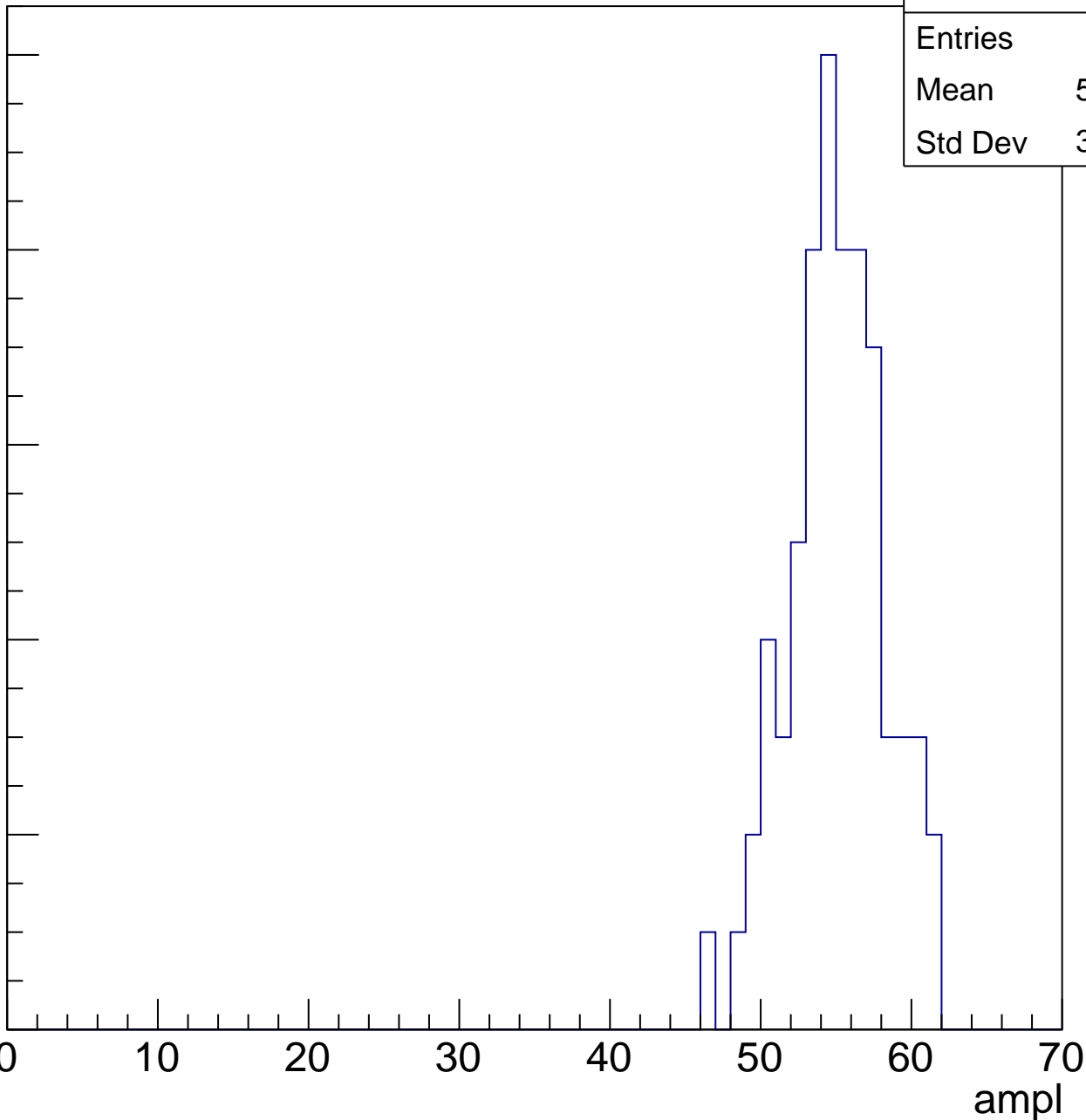
40

50

60

70

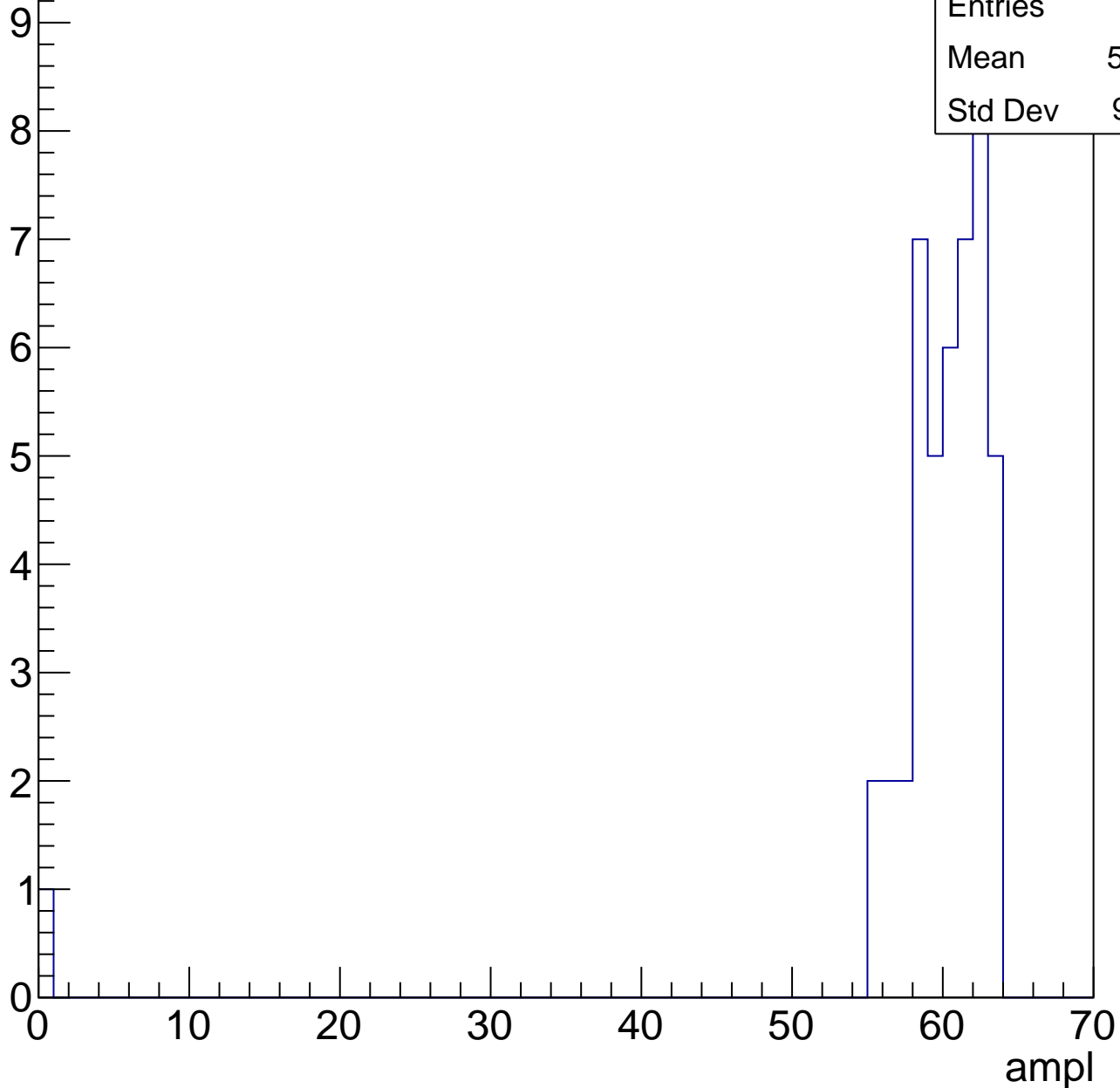
ampl



B1L100S, U19-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

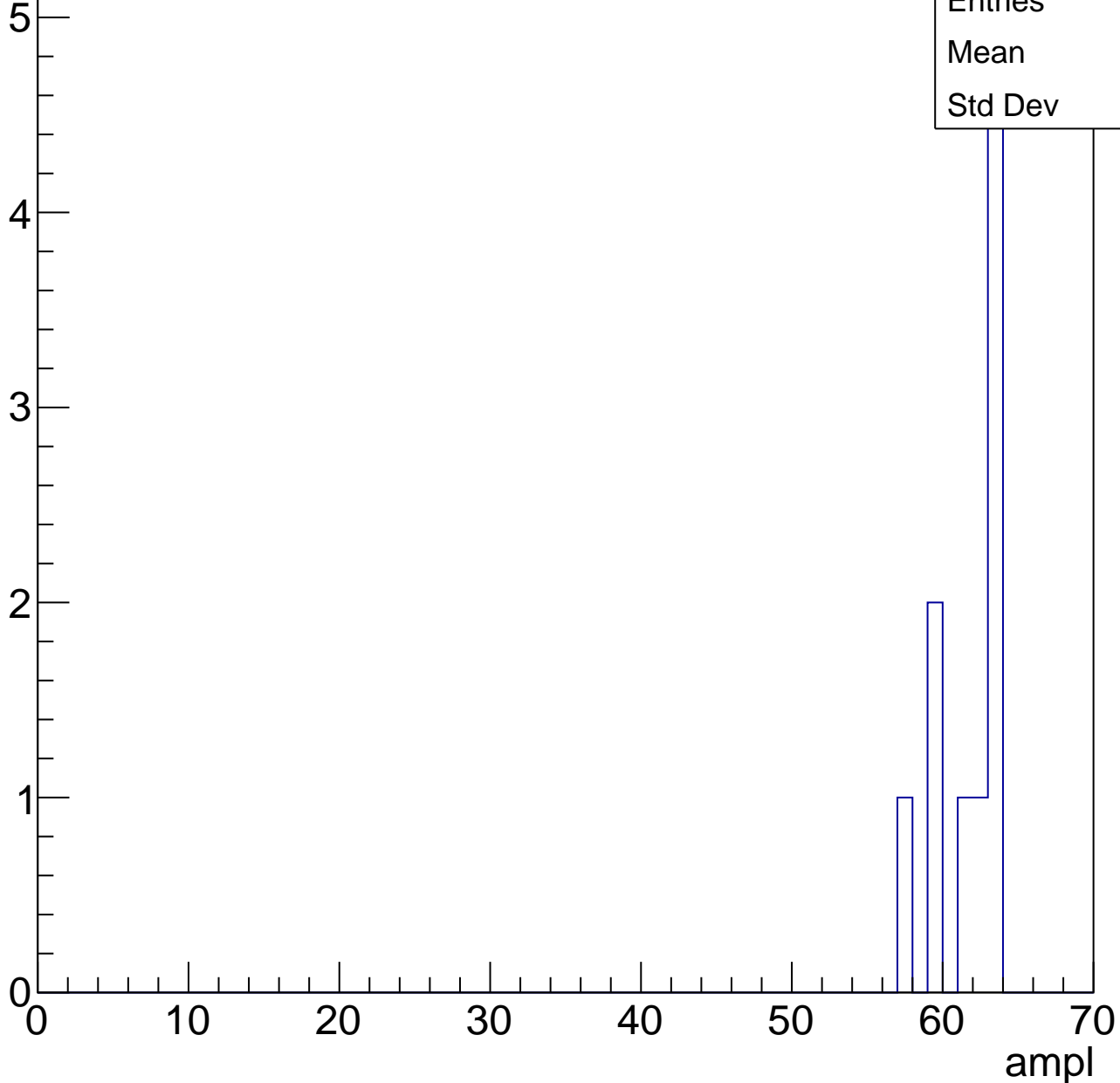


B1L100S, U19-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	61.3
Std Dev	2.1



B1L100S, U19-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch84, adc0

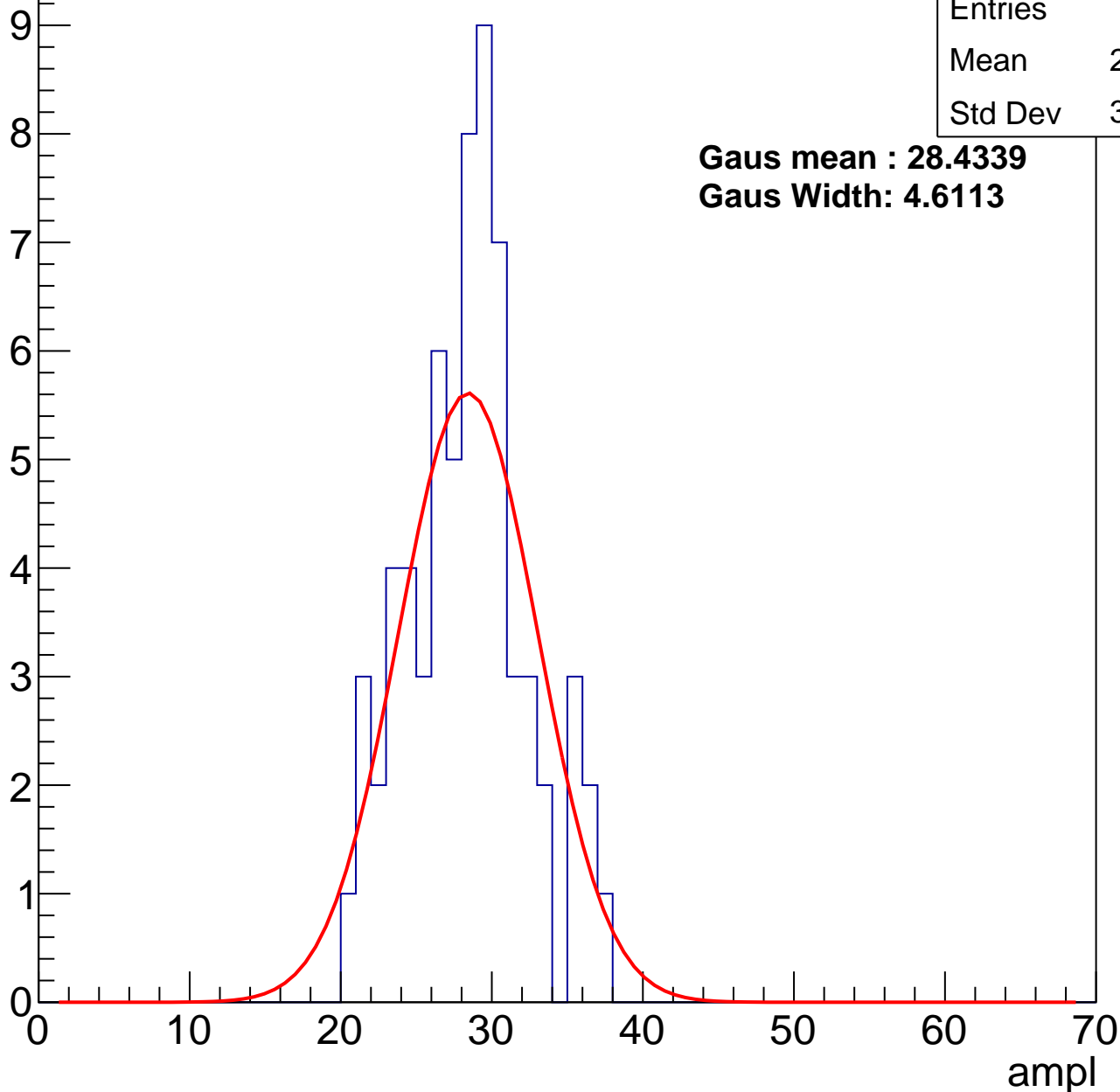
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	27.95
Std Dev	3.933

Gaus mean : 28.4339

Gaus Width: 4.6113



B1L100S, U19-ch84, adc1

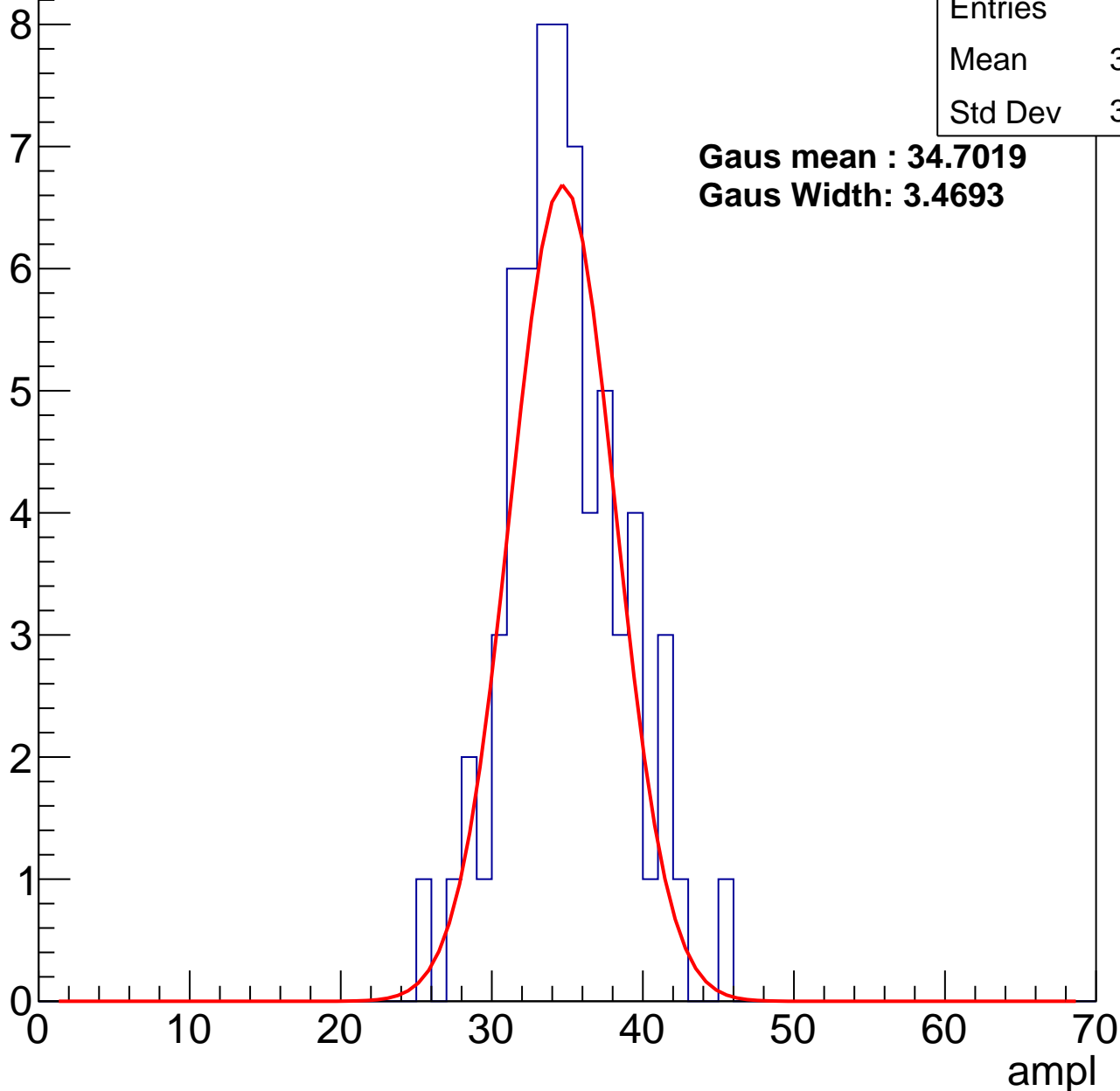
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	34.38
Std Dev	3.793

Gaus mean : 34.7019

Gaus Width: 3.4693



B1L100S, U19-ch84, adc2

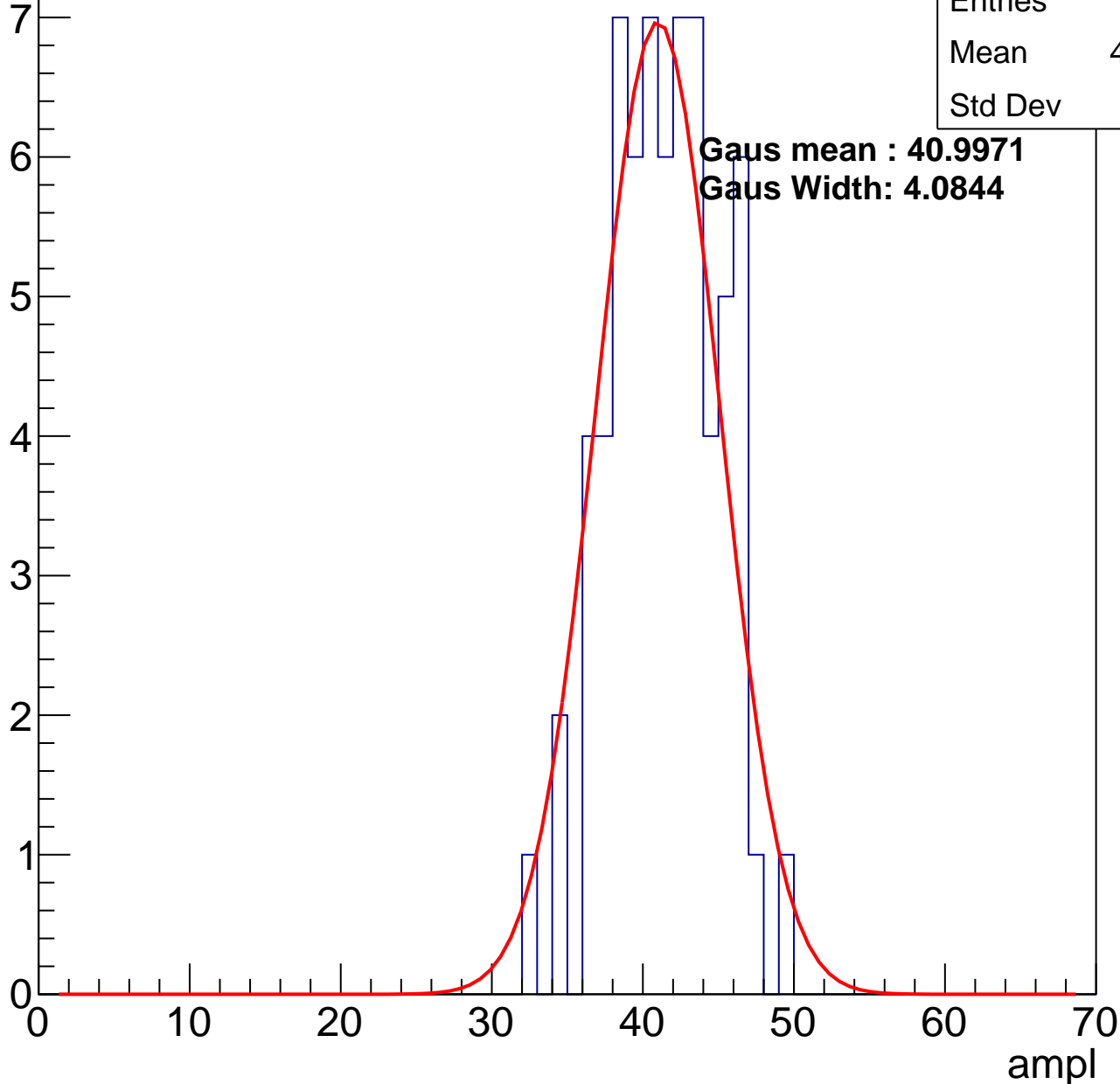
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	40.97
Std Dev	3.51

Gaus mean : 40.9971

Gaus Width: 4.0844

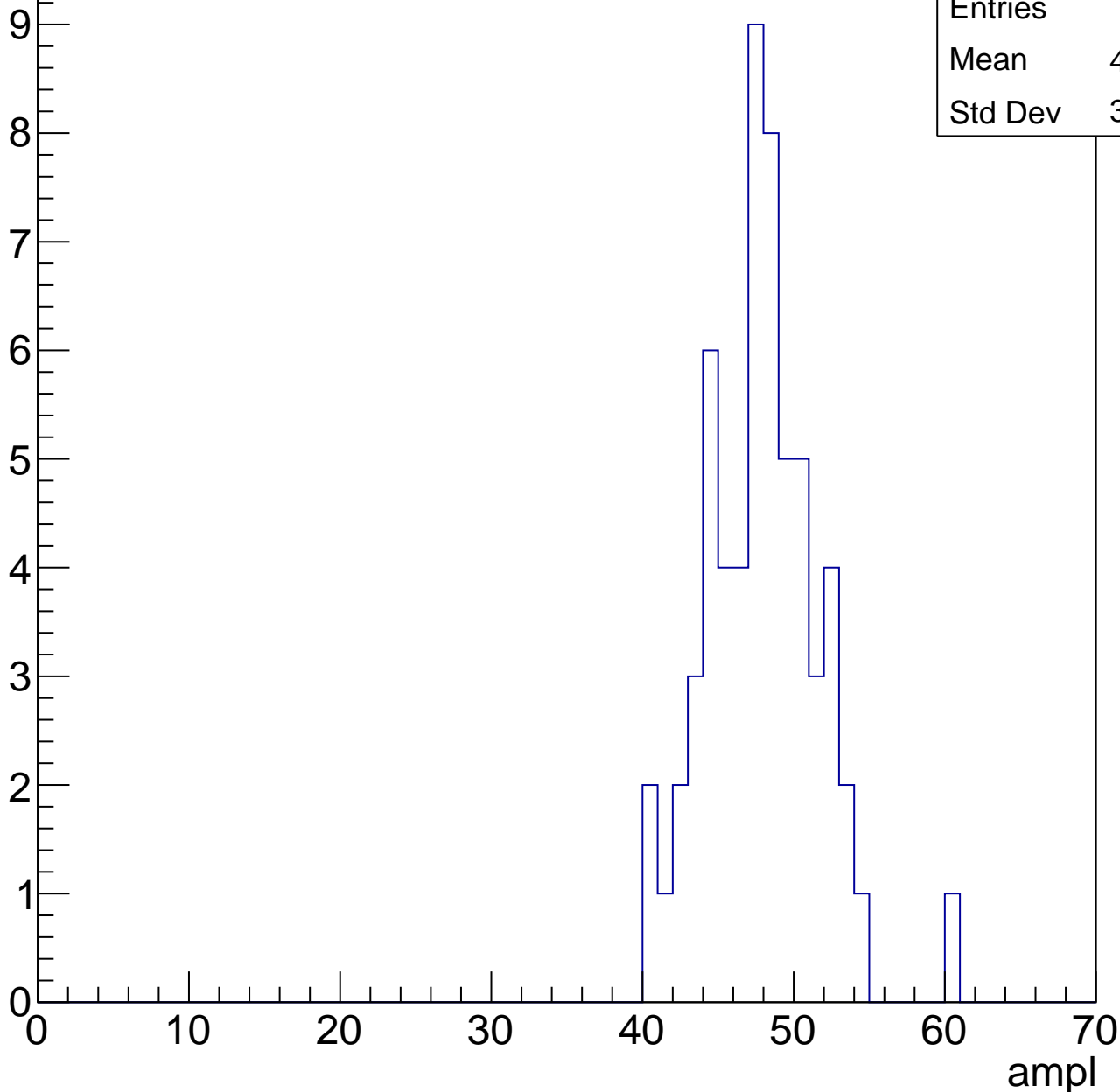


B1L100S, U19-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

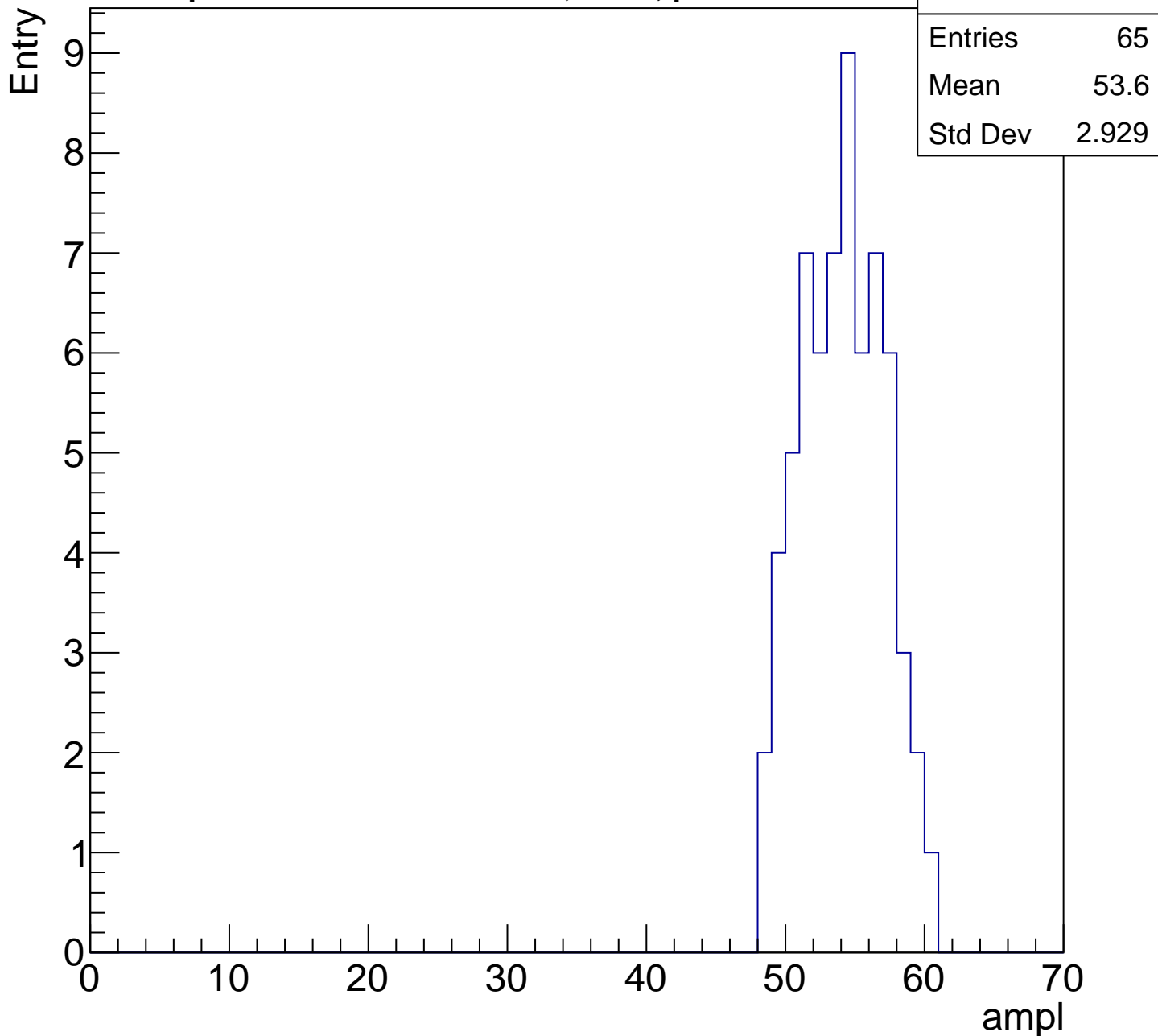
Entry

Entries	60
Mean	47.42
Std Dev	3.666



B1L100S, U19-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U19-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 47

Mean 59.06

Std Dev 2.462

ampl

0

10

20

30

40

50

60

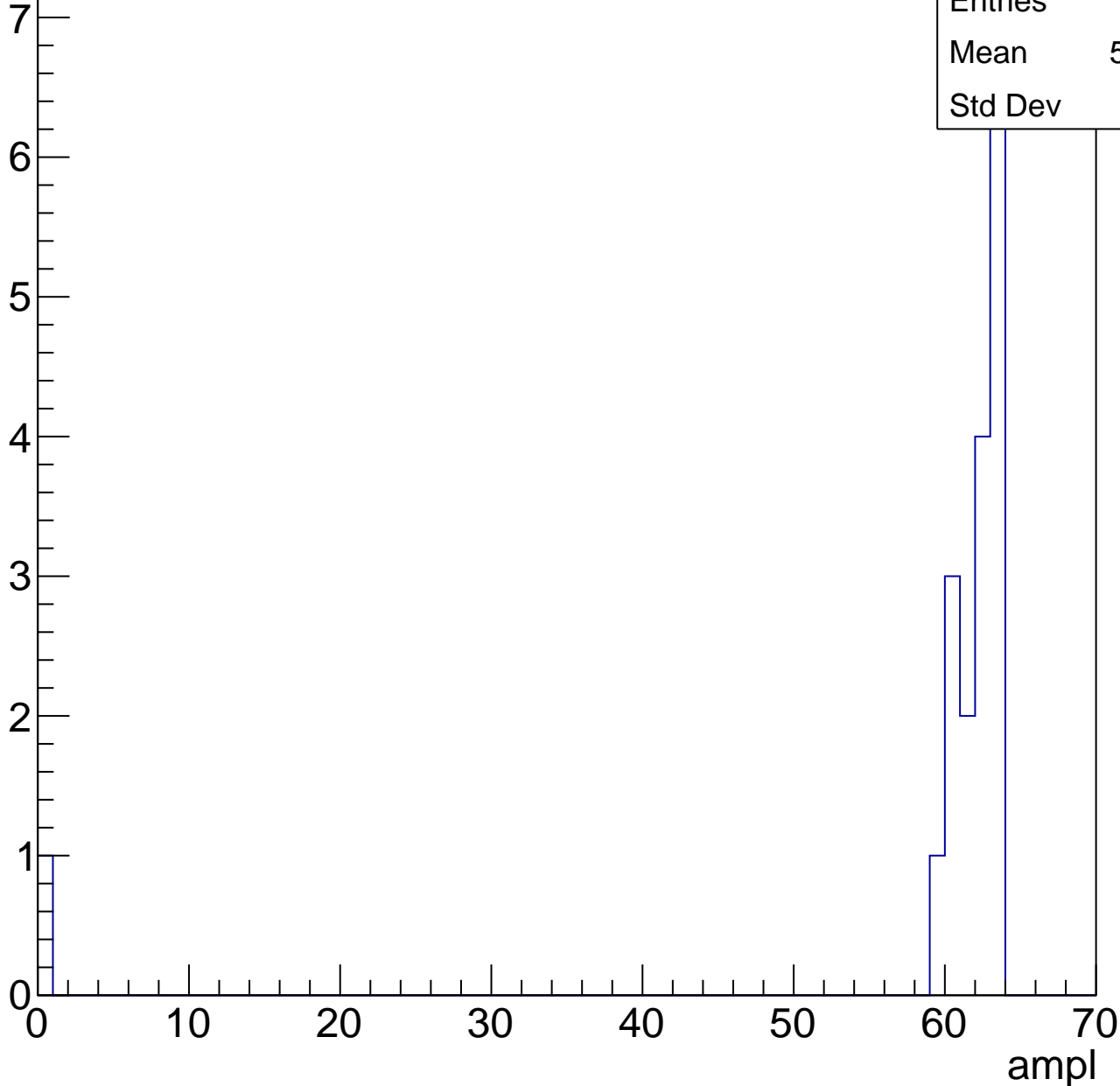
70

B1L100S, U19-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	58.33
Std Dev	14.2



B1L100S, U19-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch85, adc0

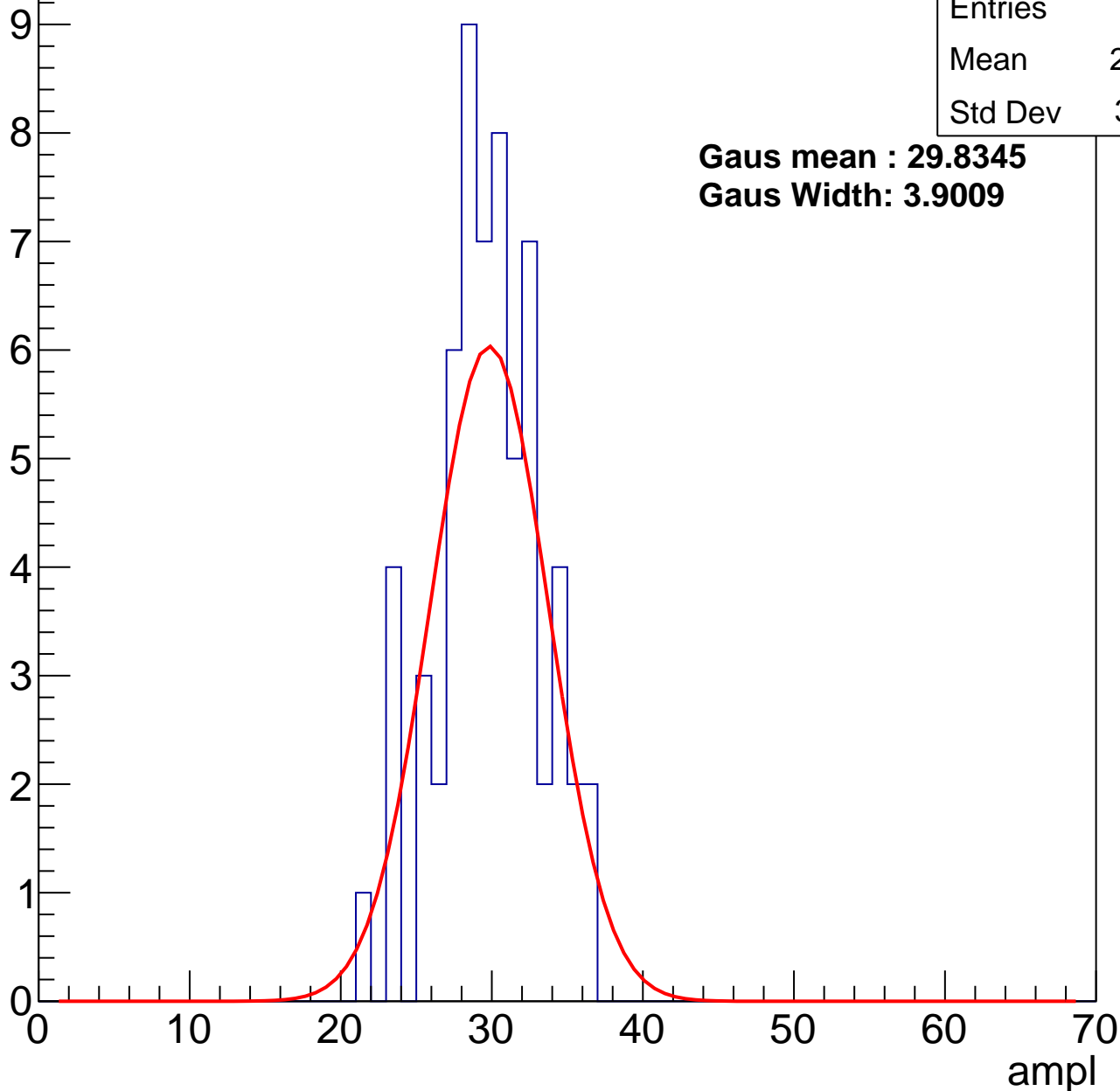
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	29.35
Std Dev	3.341

Gaus mean : 29.8345

Gaus Width: 3.9009



B1L100S, U19-ch85, adc1

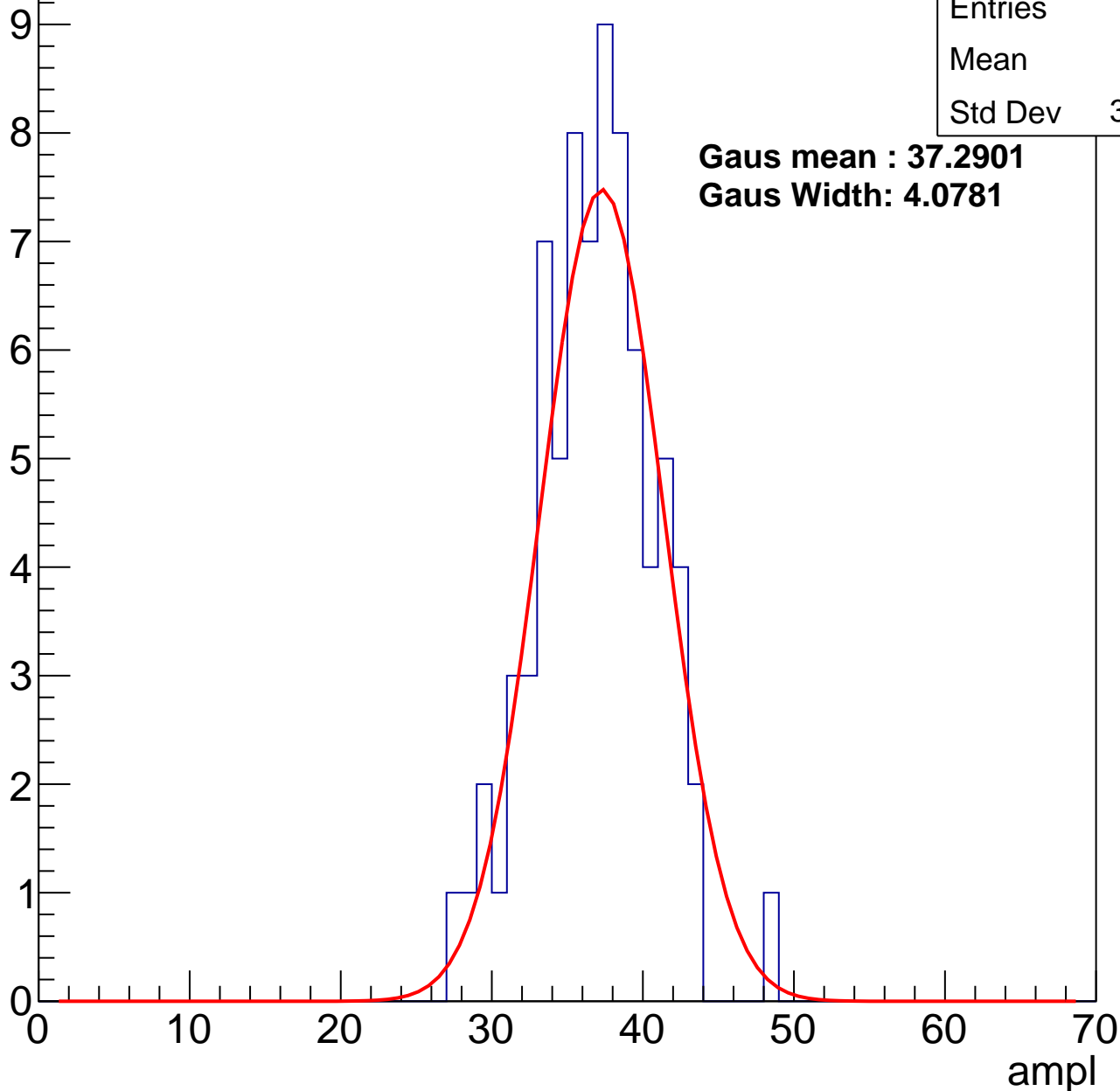
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	36.4
Std Dev	3.865

Gaus mean : 37.2901

Gaus Width: 4.0781



B1L100S, U19-ch85, adc2

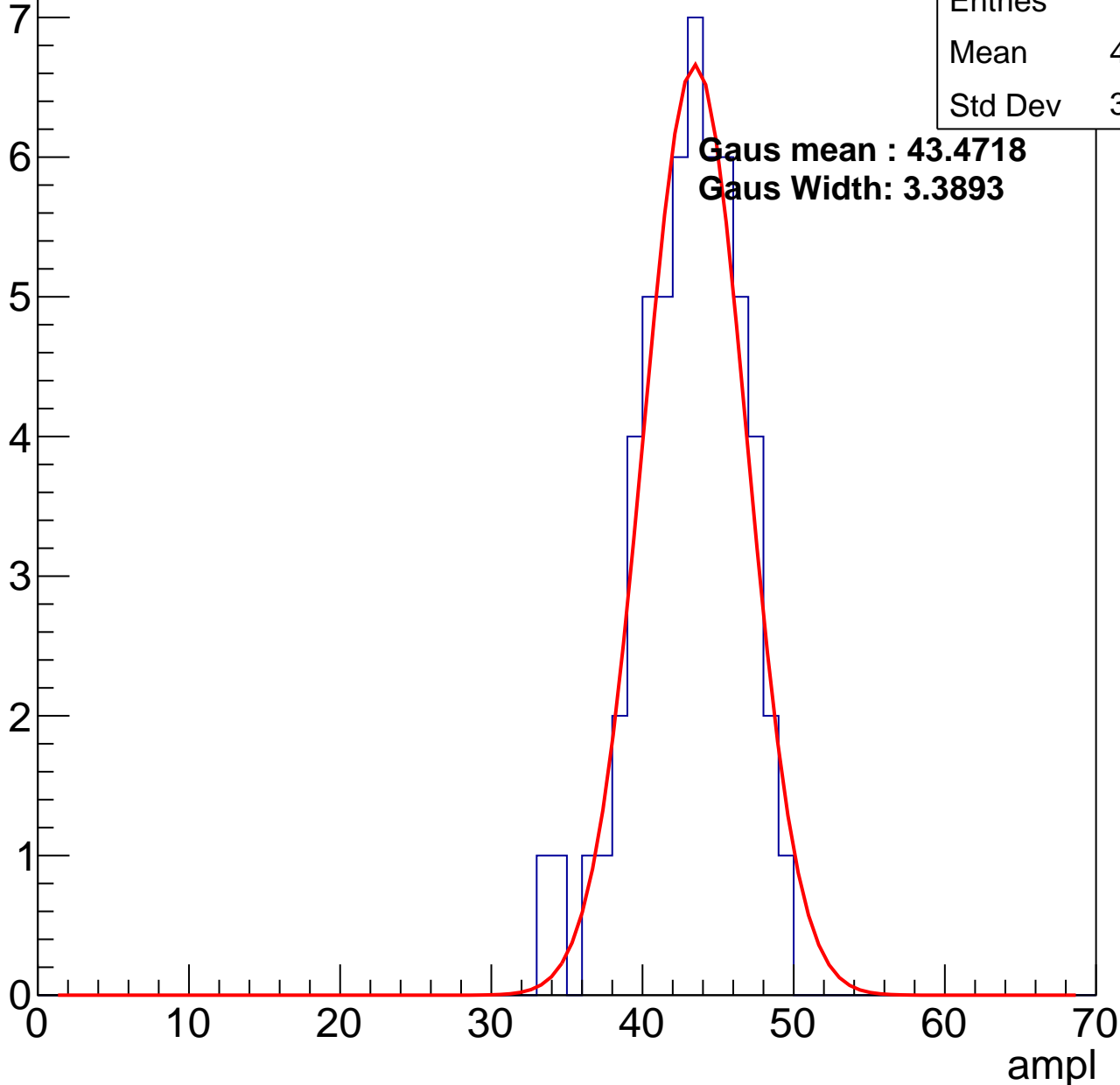
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.58
Std Dev	3.418

Gaus mean : 43.4718

Gaus Width: 3.3893



B1L100S, U19-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

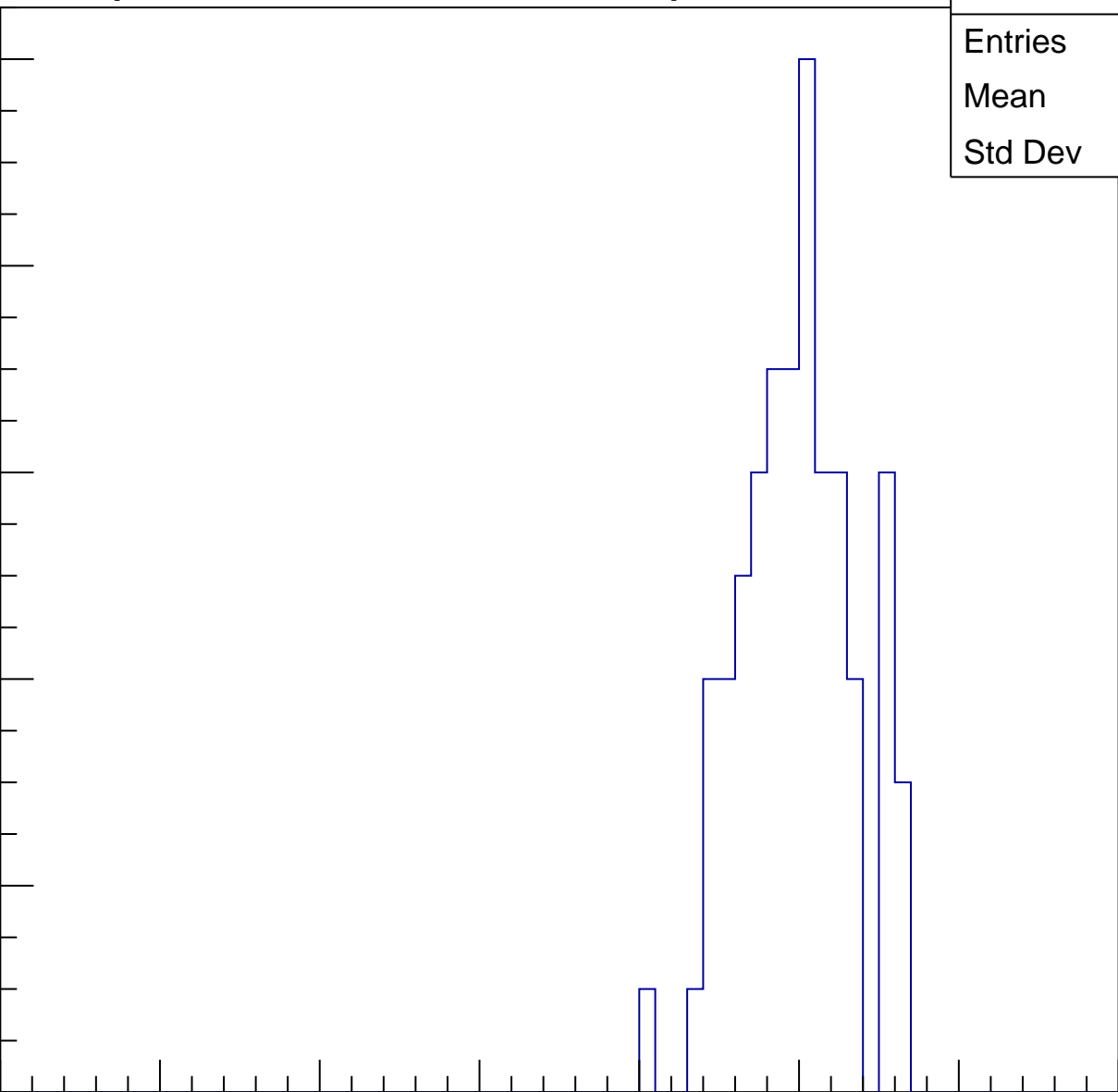
Entries	70
Mean	49.4
Std Dev	3.507

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	52
Mean	55.21
Std Dev	3.121

Entry

10

8

6

4

2

0

0

10

20

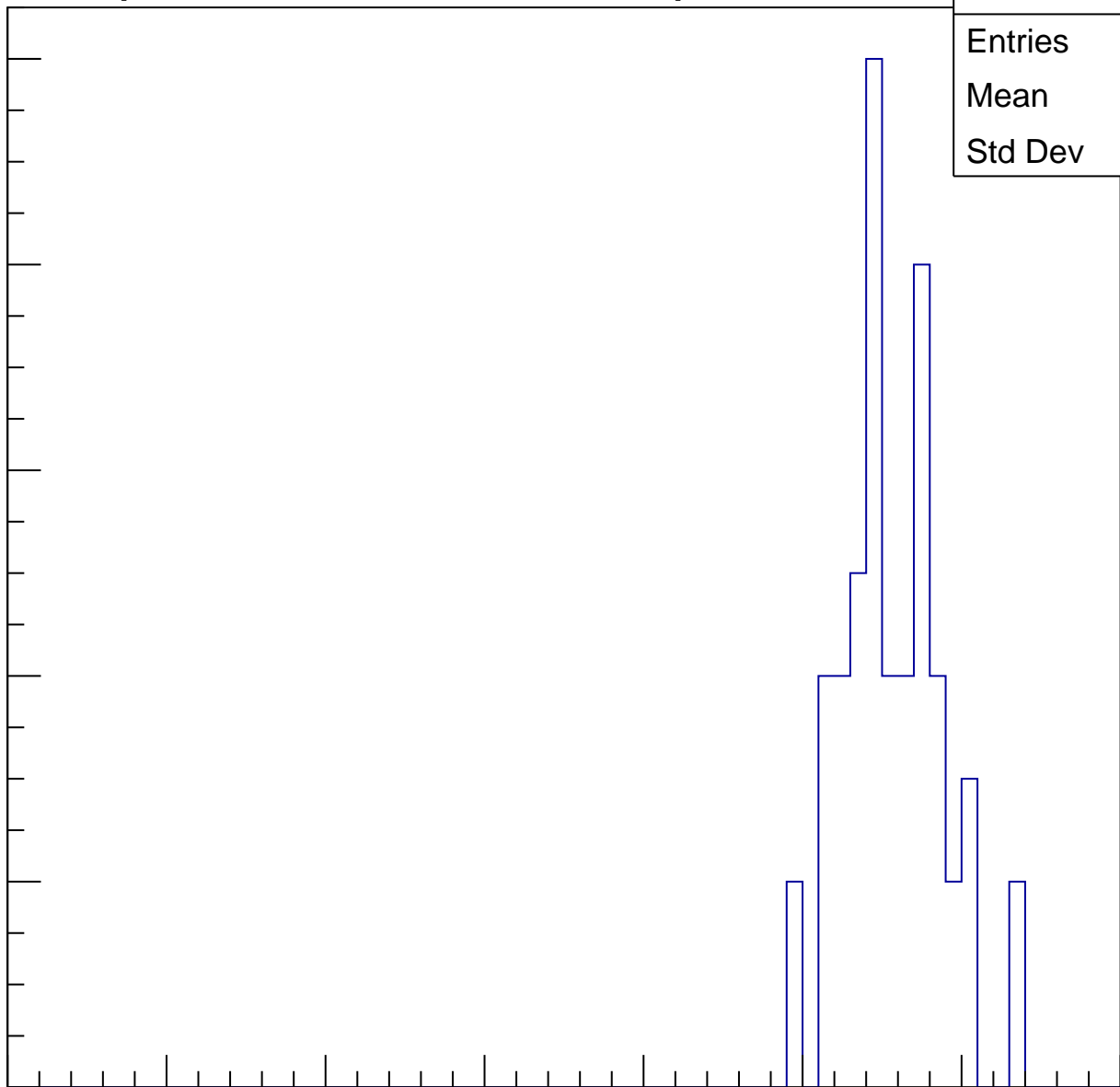
30

40

50

60

ampl

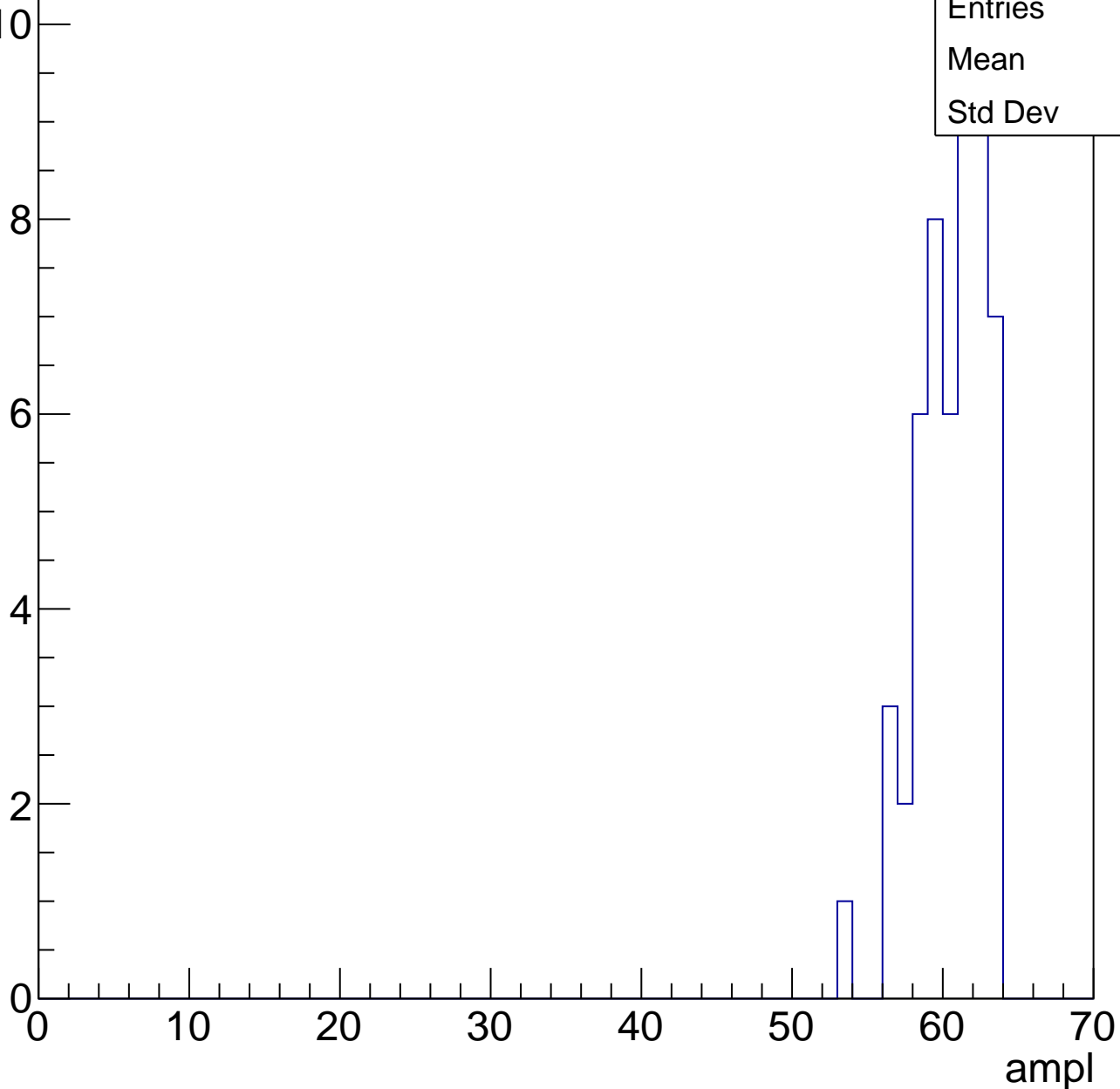


B1L100S, U19-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

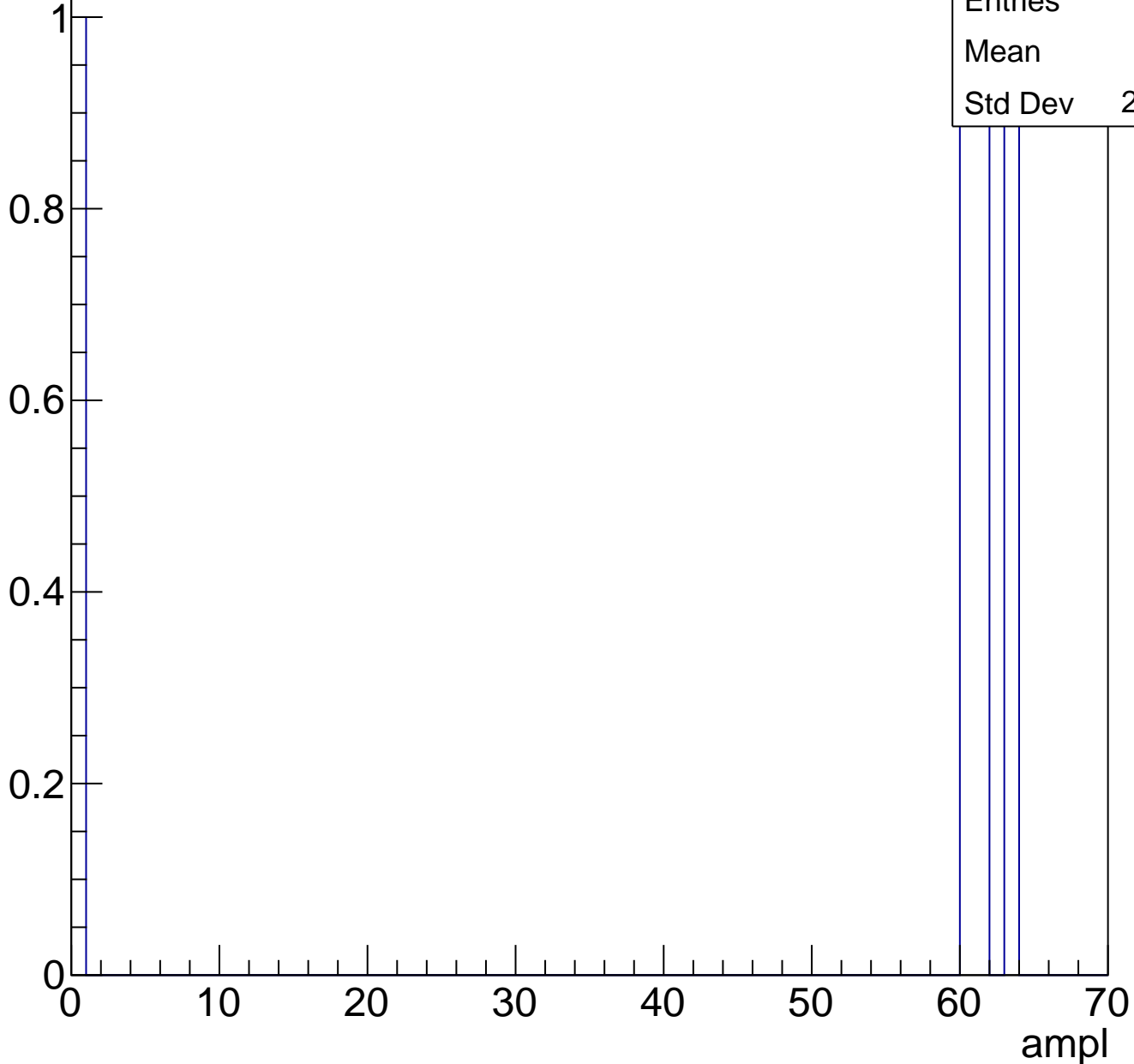
Entries	52
Mean	60.1
Std Dev	2.23



B1L100S, U19-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch86, adc0

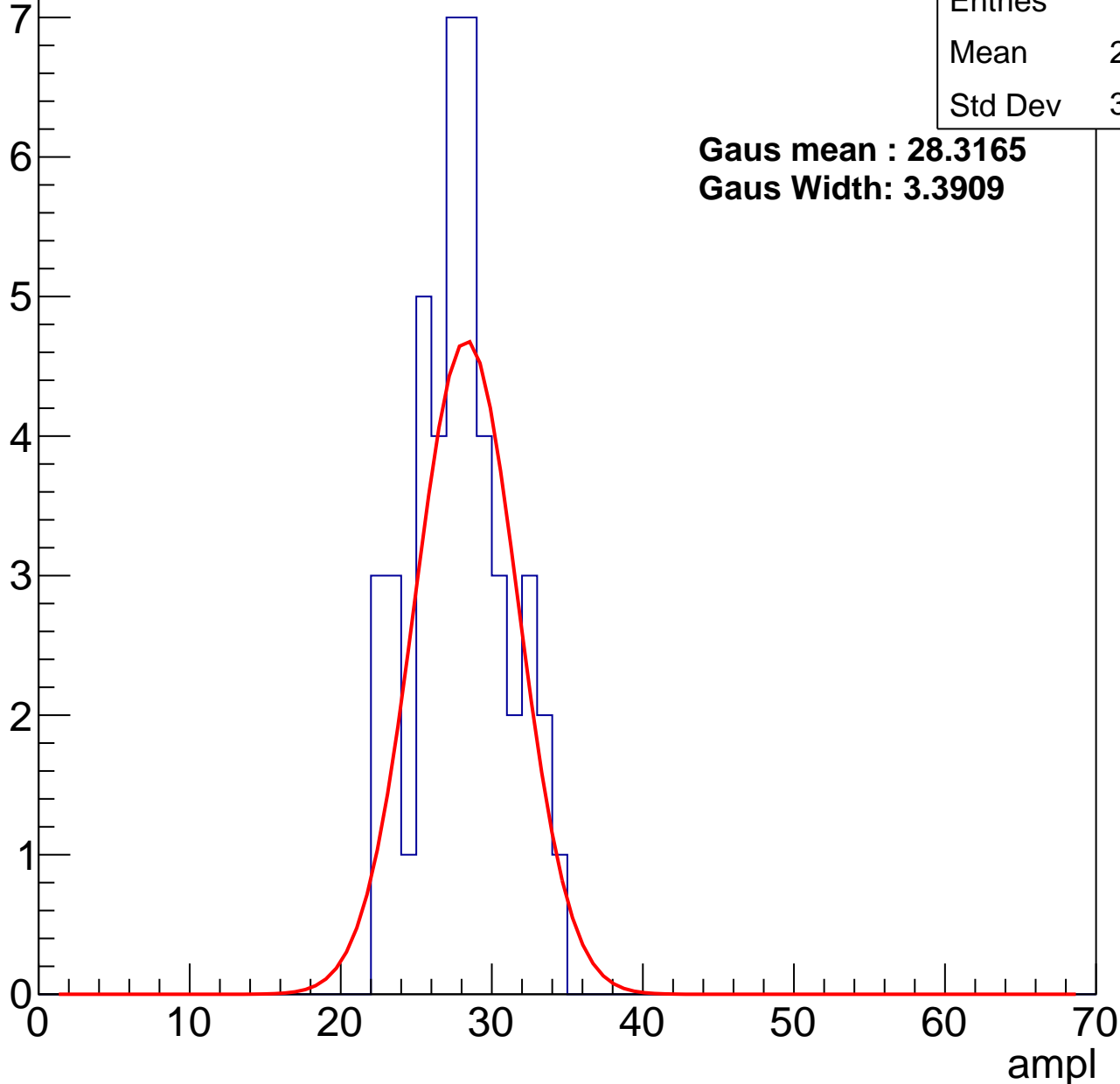
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	27.49
Std Dev	3.067

Gaus mean : 28.3165

Gaus Width: 3.3909



B1L100S, U19-ch86, adc1

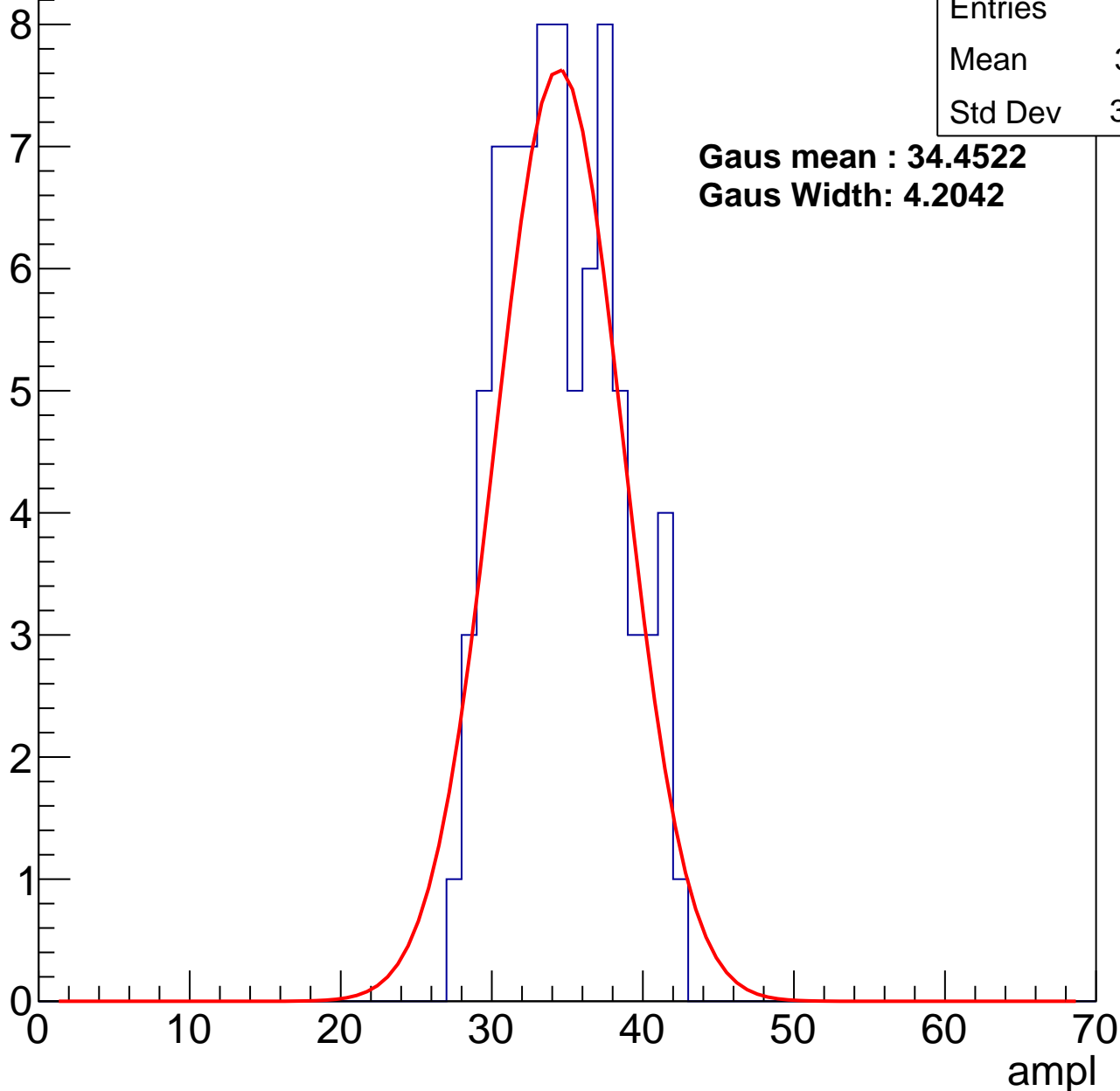
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	34.11
Std Dev	3.712

Gaus mean : 34.4522

Gaus Width: 4.2042



B1L100S, U19-ch86, adc2

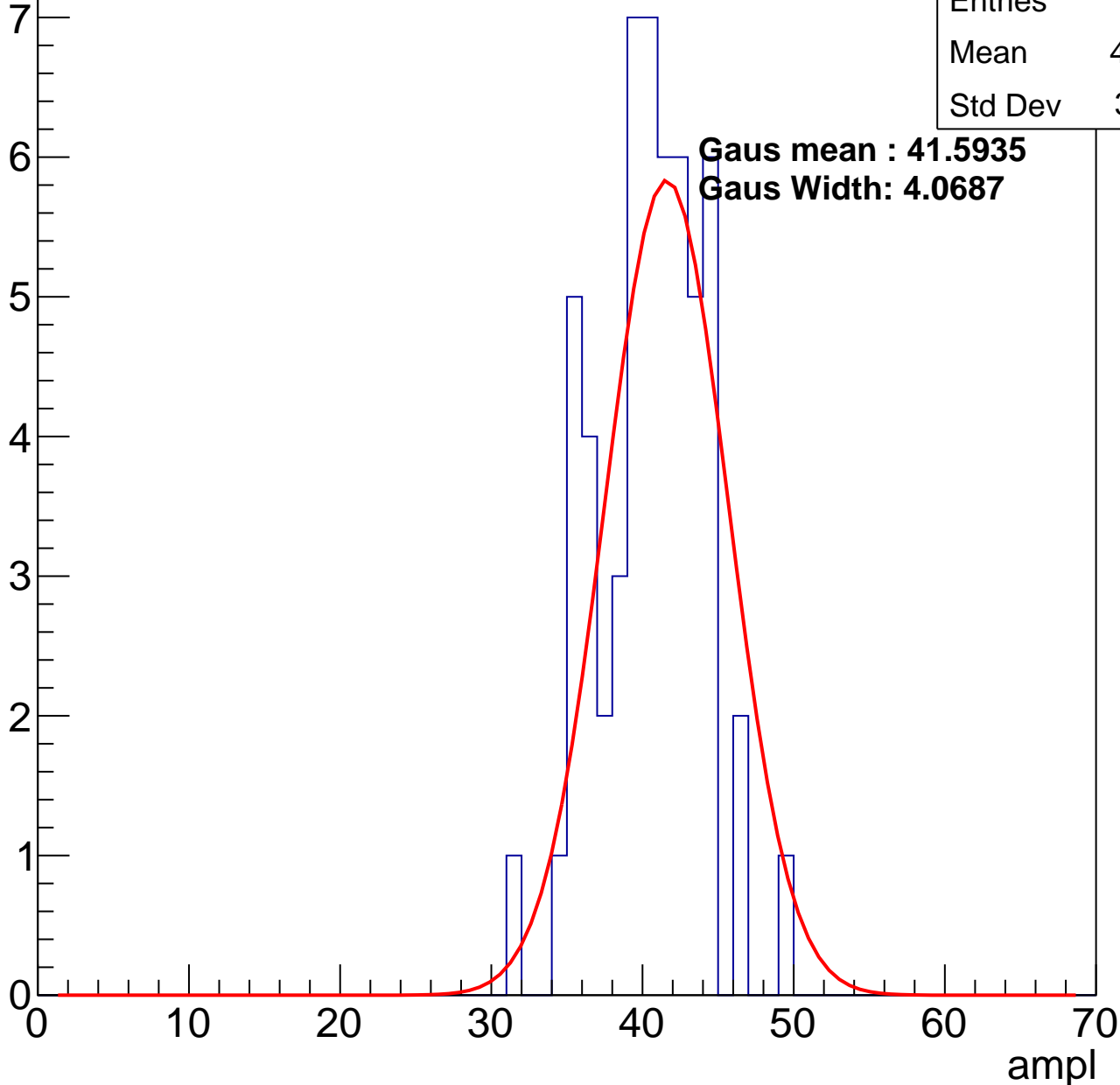
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	40.05
Std Dev	3.451

Gaus mean : 41.5935

Gaus Width: 4.0687



B1L100S, U19-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	46.91
Std Dev	3.133

Entry

10

8

6

4

2

0

0

10

20

30

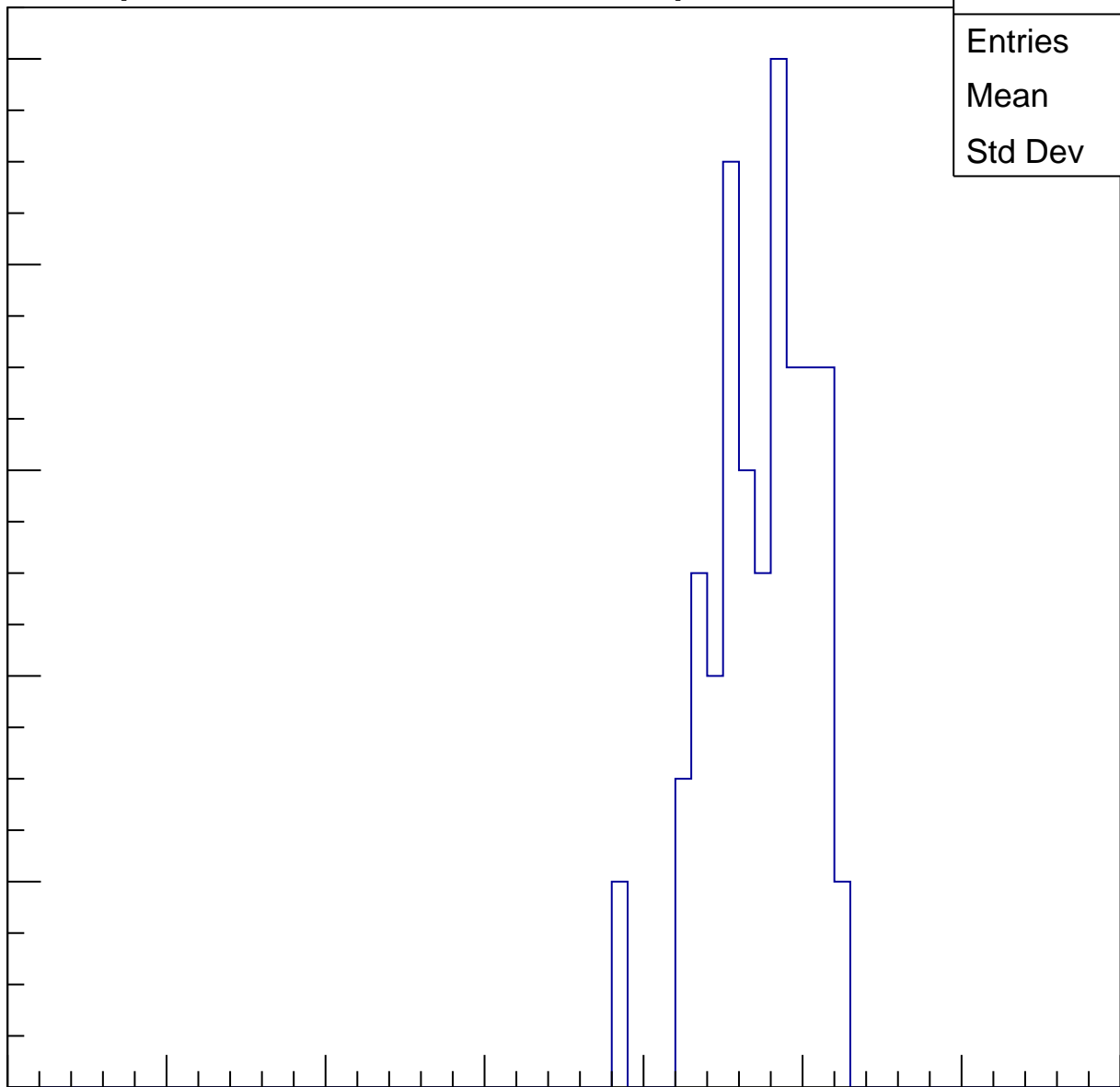
40

50

60

70

ampl

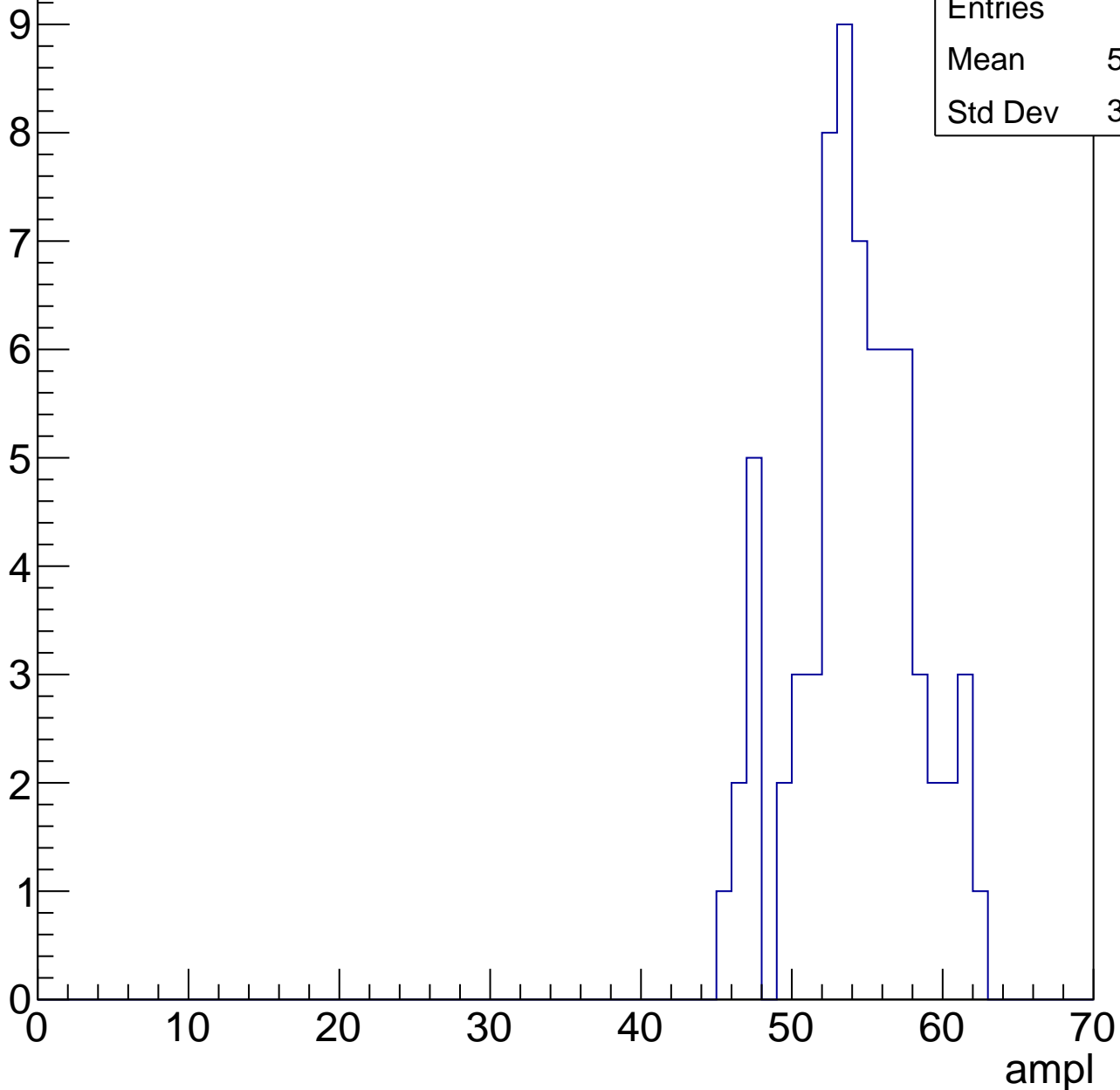


B1L100S, U19-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	53.75
Std Dev	3.958



B1L100S, U19-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	48
Mean	58.77
Std Dev	2.793

ampl

0

10

20

30

40

50

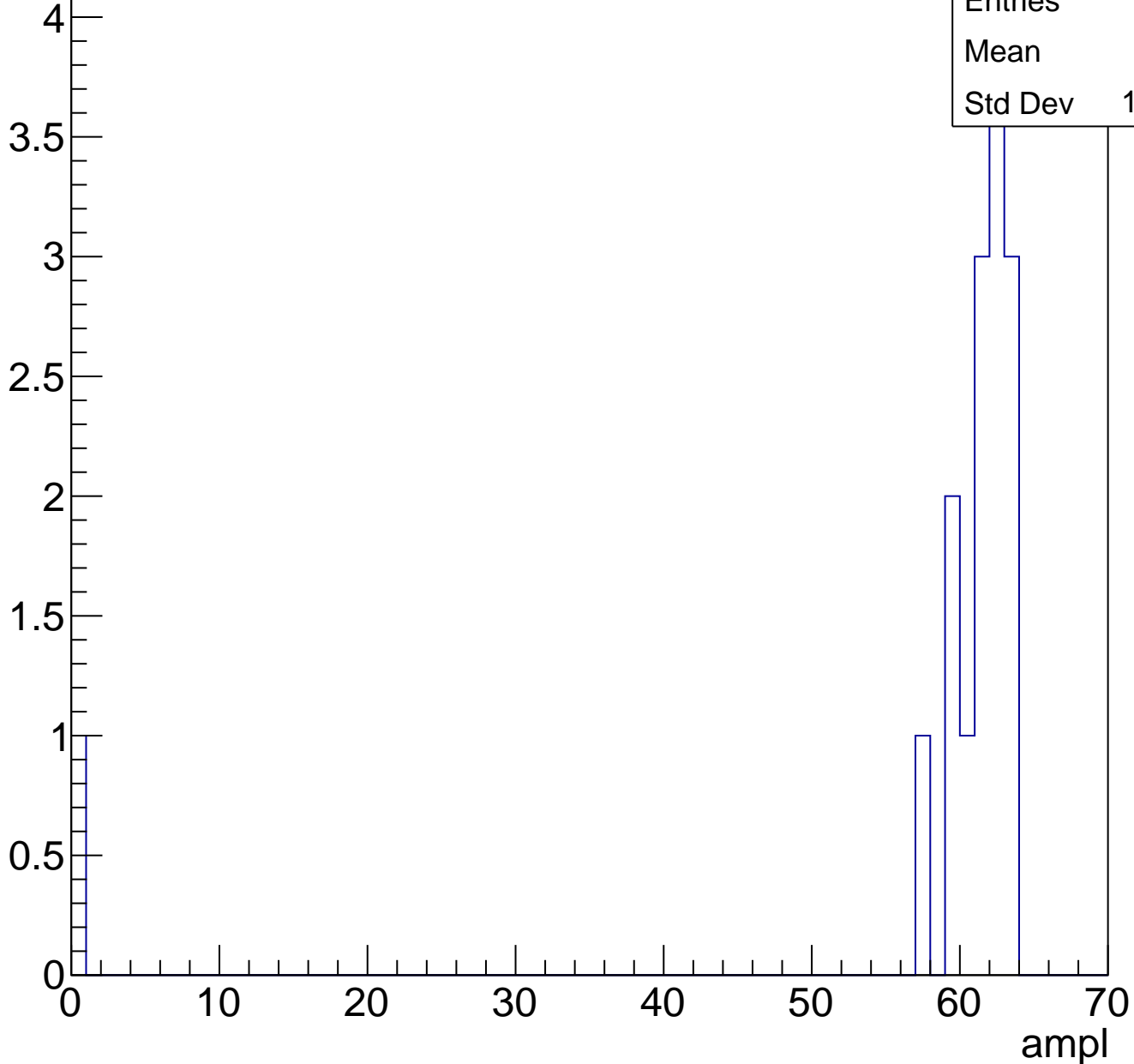
60

70

B1L100S, U19-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

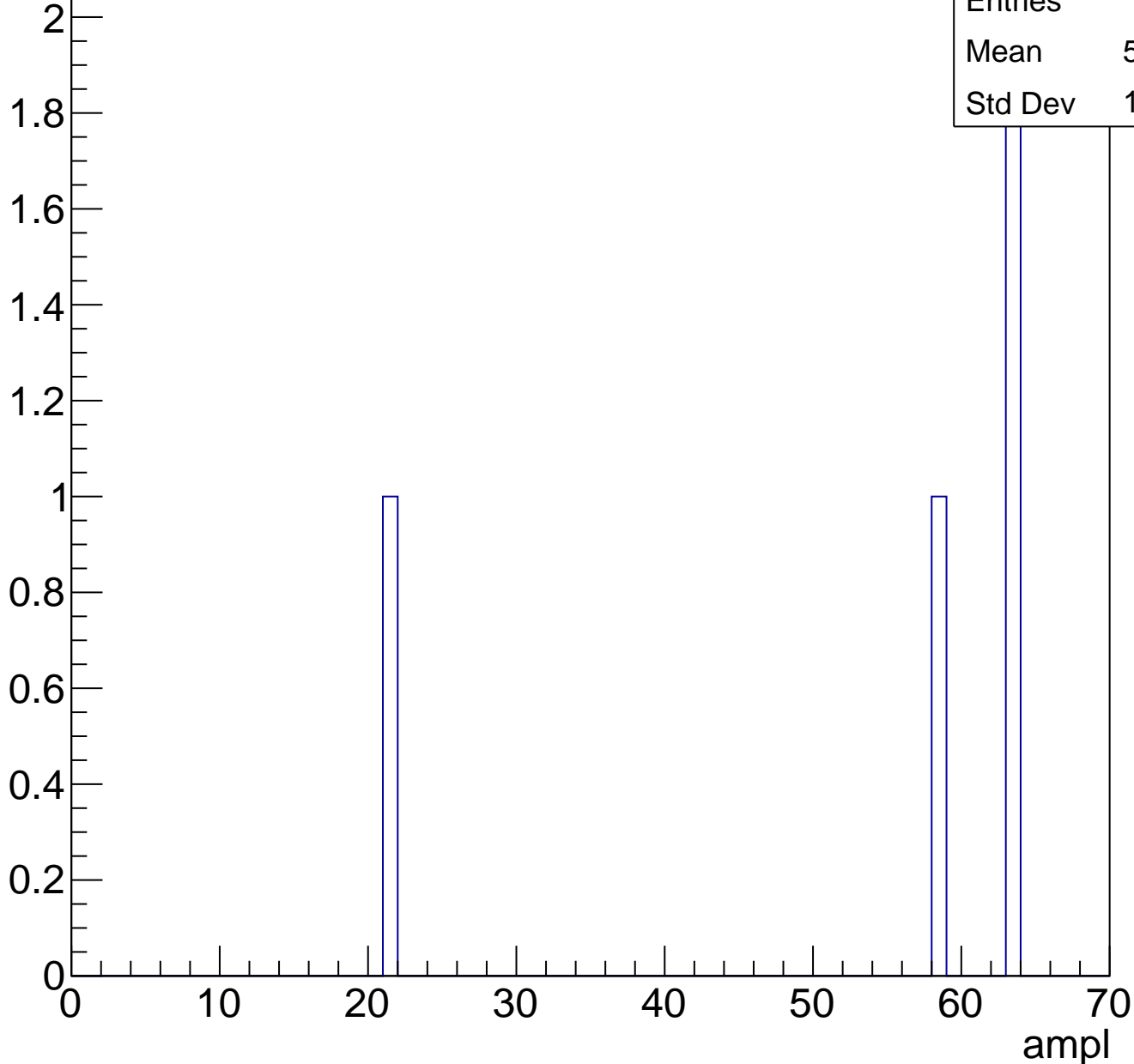
Entry



B1L100S, U19-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch87, adc0

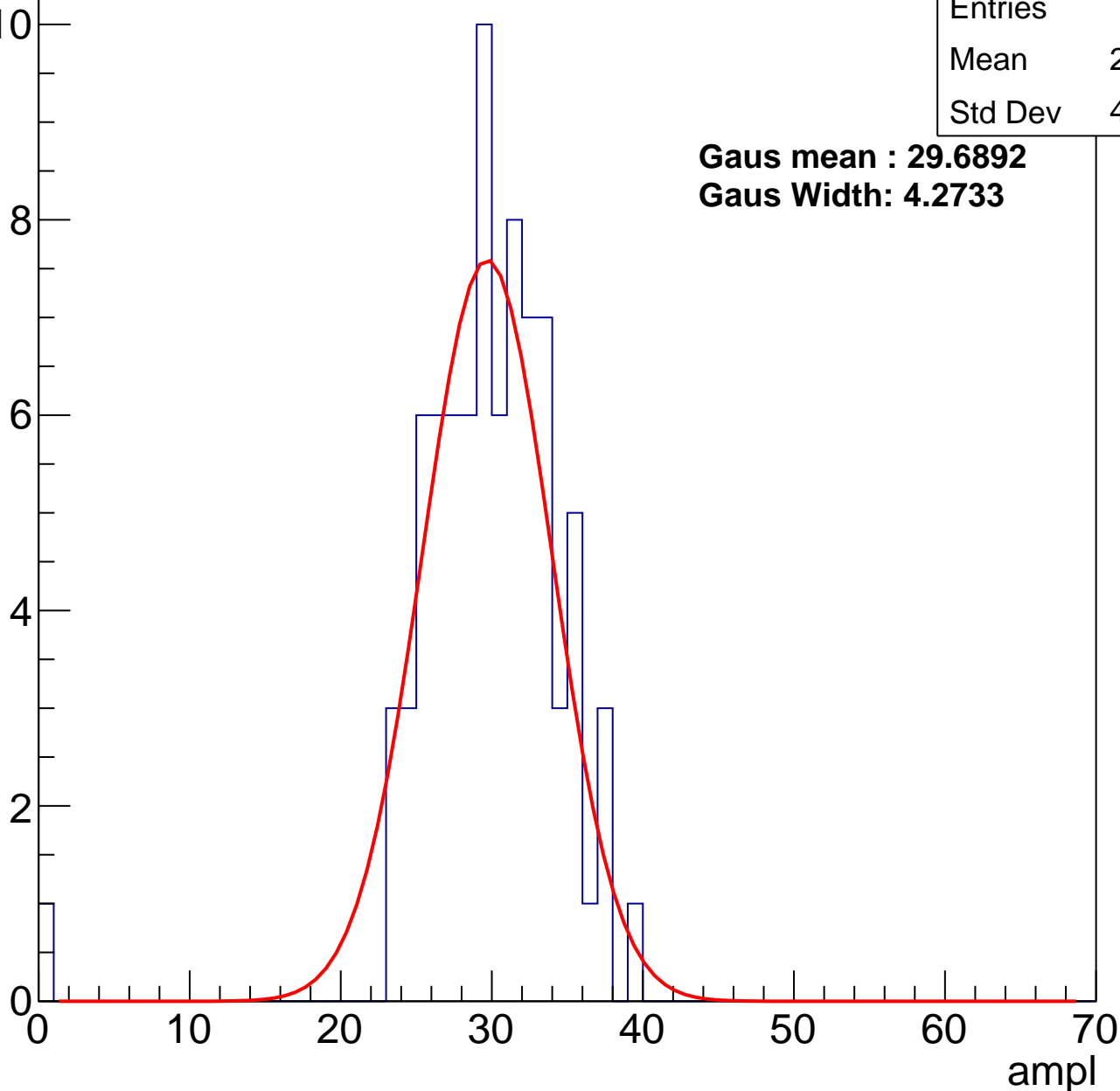
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	29.43
Std Dev	4.936

Gaus mean : 29.6892

Gaus Width: 4.2733



B1L100S, U19-ch87, adc1

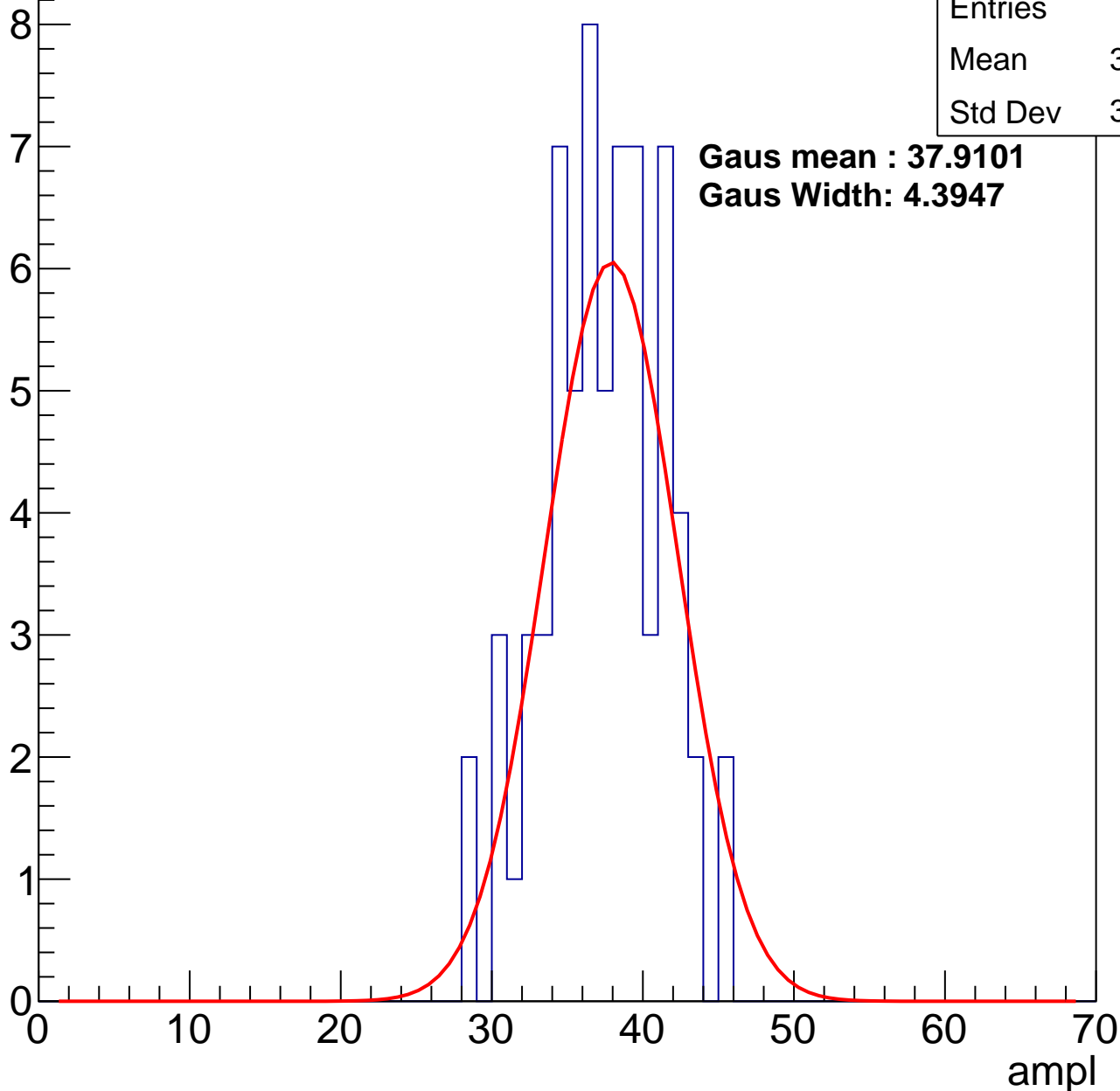
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.93
Std Dev	3.869

Gaus mean : 37.9101

Gaus Width: 4.3947



B1L100S, U19-ch87, adc2

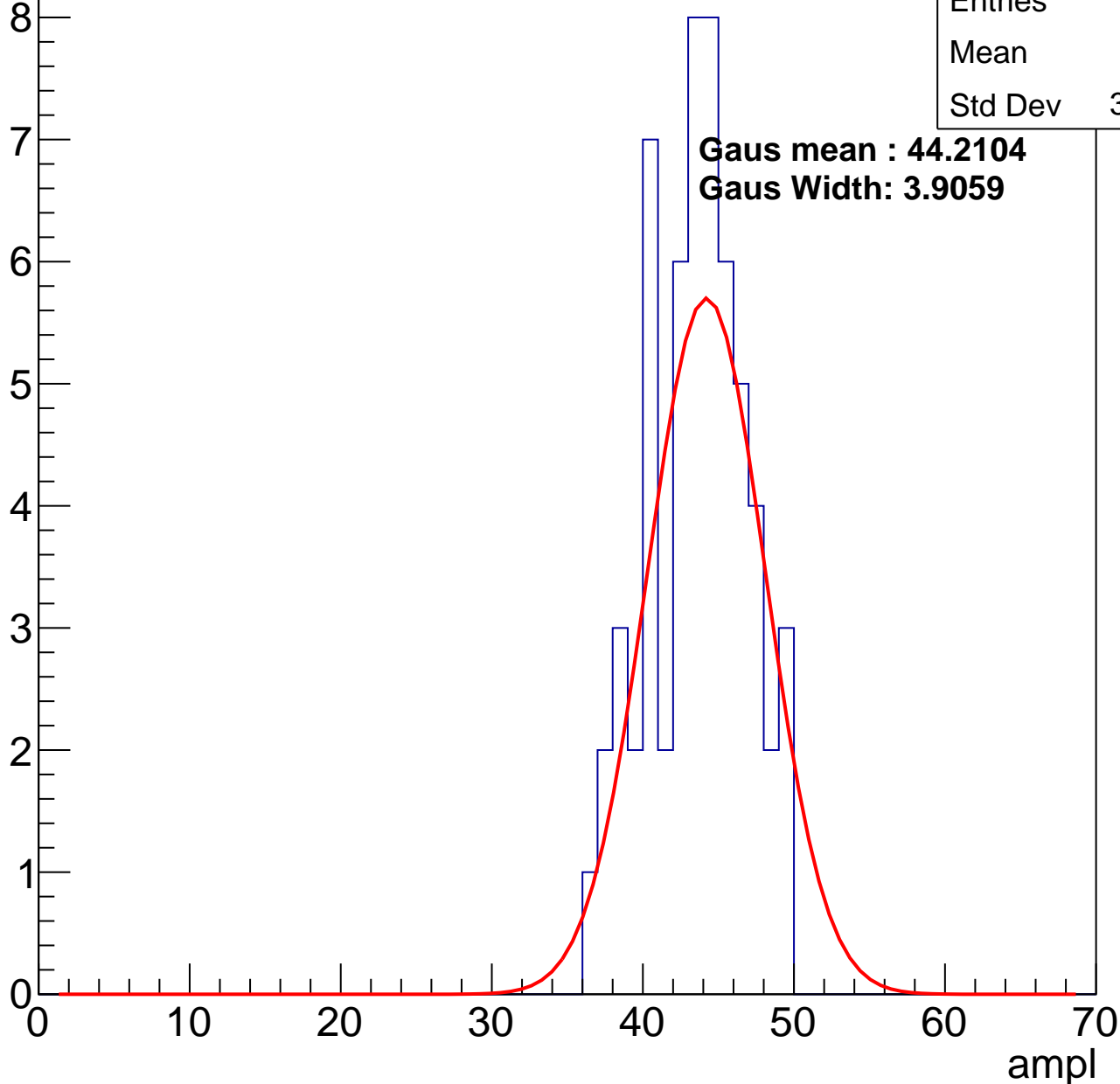
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.1
Std Dev	3.198

Gaus mean : 44.2104

Gaus Width: 3.9059

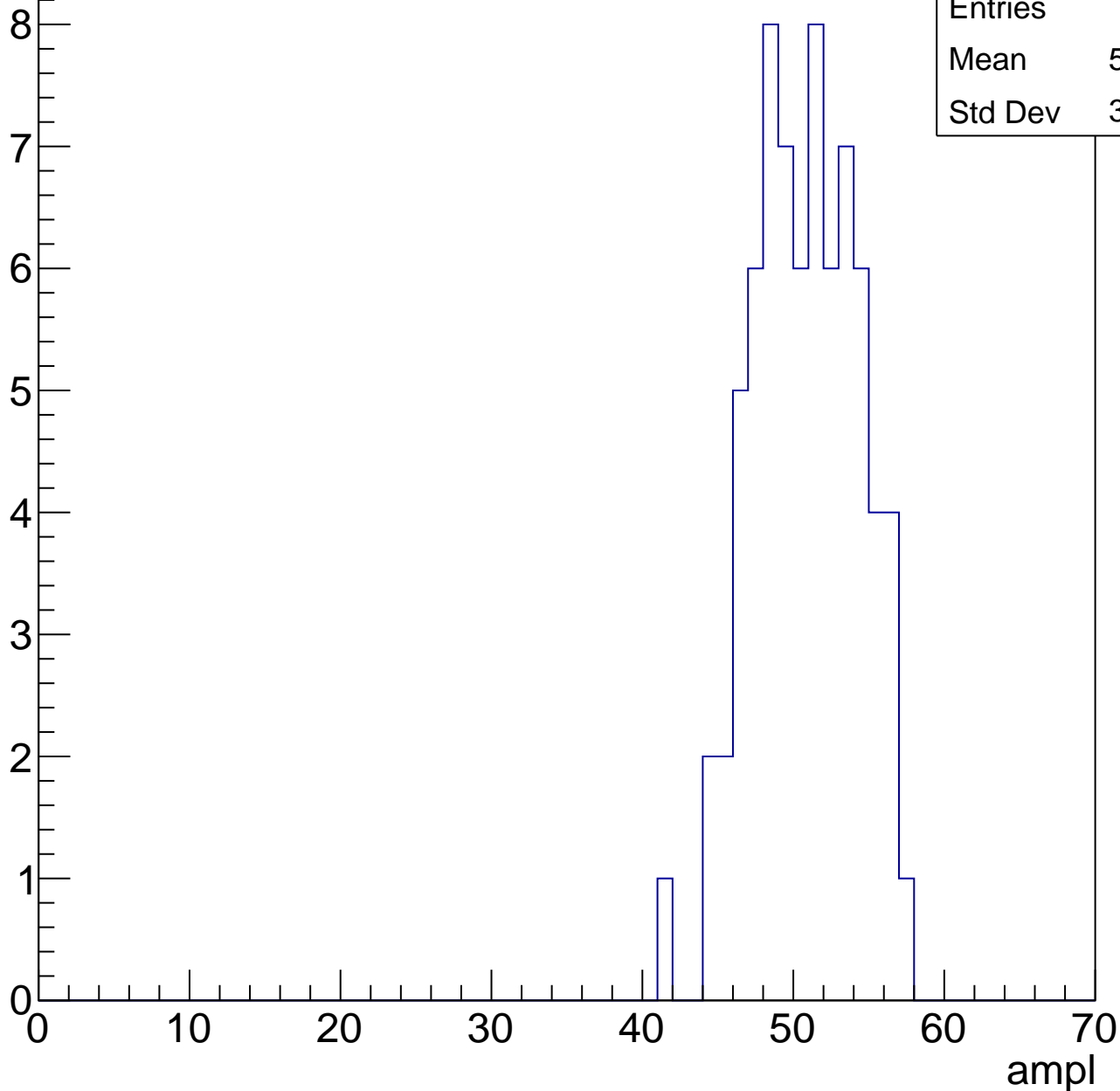


B1L100S, U19-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

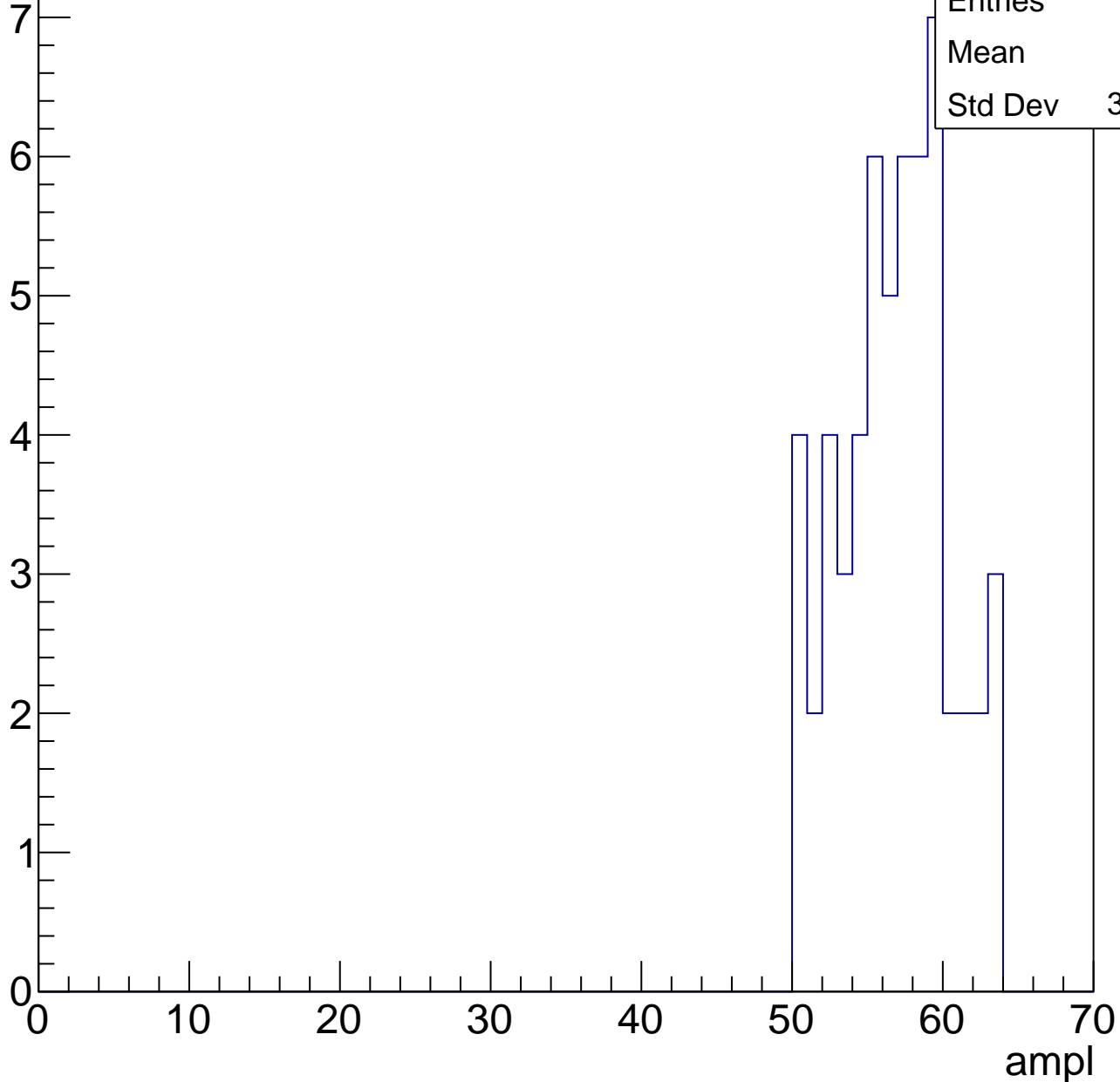
Entries	73
Mean	50.33
Std Dev	3.417



B1L100S, U19-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

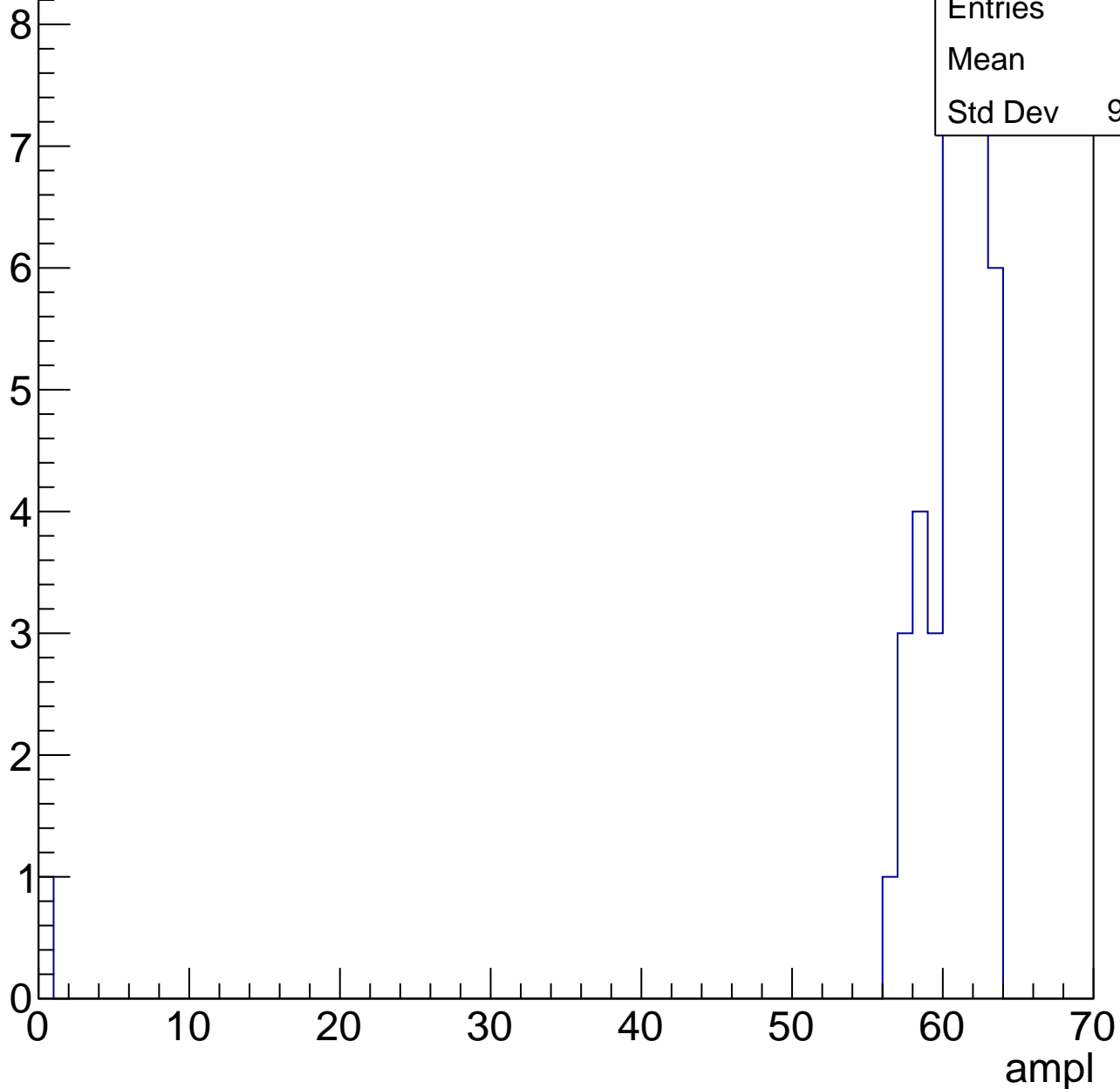


Entries	56
Mean	56.3
Std Dev	3.525

B1L100S, U19-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch88, adc0

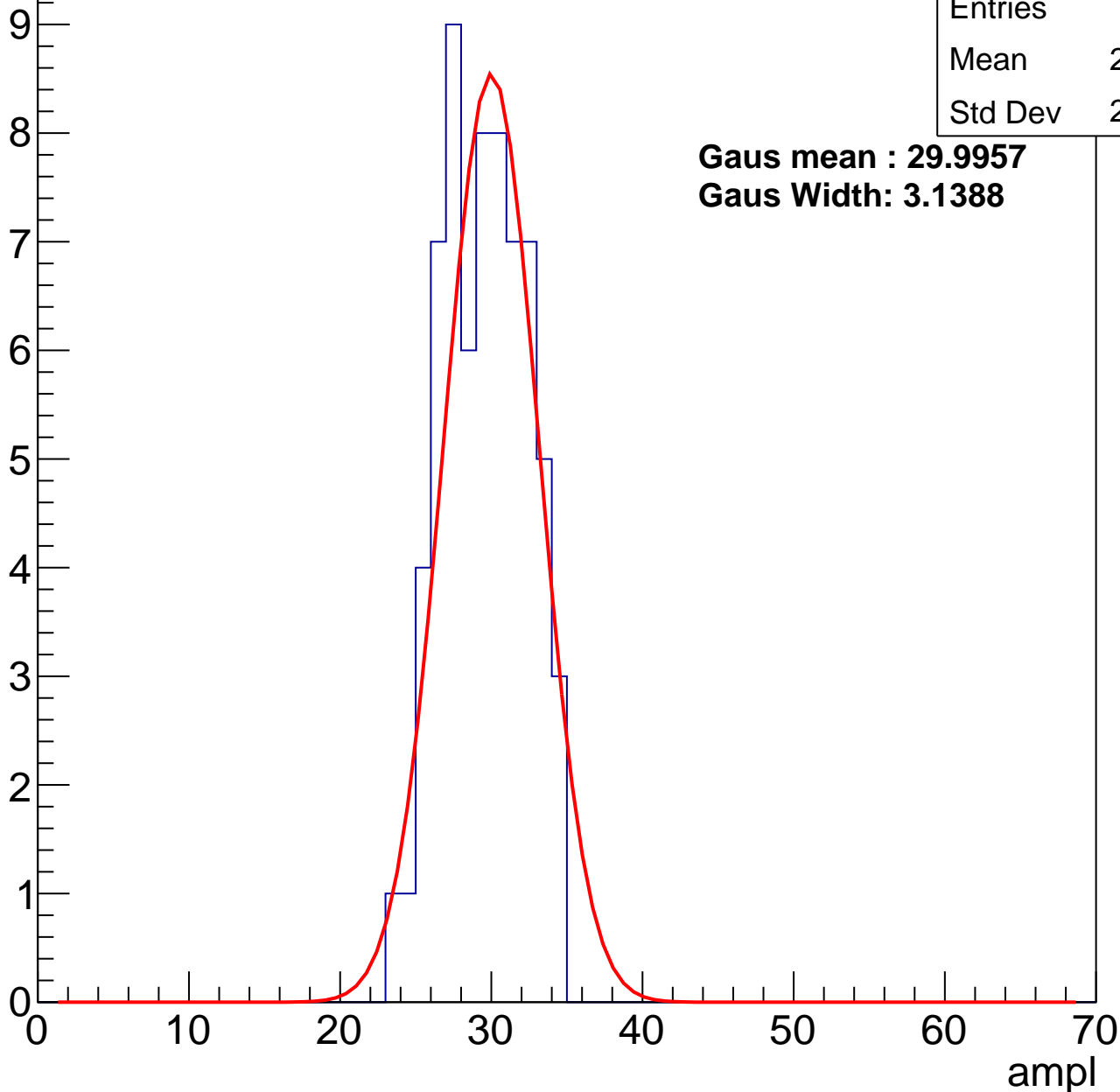
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.09
Std Dev	2.706

Gaus mean : 29.9957

Gaus Width: 3.1388



B1L100S, U19-ch88, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	35.62
Std Dev	3.387

Gaus mean : 36.2626

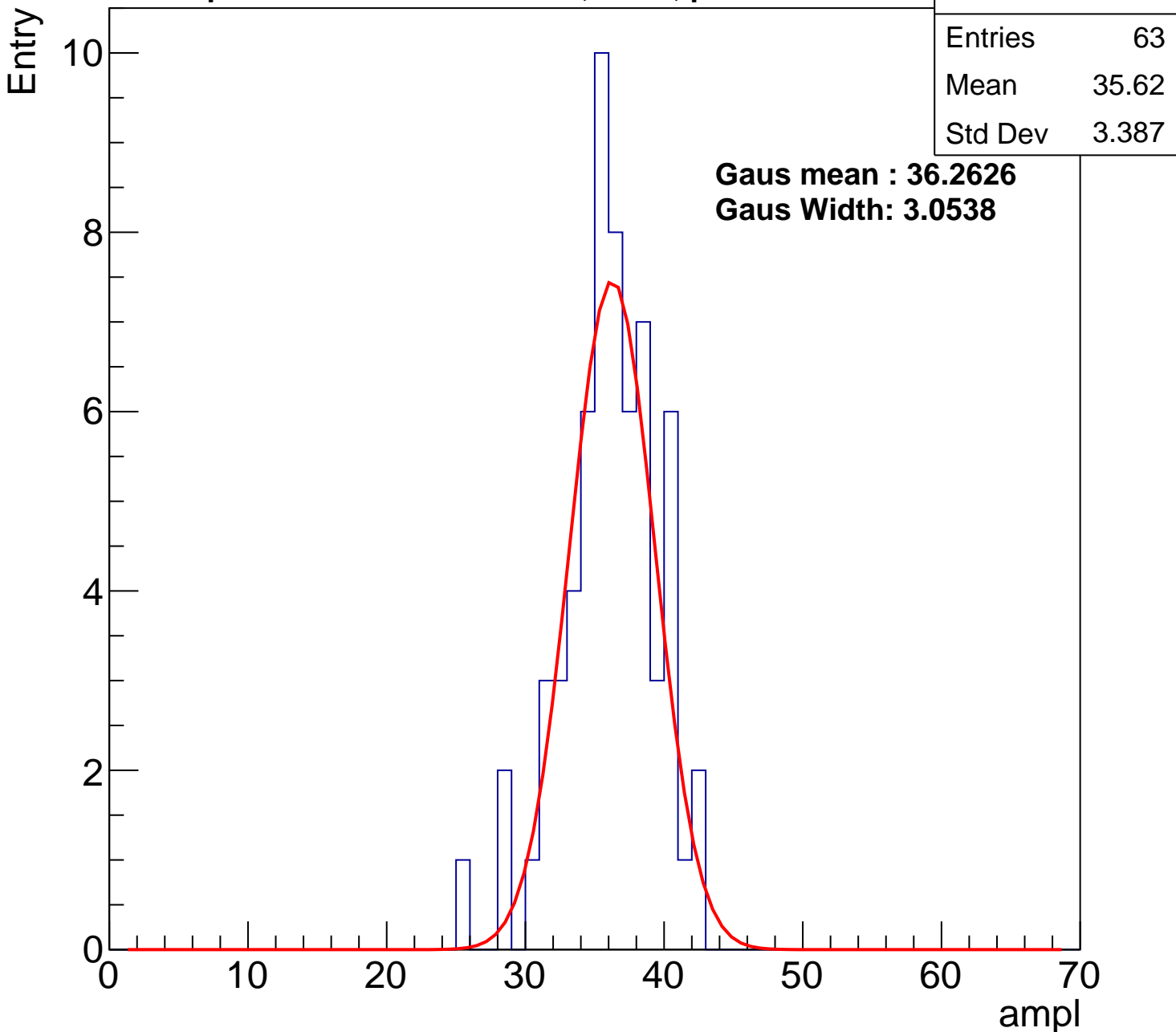
Gaus Width: 3.0538

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch88, adc2

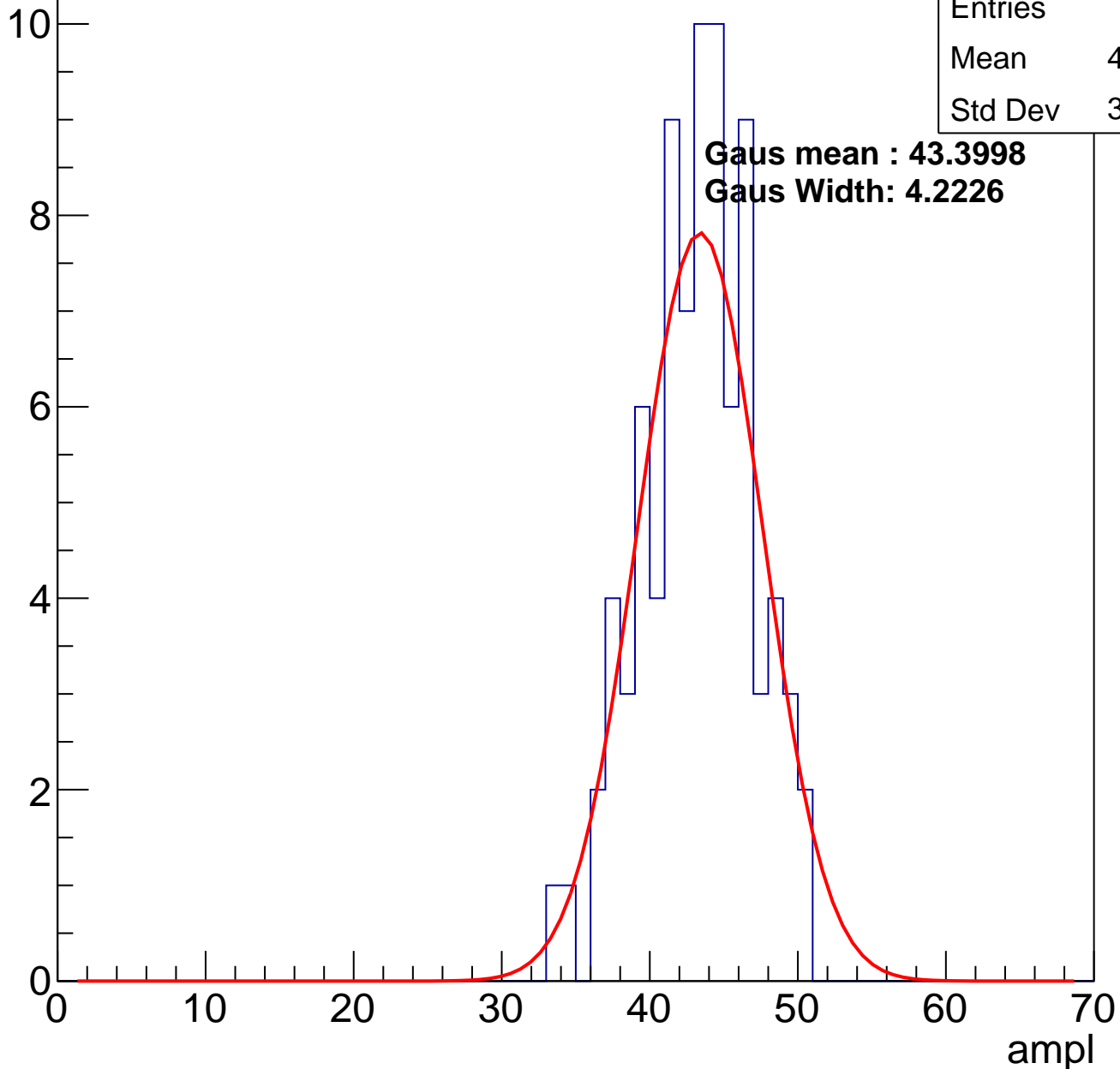
calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	42.76
Std Dev	3.679

Gaus mean : 43.3998

Gaus Width: 4.2226

Entry

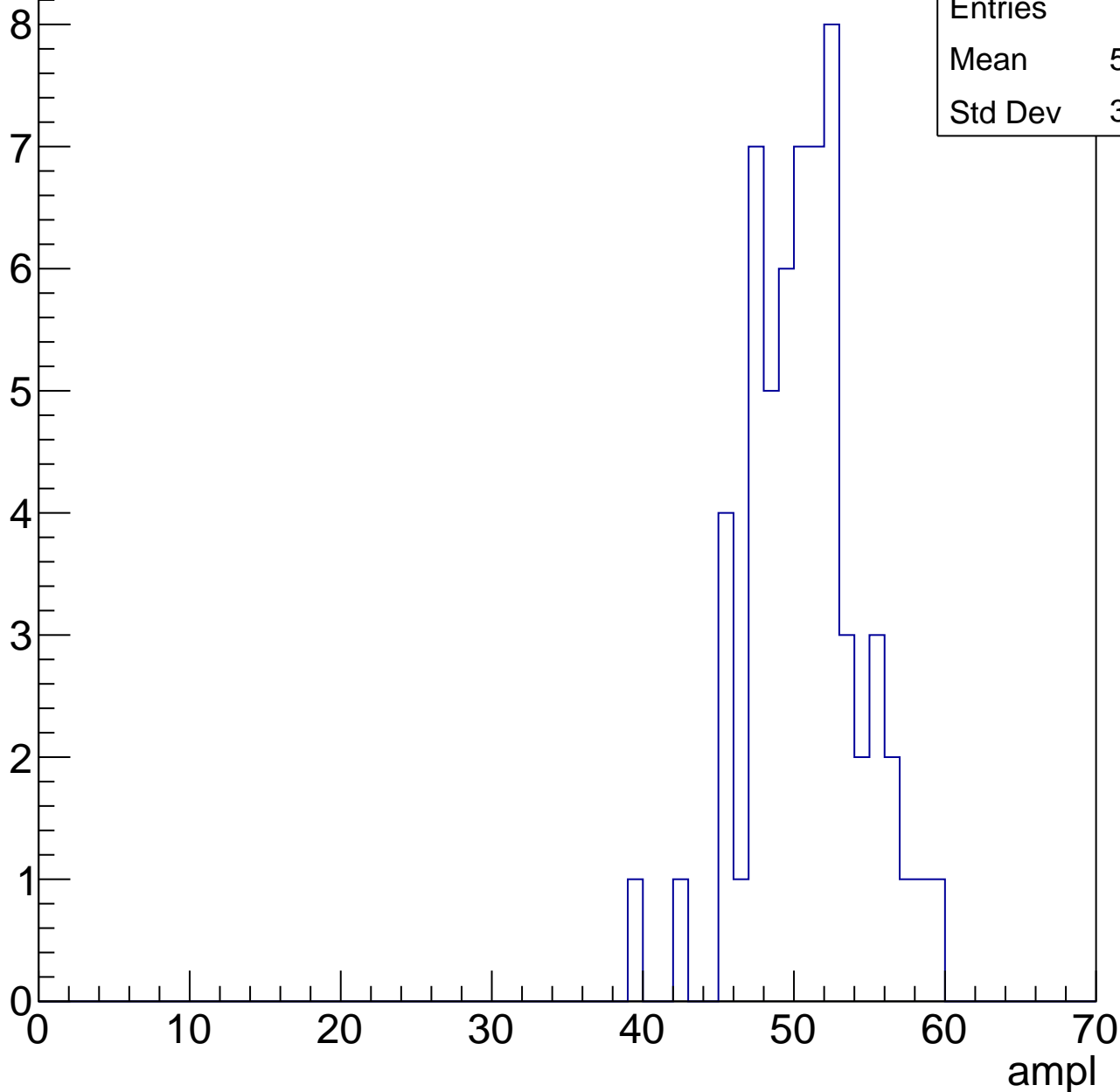


B1L100S, U19-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	50.18
Std Dev	3.713



B1L100S, U19-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 54.83

Std Dev 2.808

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

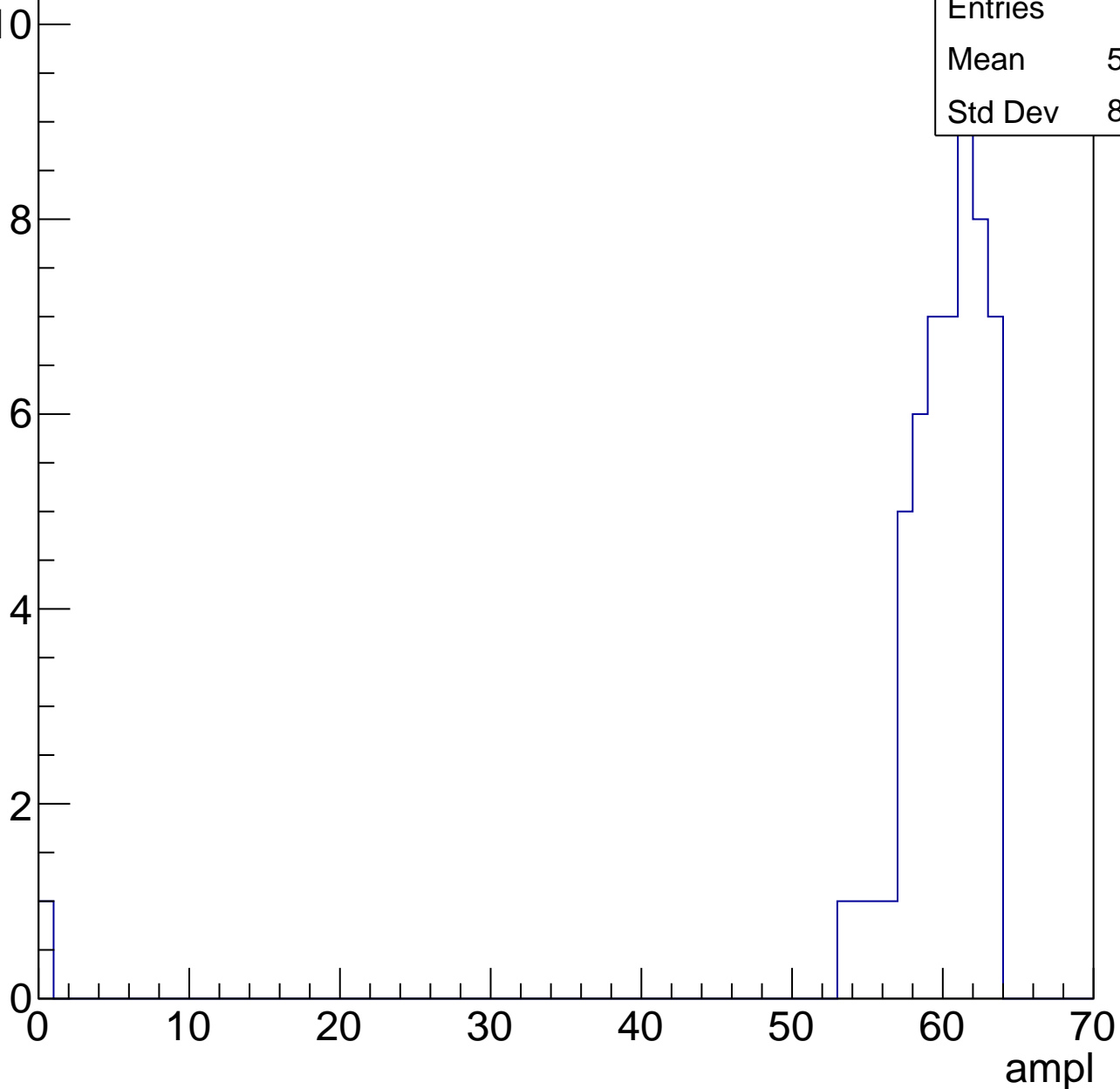
7

B1L100S, U19-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	58.75
Std Dev	8.334



B1L100S, U19-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

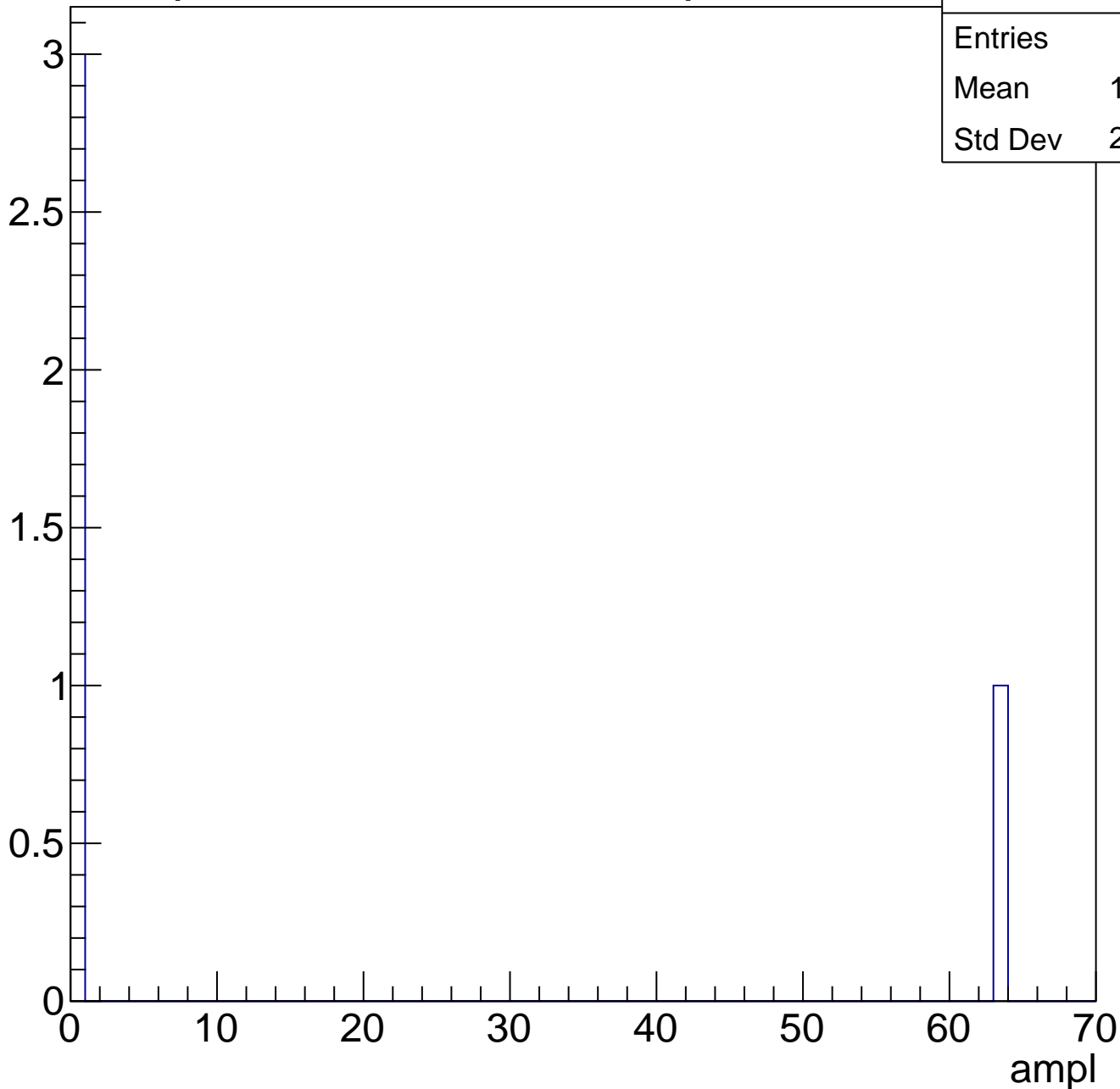
Entry



B1L100S, U19-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch89, adc0

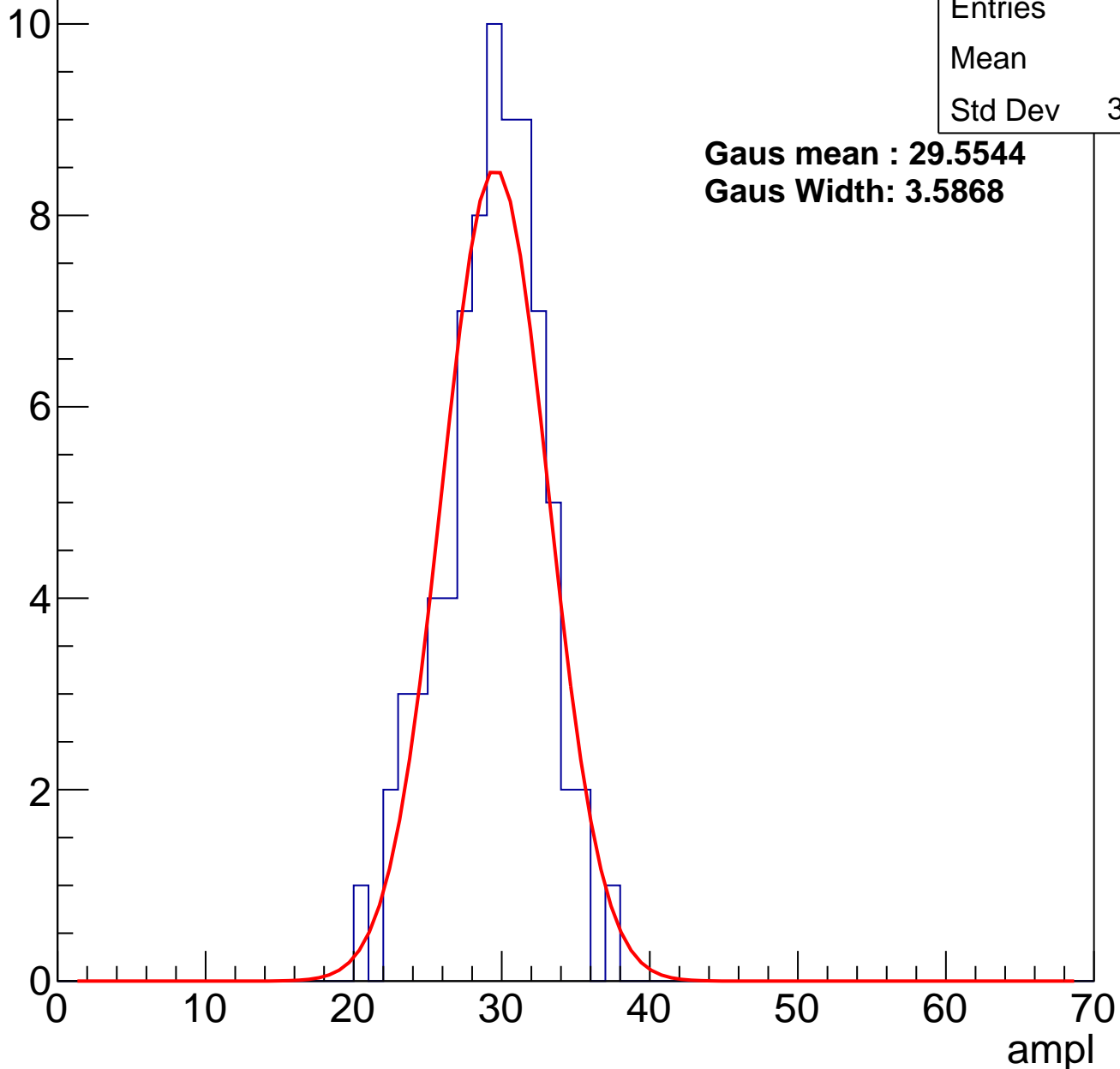
calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	28.9
Std Dev	3.364

Gaus mean : 29.5544

Gaus Width: 3.5868

Entry



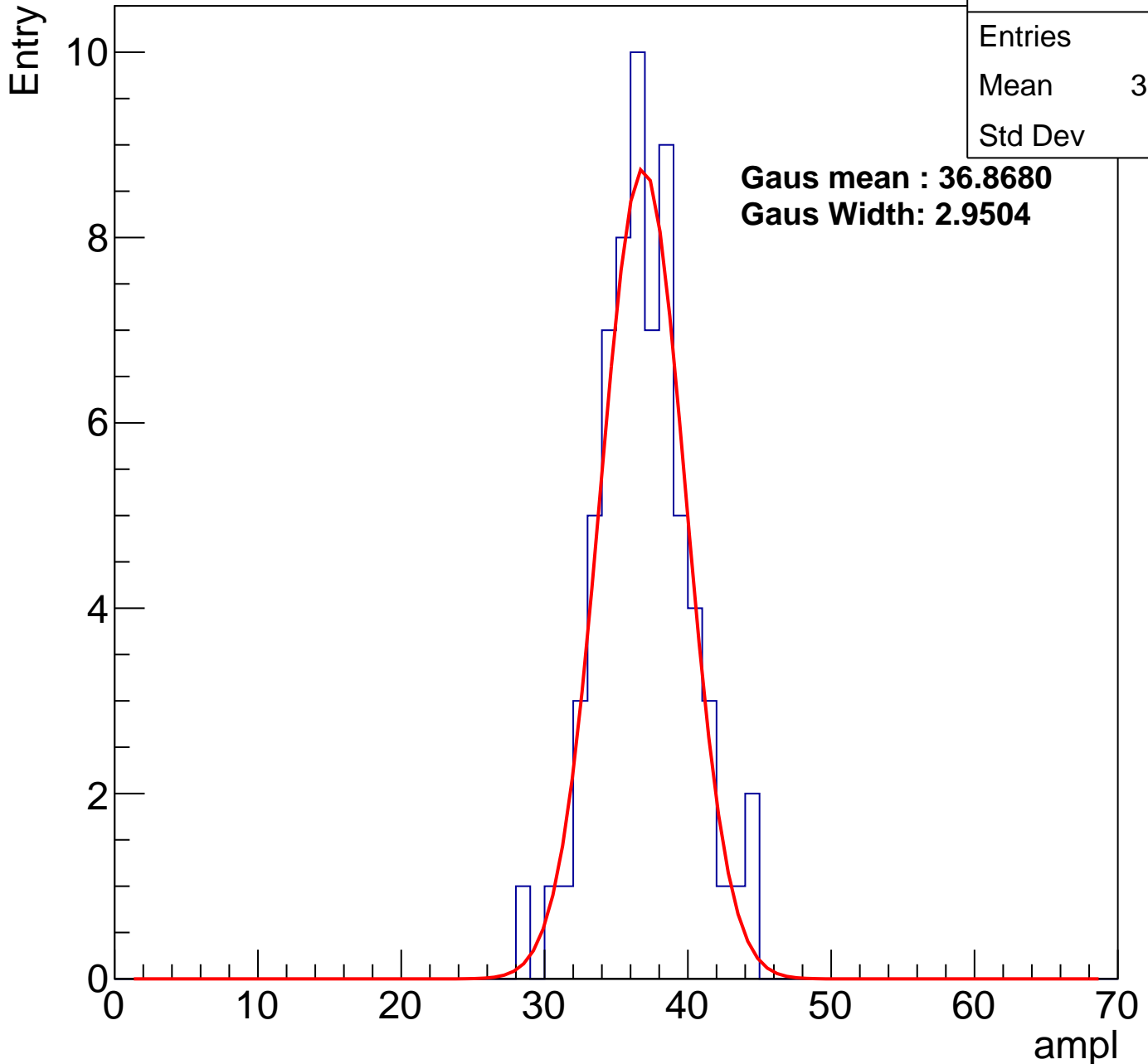
B1L100S, U19-ch89, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	36.47
Std Dev	3.16

Gaus mean : 36.8680

Gaus Width: 2.9504

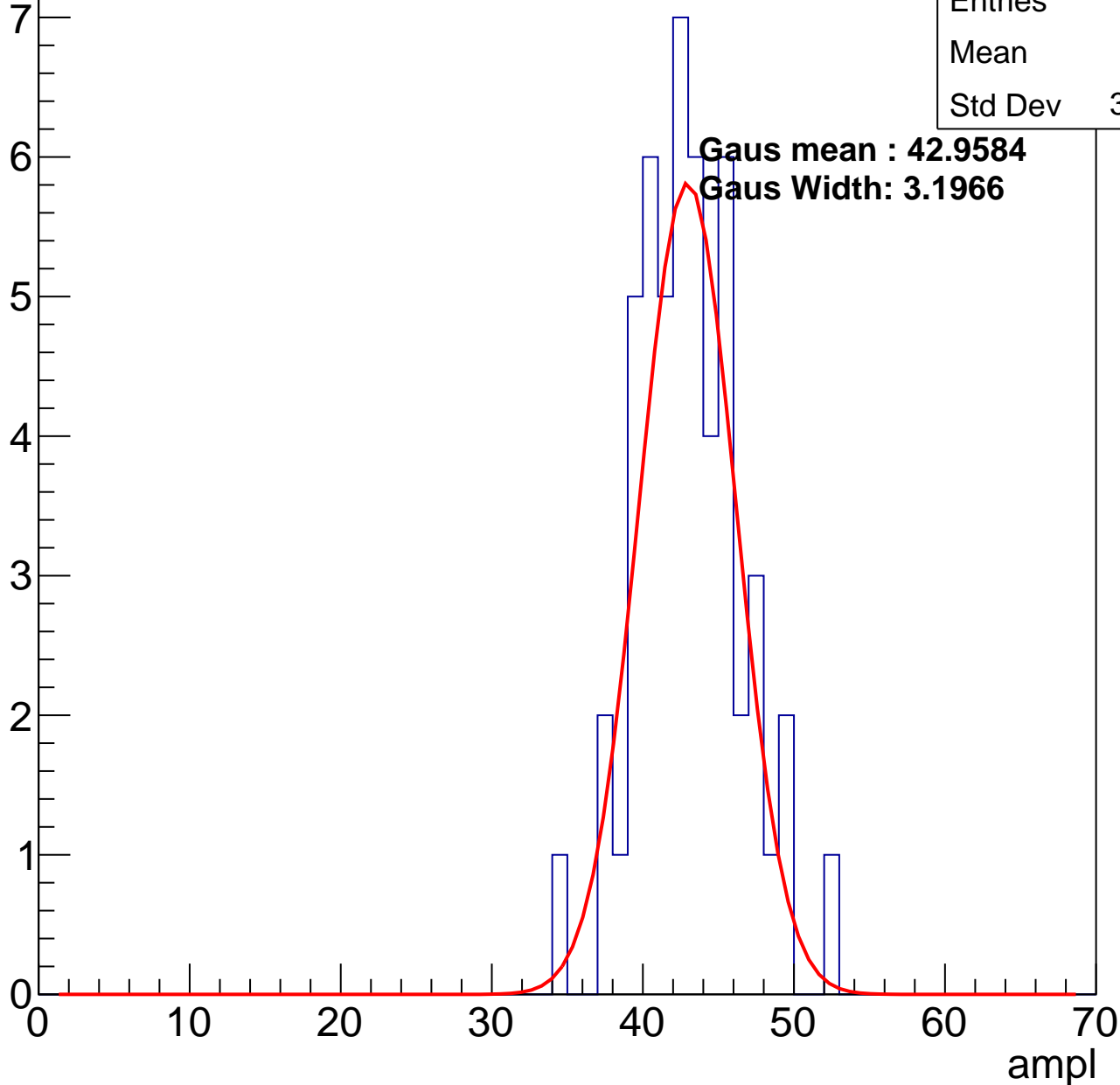


B1L100S, U19-ch89, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	42.6
Std Dev	3.404

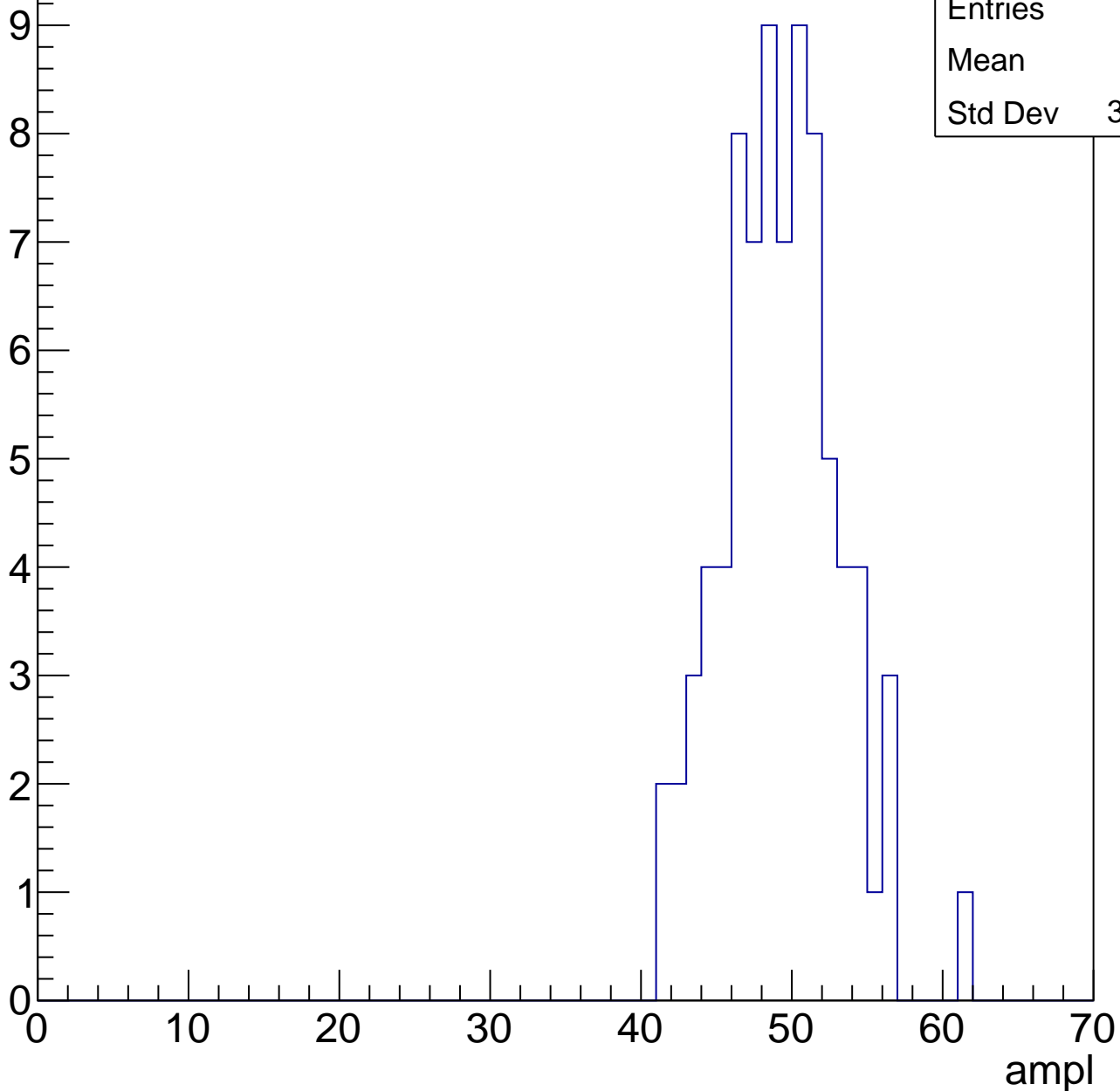


B1L100S, U19-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

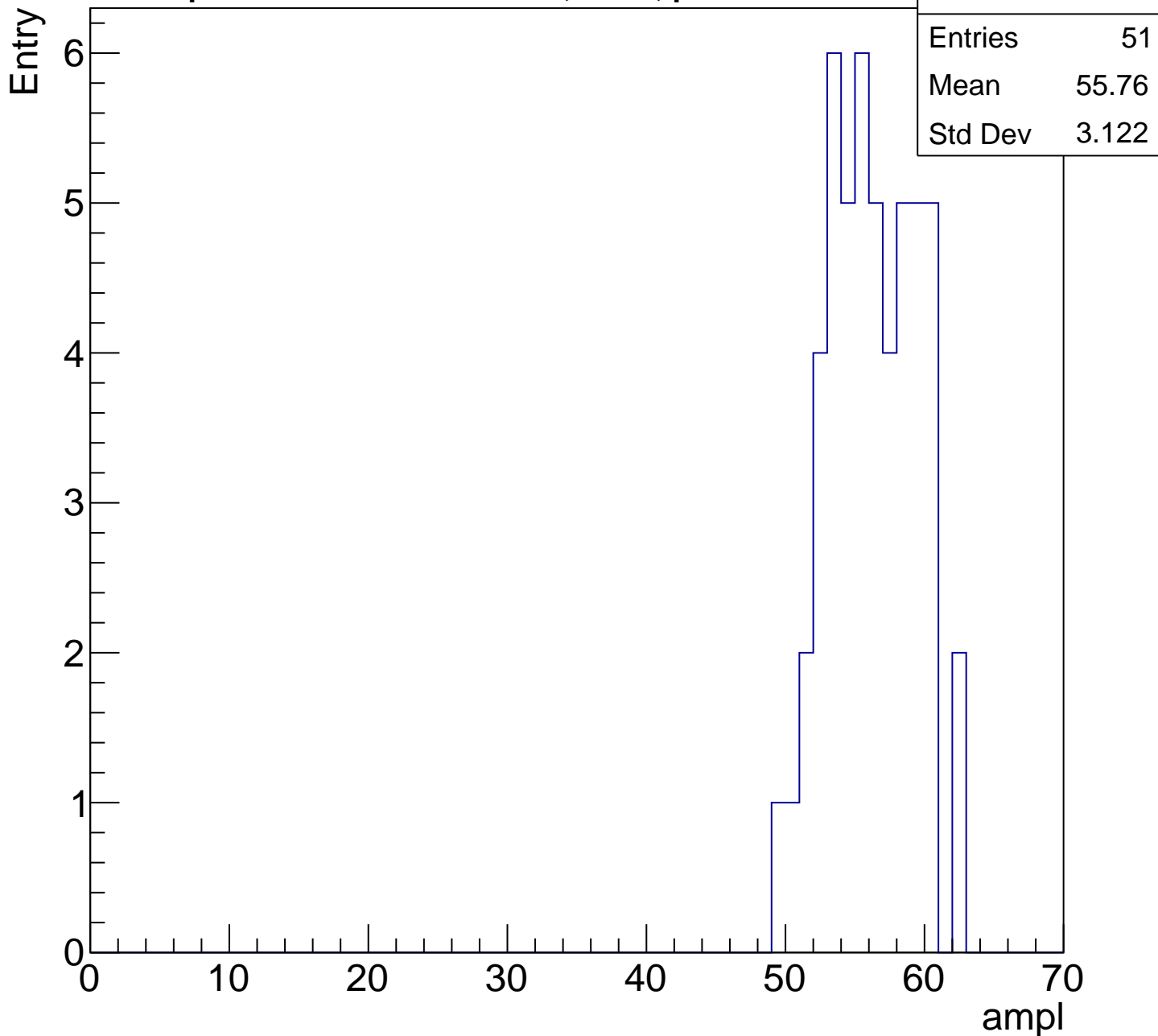
Entry

Entries	81
Mean	48.8
Std Dev	3.818



B1L100S, U19-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

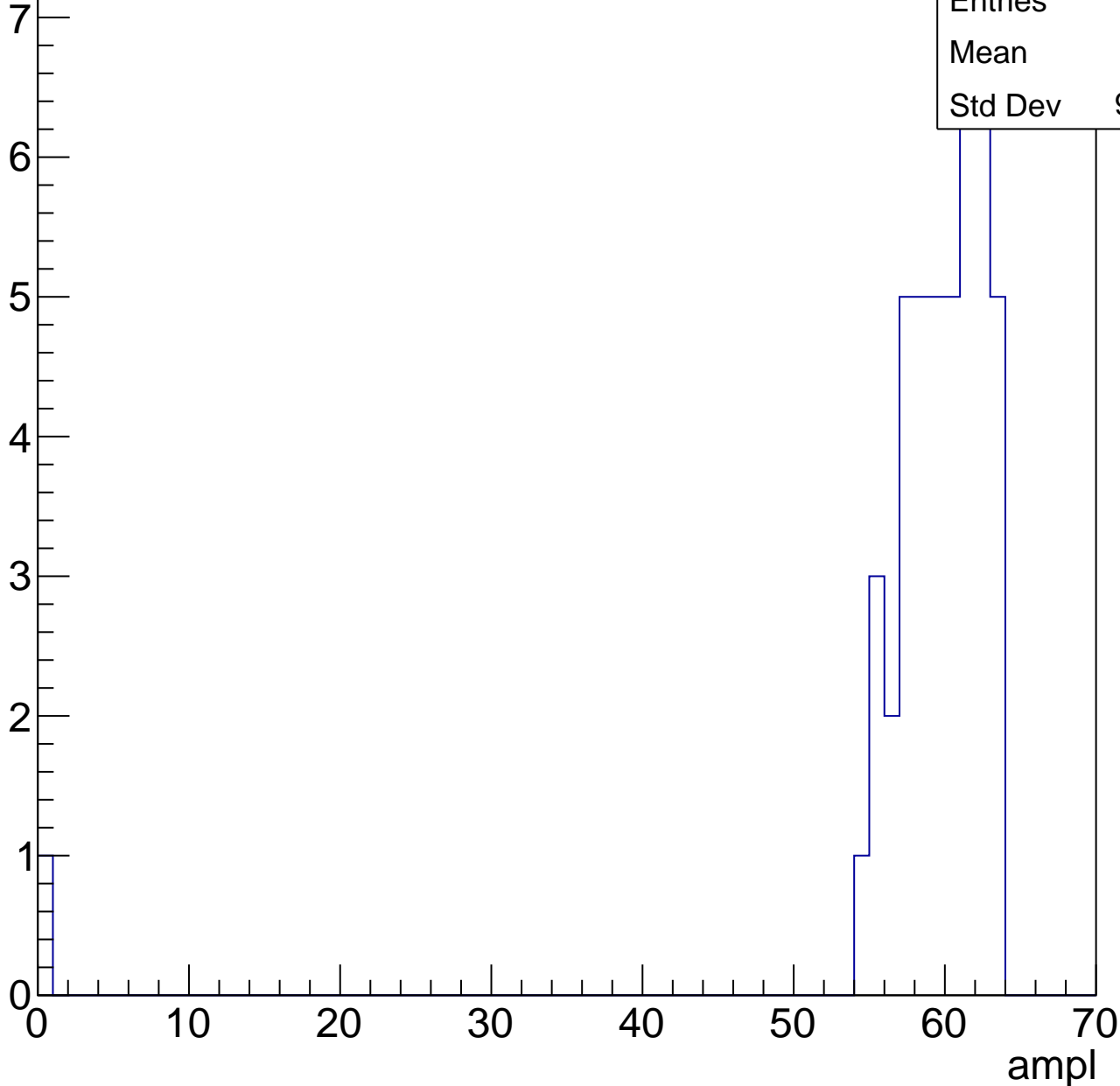


B1L100S, U19-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.2
Std Dev	9.021

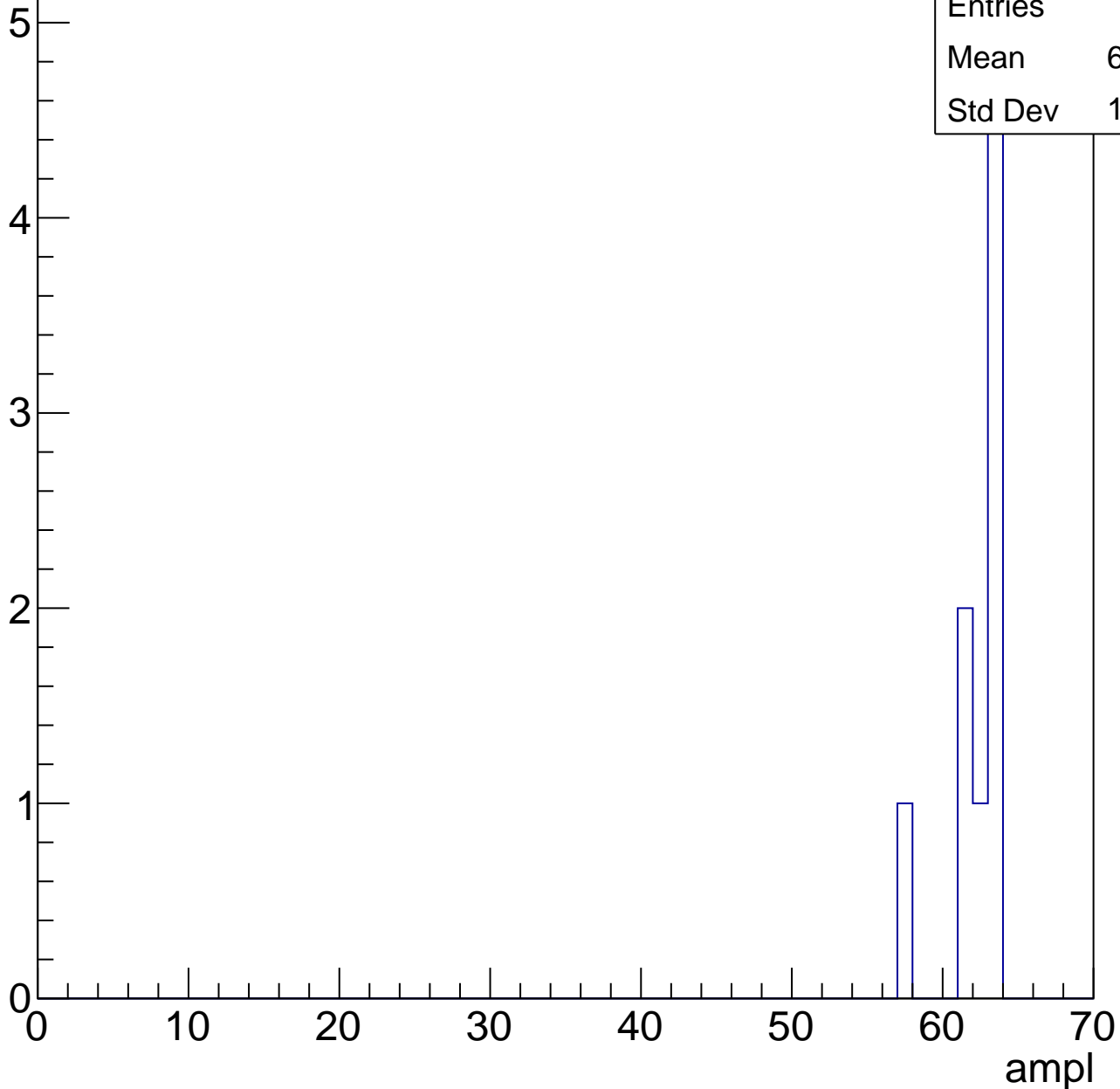


B1L100S, U19-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	61.78
Std Dev	1.872



B1L100S, U19-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U19-ch90, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	27.71
Std Dev	5.571

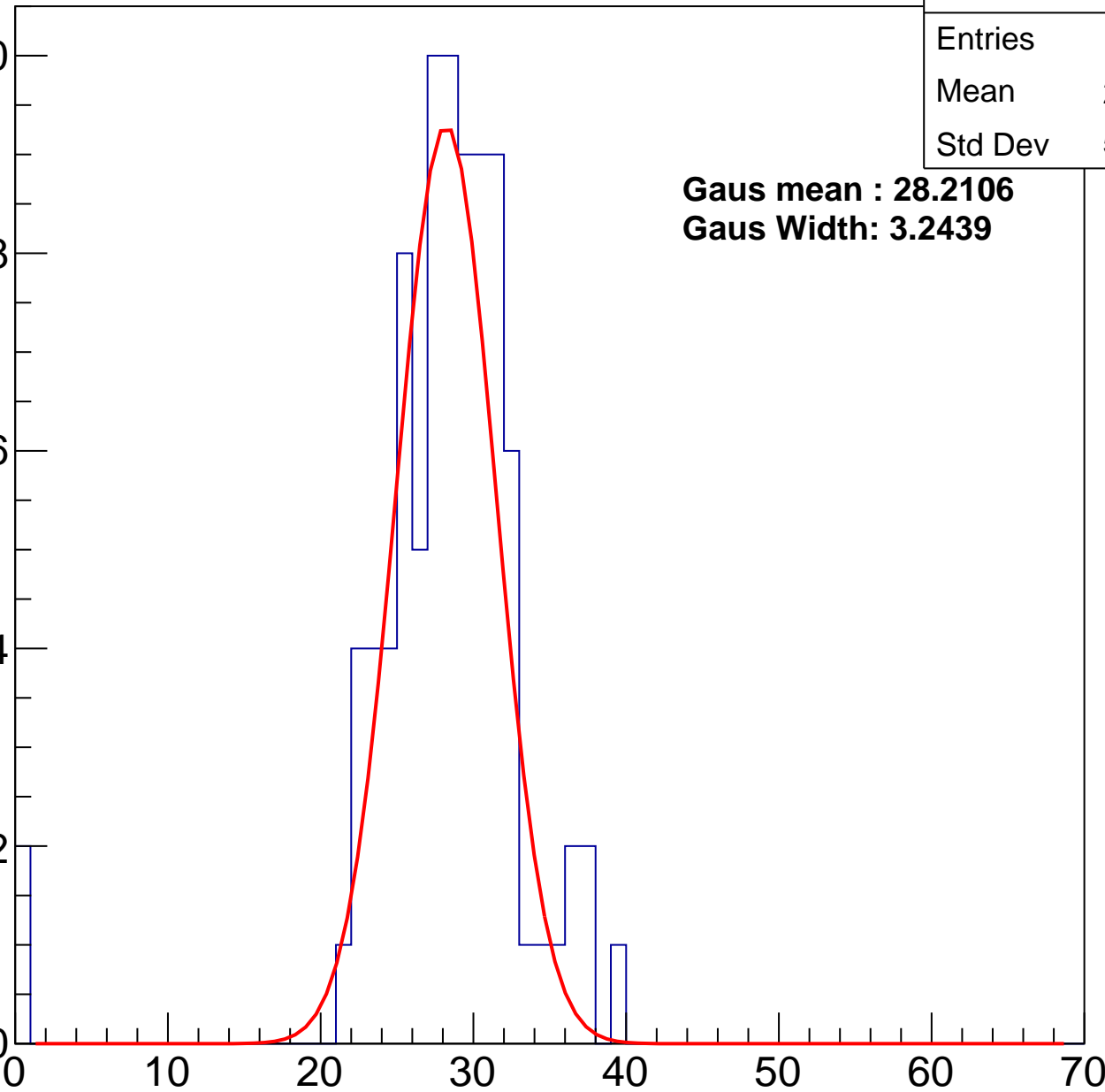
Gaus mean : 28.2106

Gaus Width: 3.2439

Entry

10
8
6
4
2
0

ampl



B1L100S, U19-ch90, adc1

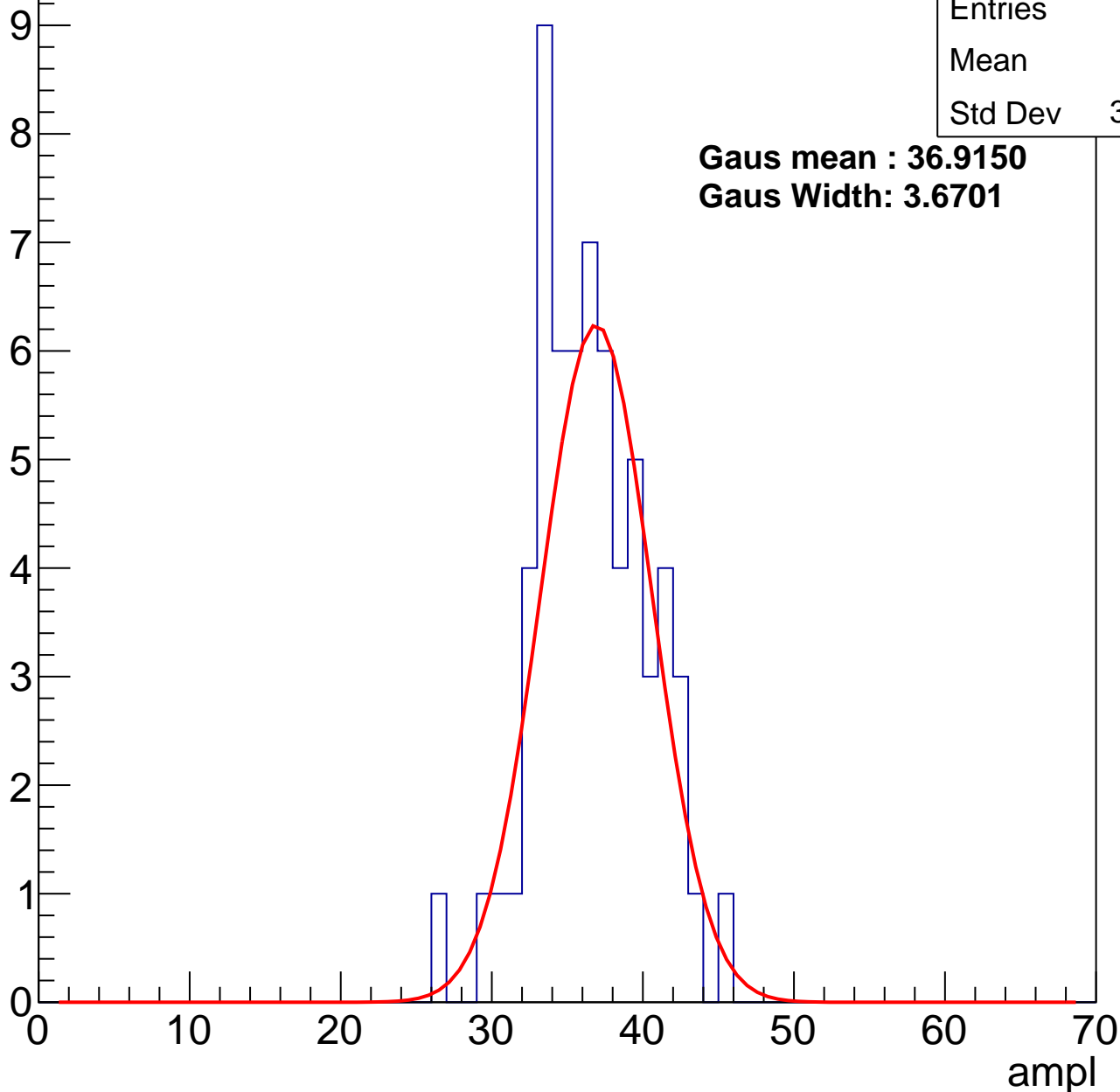
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	36.1
Std Dev	3.646

Gaus mean : 36.9150

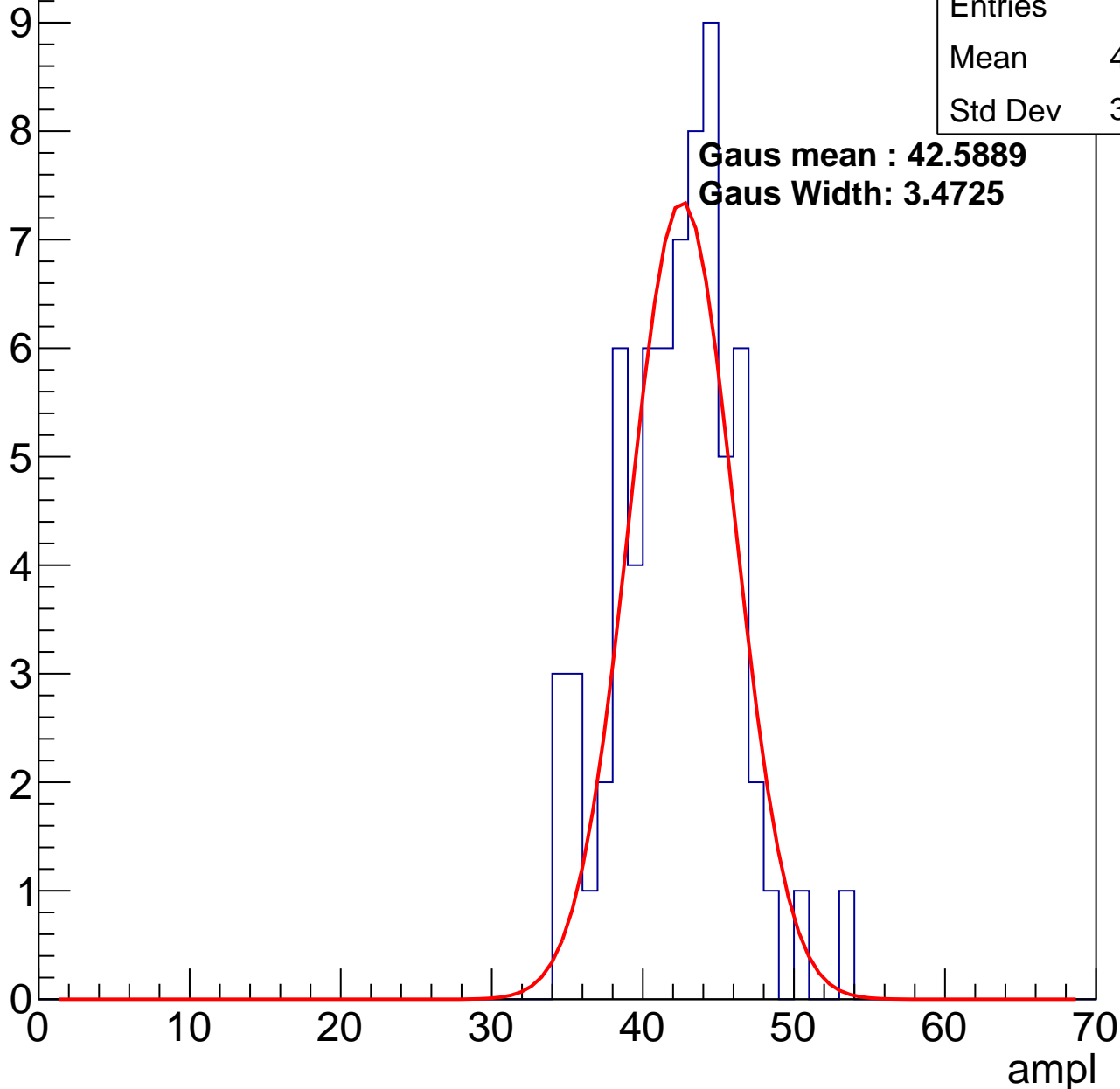
Gaus Width: 3.6701



B1L100S, U19-ch90, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

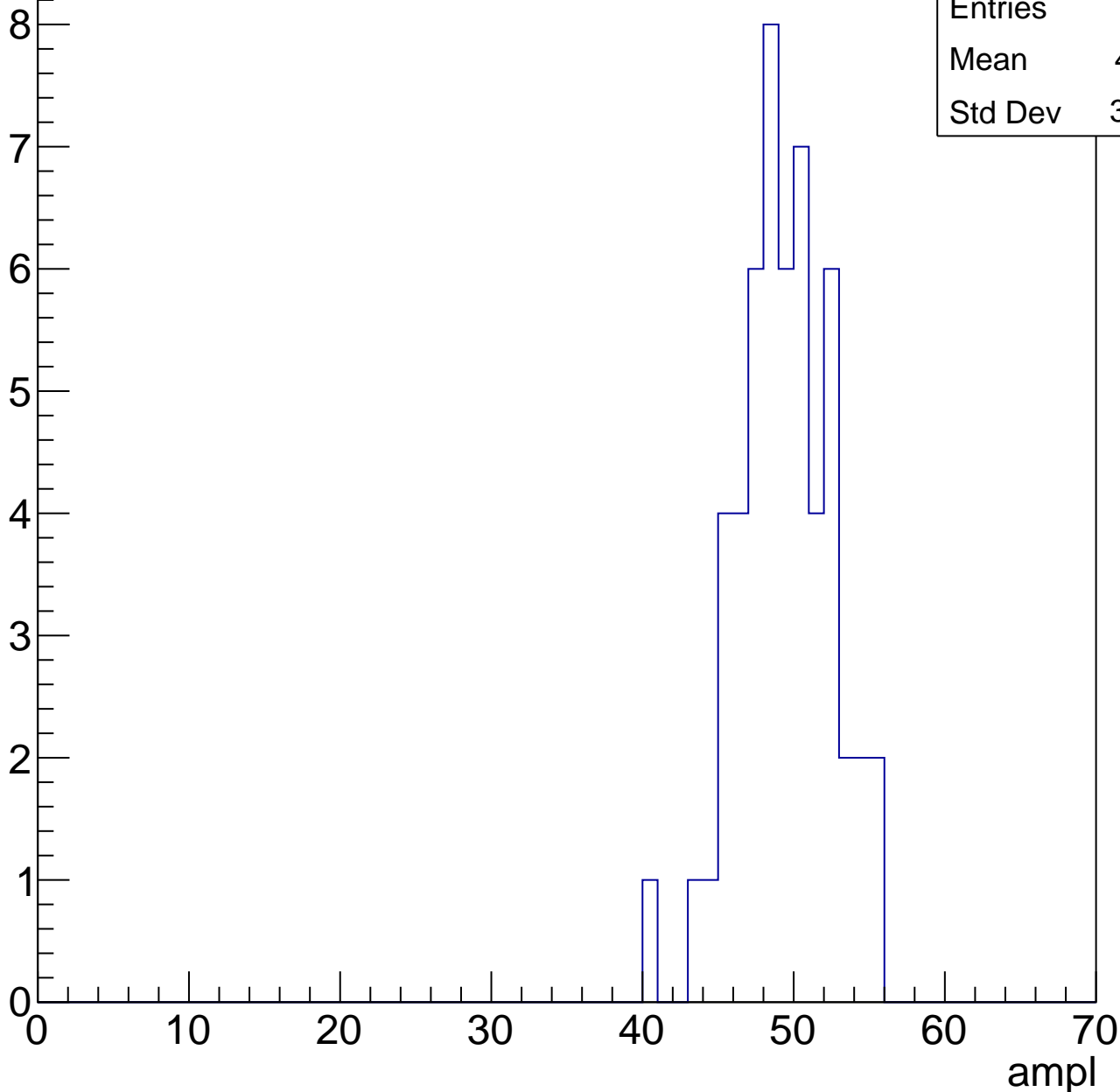


B1L100S, U19-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	48.91
Std Dev	3.057

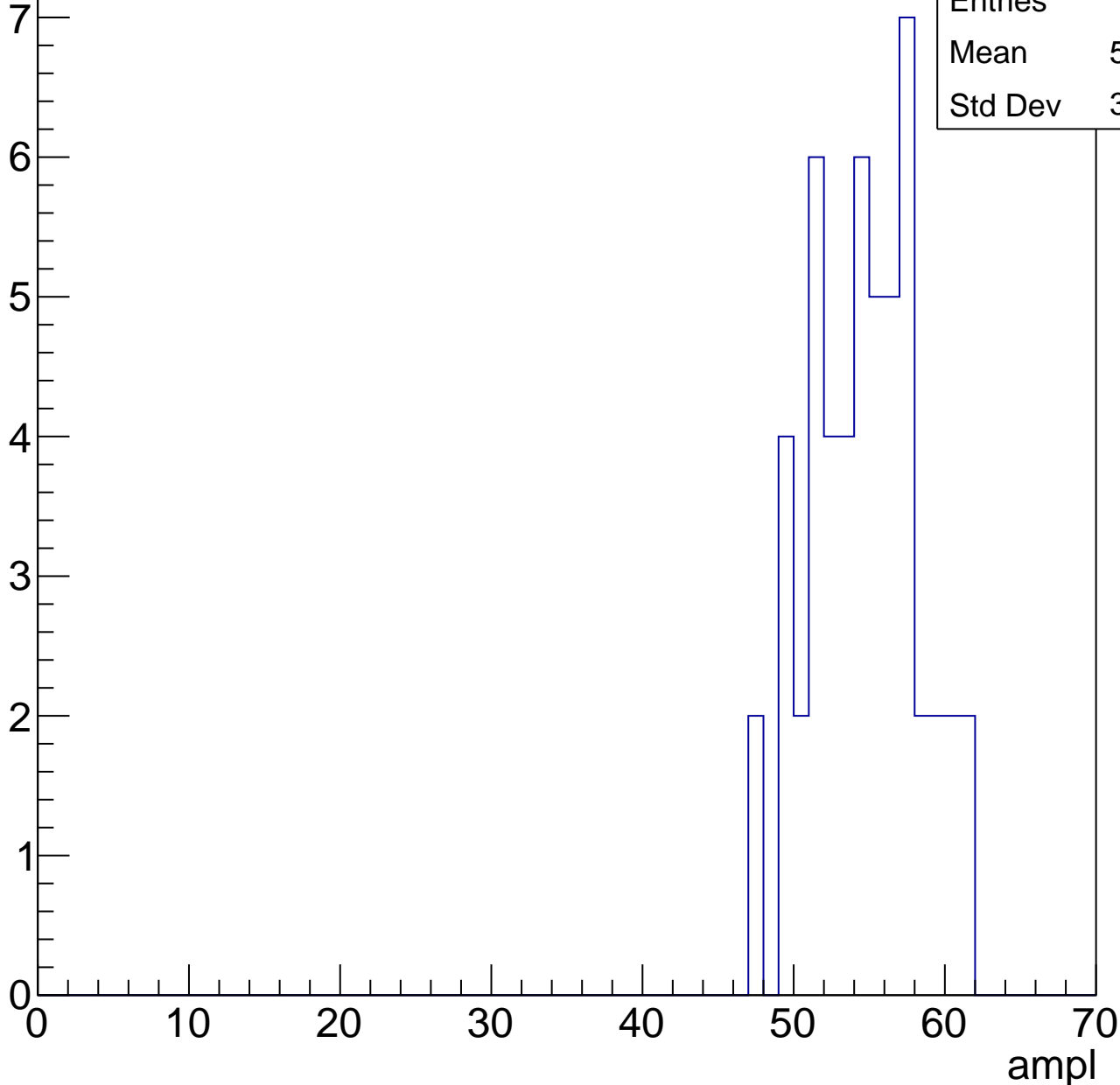


B1L100S, U19-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	54.15
Std Dev	3.483

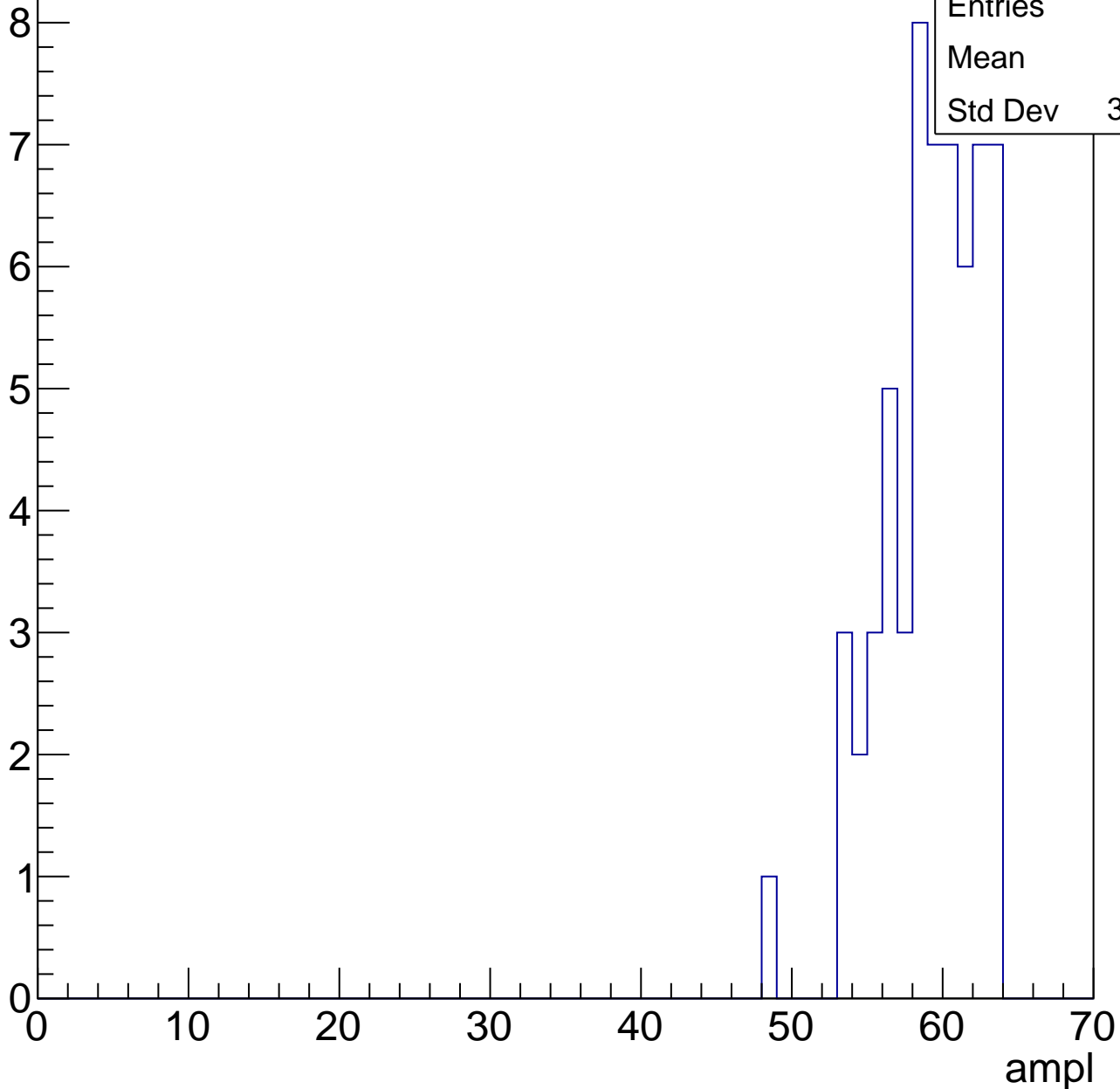


B1L100S, U19-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	58.8
Std Dev	3.177



B1L100S, U19-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

10

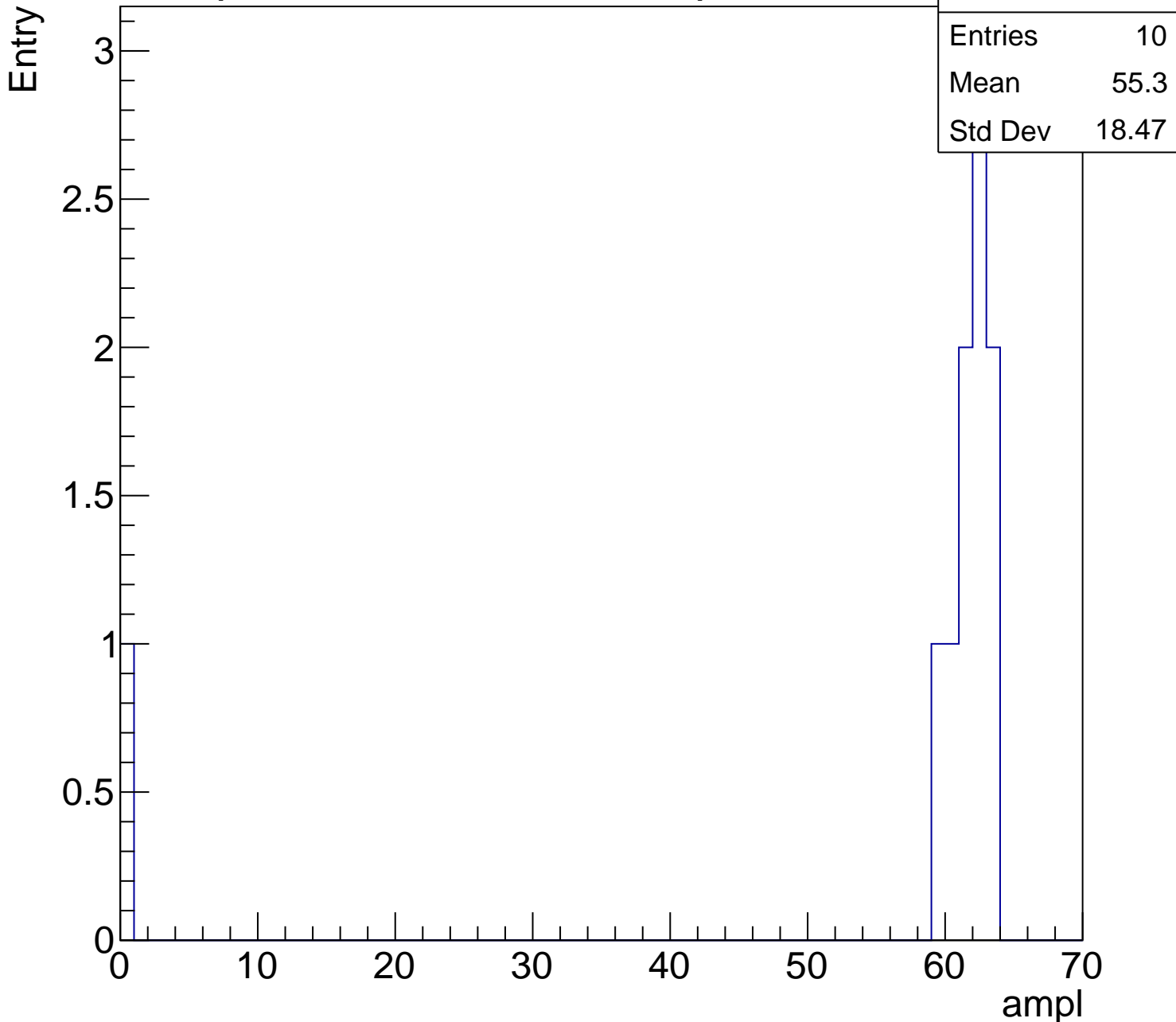
Mean

55.3

Std Dev

18.47

ampl



B1L100S, U19-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U19-ch91, adc0

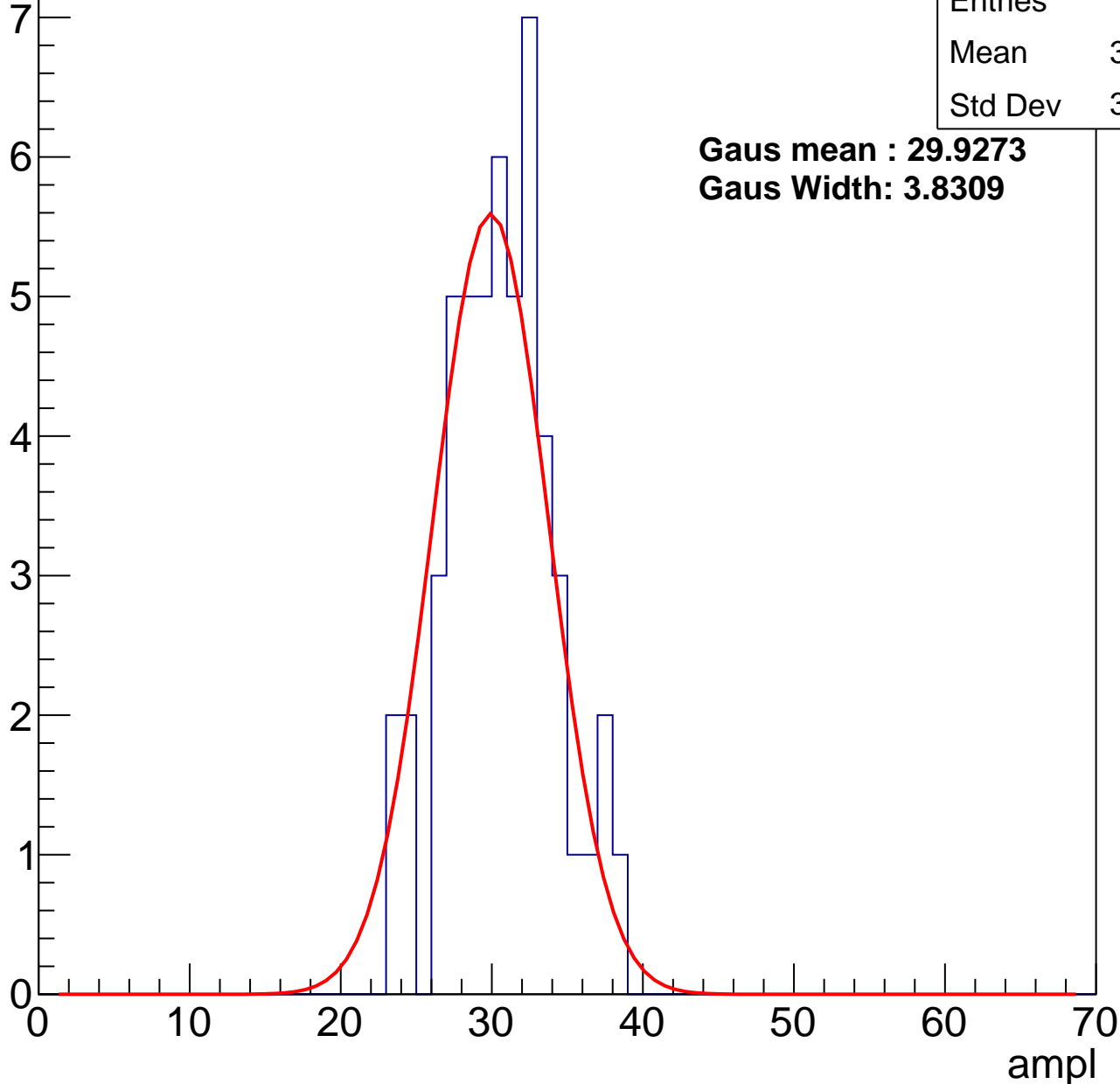
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	30.15
Std Dev	3.472

Gaus mean : 29.9273

Gaus Width: 3.8309



B1L100S, U19-ch91, adc1

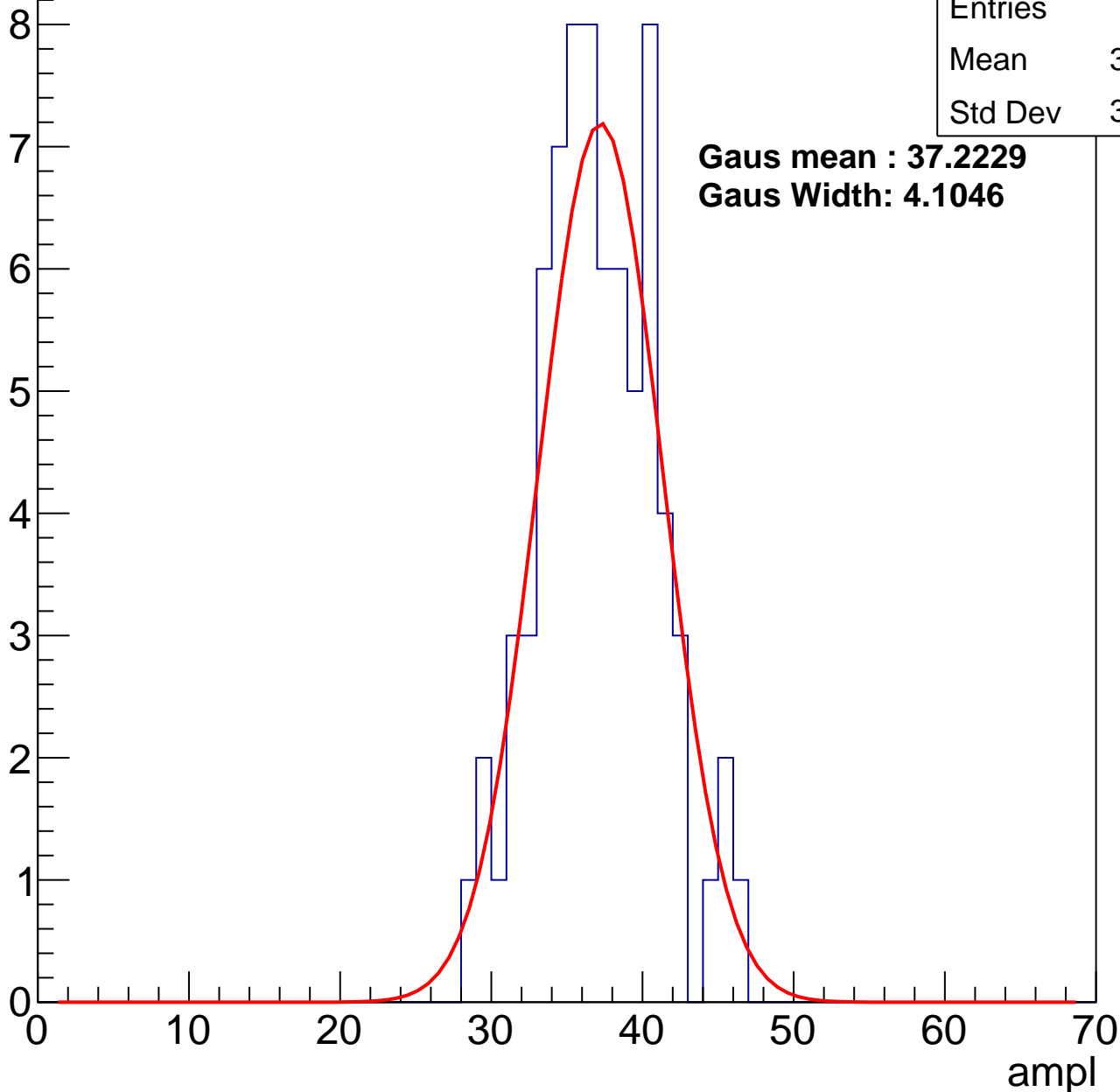
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.59
Std Dev	3.868

Gaus mean : 37.2229

Gaus Width: 4.1046



B1L100S, U19-ch91, adc2

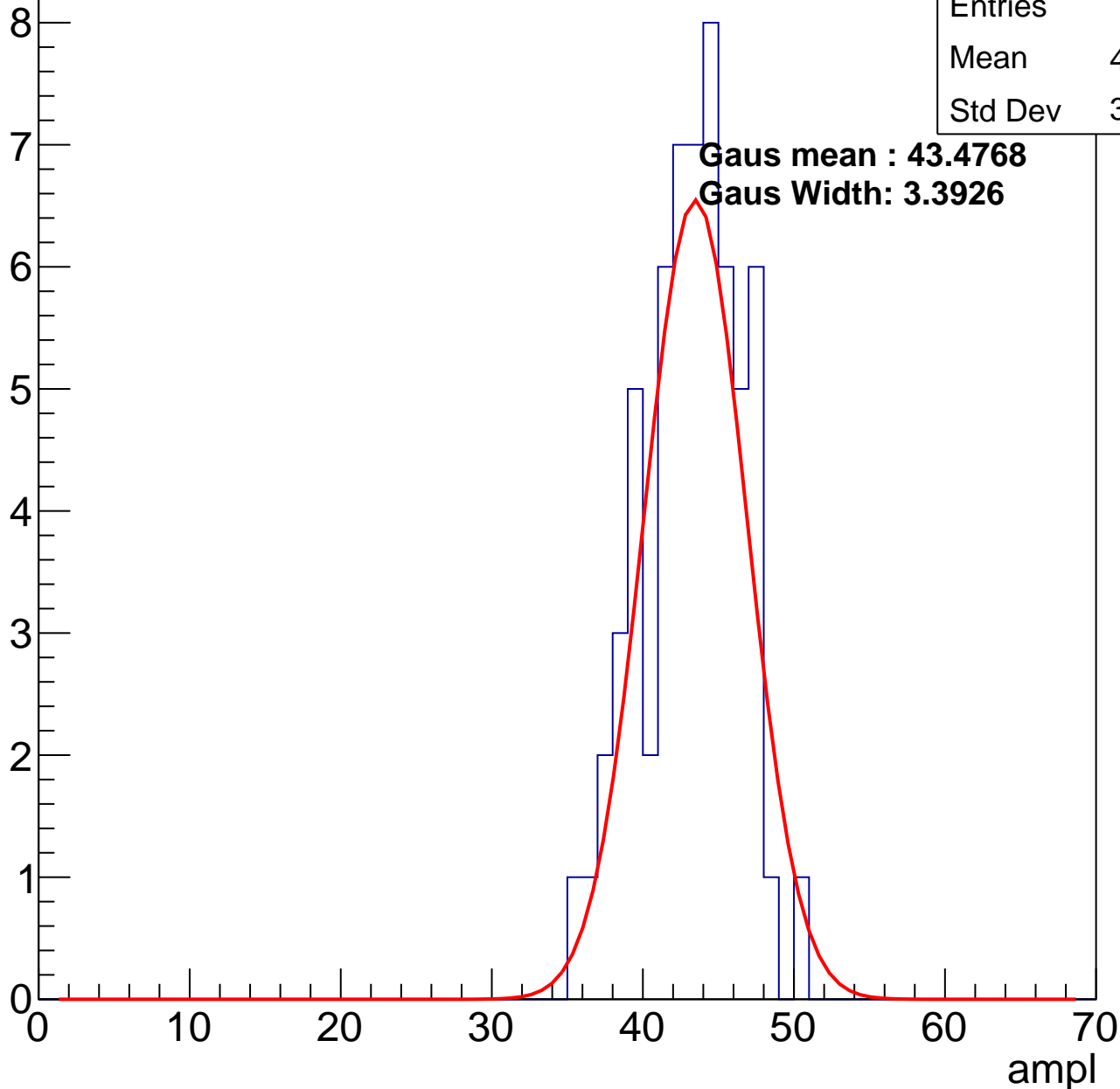
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.74
Std Dev	3.218

Gaus mean : 43.4768

Gaus Width: 3.3926



B1L100S, U19-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	49.91
Std Dev	3.423

Entry

10

8

6

4

2

0

0

10

20

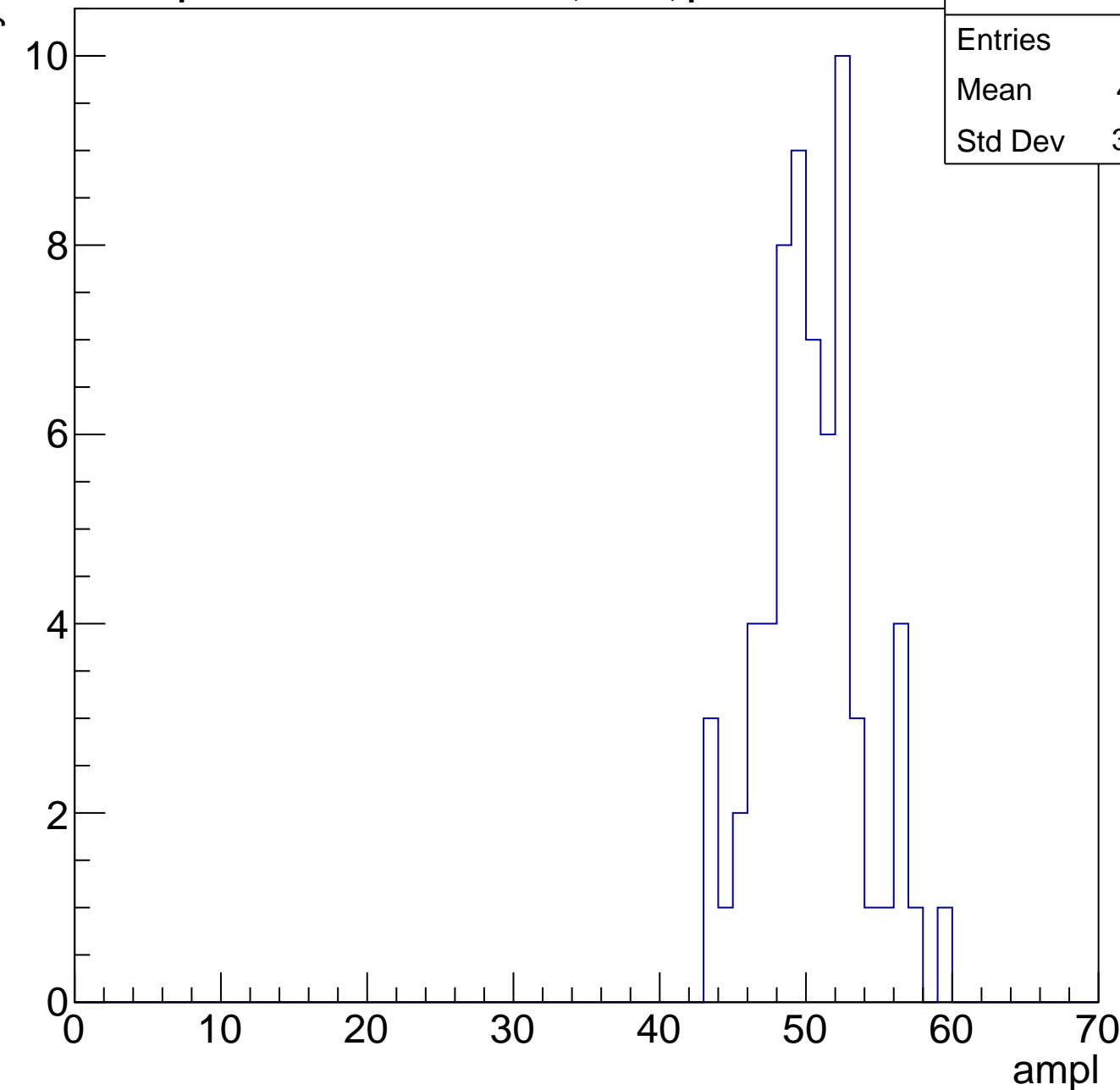
30

40

50

60

ampl

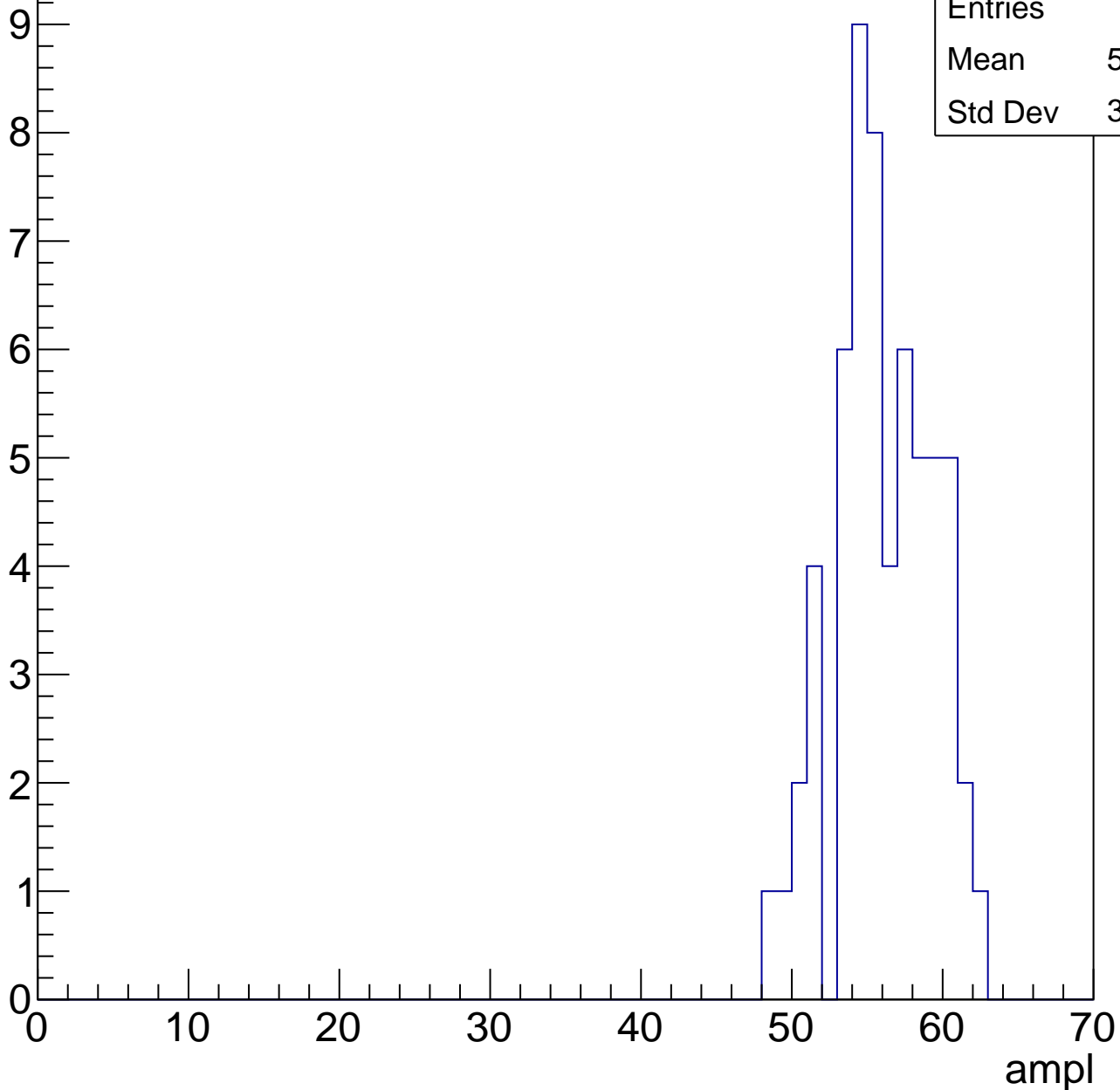


B1L100S, U19-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	55.59
Std Dev	3.216

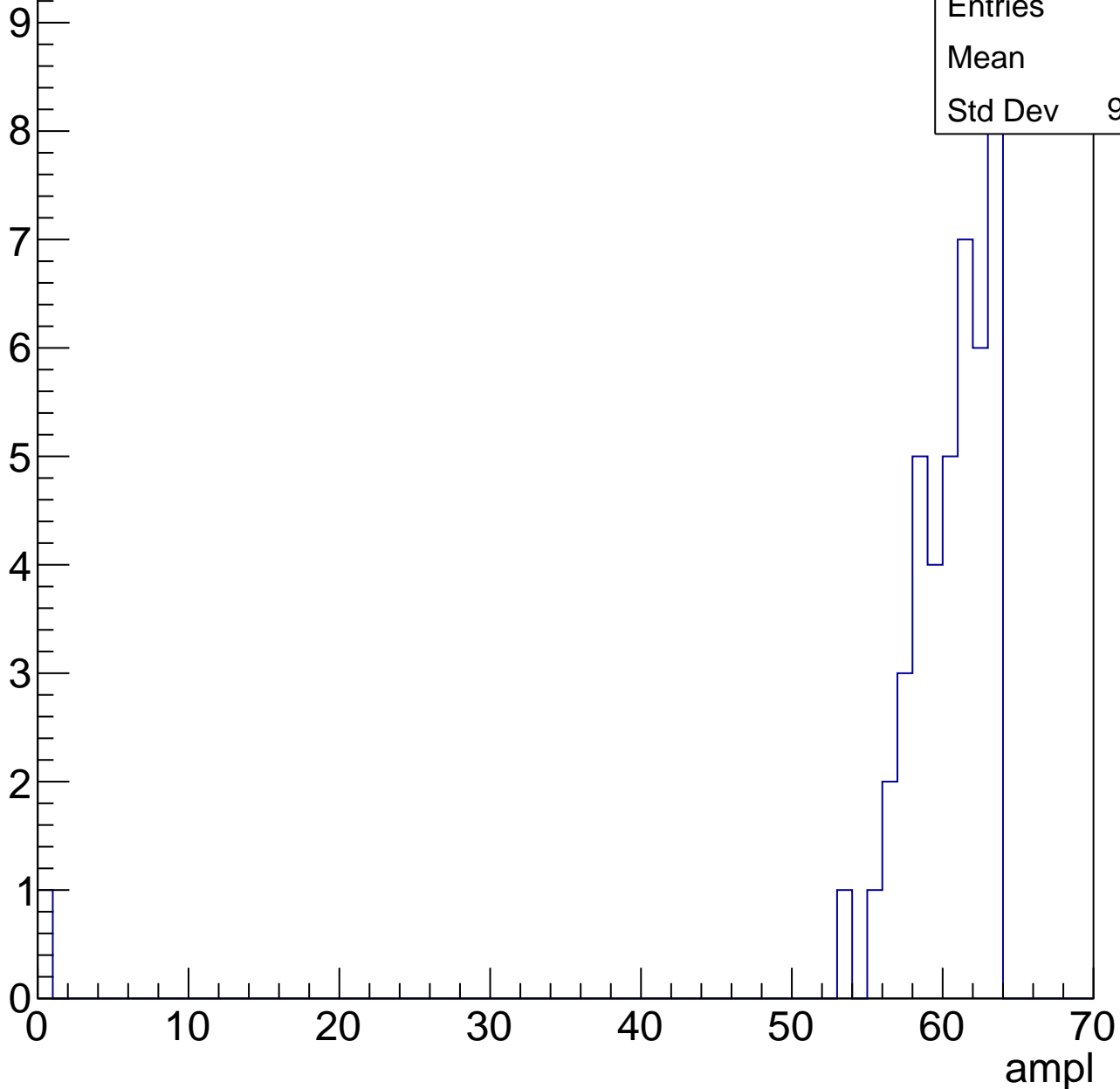


B1L100S, U19-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

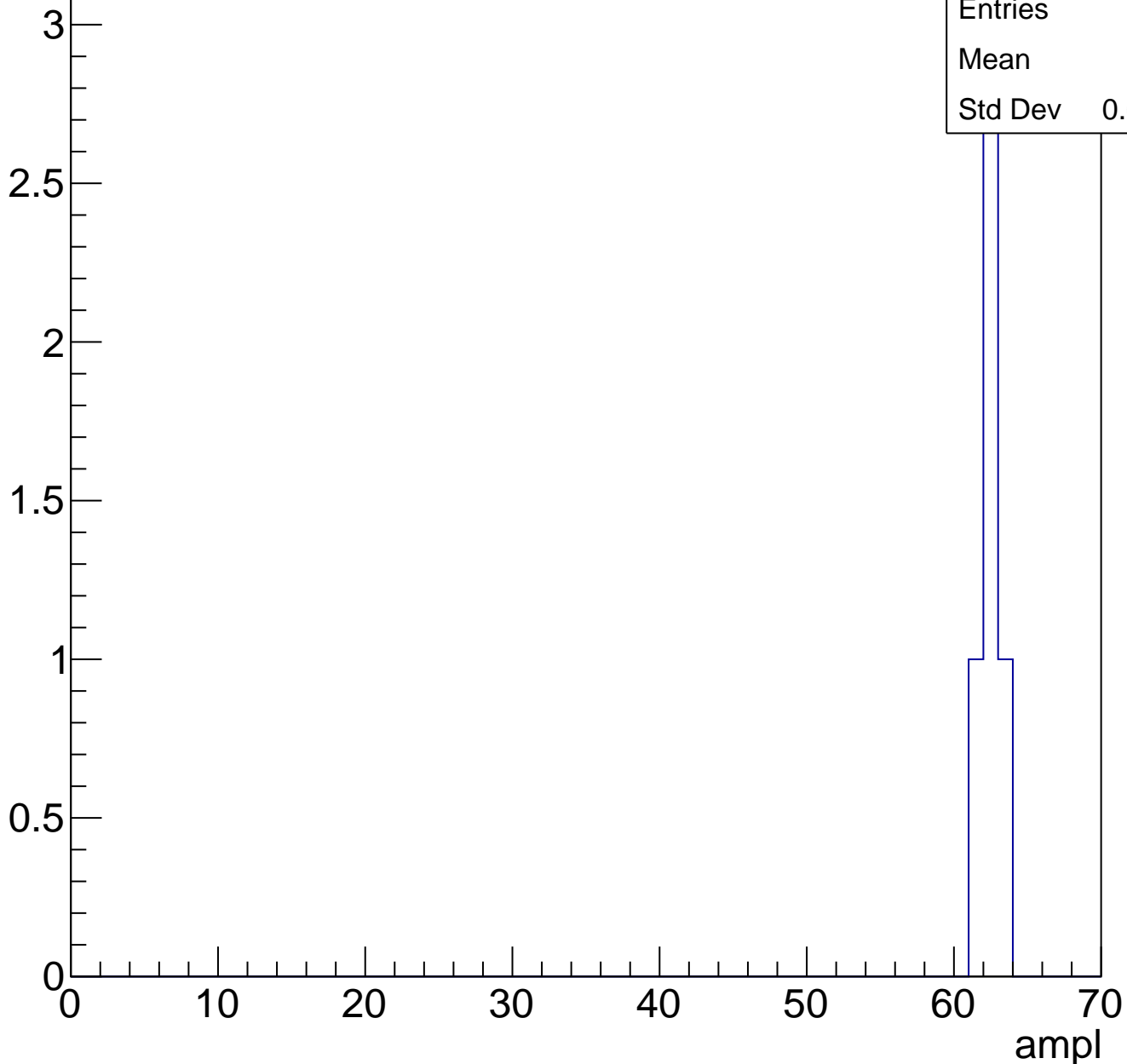
Entries	44
Mean	58.7
Std Dev	9.287



B1L100S, U19-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch92, adc0

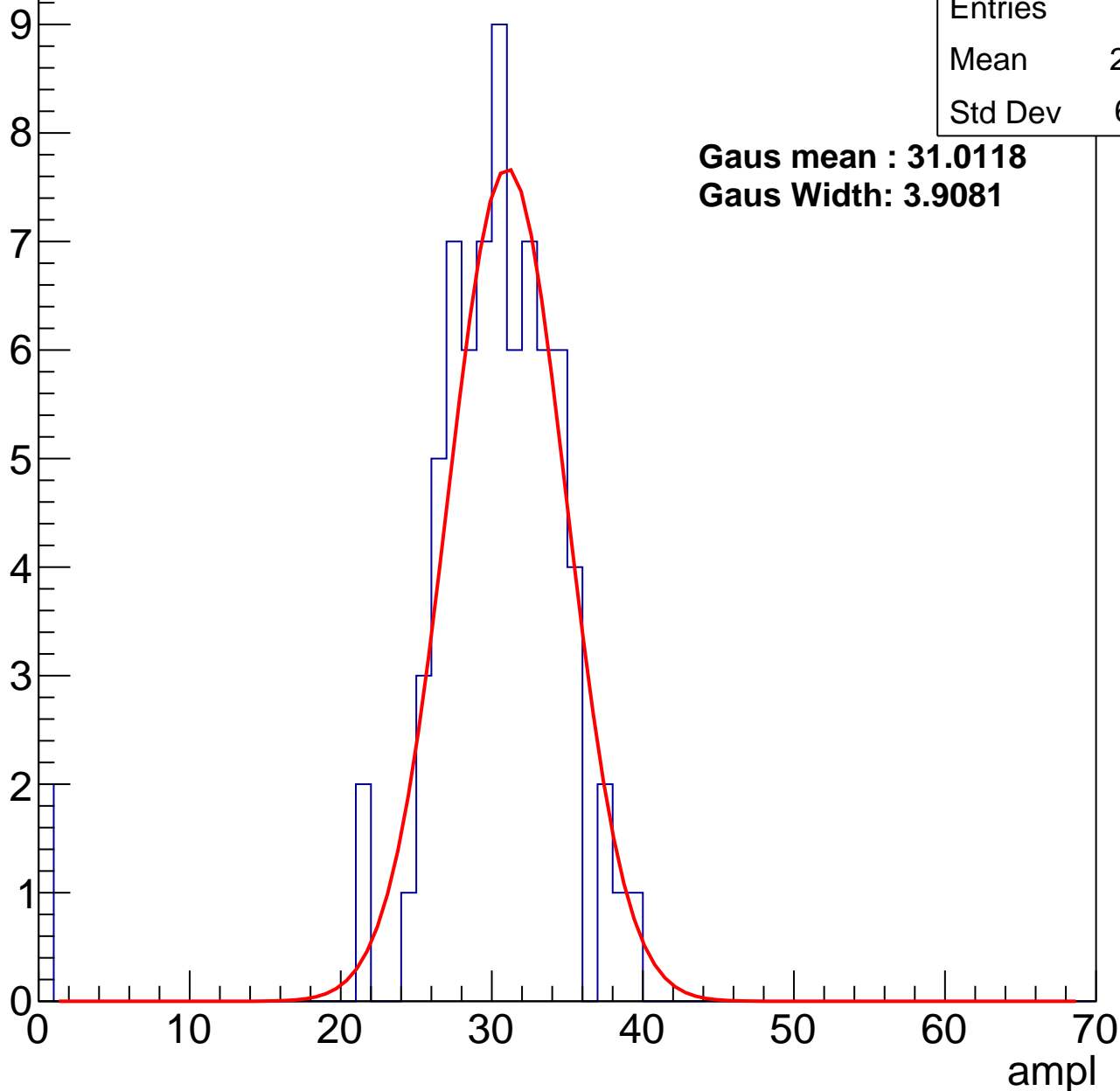
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.39
Std Dev	6.051

Gaus mean : 31.0118

Gaus Width: 3.9081



B1L100S, U19-ch92, adc1

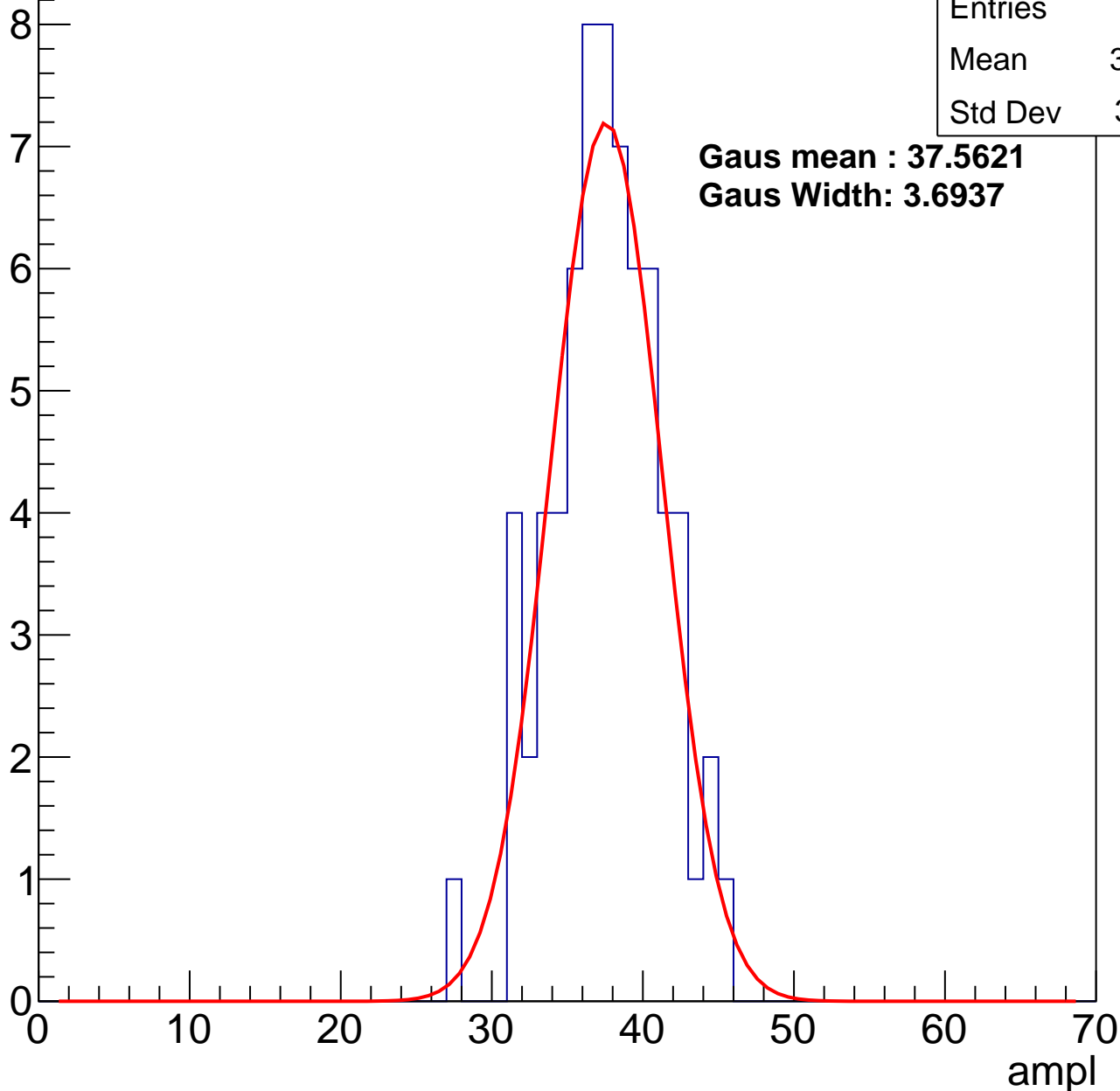
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.13
Std Dev	3.581

Gaus mean : 37.5621

Gaus Width: 3.6937



B1L100S, U19-ch92, adc2

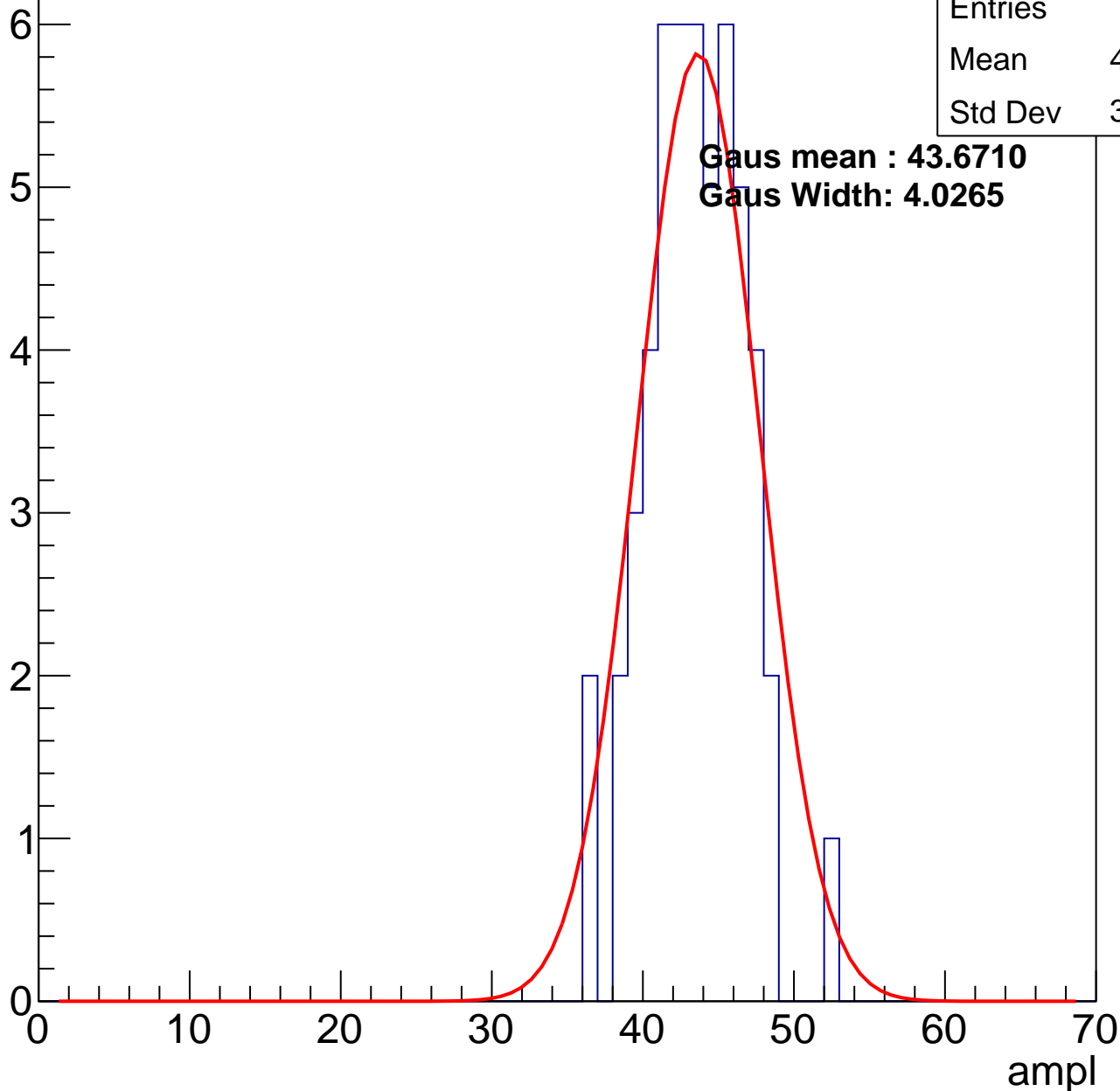
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.02
Std Dev	3.195

Gaus mean : 43.6710

Gaus Width: 4.0265

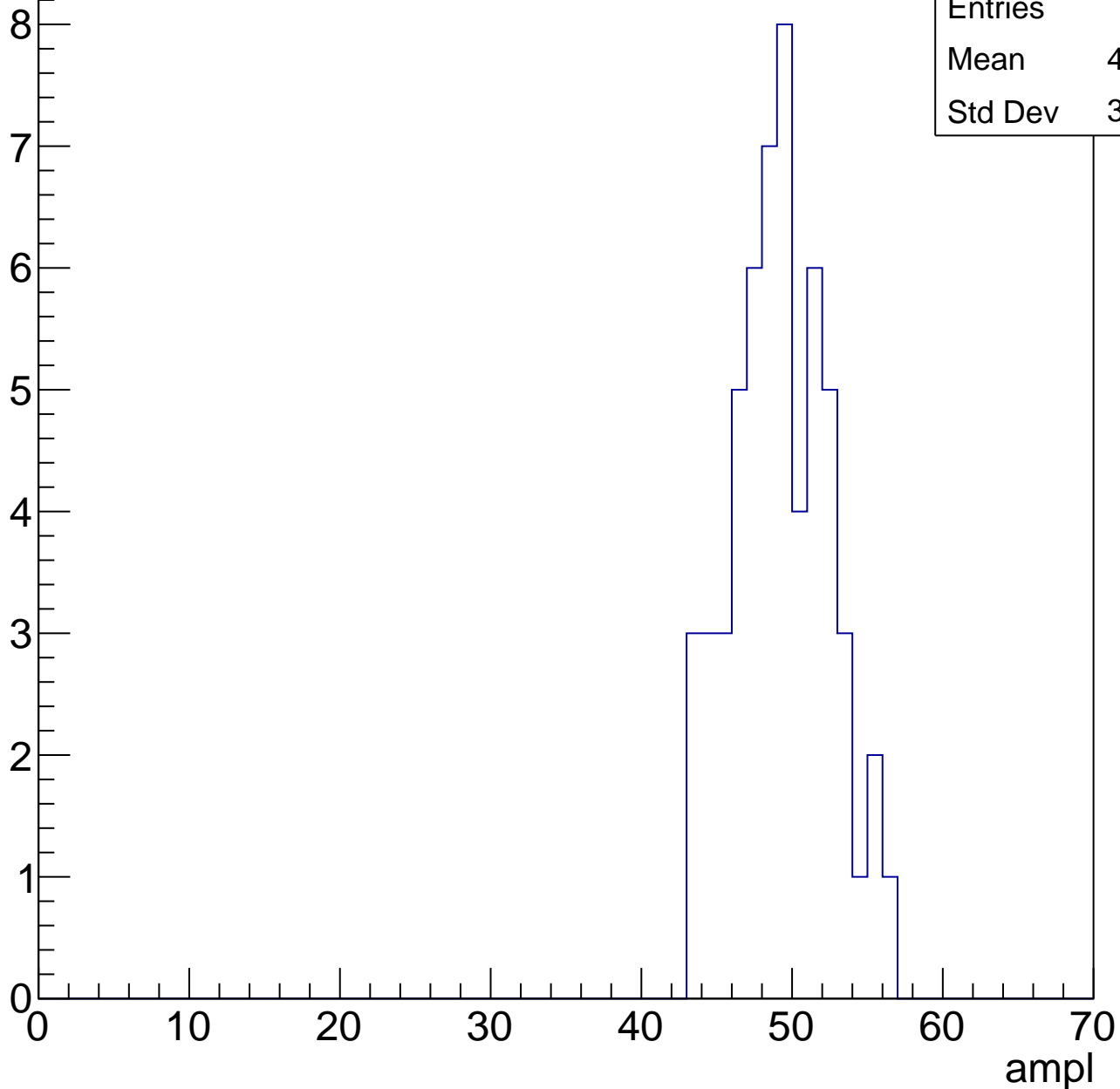


B1L100S, U19-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

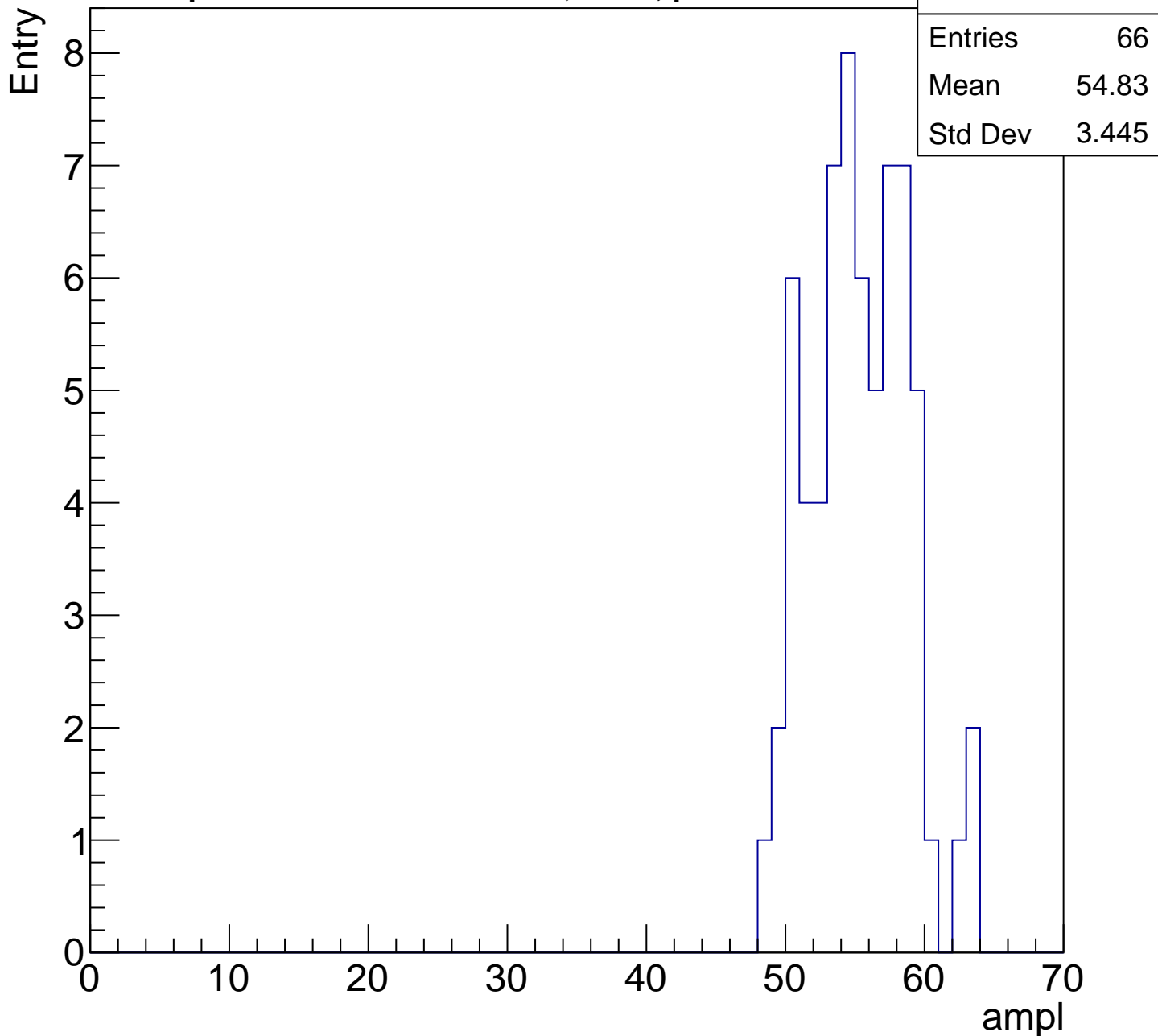
Entry

Entries	57
Mean	48.79
Std Dev	3.166



B1L100S, U19-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

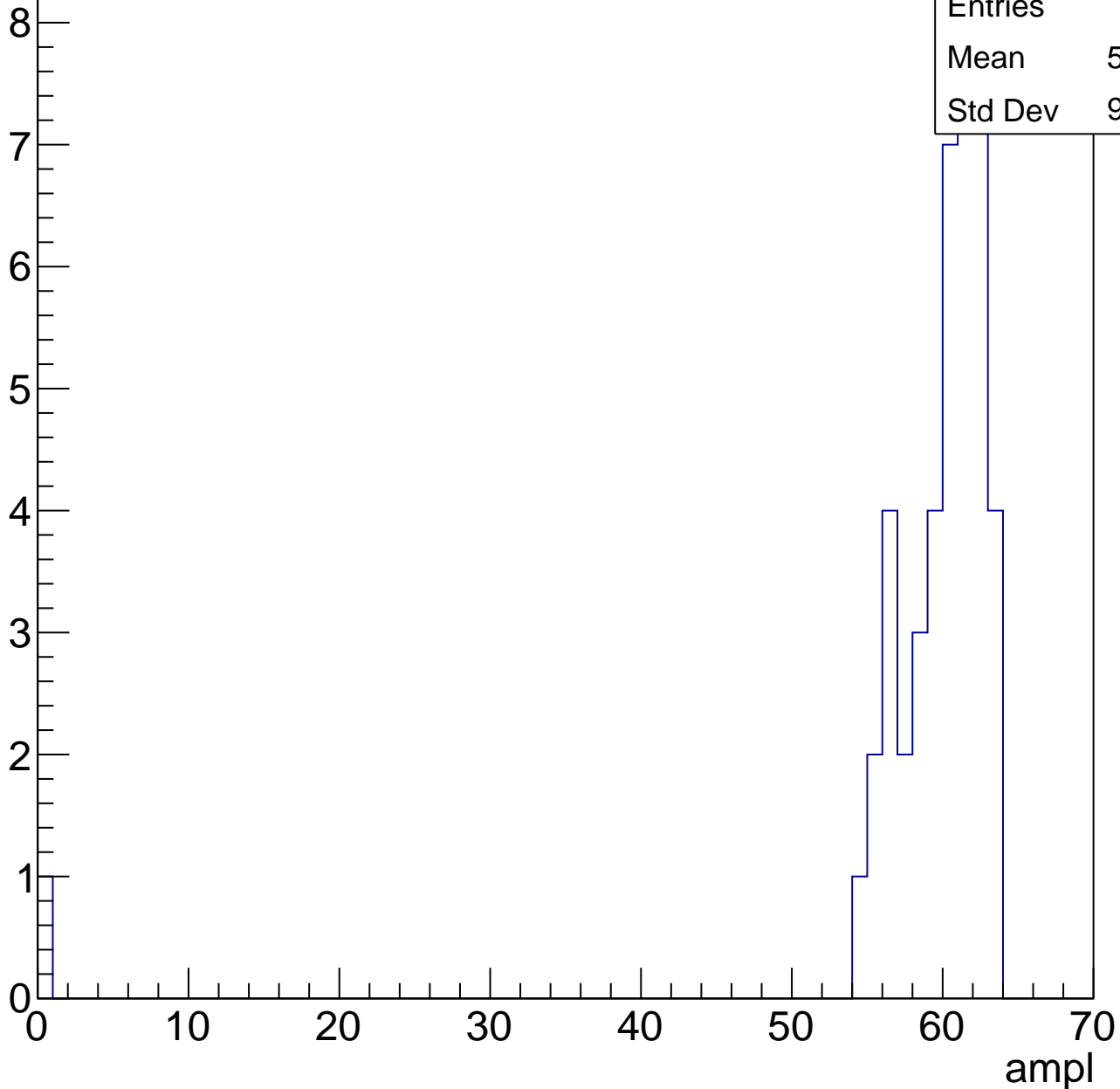


B1L100S, U19-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

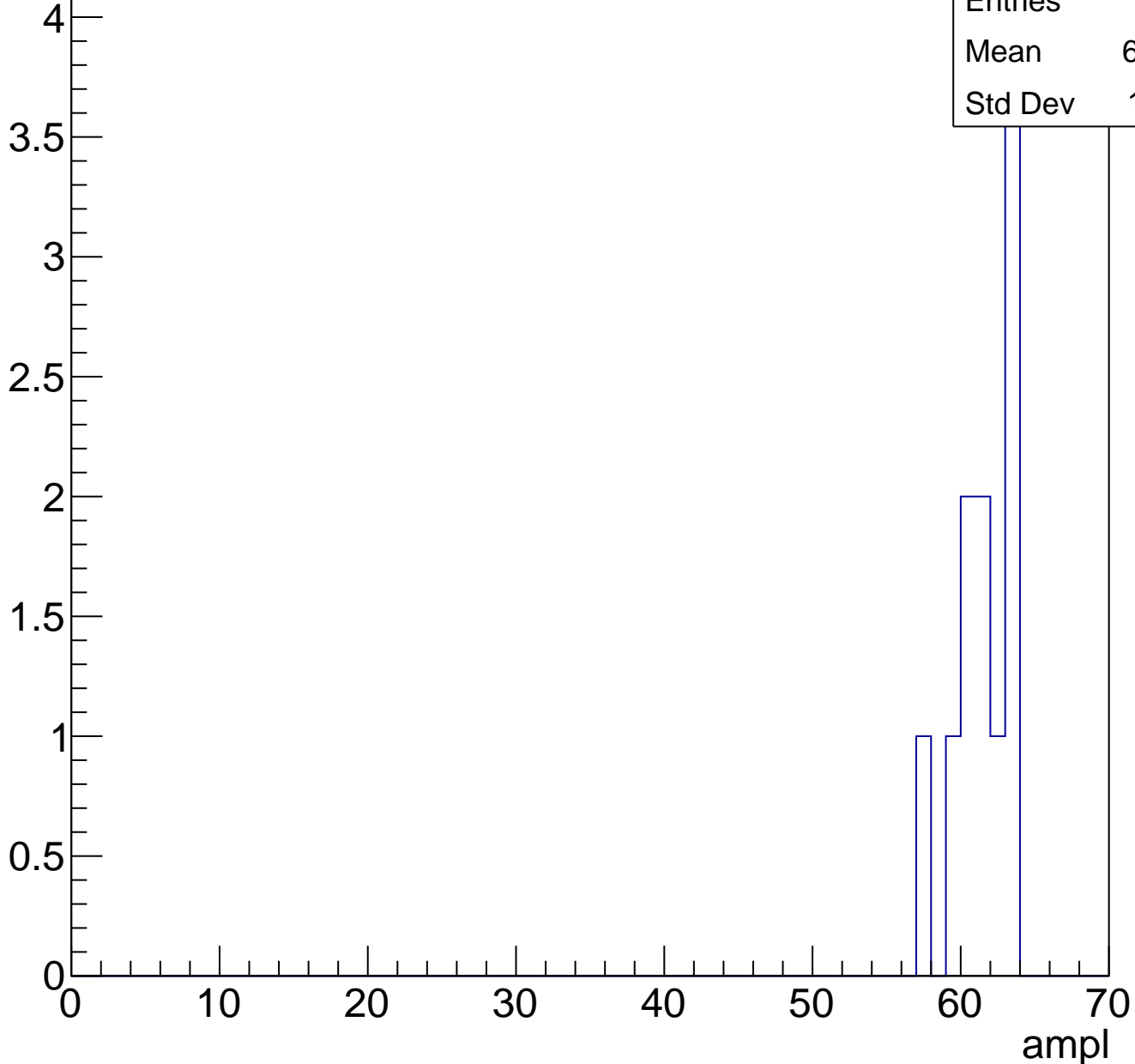
Entries	44
Mean	58.36
Std Dev	9.222



B1L100S, U19-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	29.62
Std Dev	3.381

Gaus mean : 30.3258

Gaus Width: 3.6735

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L100S, U19-ch93, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	50
Mean	36.66
Std Dev	3.16

Gaus mean : 36.9429

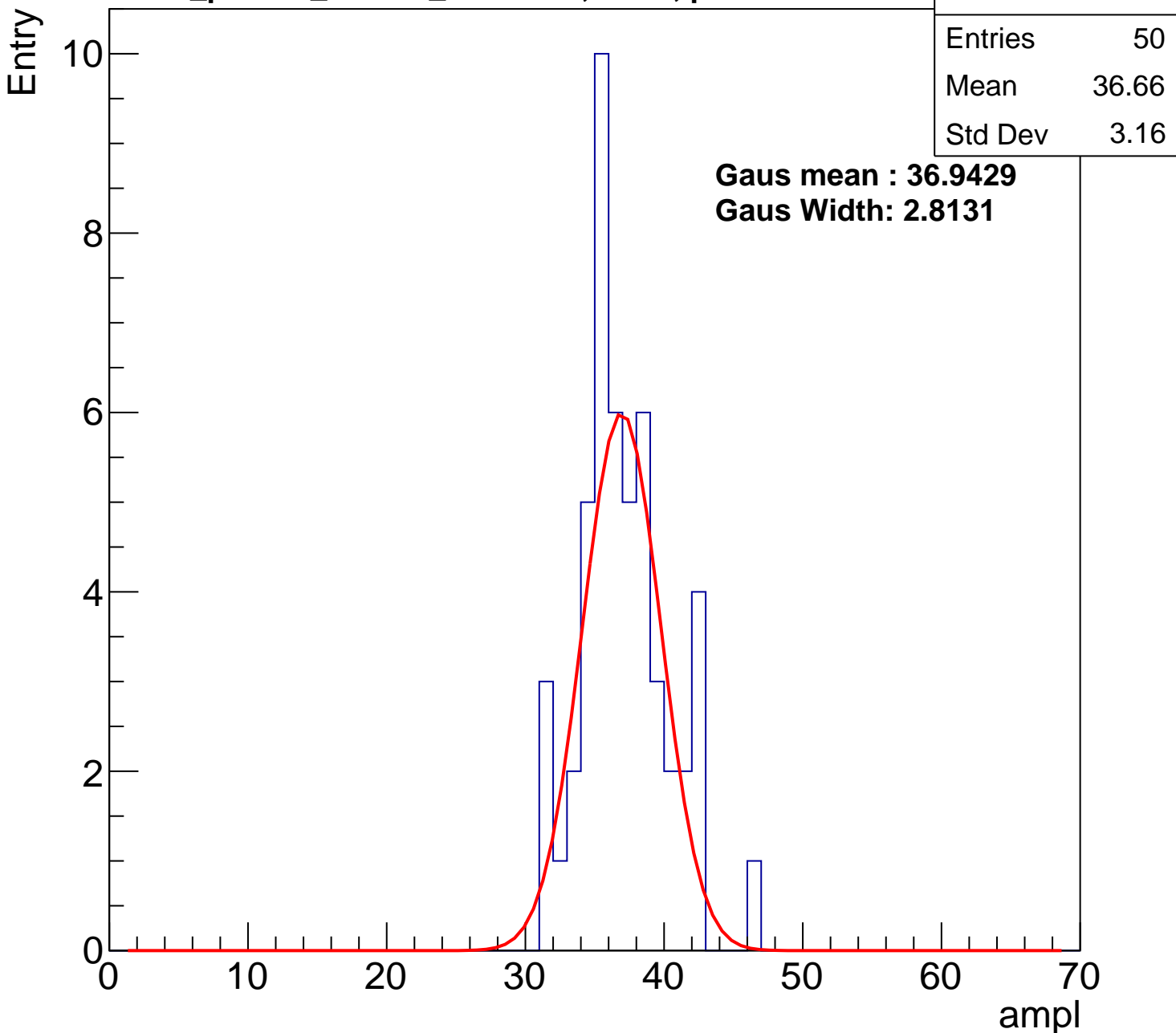
Gaus Width: 2.8131

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch93, adc2

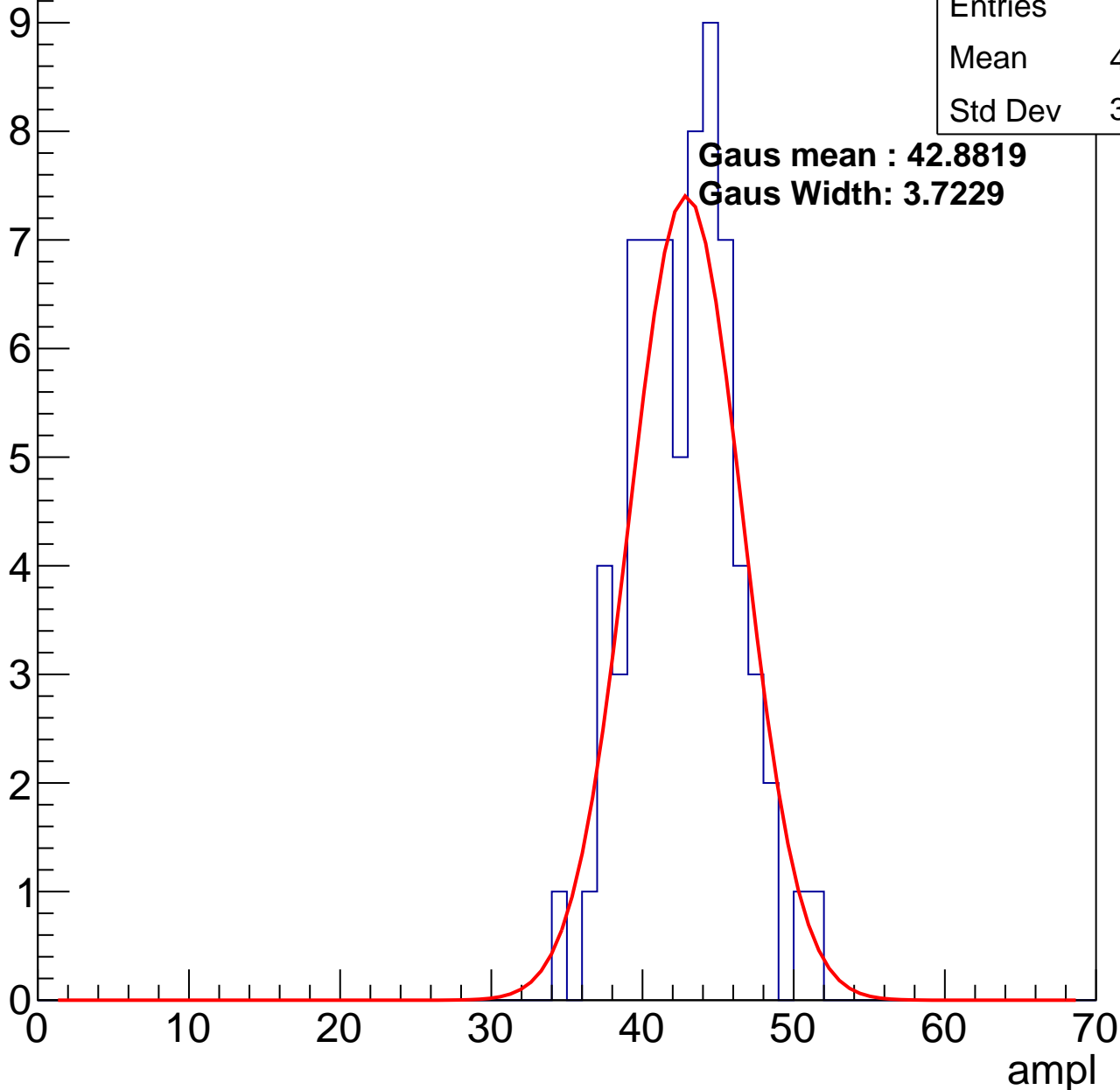
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.27
Std Dev	3.389

Gaus mean : 42.8819

Gaus Width: 3.7229



B1L100S, U19-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

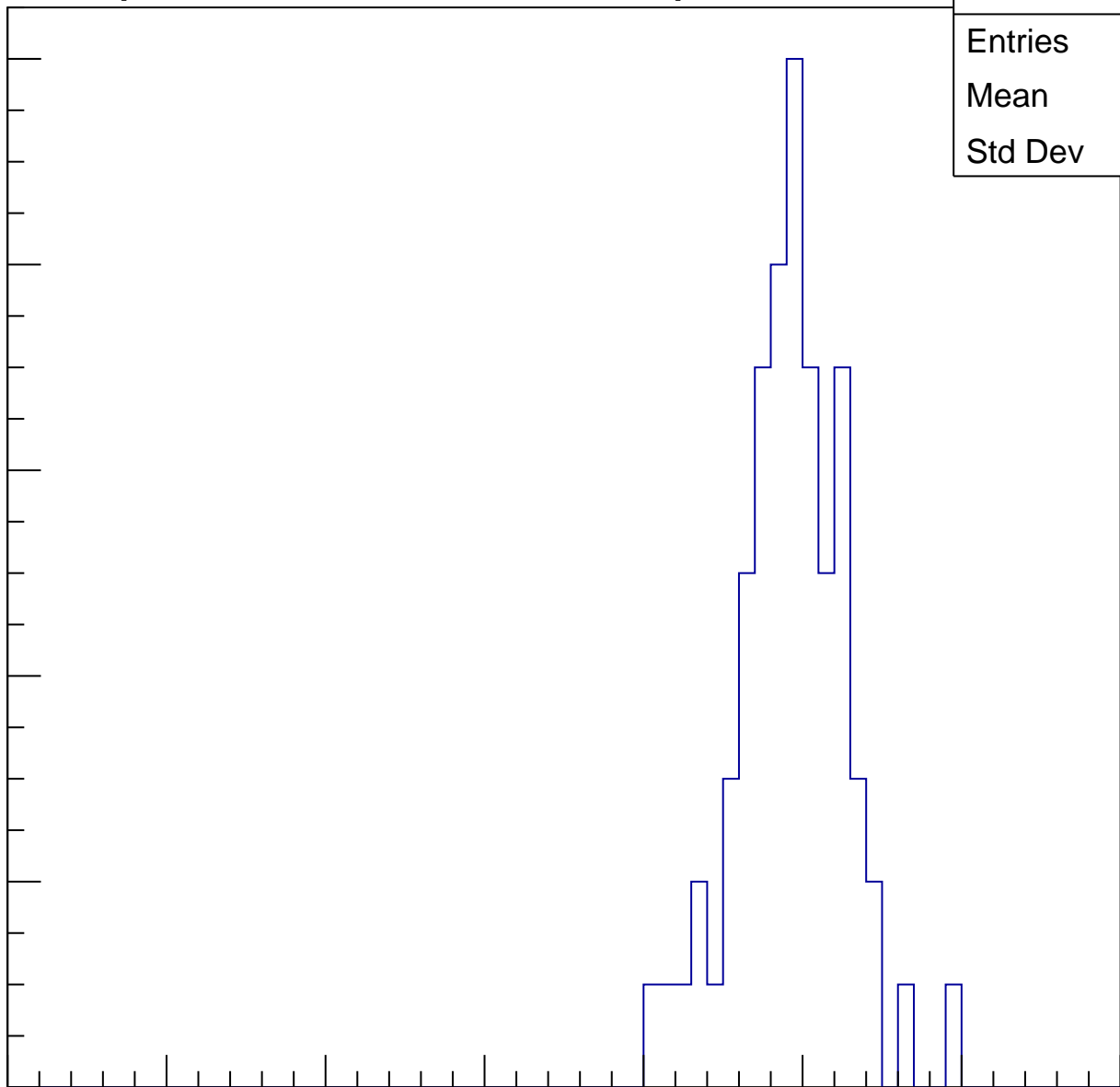
Entries	65
Mean	48.8
Std Dev	3.407

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

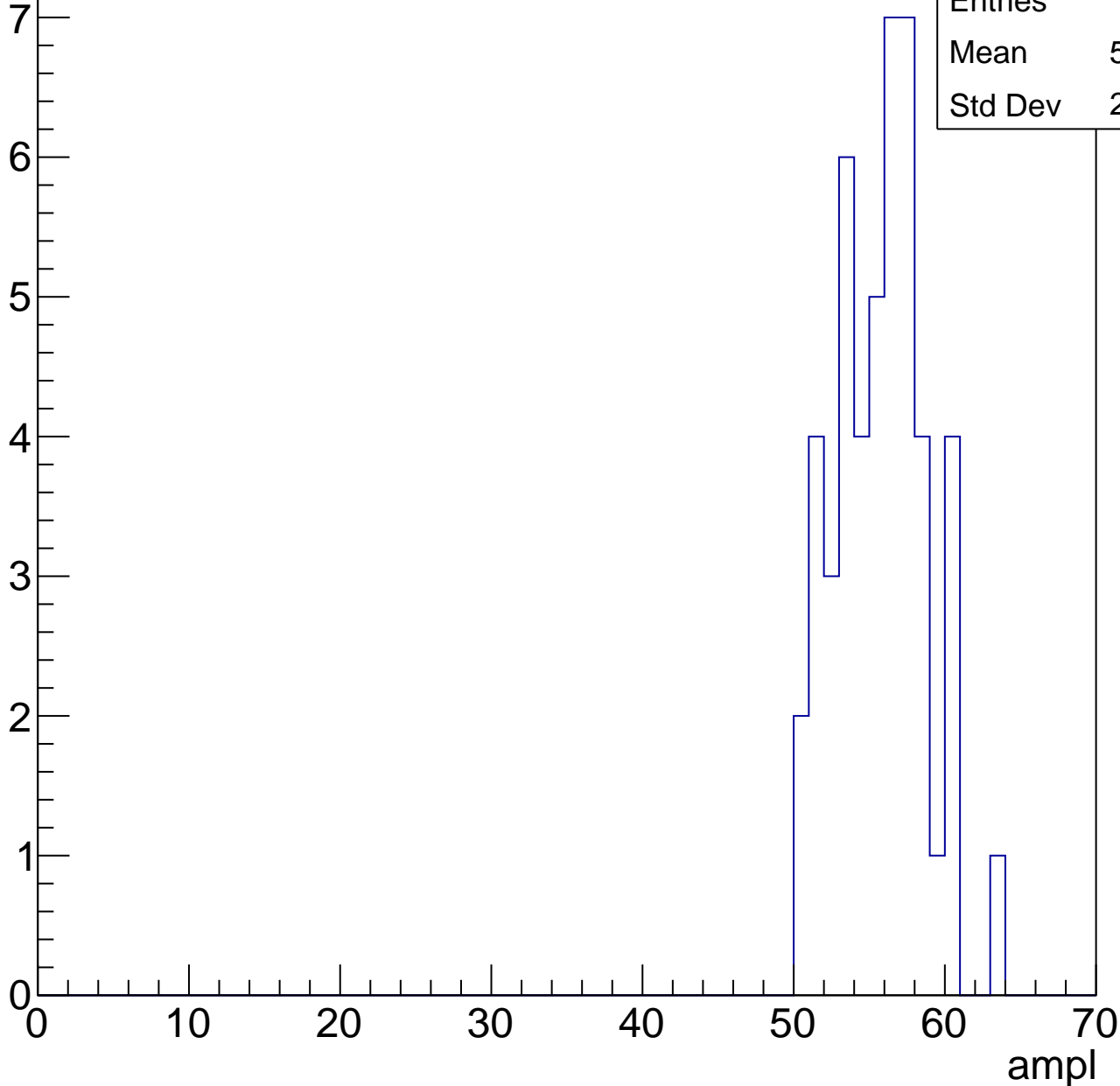


B1L100S, U19-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	55.29
Std Dev	2.944

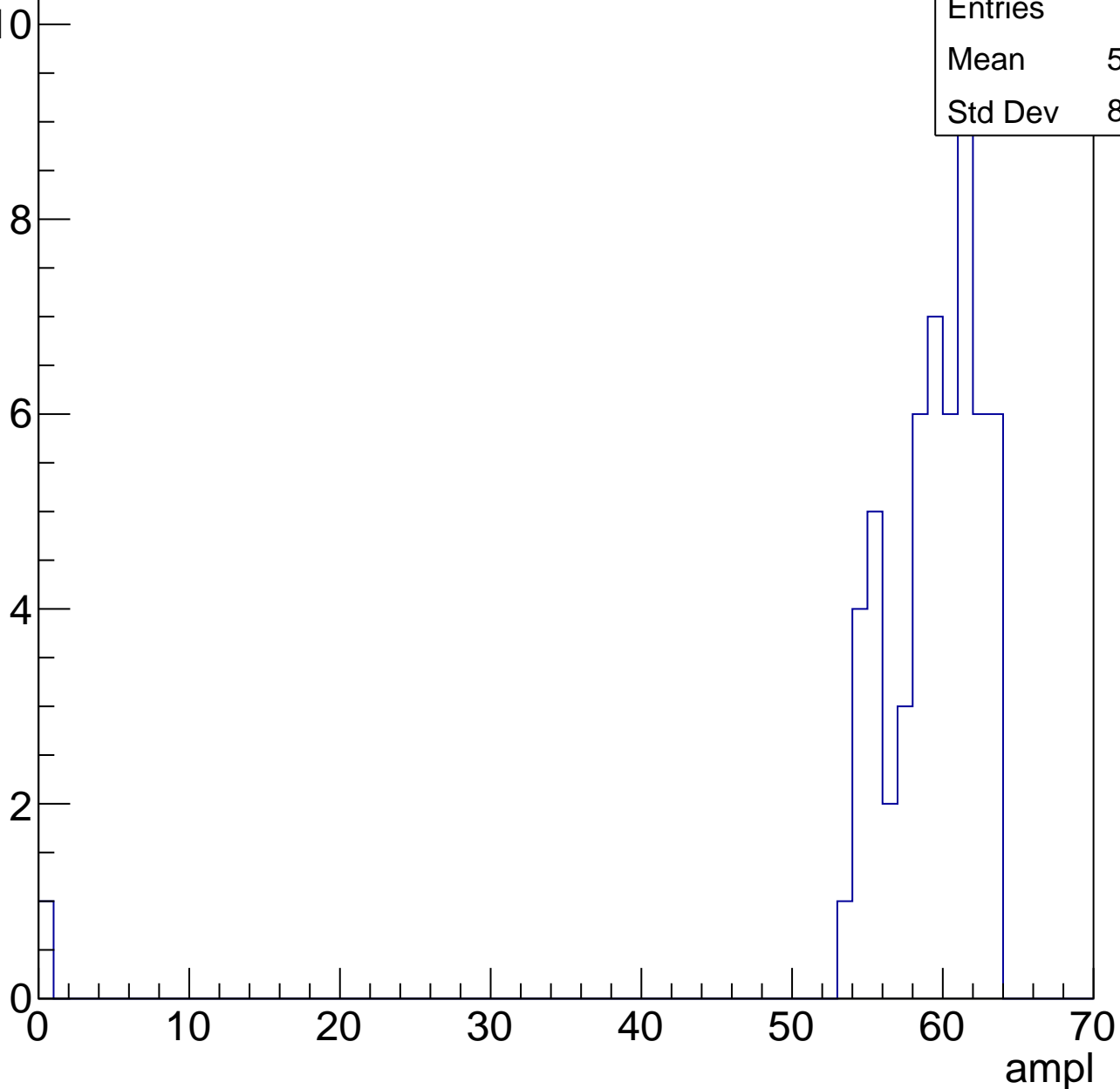


B1L100S, U19-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

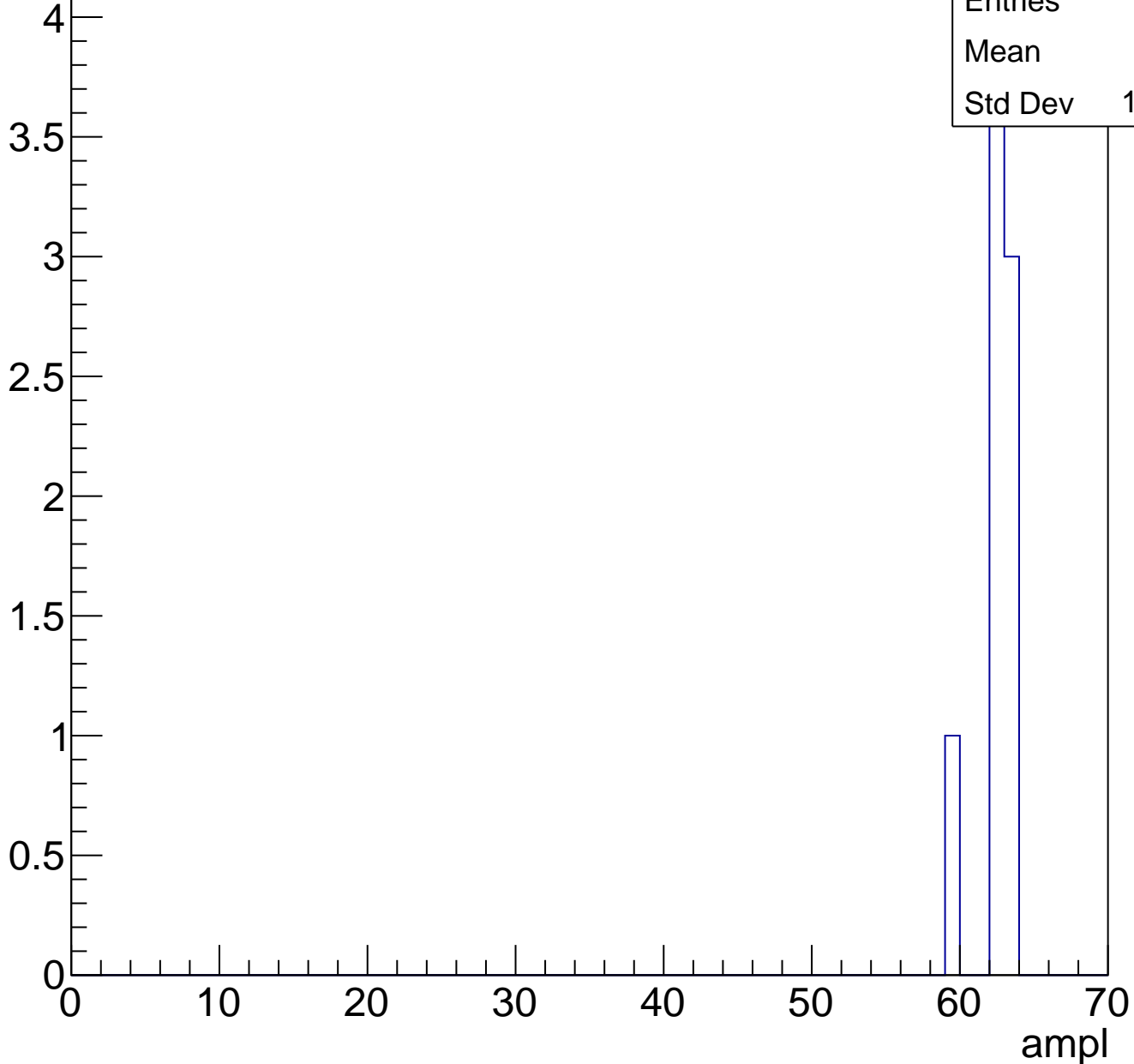
Entries	57
Mean	58.04
Std Dev	8.246



B1L100S, U19-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U19-ch94, adc0

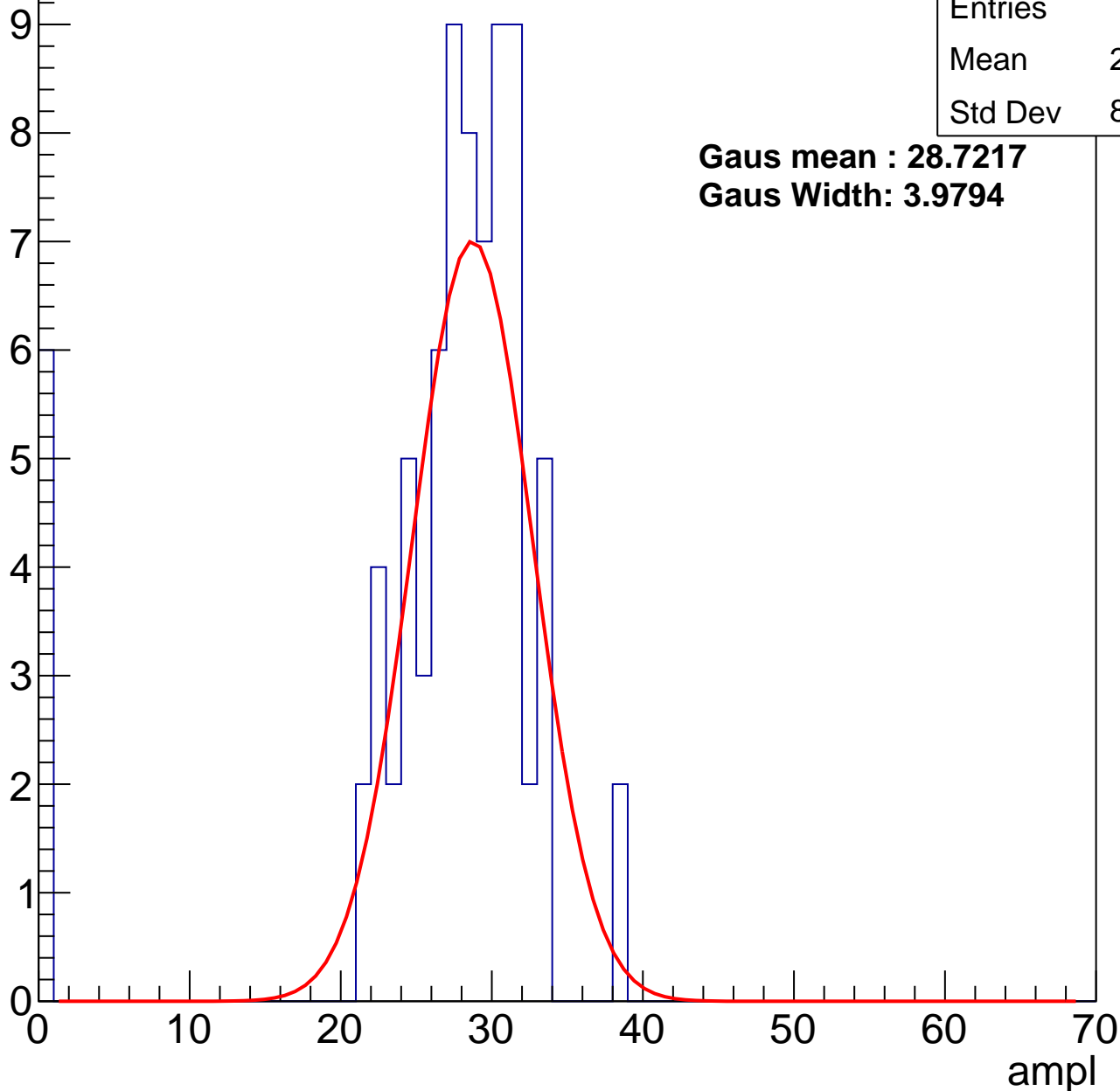
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	25.96
Std Dev	8.182

Gaus mean : 28.7217

Gaus Width: 3.9794

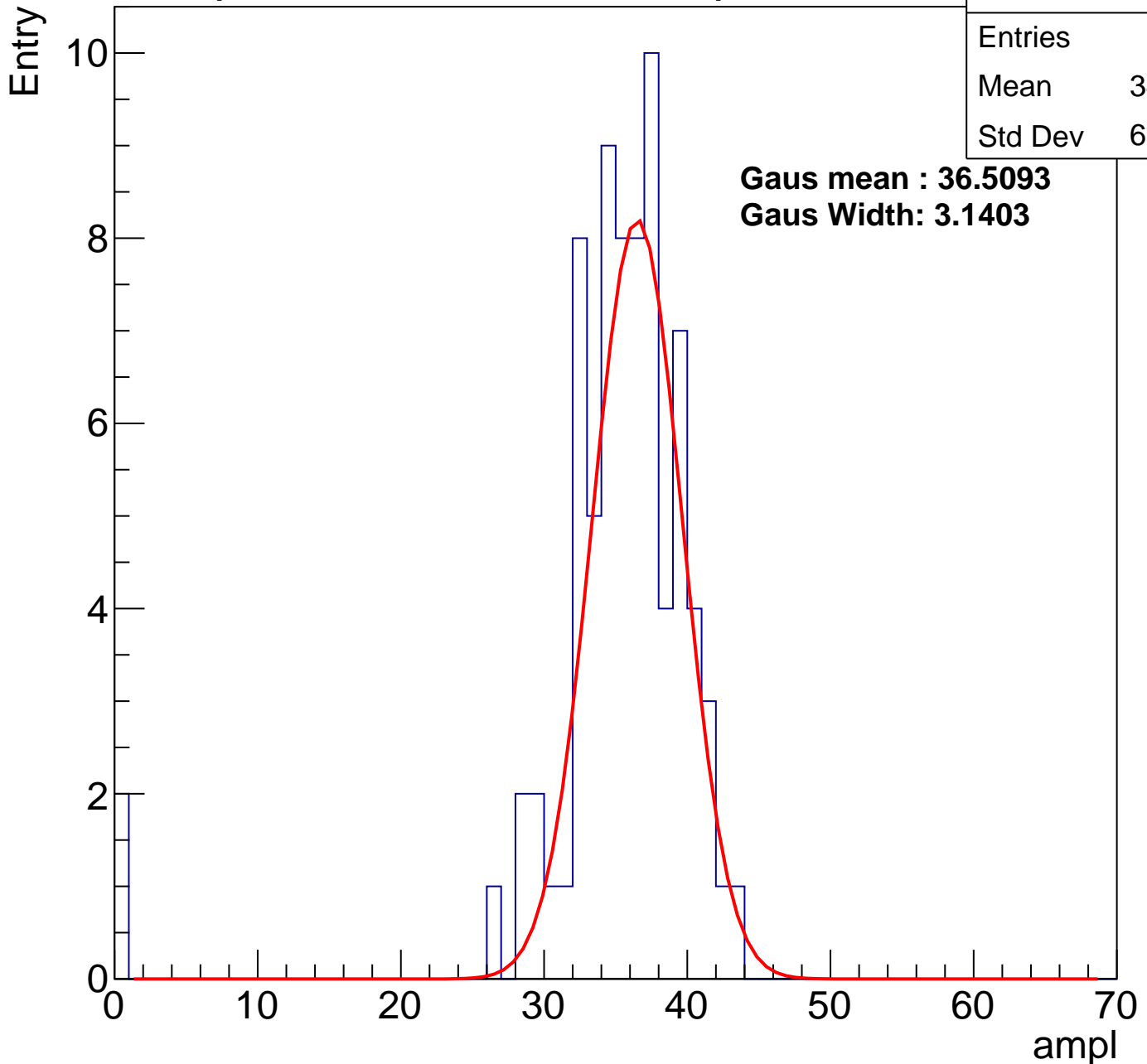


B1L100S, U19-ch94, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	34.53
Std Dev	6.589

Gaus mean : 36.5093
Gaus Width: 3.1403



B1L100S, U19-ch94, adc2

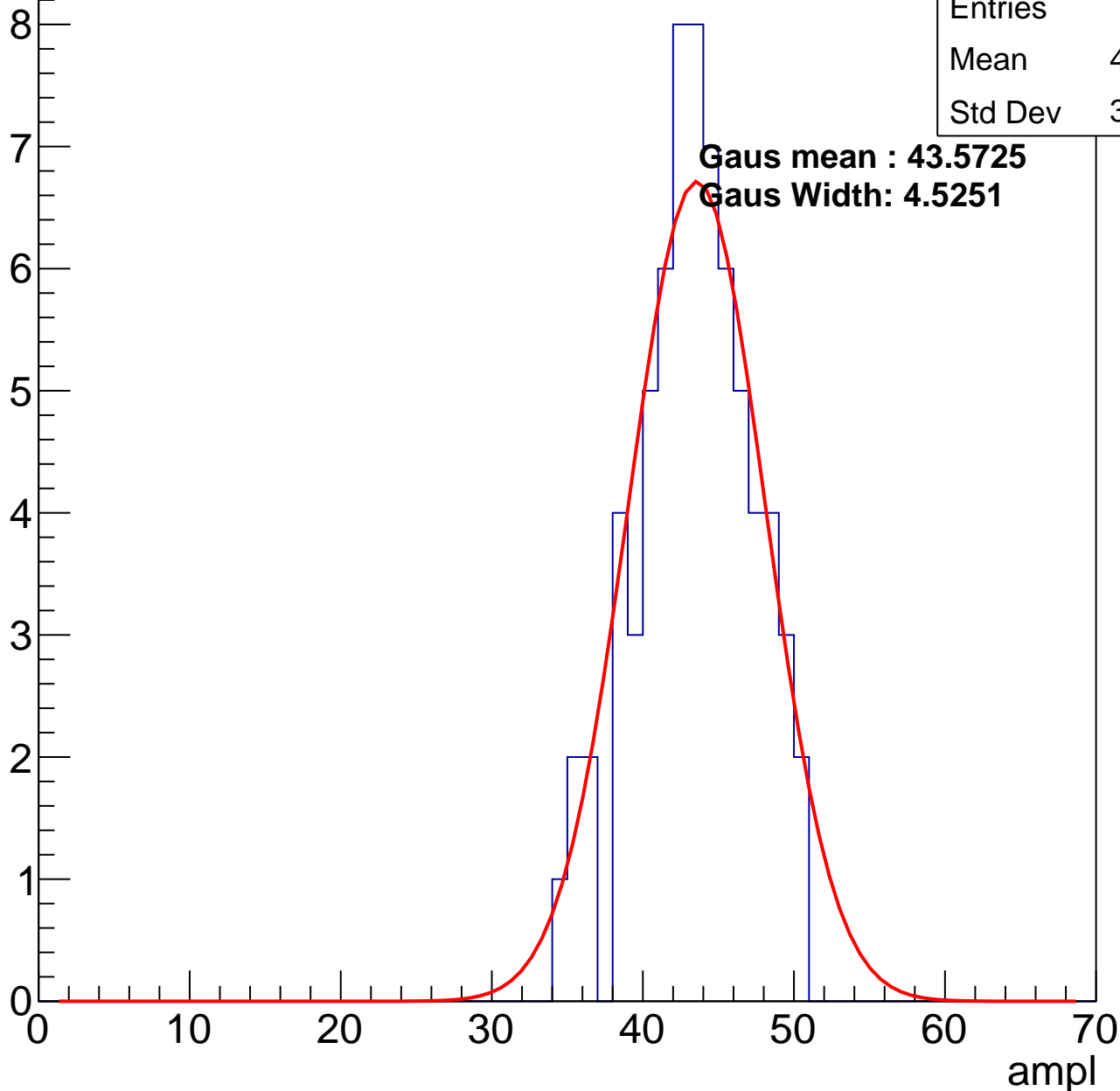
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.94
Std Dev	3.737

Gaus mean : 43.5725

Gaus Width: 4.5251

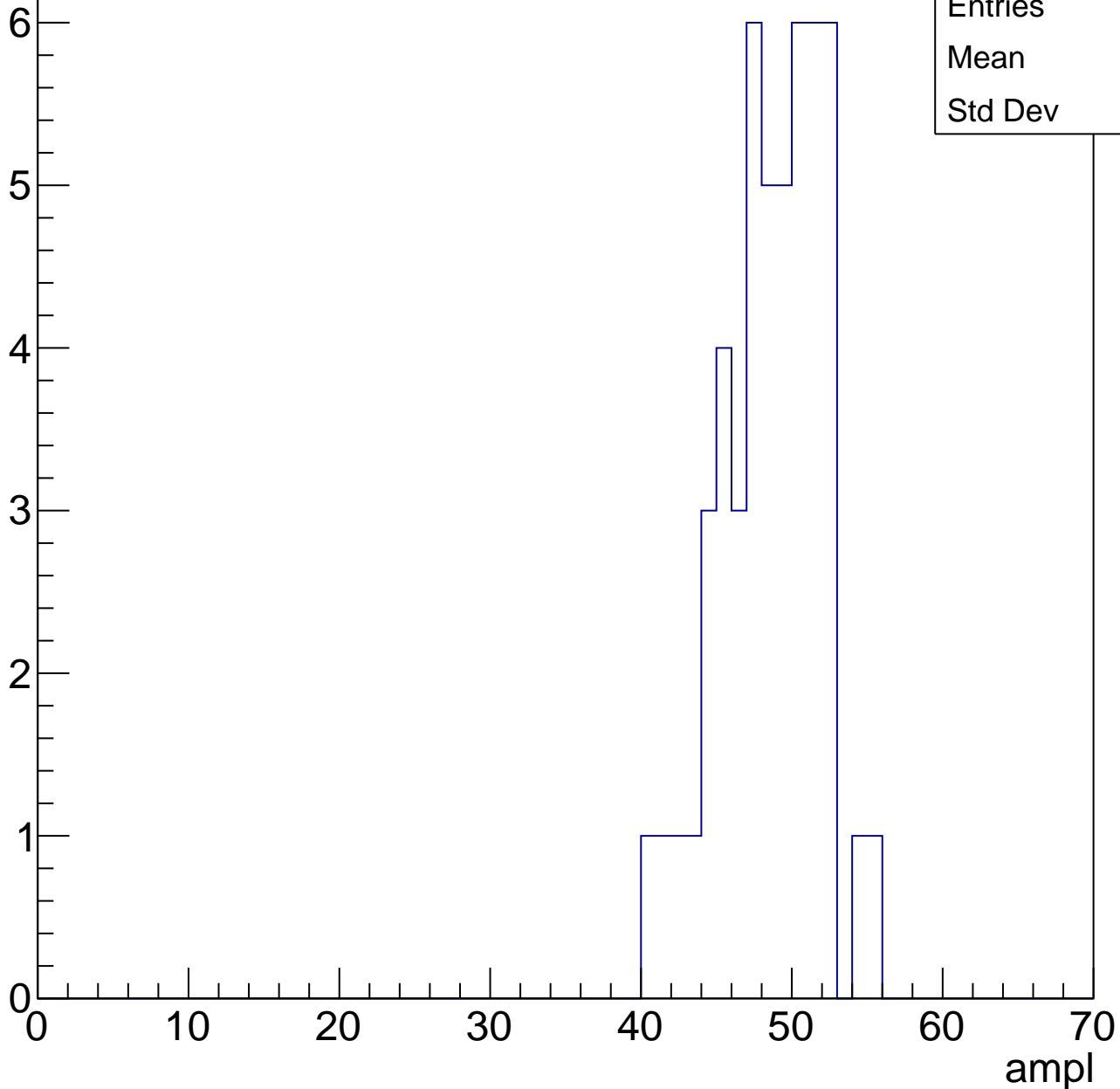


B1L100S, U19-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

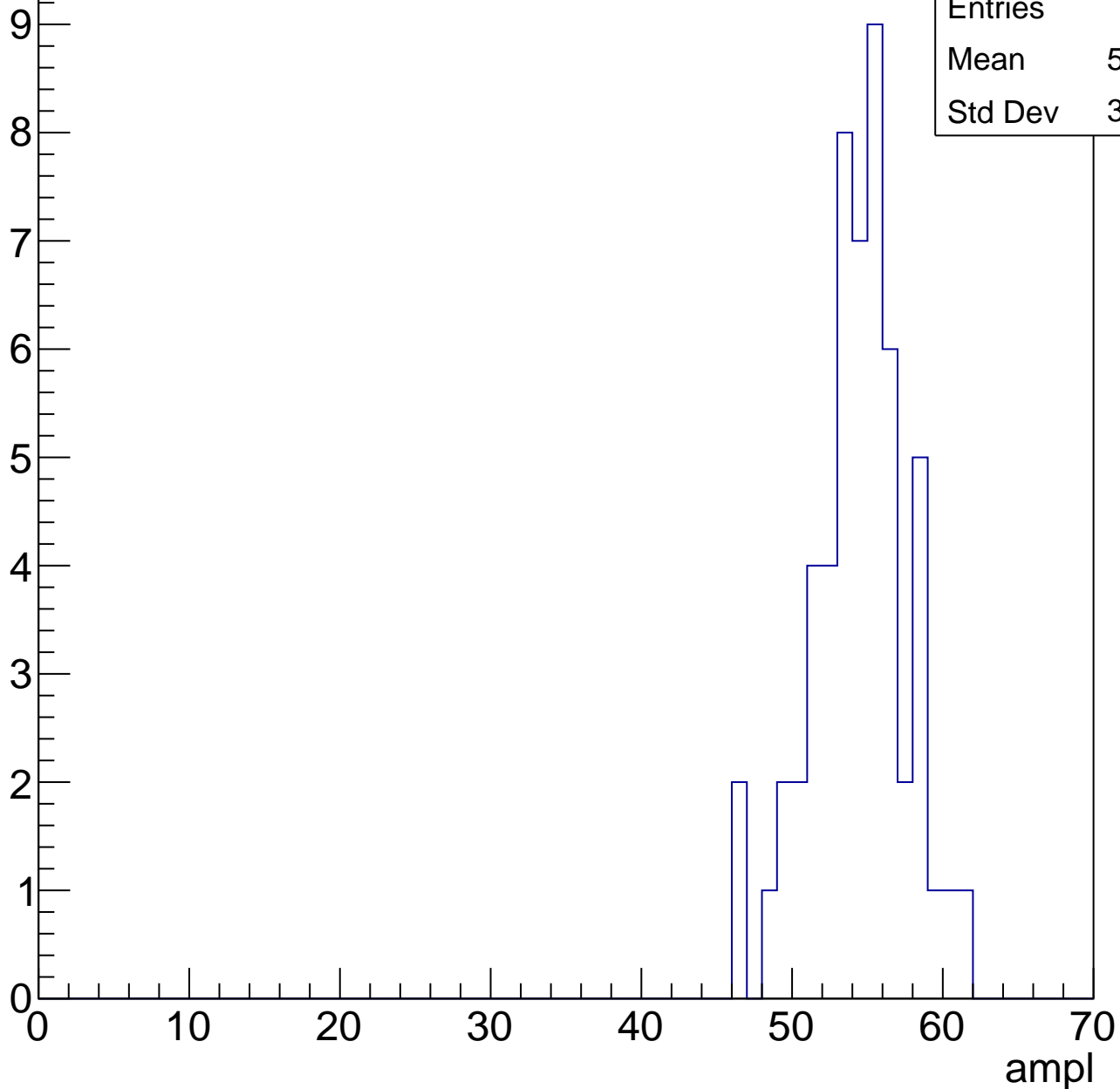
Entries	50
Mean	48.2
Std Dev	3.28



B1L100S, U19-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

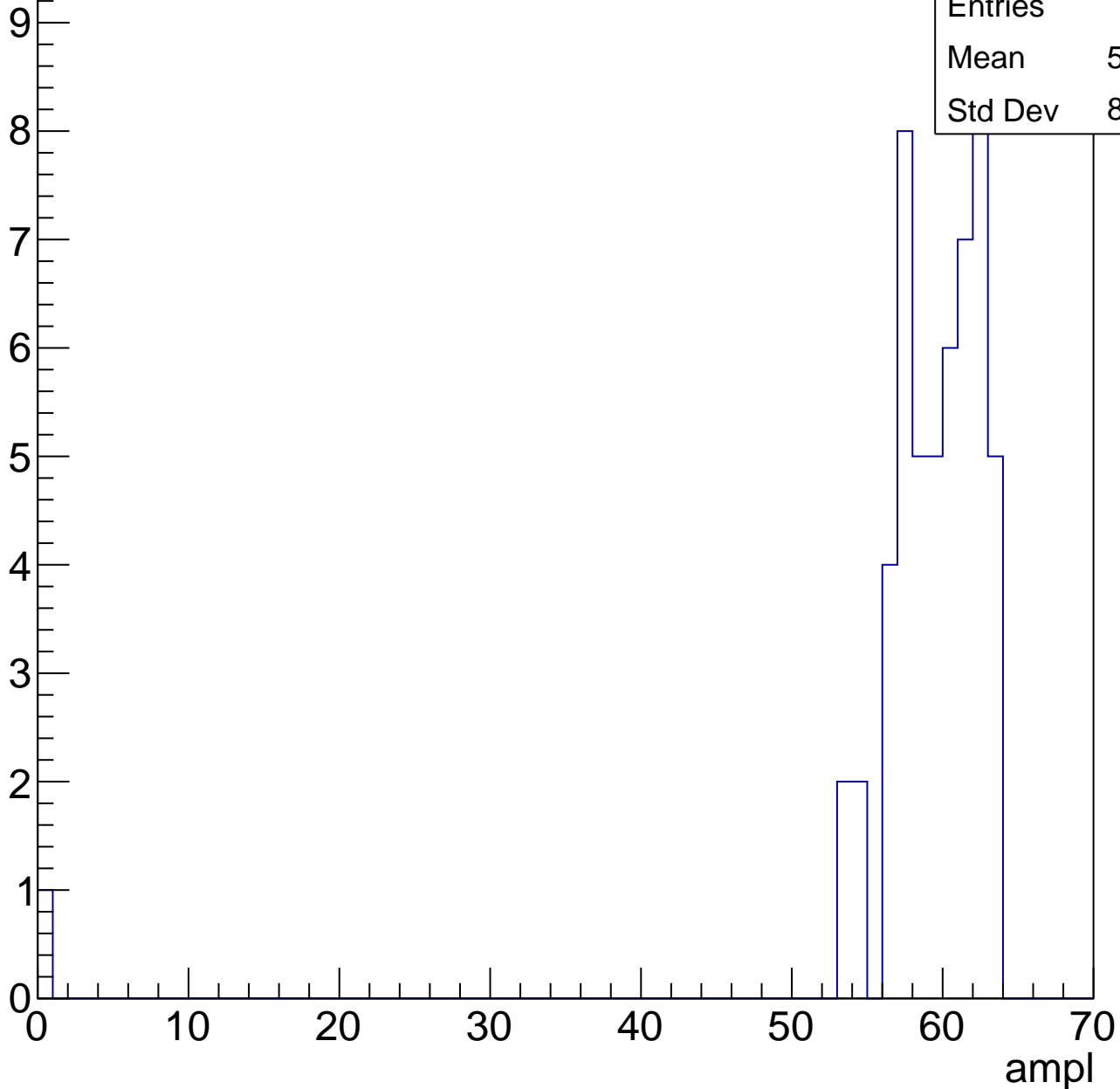


B1L100S, U19-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	58.13
Std Dev	8.422

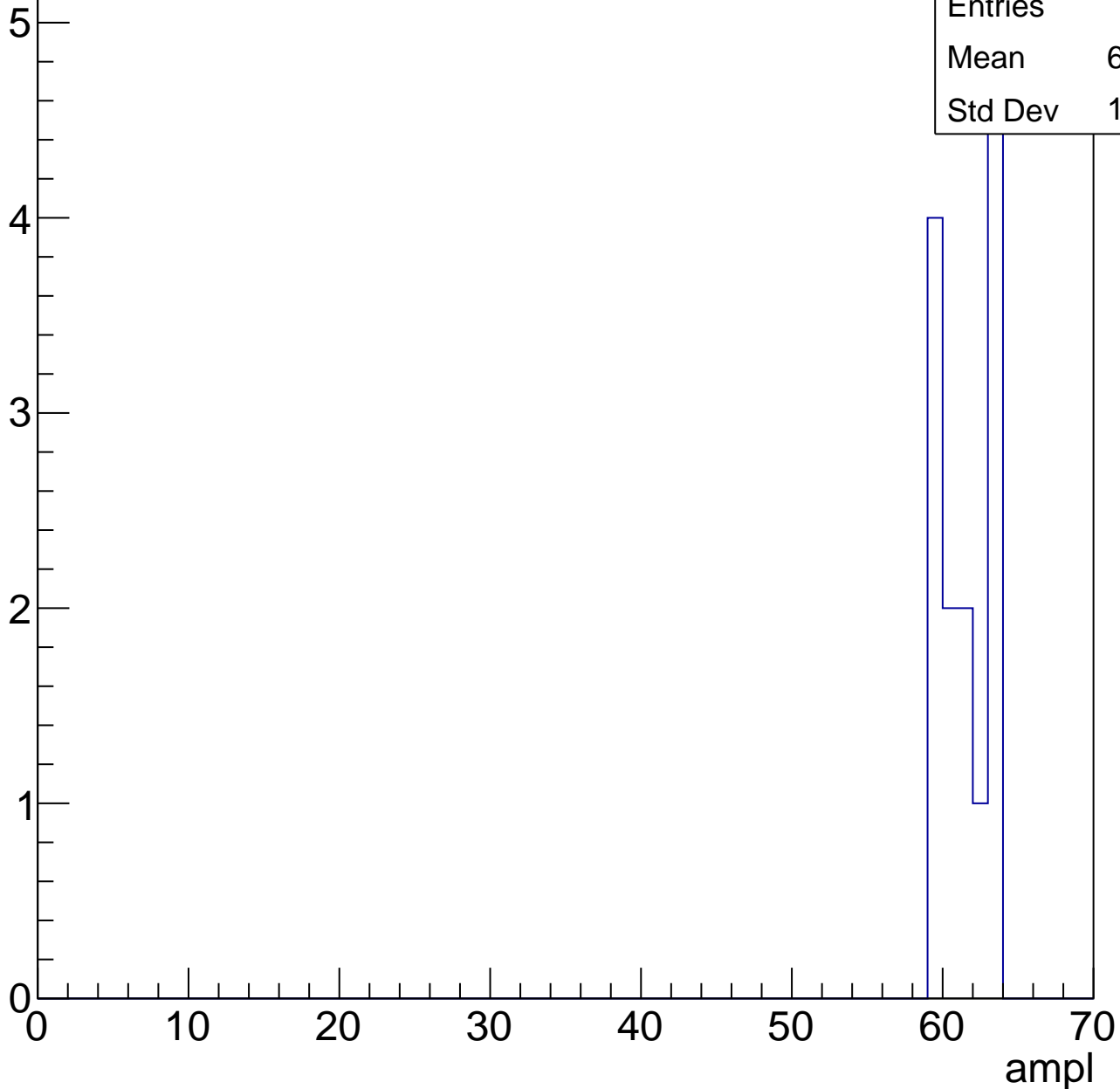


B1L100S, U19-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	61.07
Std Dev	1.668



B1L100S, U19-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch95, adc0

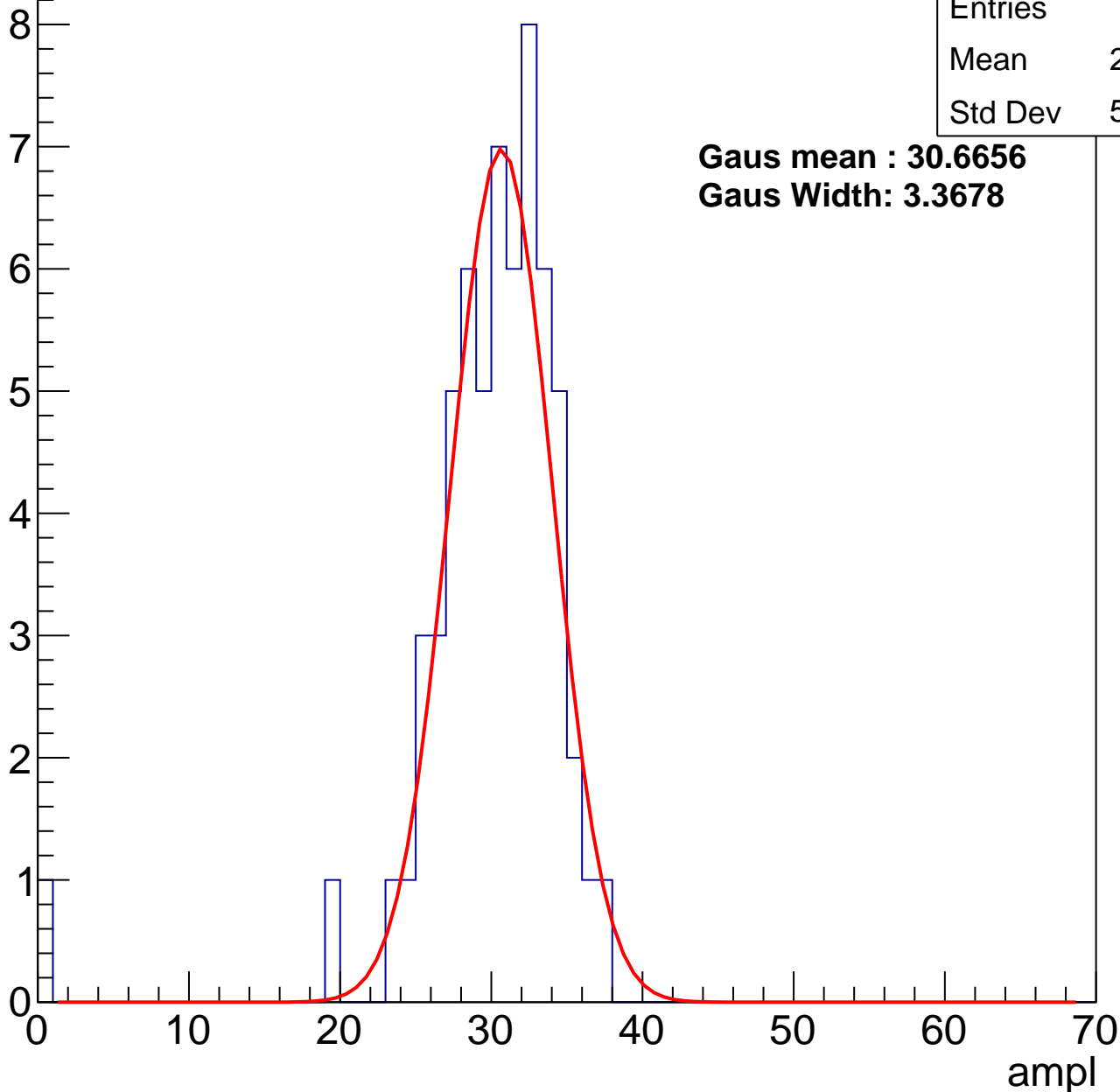
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	29.52
Std Dev	5.073

Gaus mean : 30.6656

Gaus Width: 3.3678



B1L100S, U19-ch95, adc1

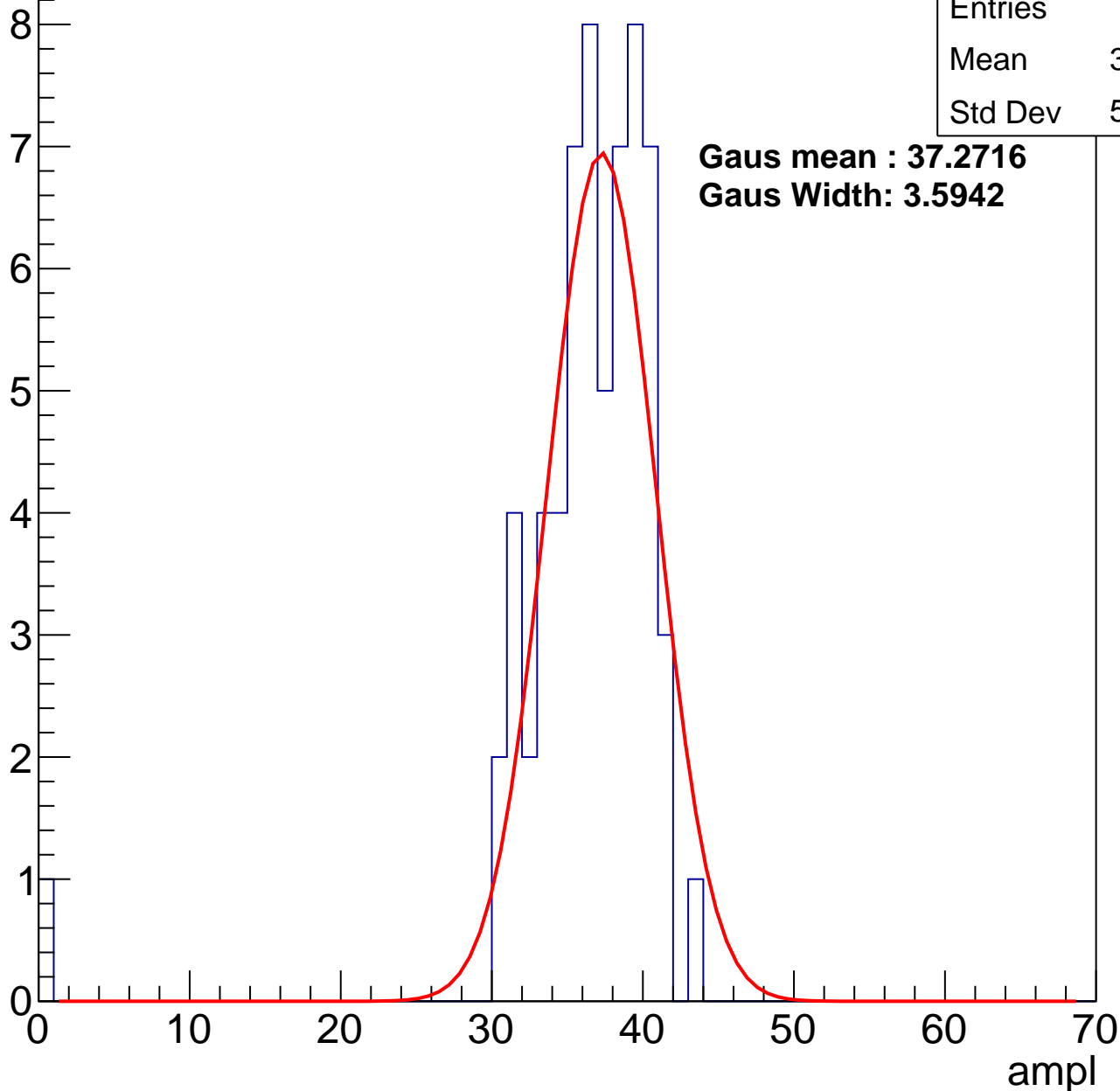
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	35.84
Std Dev	5.489

Gaus mean : 37.2716

Gaus Width: 3.5942



B1L100S, U19-ch95, adc2

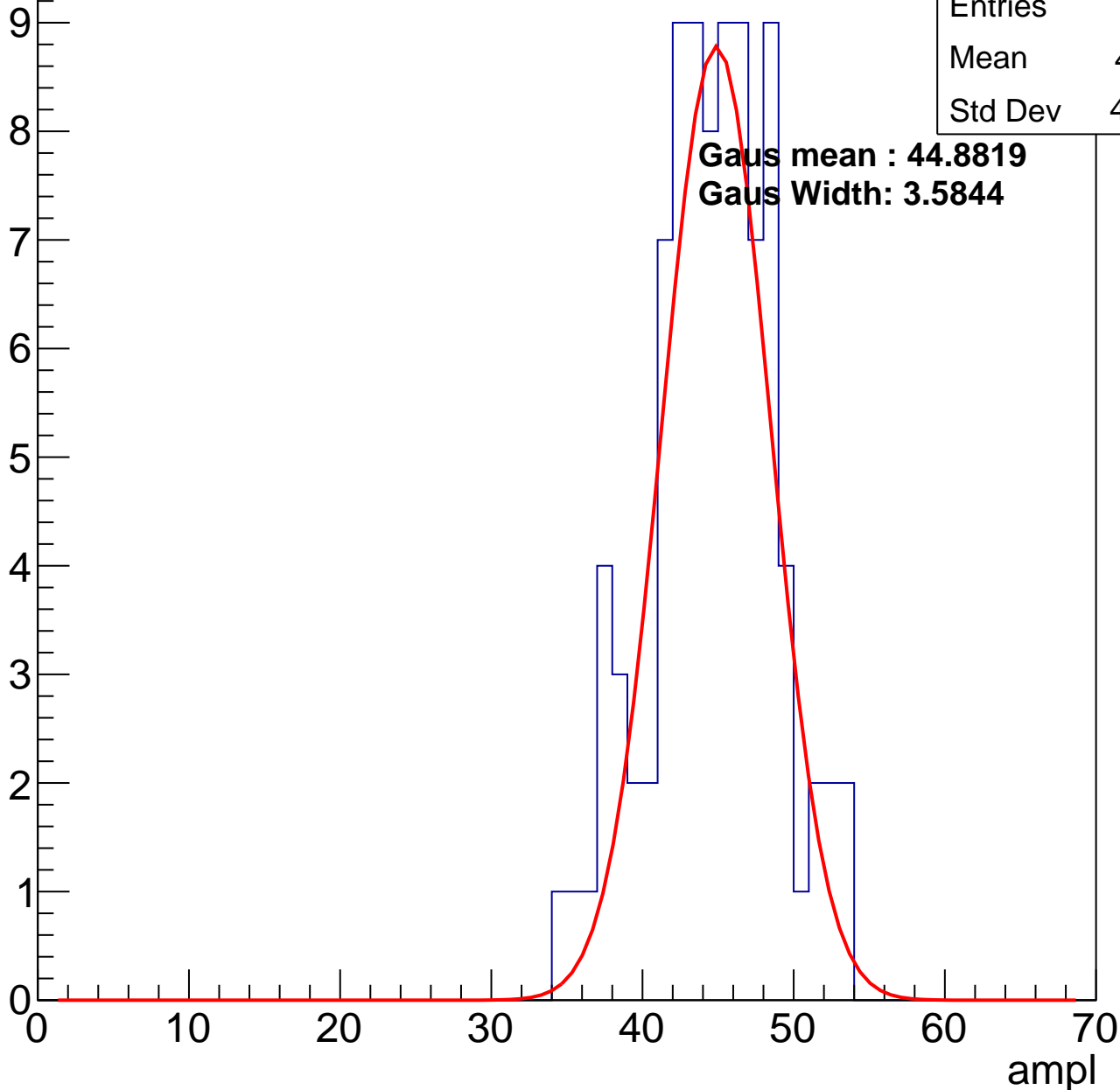
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	92
Mean	44.21
Std Dev	4.053

Gaus mean : 44.8819

Gaus Width: 3.5844

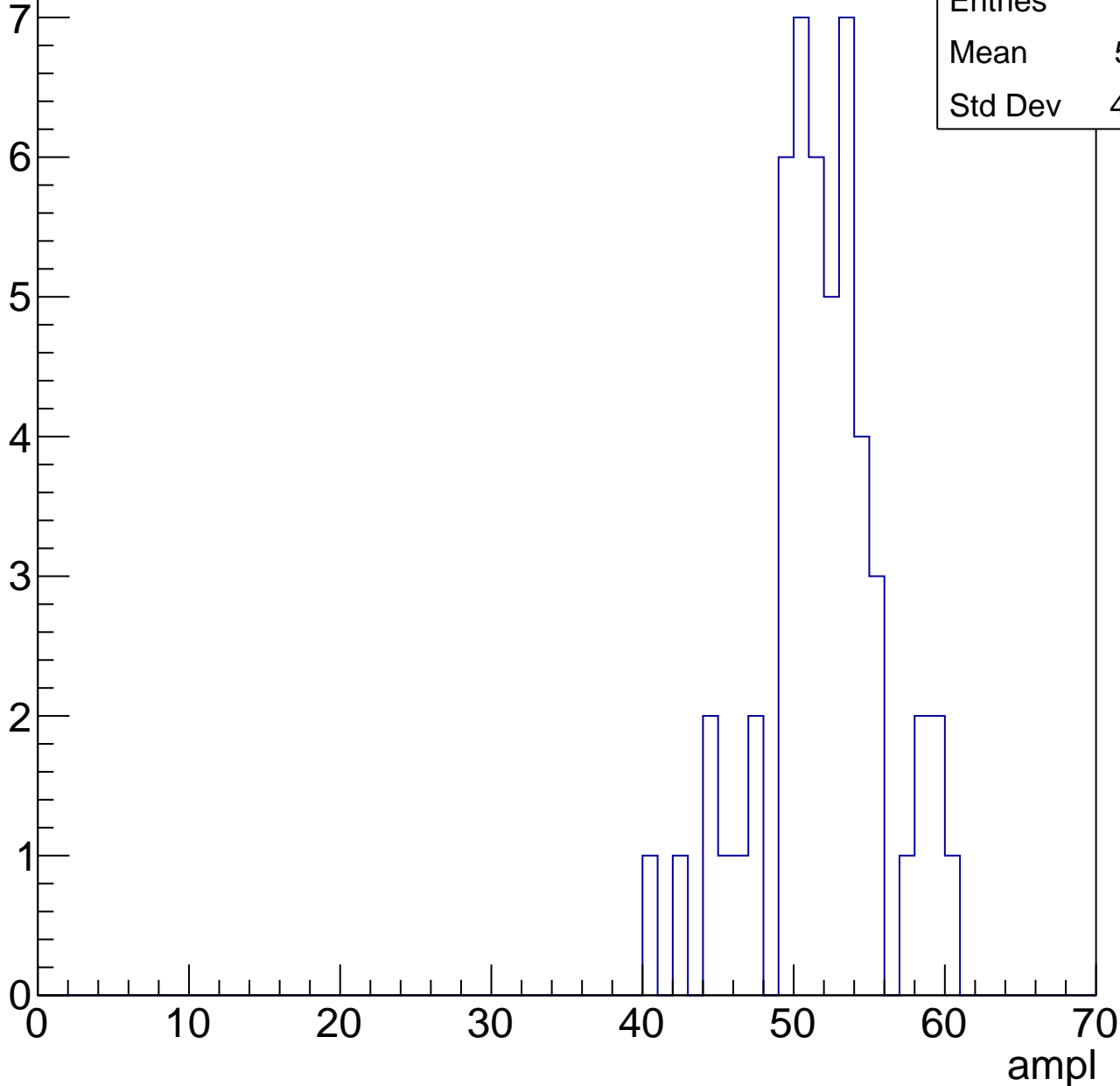


B1L100S, U19-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

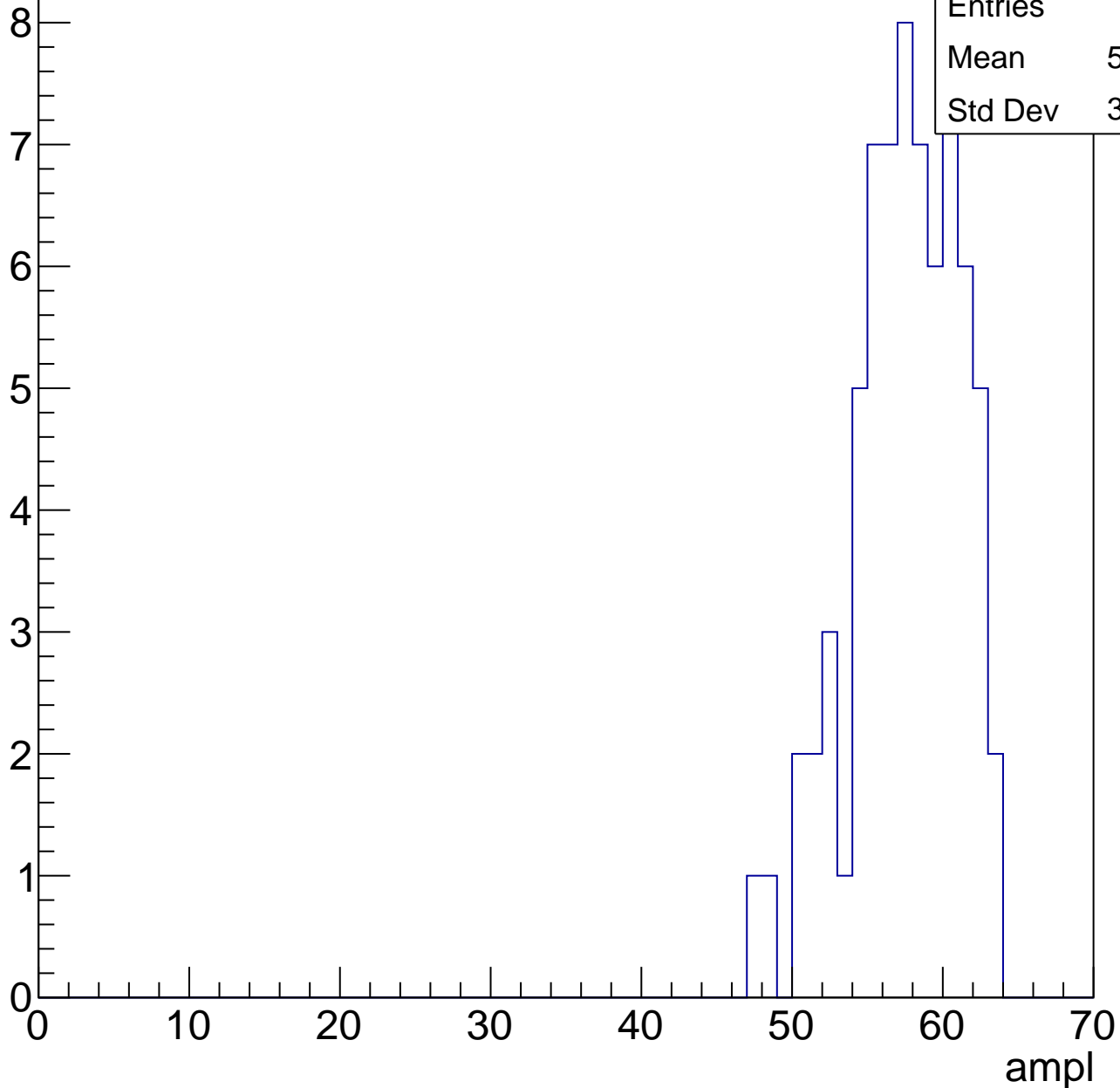
Entries	52
Mean	51.31
Std Dev	4.112



B1L100S, U19-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



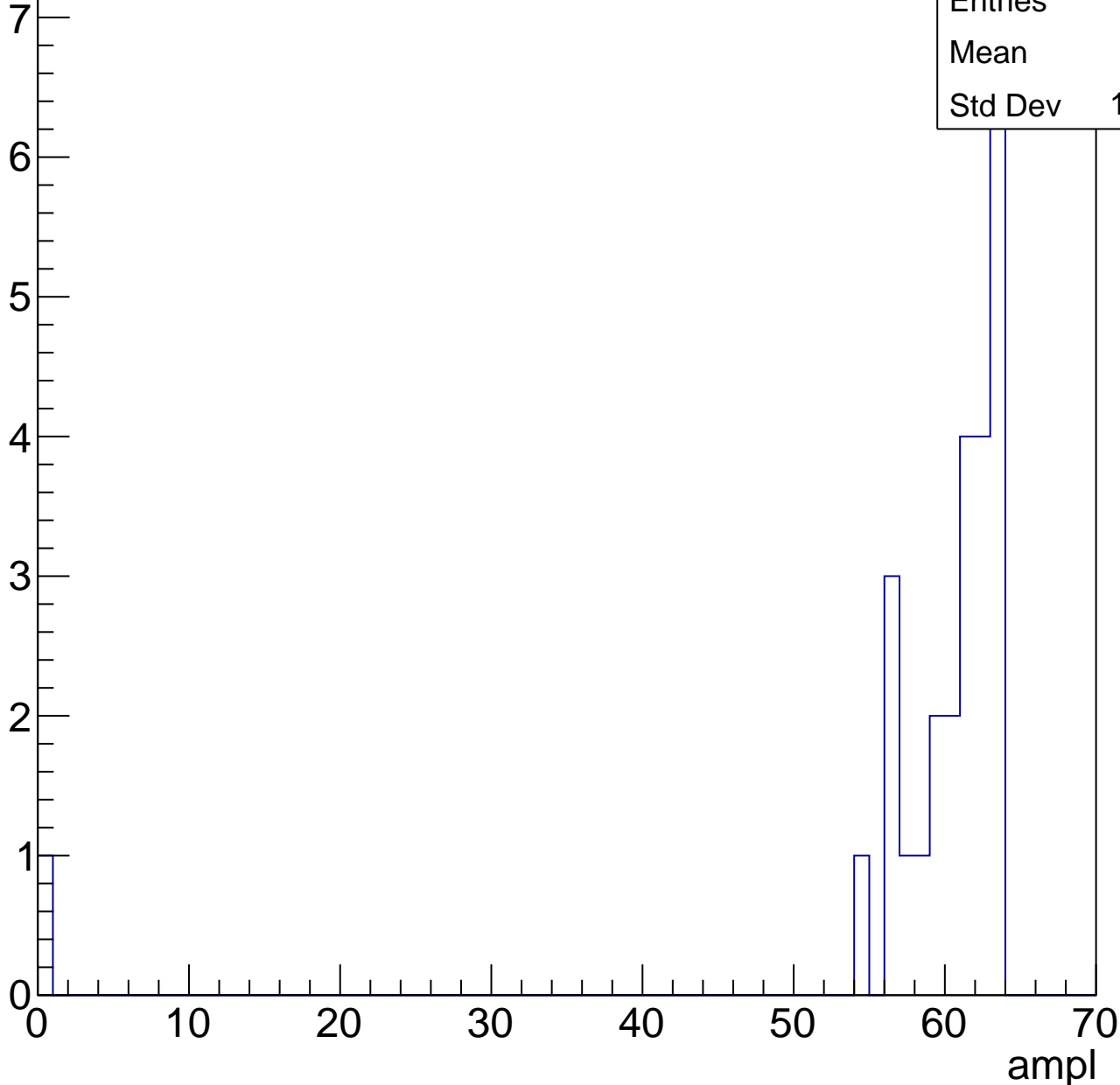
Entries	71
Mean	57.06
Std Dev	3.595

B1L100S, U19-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58
Std Dev	11.89



B1L100S, U19-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch96, adc0

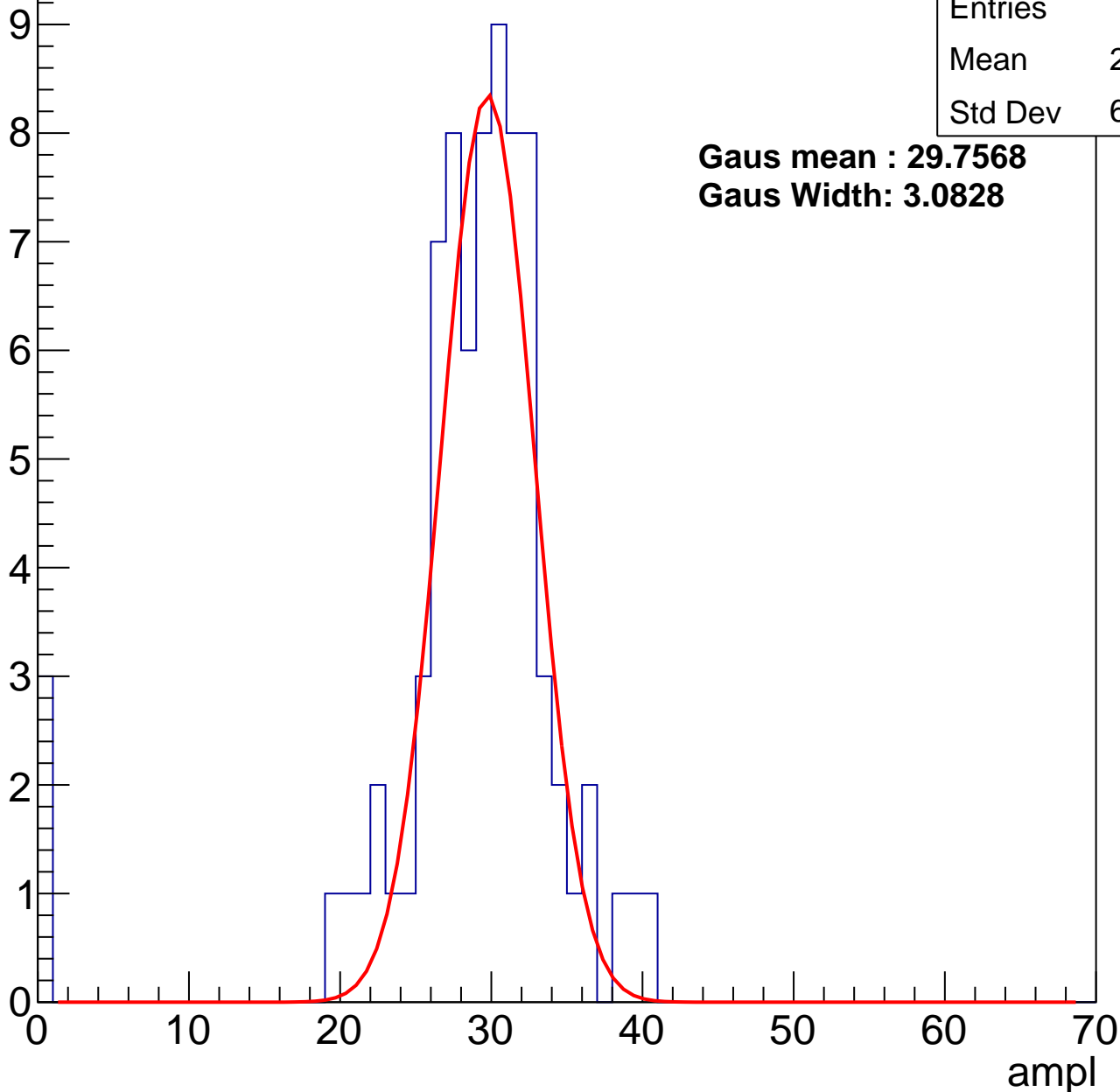
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.06
Std Dev	6.845

Gaus mean : 29.7568

Gaus Width: 3.0828



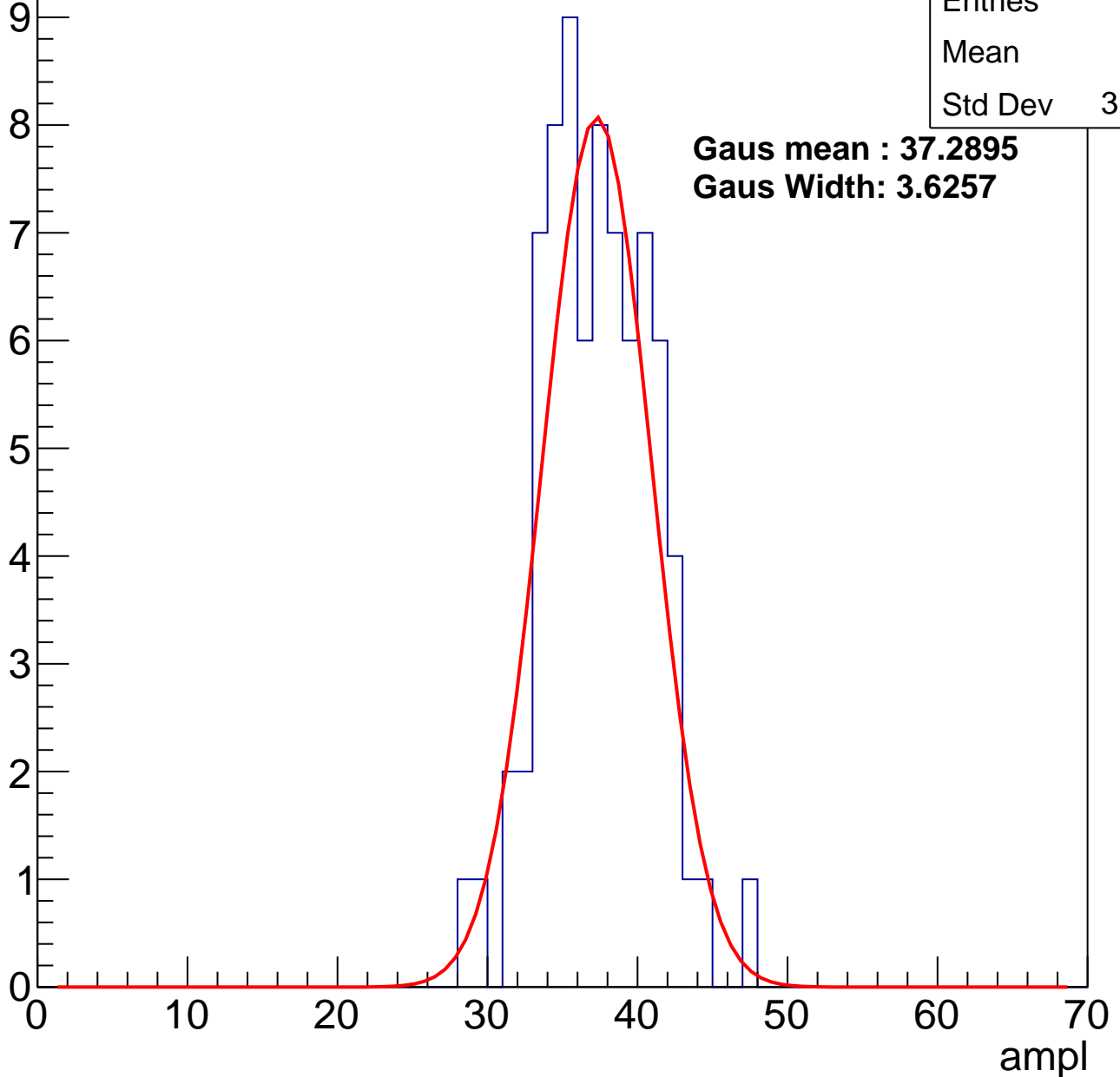
B1L100S, U19-ch96, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	36.9
Std Dev	3.544

Gaus mean : 37.2895
Gaus Width: 3.6257



B1L100S, U19-ch96, adc2

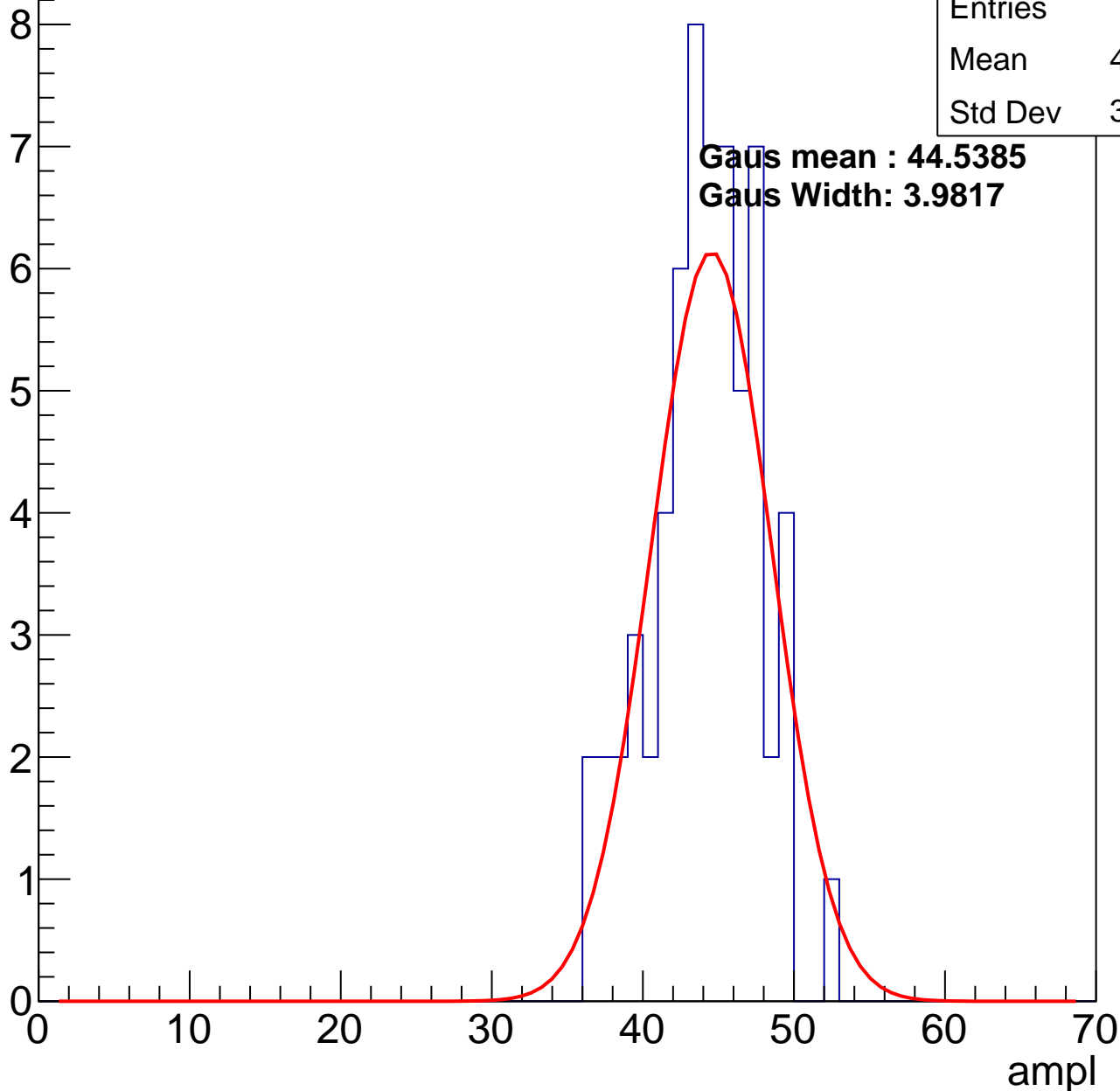
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.63
Std Dev	3.479

Gaus mean : 44.5385

Gaus Width: 3.9817

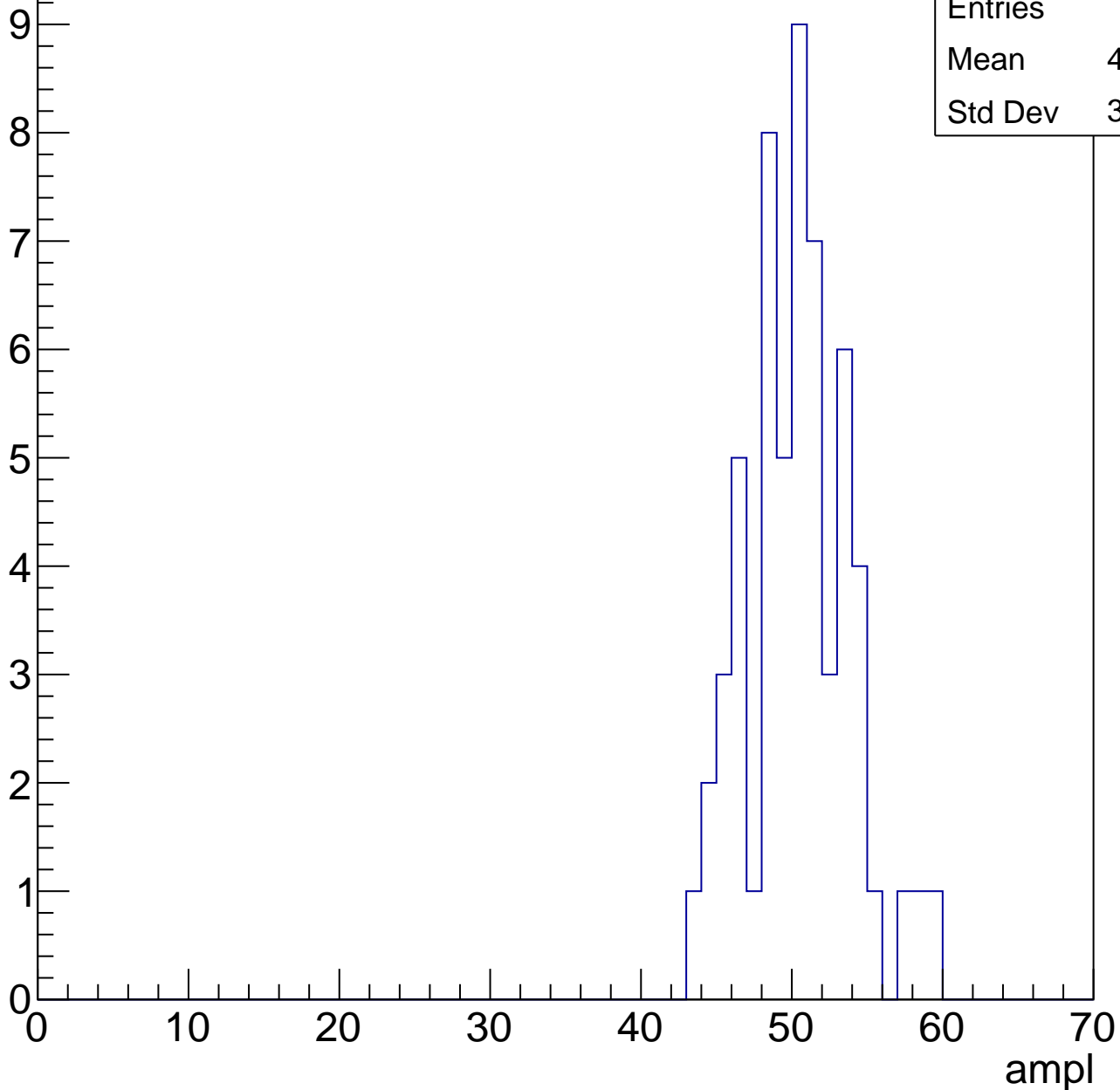


B1L100S, U19-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

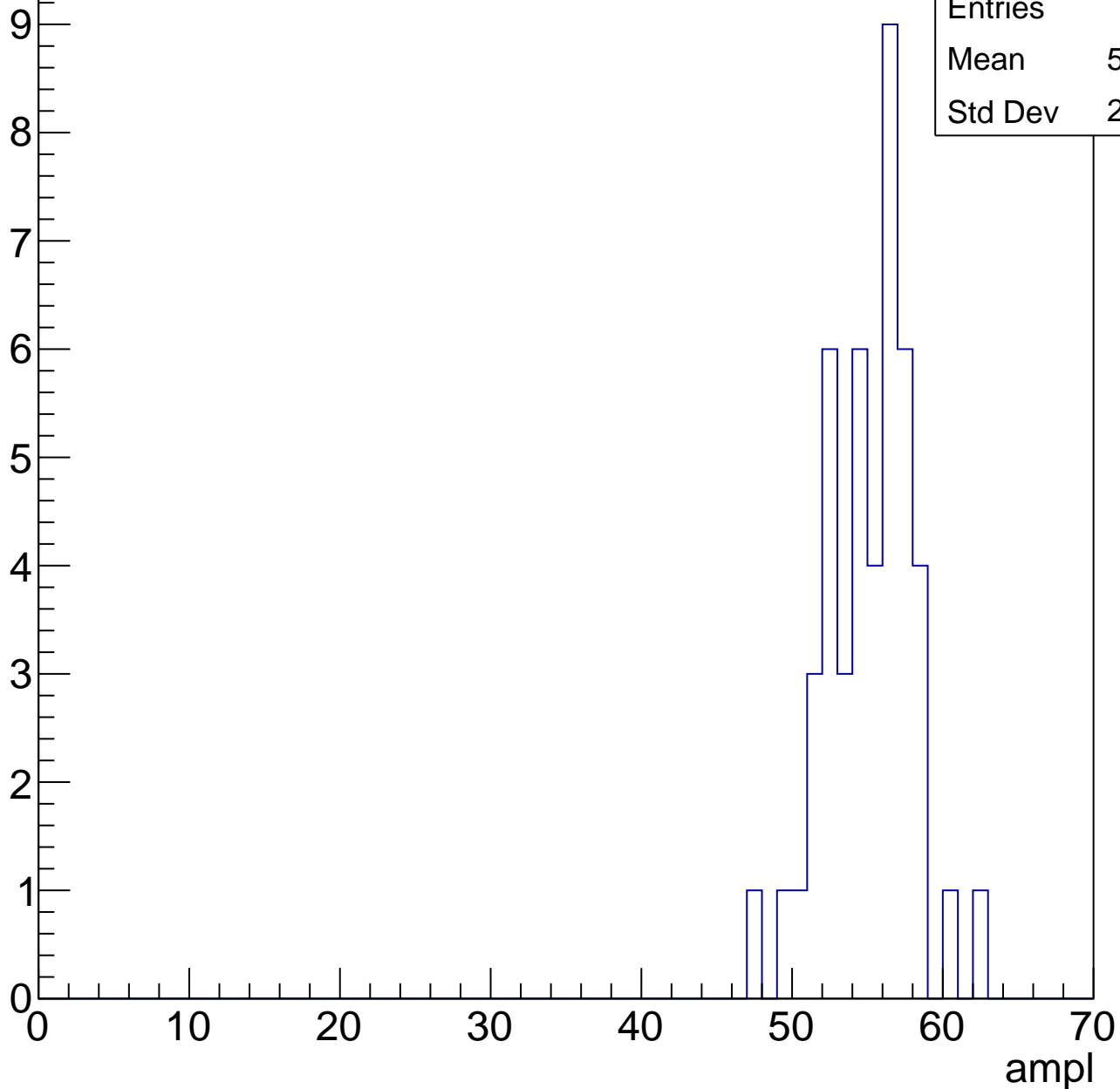
Entries	58
Mean	49.97
Std Dev	3.419



B1L100S, U19-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

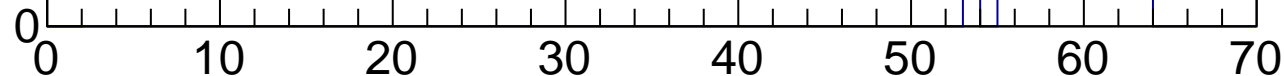
57

Mean

59.82

Std Dev

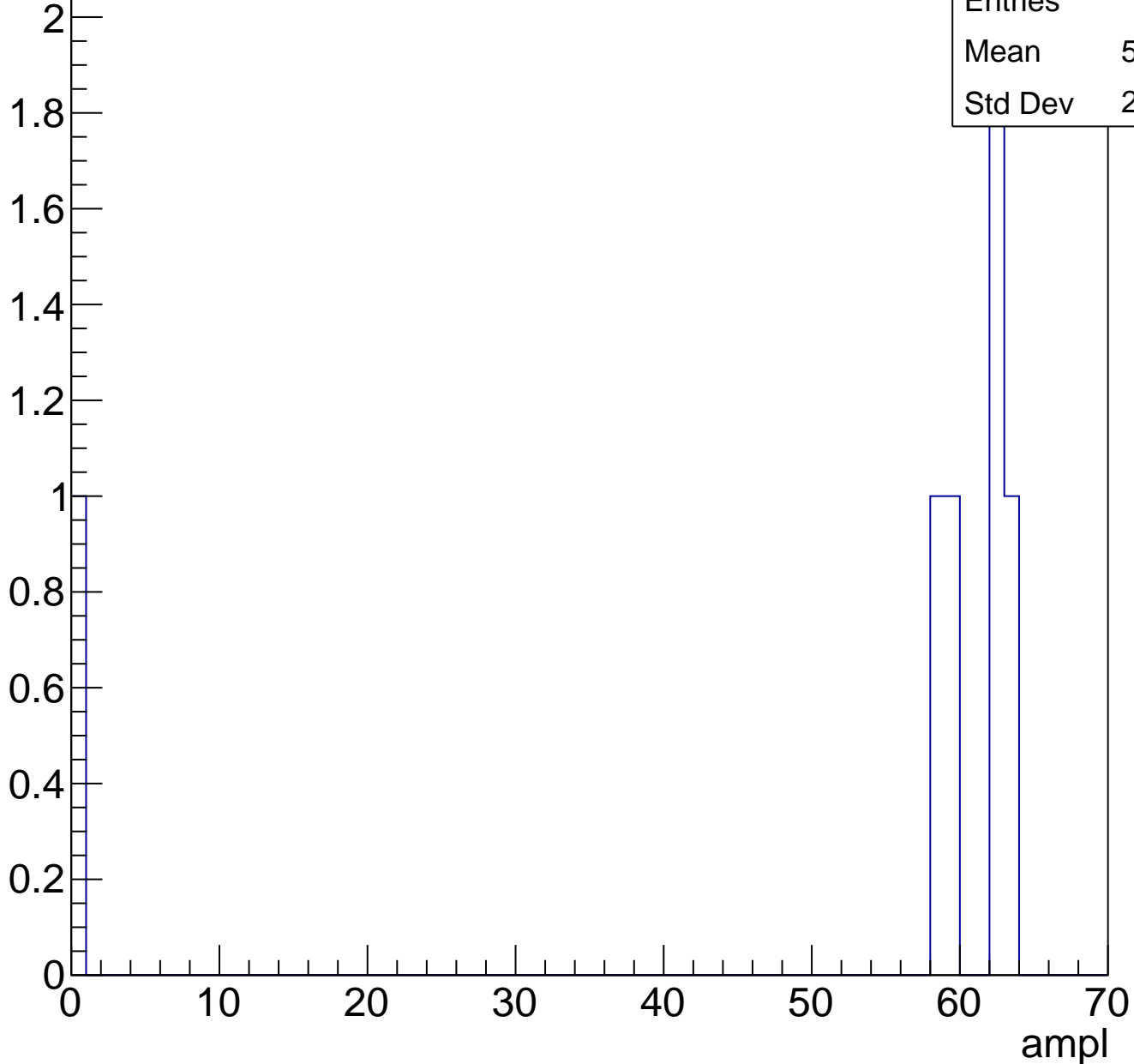
2.507



B1L100S, U19-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch97, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	29.85
Std Dev	3.538

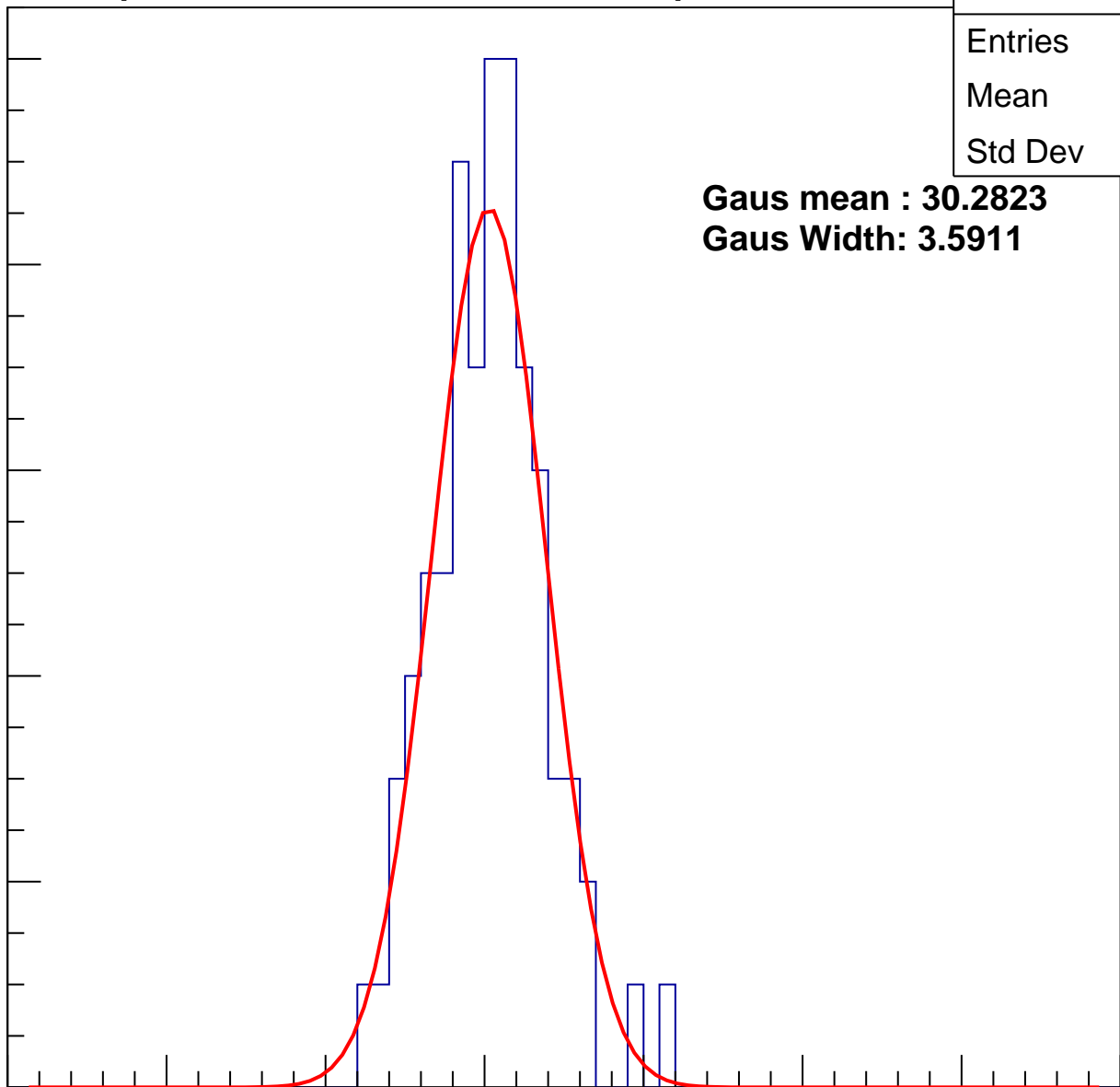
Gaus mean : 30.2823
Gaus Width: 3.5911

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	36.91
Std Dev	3.18

Gaus mean : 37.0191

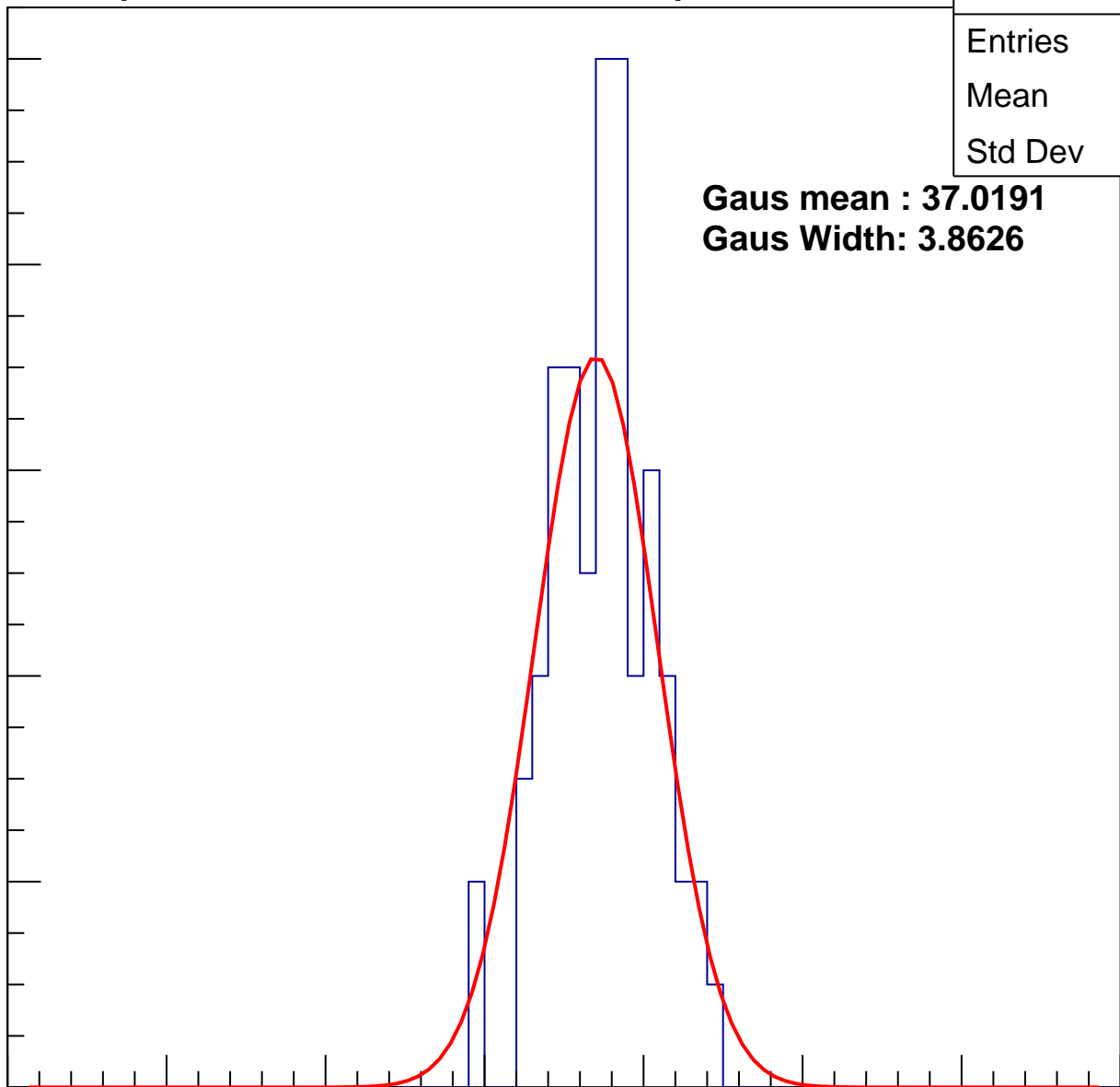
Gaus Width: 3.8626

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch97, adc2

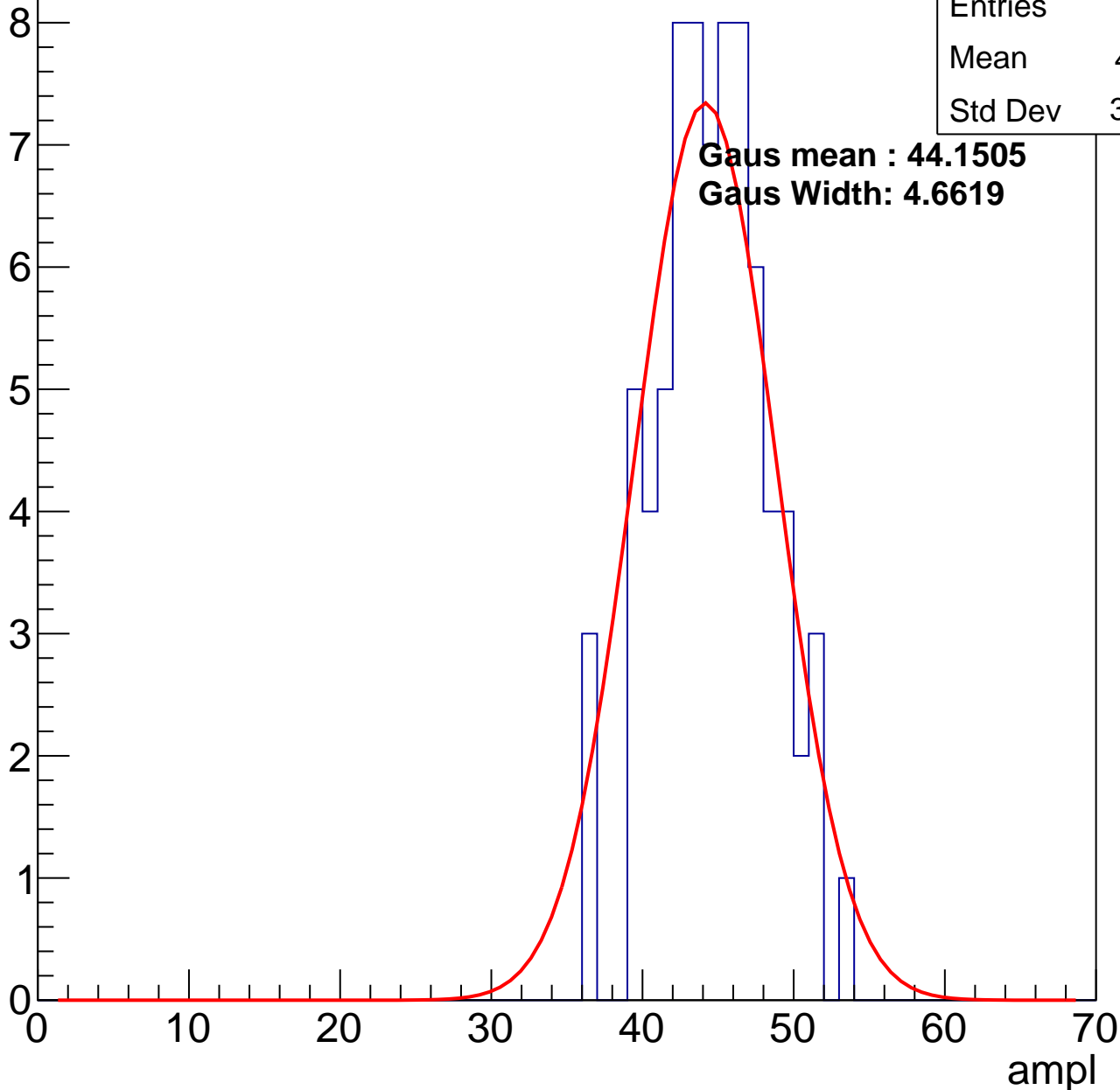
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	44.21
Std Dev	3.665

Gaus mean : 44.1505

Gaus Width: 4.6619

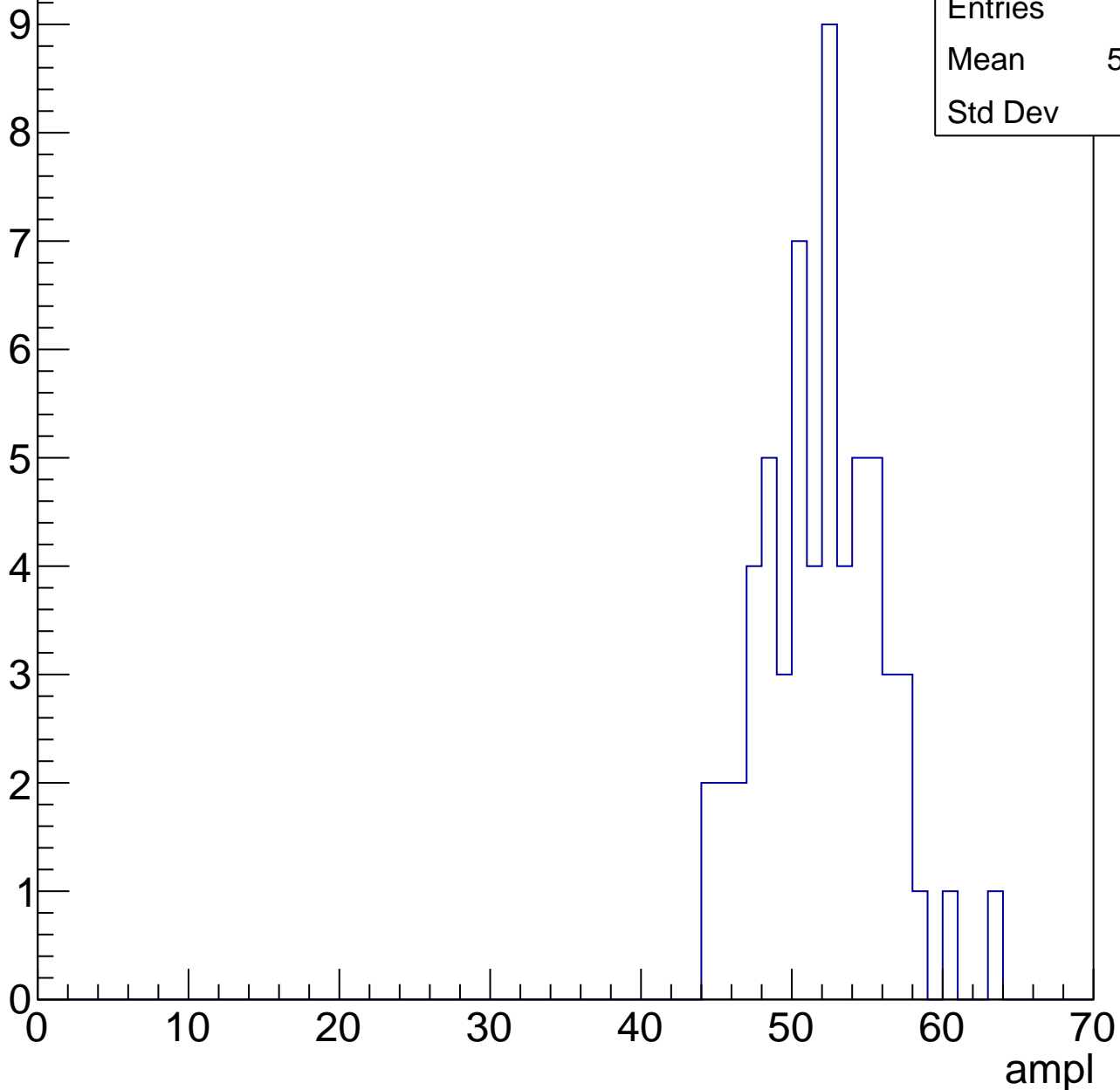


B1L100S, U19-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	51.54
Std Dev	3.92

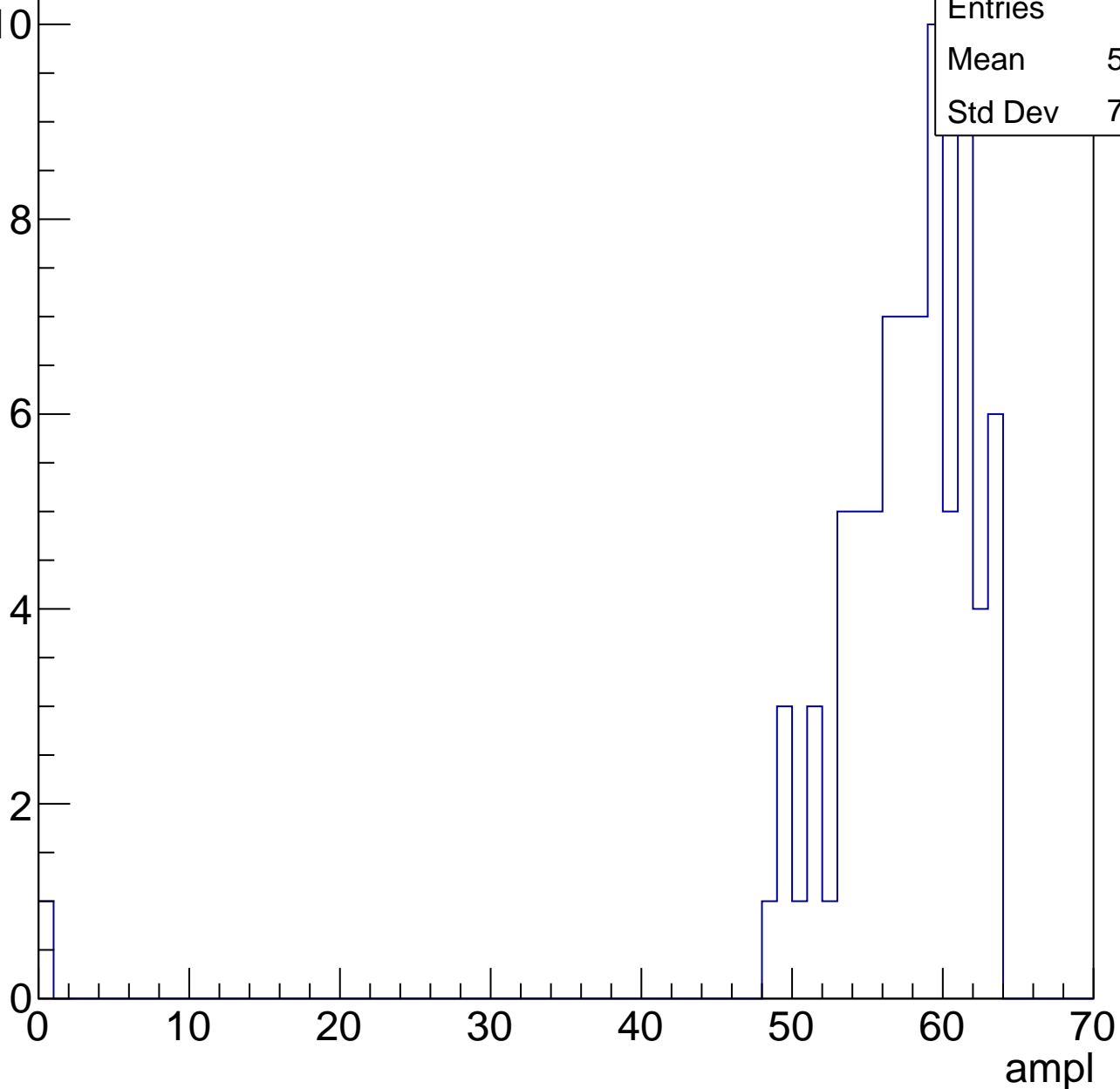


B1L100S, U19-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	56.52
Std Dev	7.408

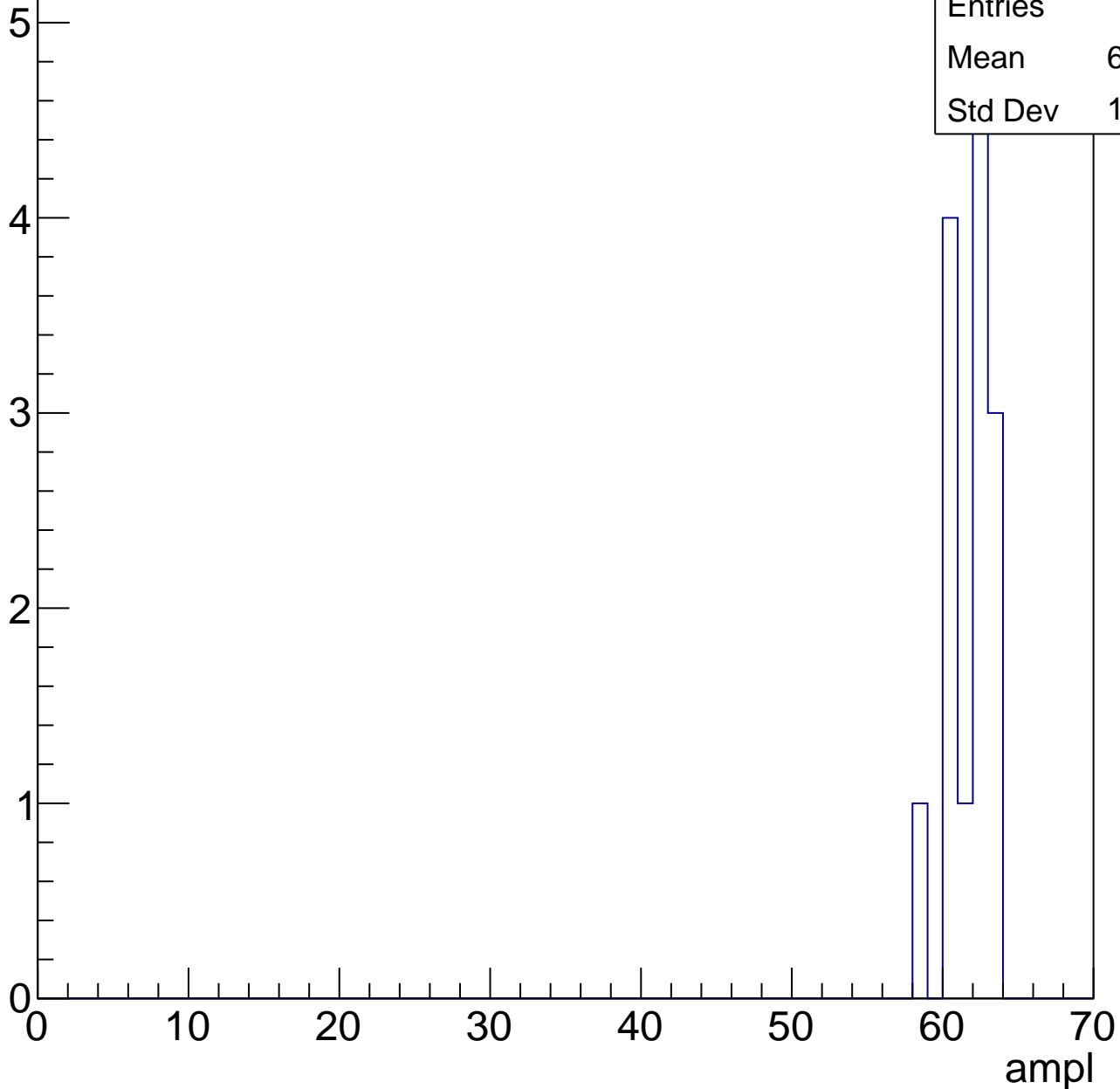


B1L100S, U19-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

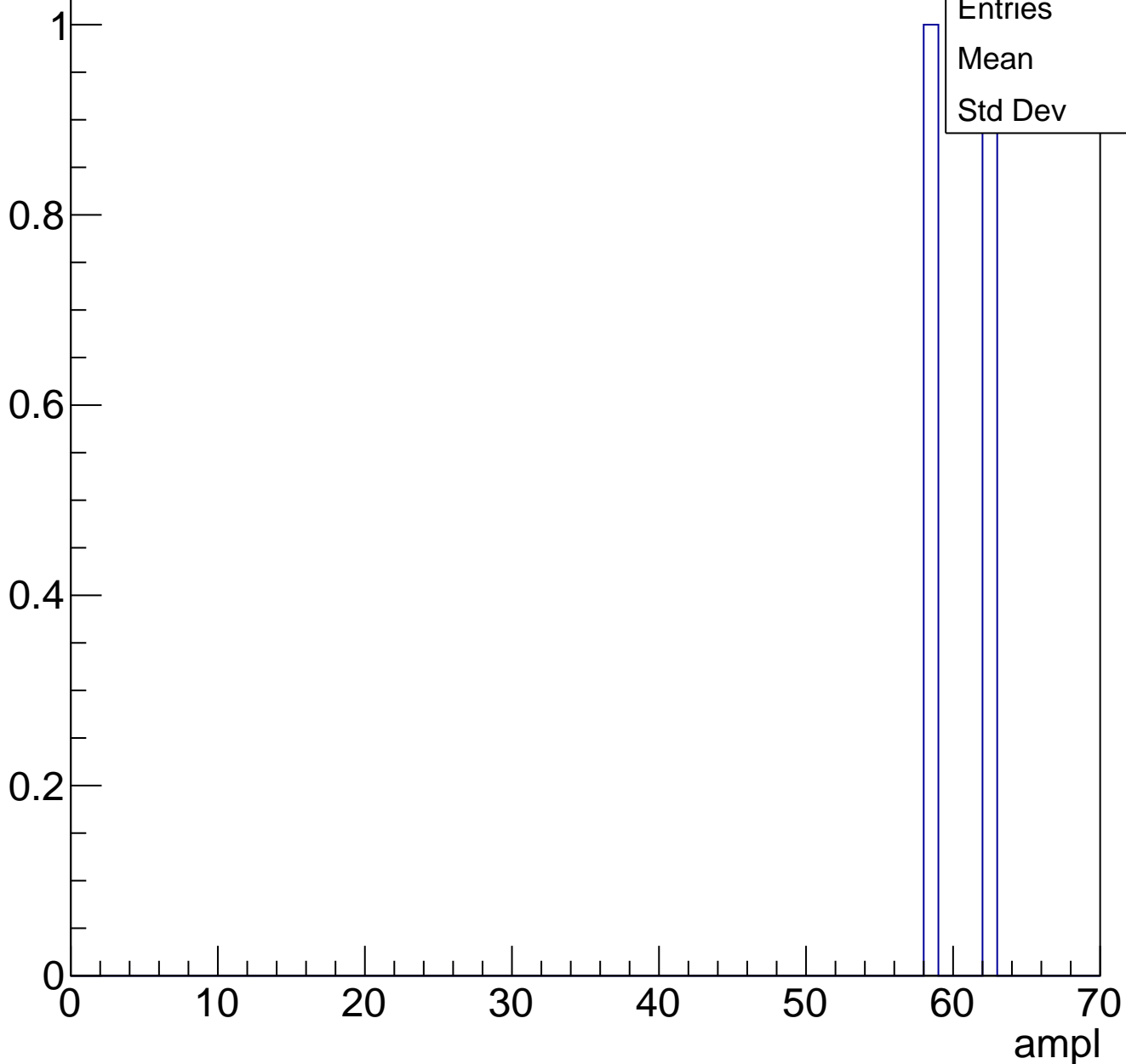
Entries	14
Mean	61.29
Std Dev	1.436



B1L100S, U19-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch98, adc0

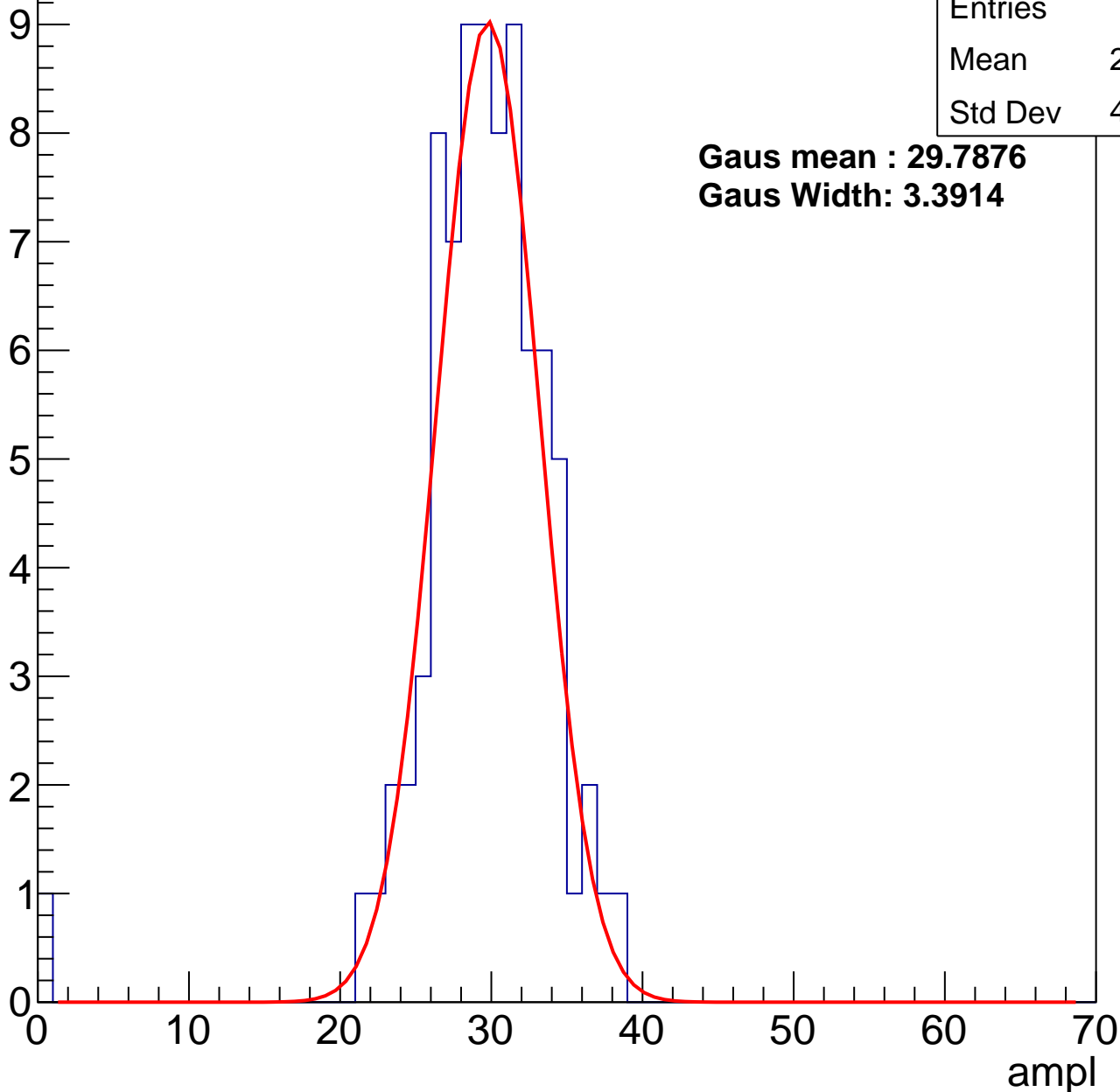
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	29.06
Std Dev	4.725

Gaus mean : 29.7876

Gaus Width: 3.3914



B1L100S, U19-ch98, adc1

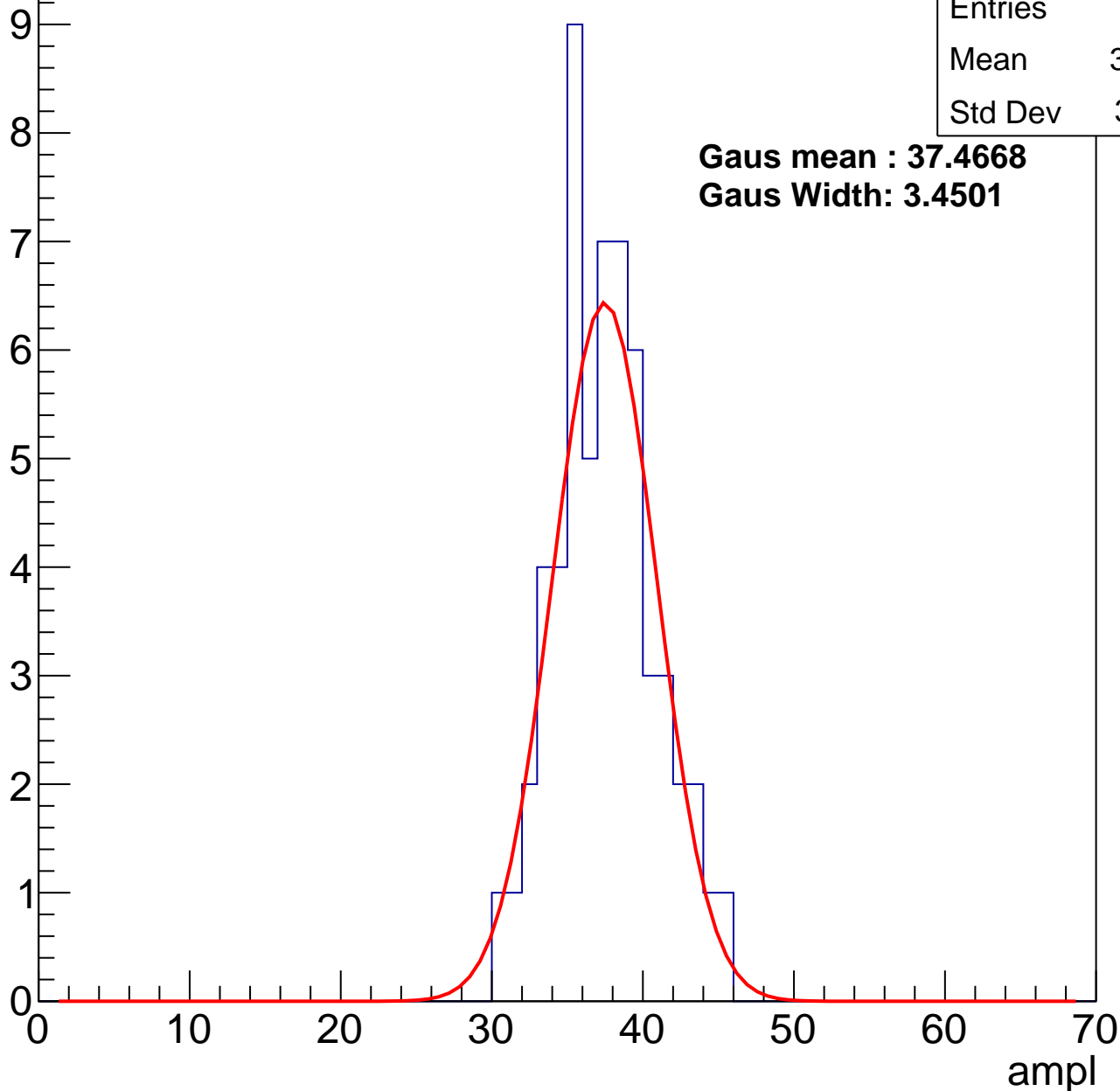
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	37.05
Std Dev	3.261

Gaus mean : 37.4668

Gaus Width: 3.4501



B1L100S, U19-ch98, adc2

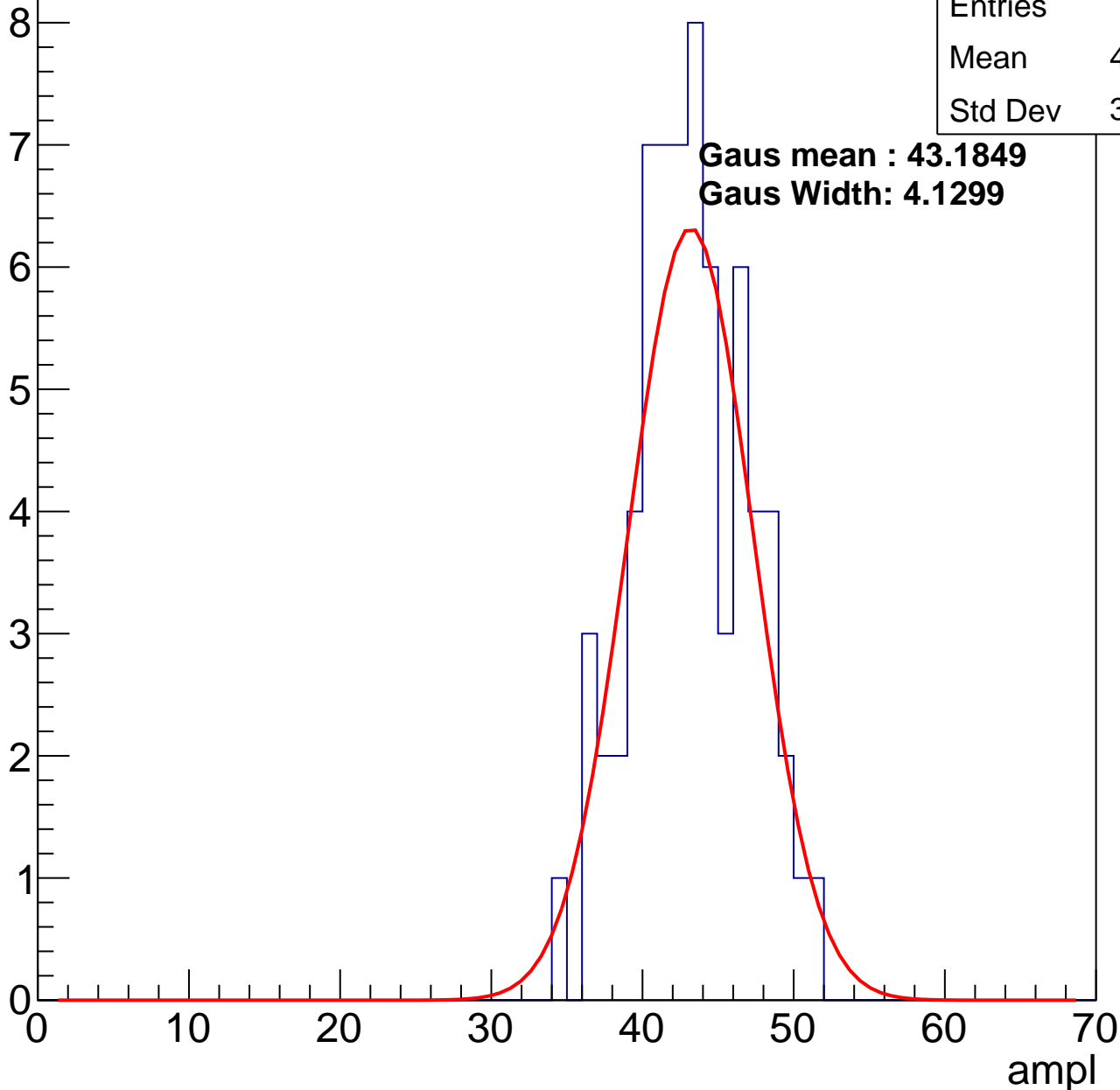
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.75
Std Dev	3.696

Gaus mean : 43.1849

Gaus Width: 4.1299

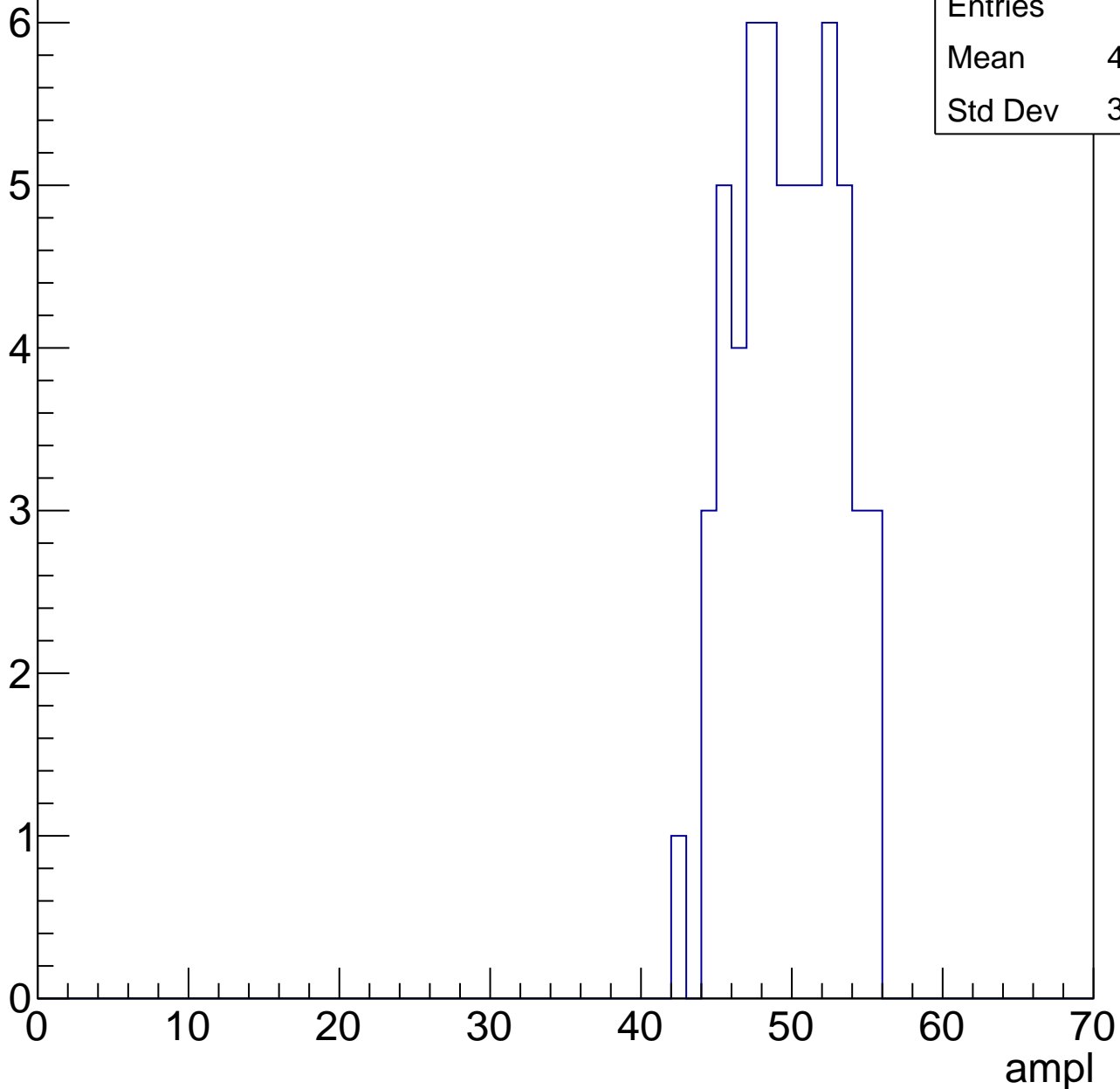


B1L100S, U19-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	49.25
Std Dev	3.267

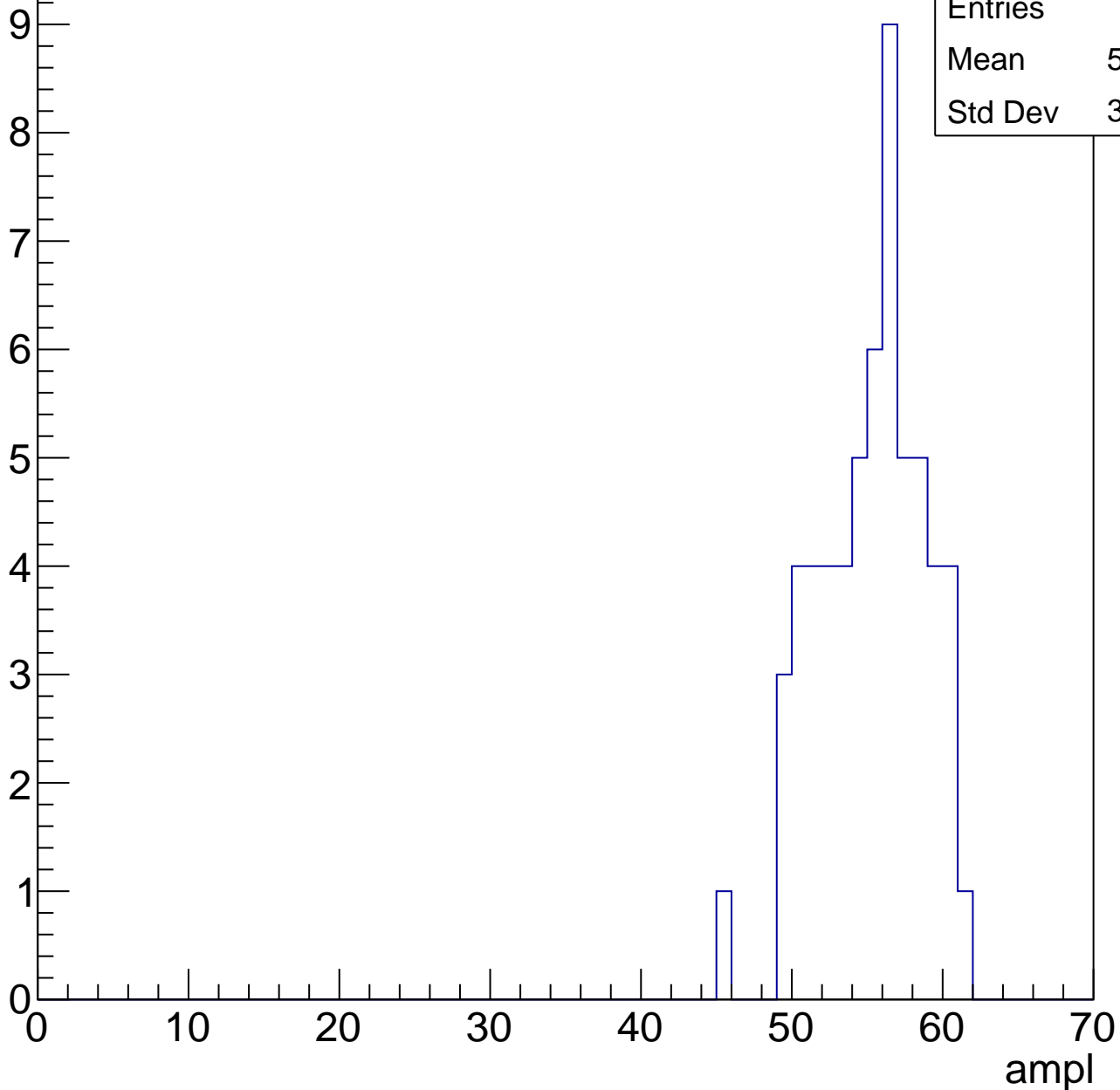


B1L100S, U19-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	54.78
Std Dev	3.445



B1L100S, U19-ch98, adc5

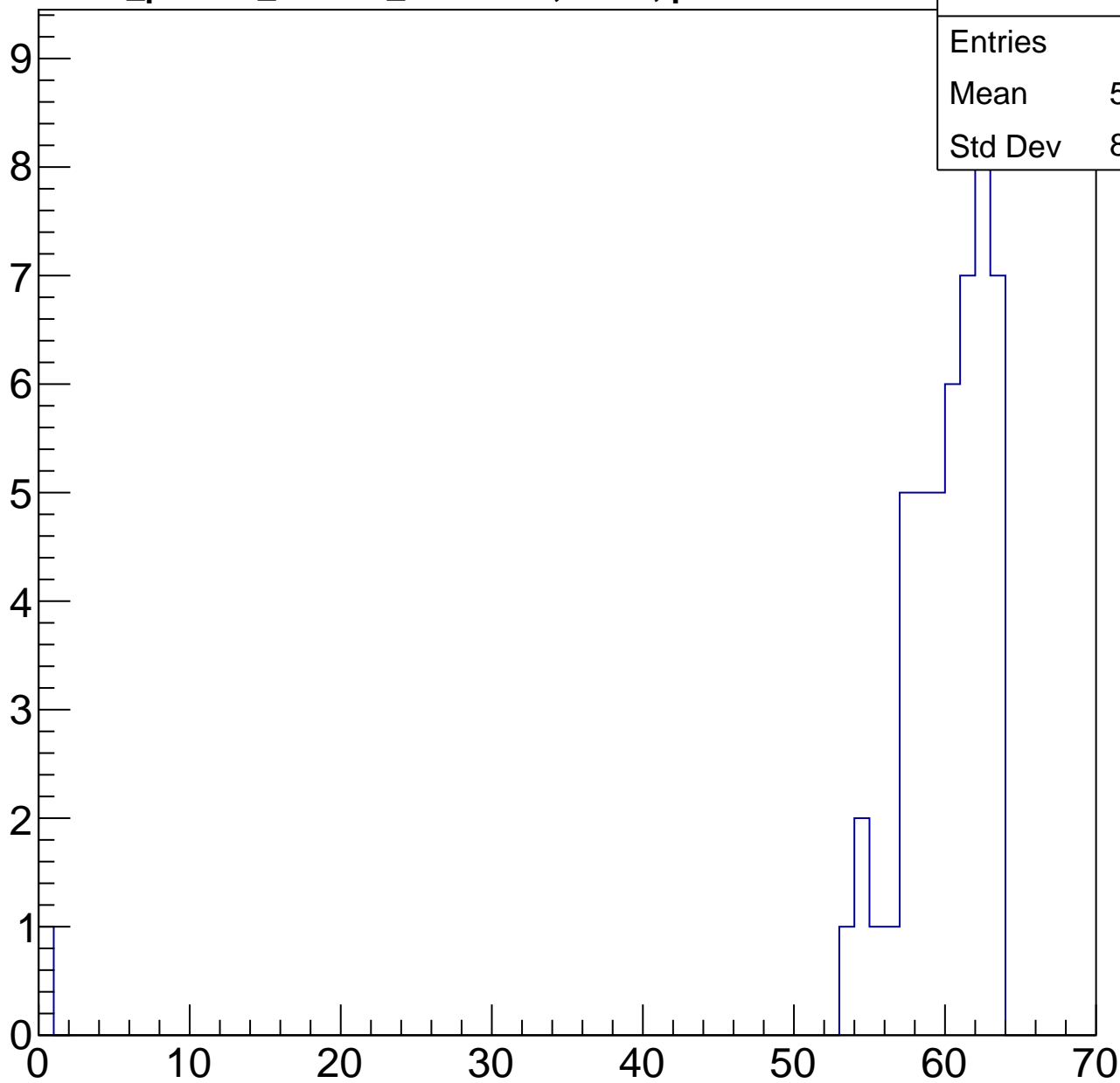
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.56
Std Dev	8.757

ampl



B1L100S, U19-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

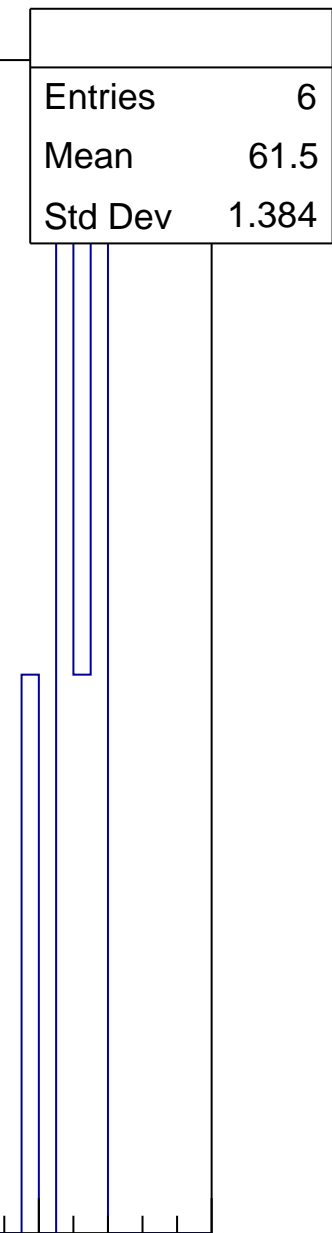
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.384

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch99, adc0

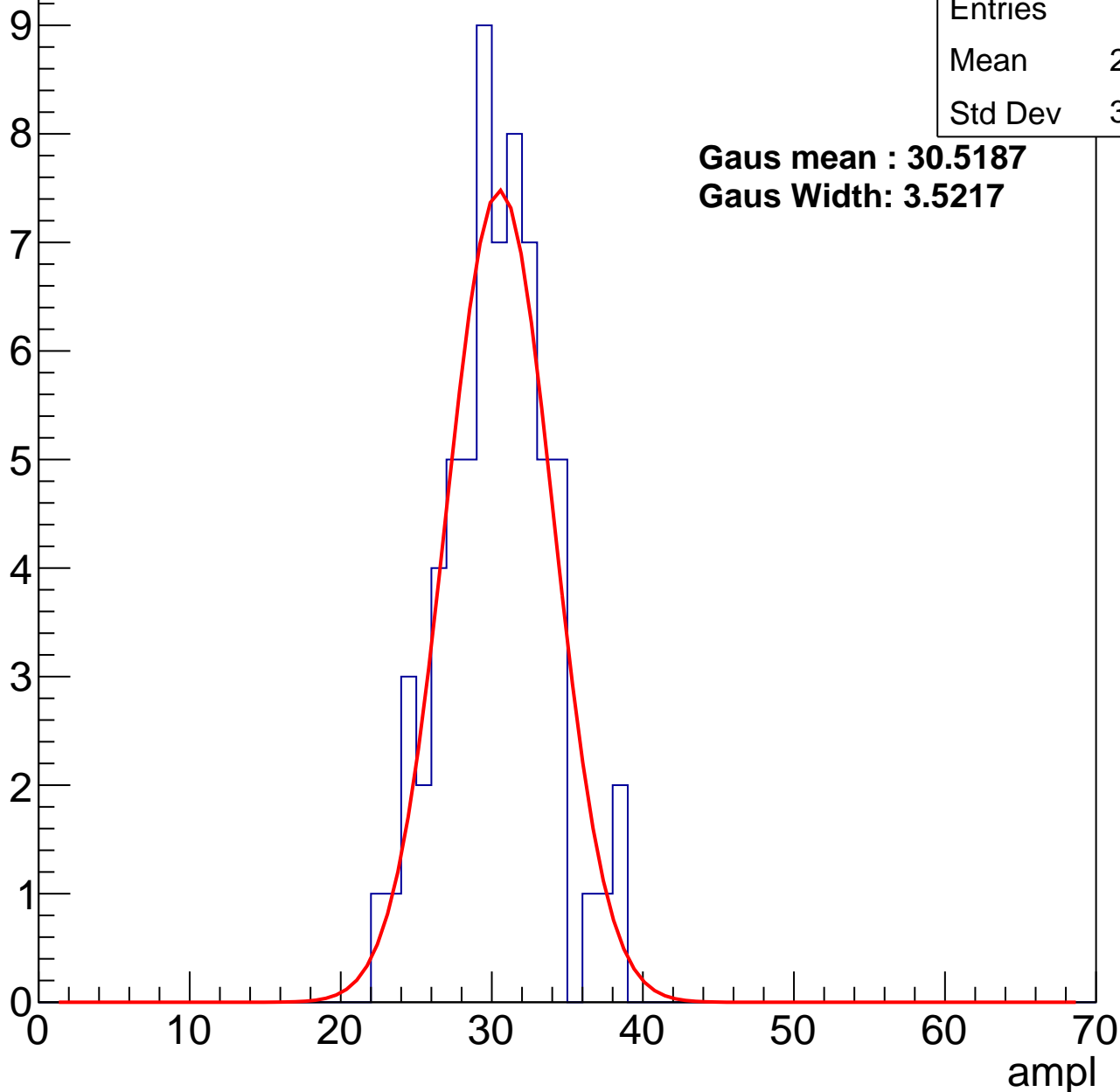
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.89
Std Dev	3.443

Gaus mean : 30.5187

Gaus Width: 3.5217



B1L100S, U19-ch99, adc1

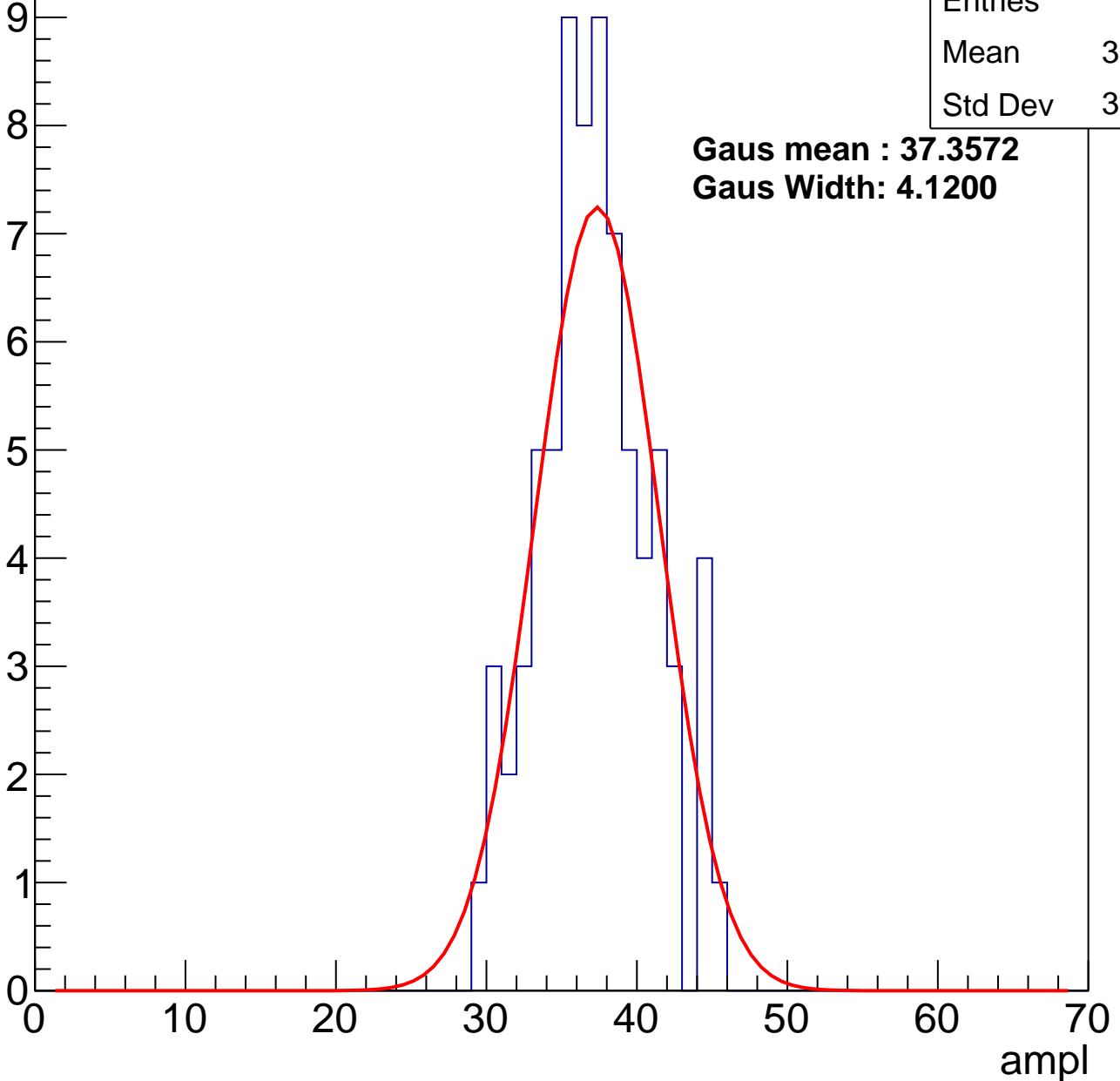
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.77
Std Dev	3.678

Gaus mean : 37.3572

Gaus Width: 4.1200



B1L100S, U19-ch99, adc2

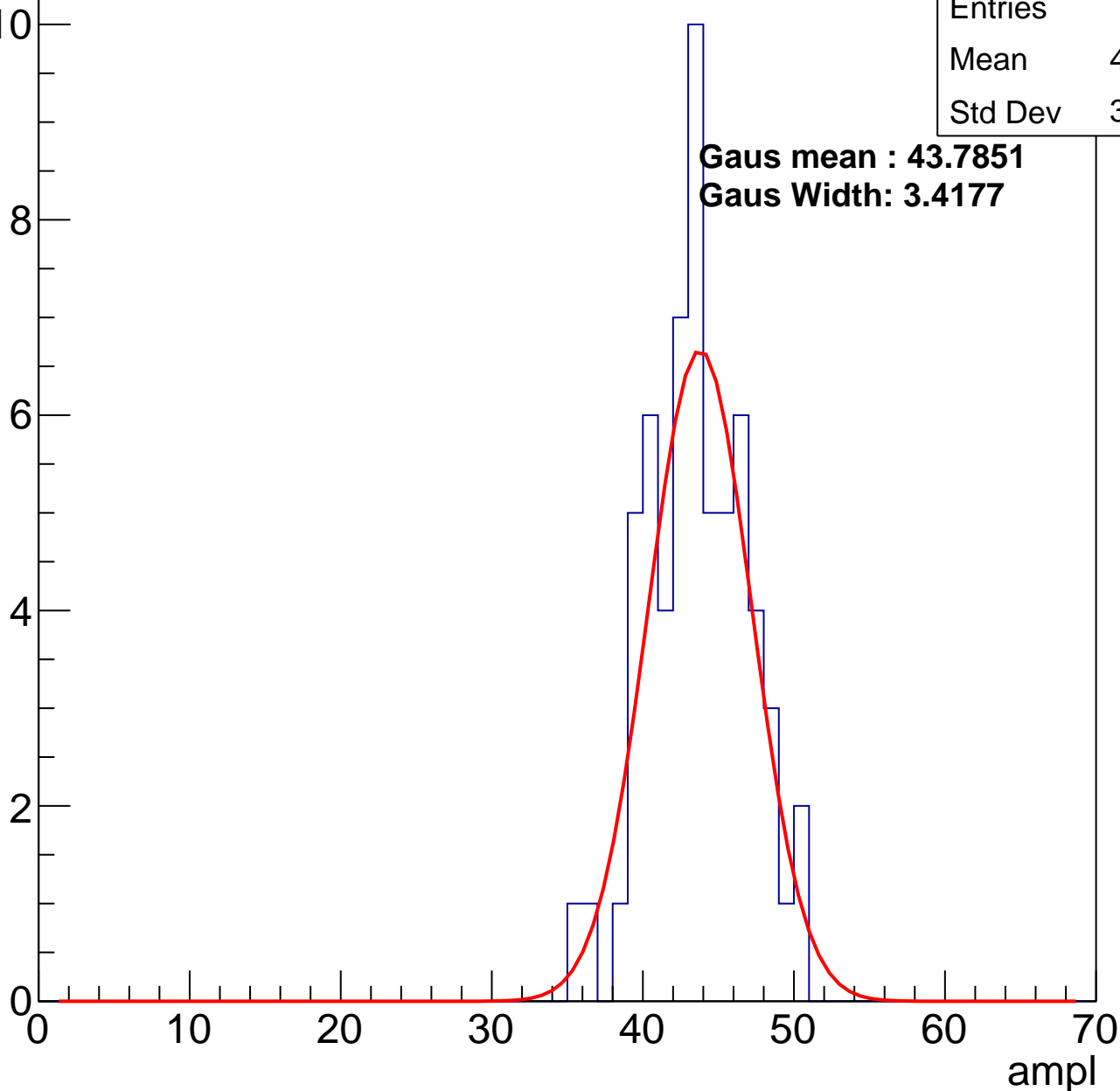
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.18
Std Dev	3.252

Gaus mean : 43.7851

Gaus Width: 3.4177

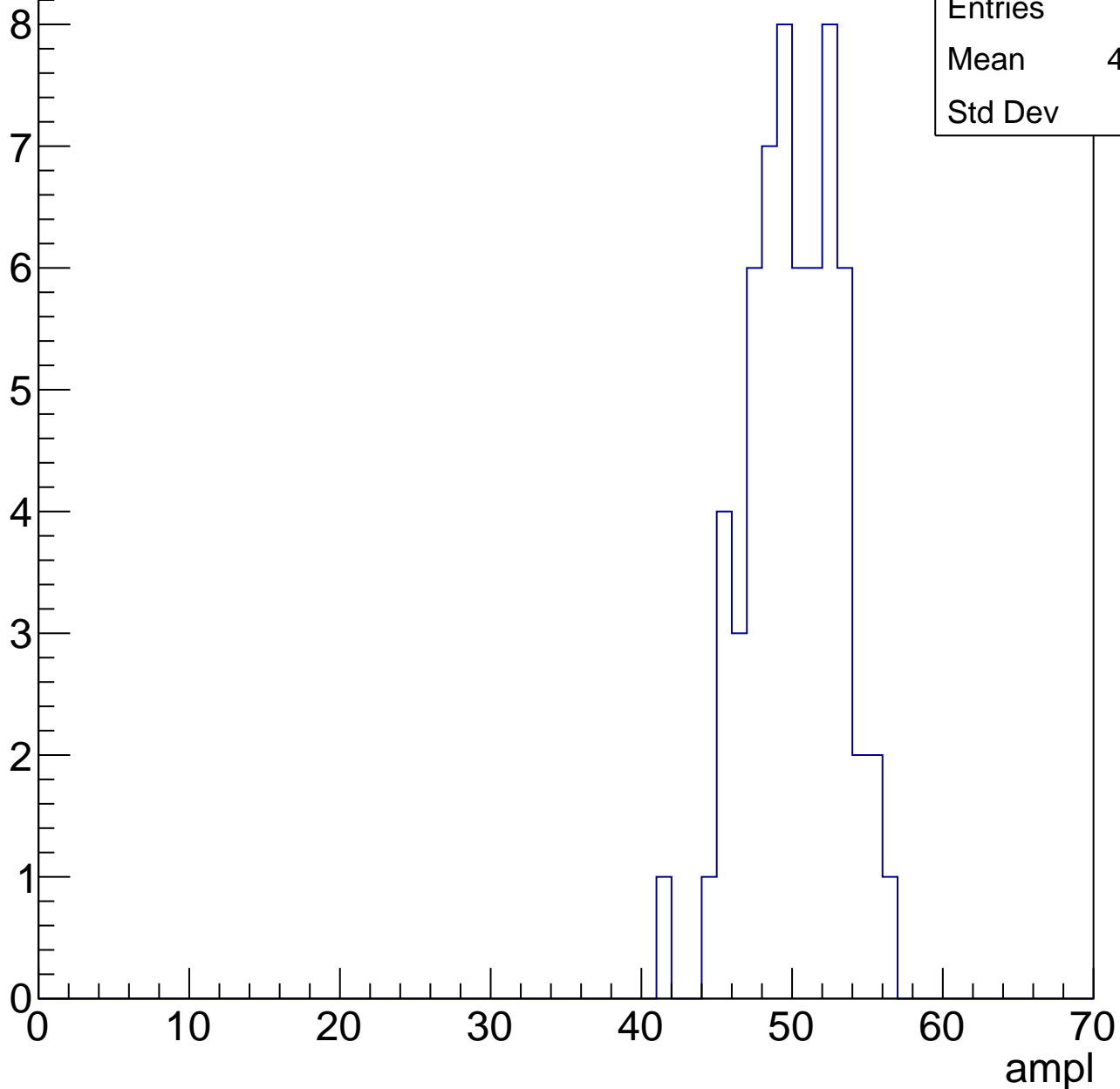


B1L100S, U19-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.62
Std Dev	3.02

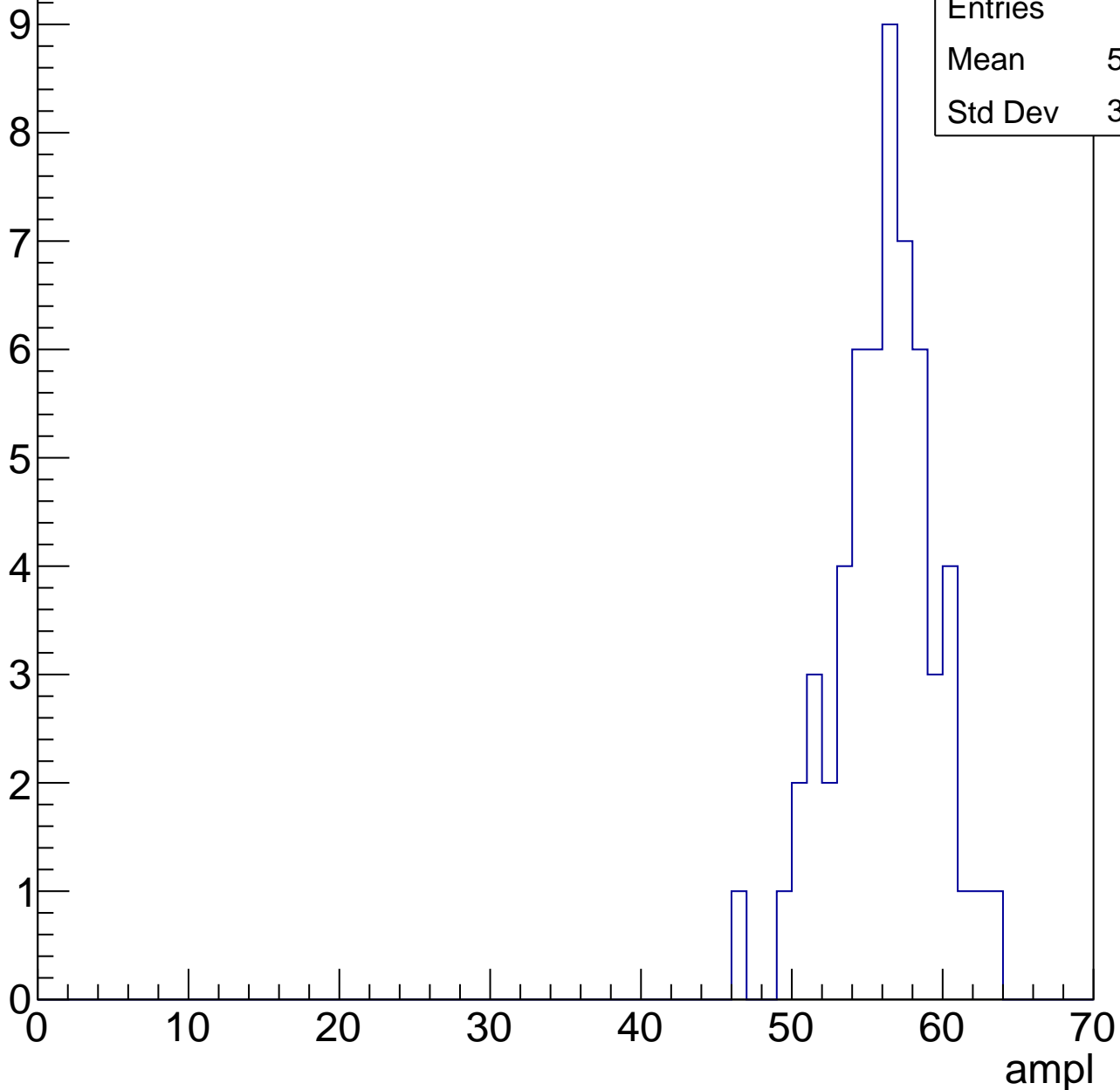


B1L100S, U19-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.65
Std Dev	3.295

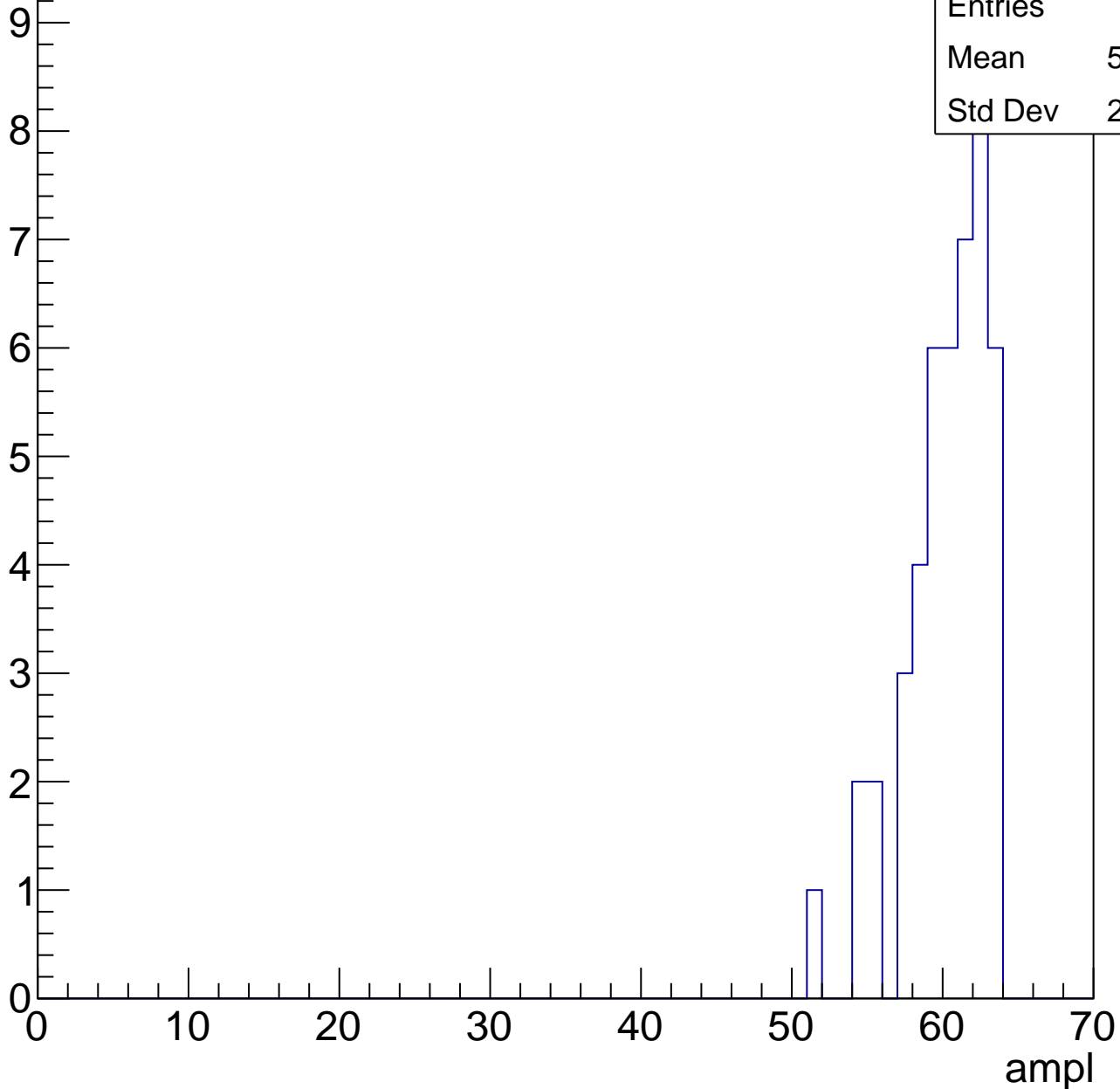


B1L100S, U19-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

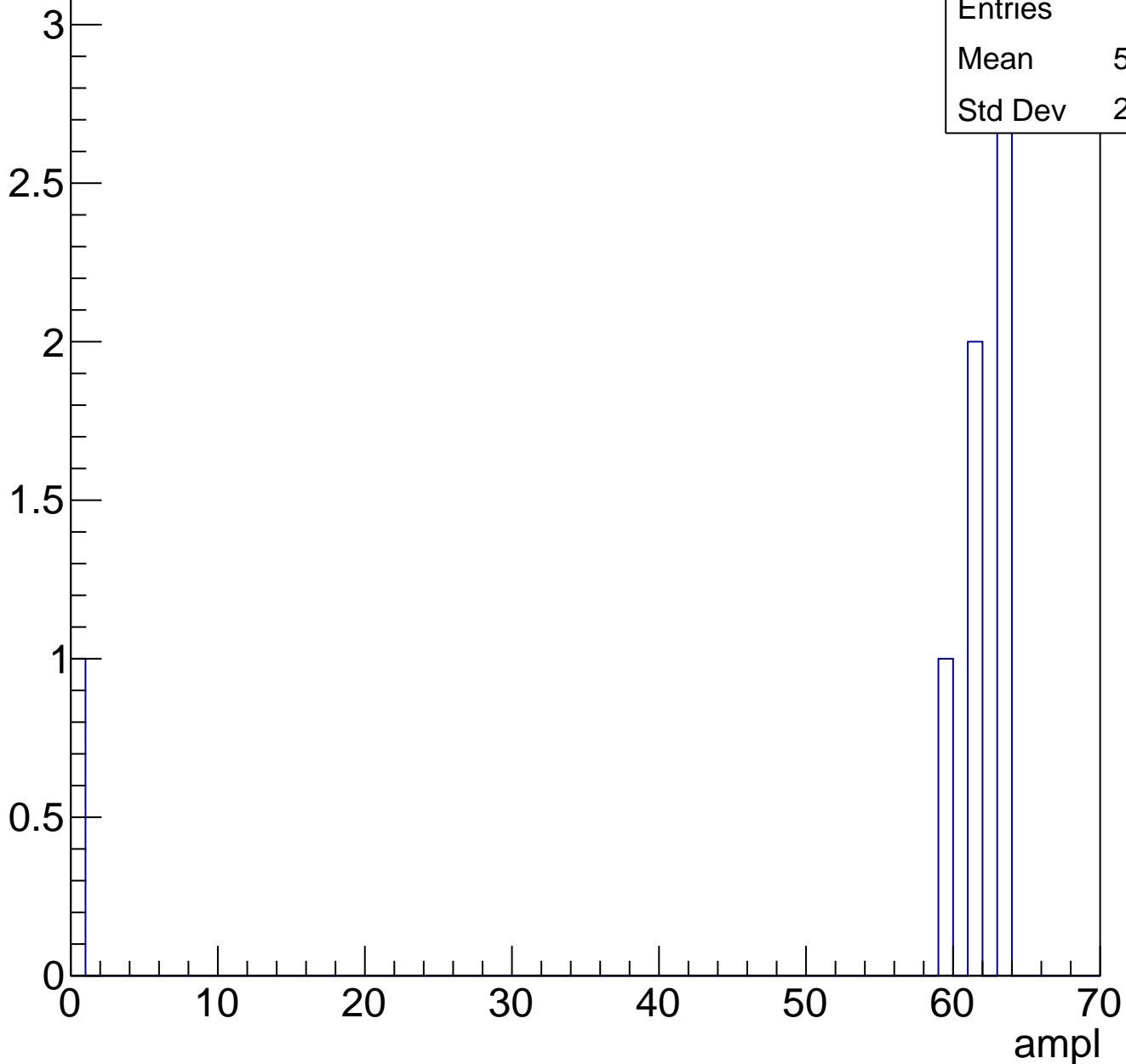
Entries	46
Mean	59.76
Std Dev	2.744



B1L100S, U19-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch100, adc0

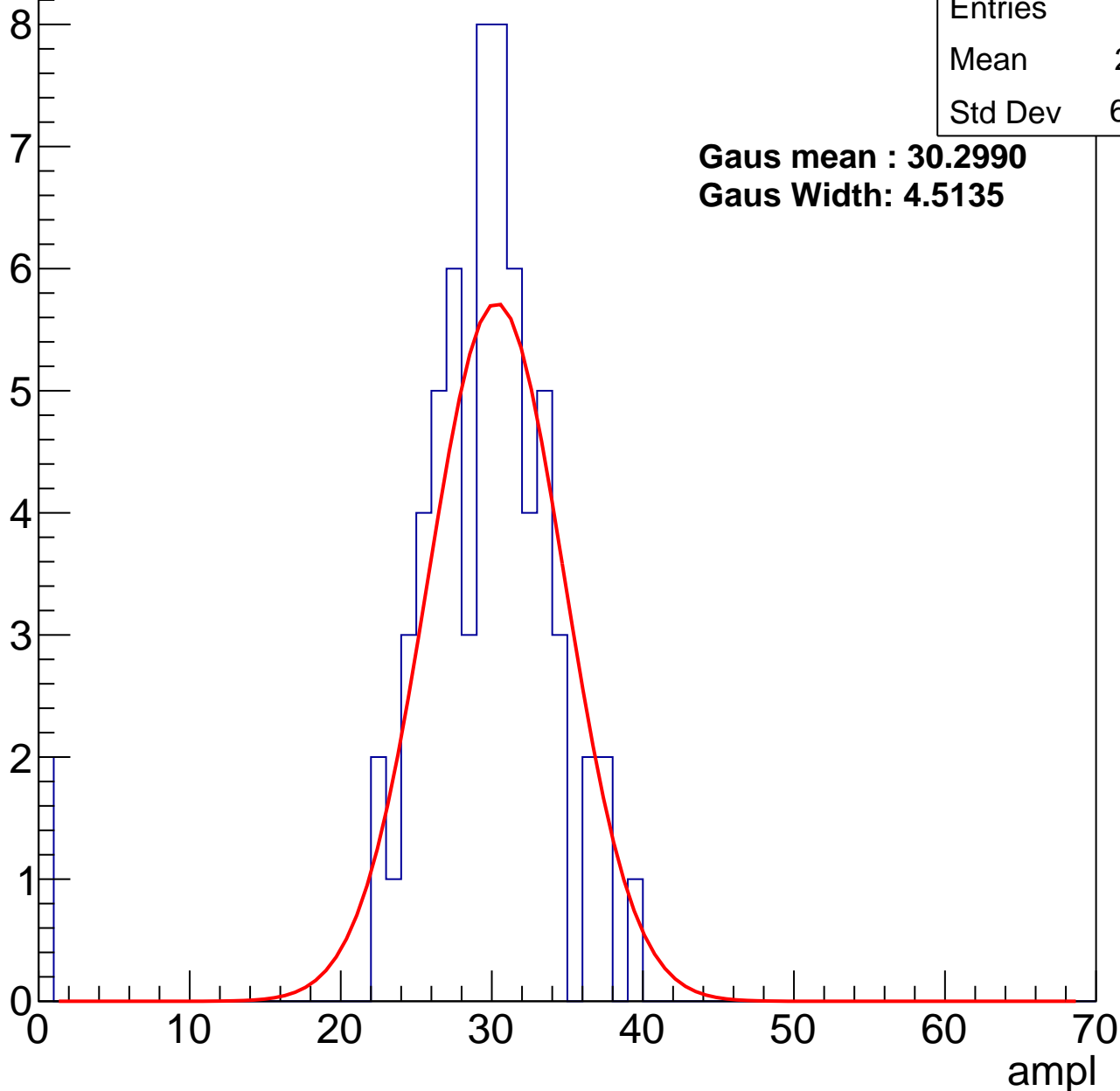
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.51
Std Dev	6.276

Gaus mean : 30.2990

Gaus Width: 4.5135



B1L100S, U19-ch100, adc1

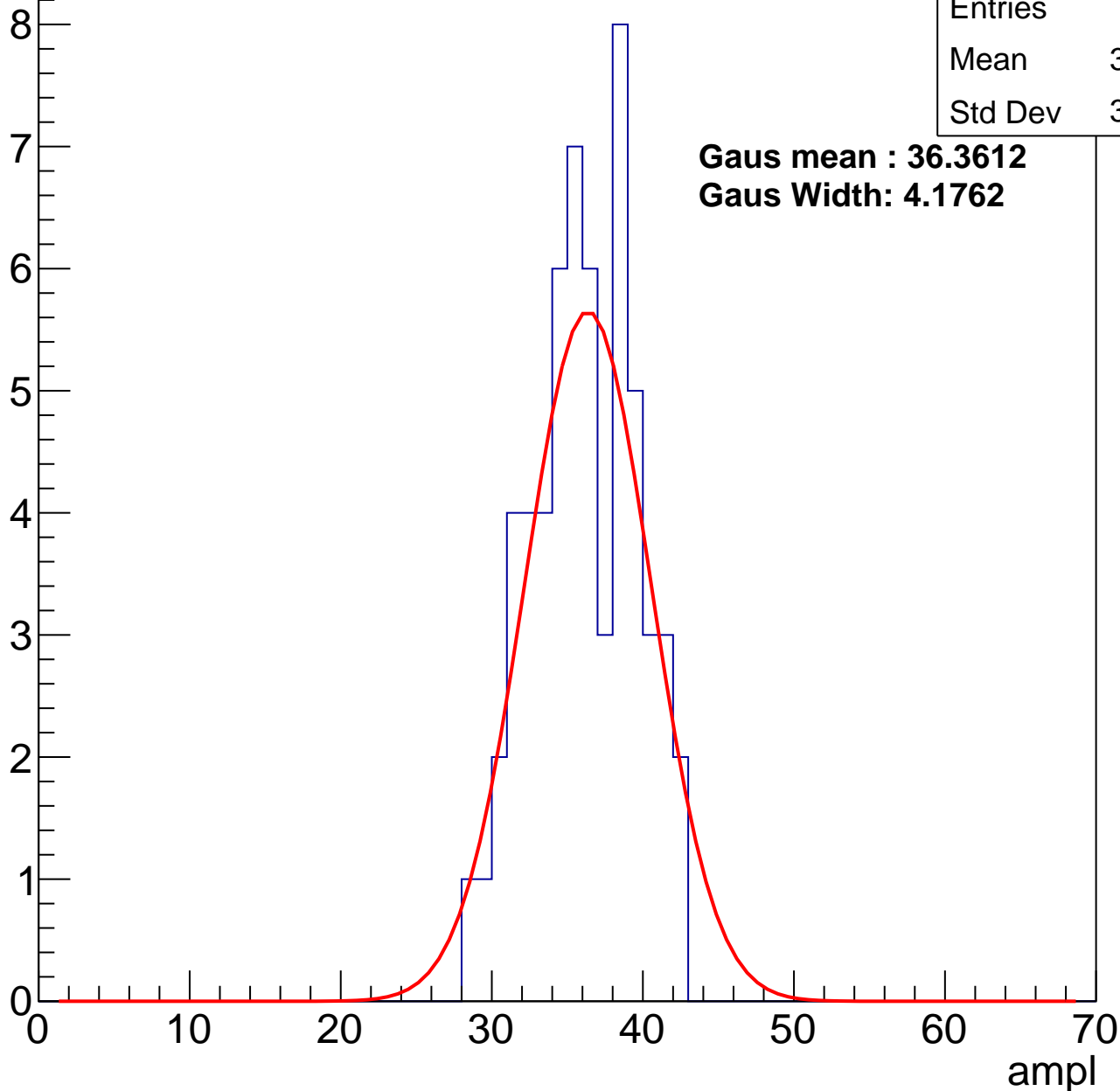
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.64
Std Dev	3.404

Gaus mean : 36.3612

Gaus Width: 4.1762



B1L100S, U19-ch100, adc2

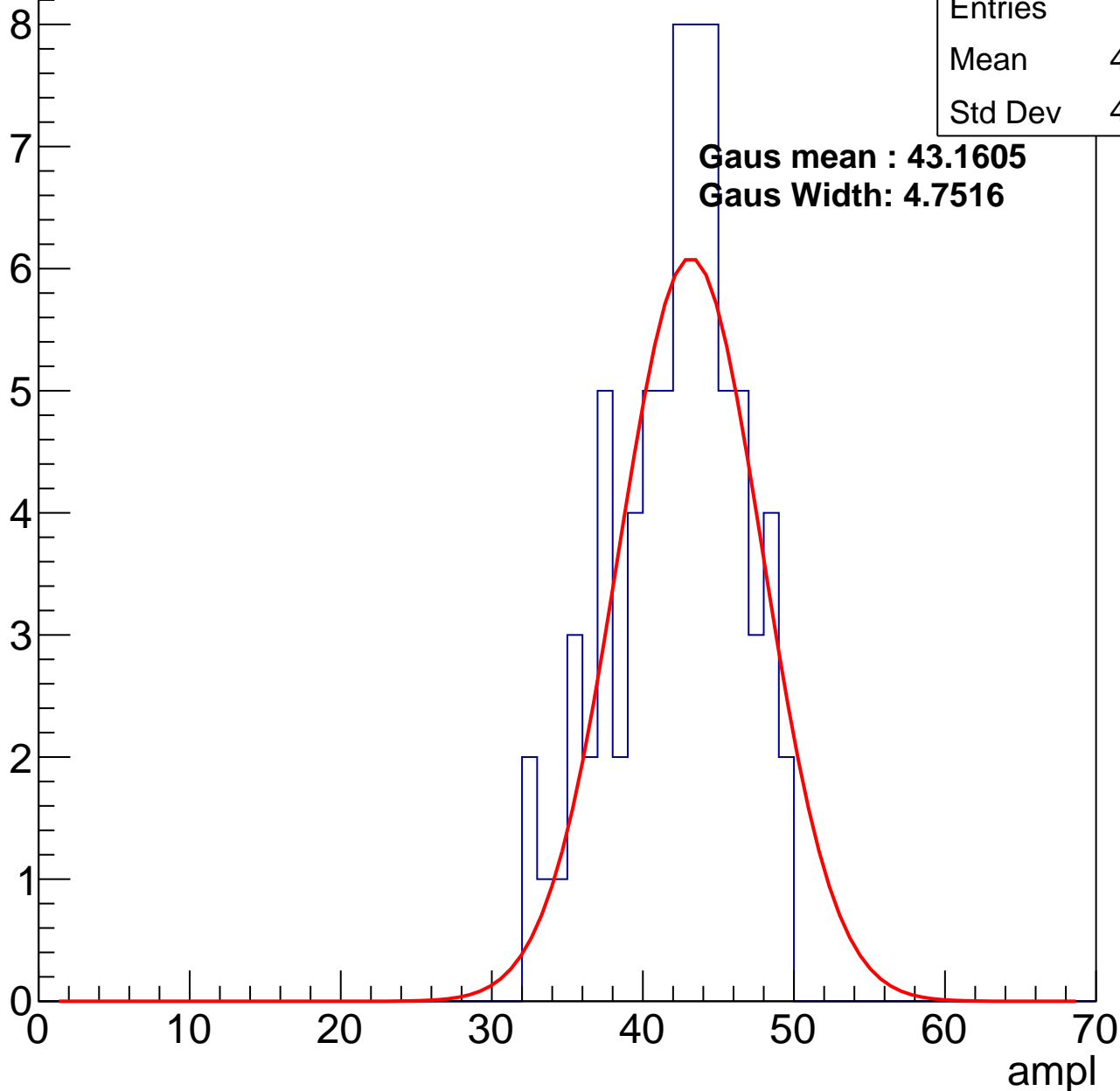
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	41.75
Std Dev	4.154

Gaus mean : 43.1605

Gaus Width: 4.7516

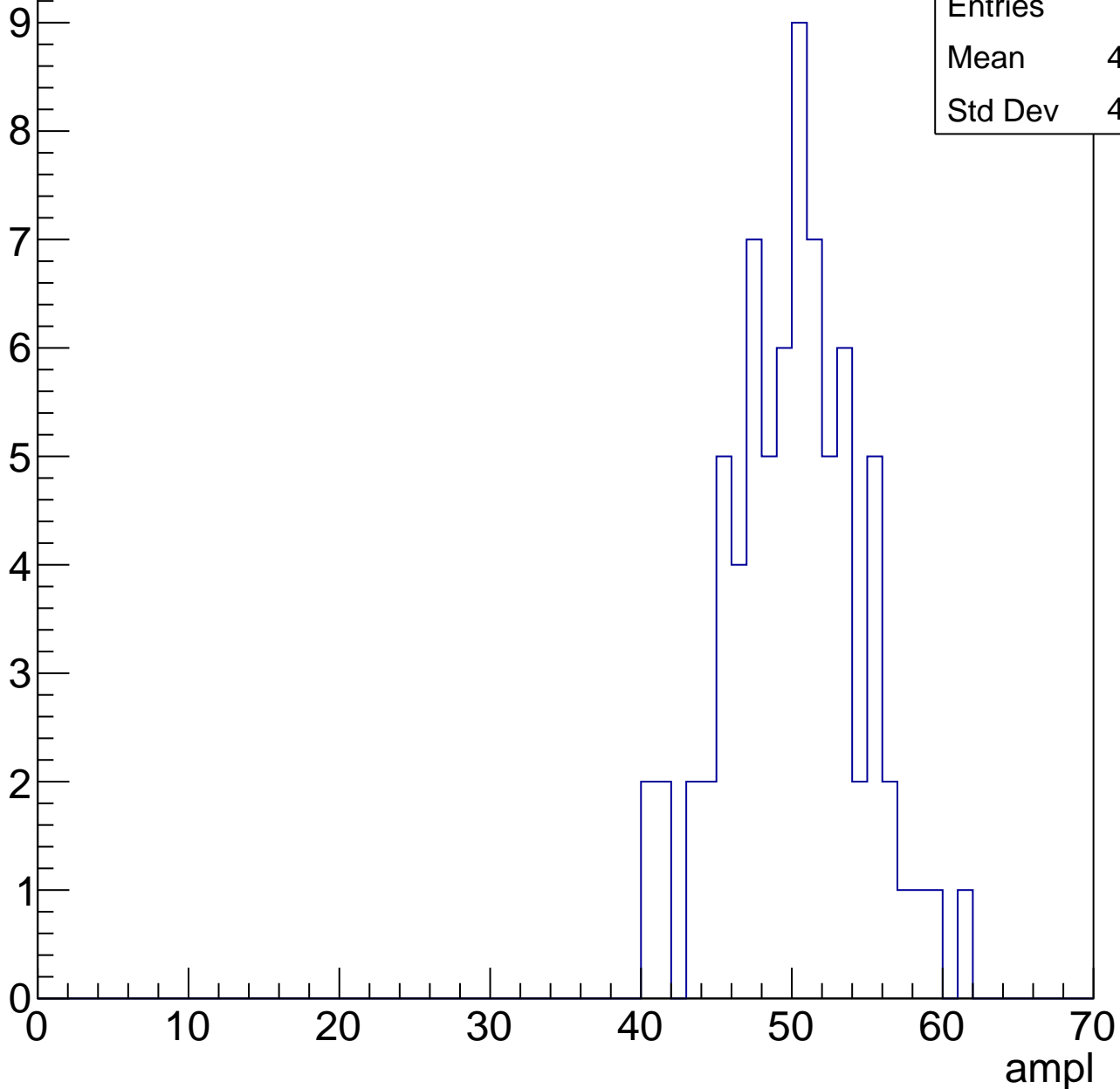


B1L100S, U19-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	49.64
Std Dev	4.359

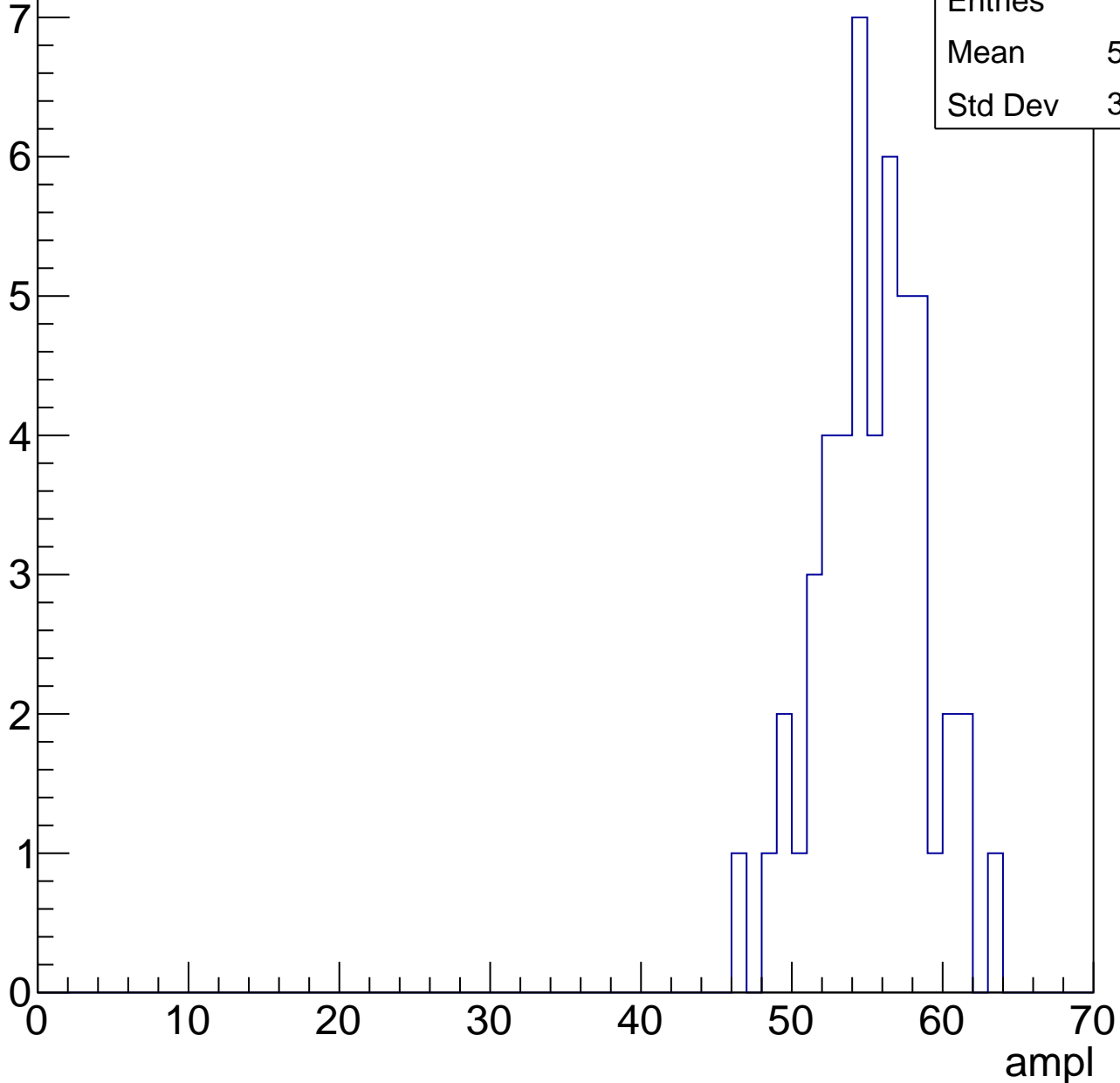


B1L100S, U19-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	54.86
Std Dev	3.517

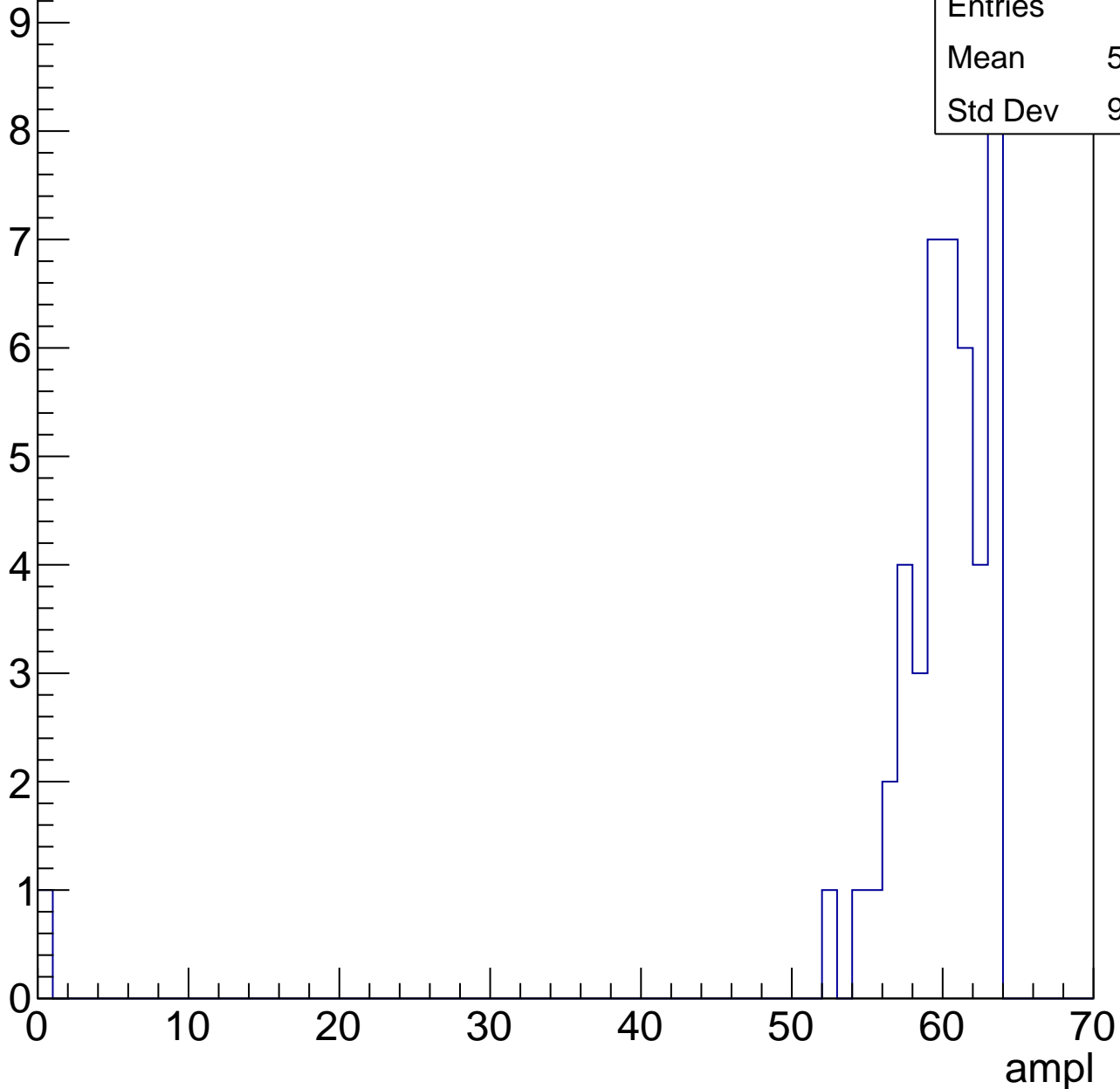


B1L100S, U19-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.46
Std Dev	9.095

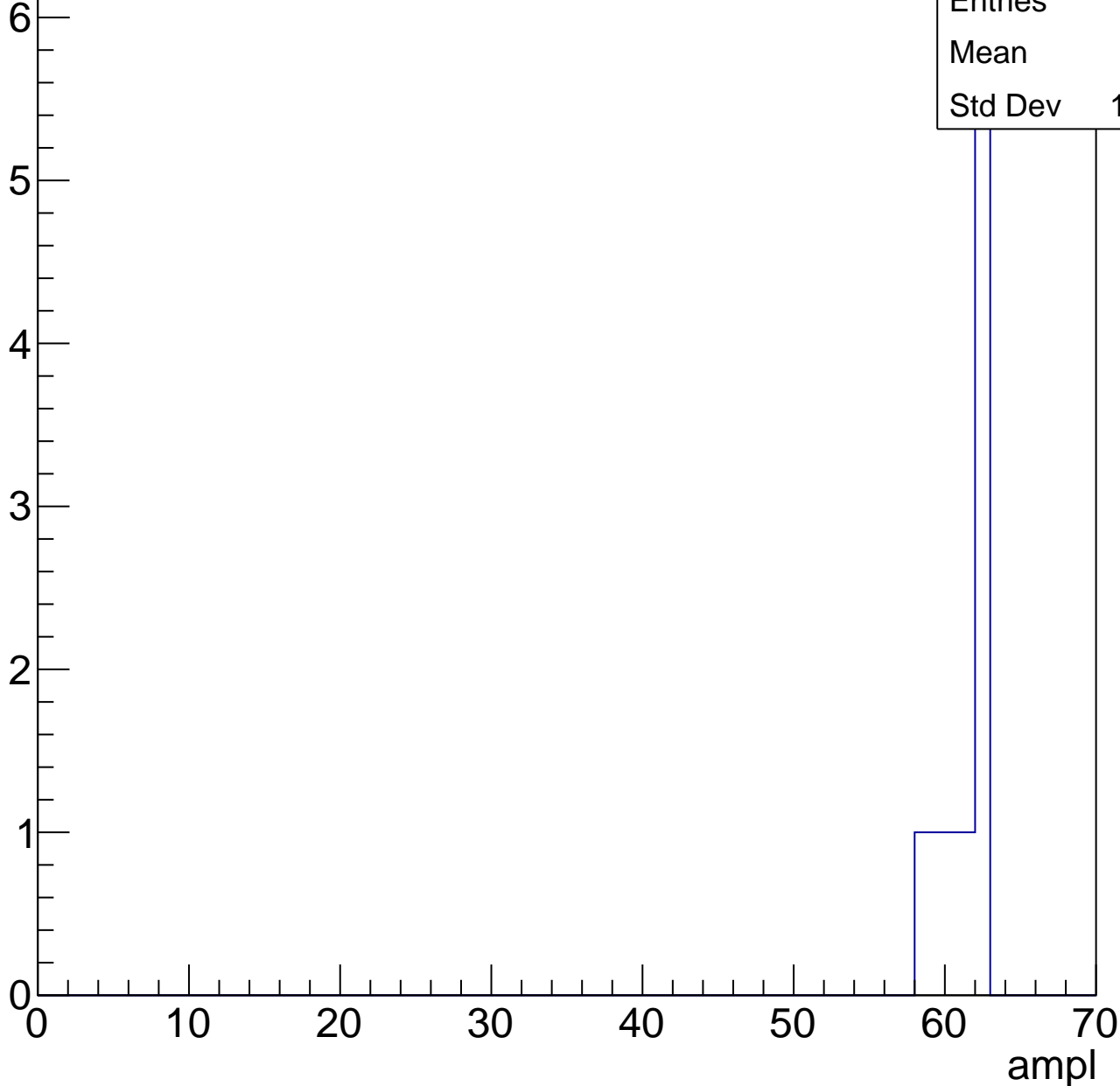


B1L100S, U19-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

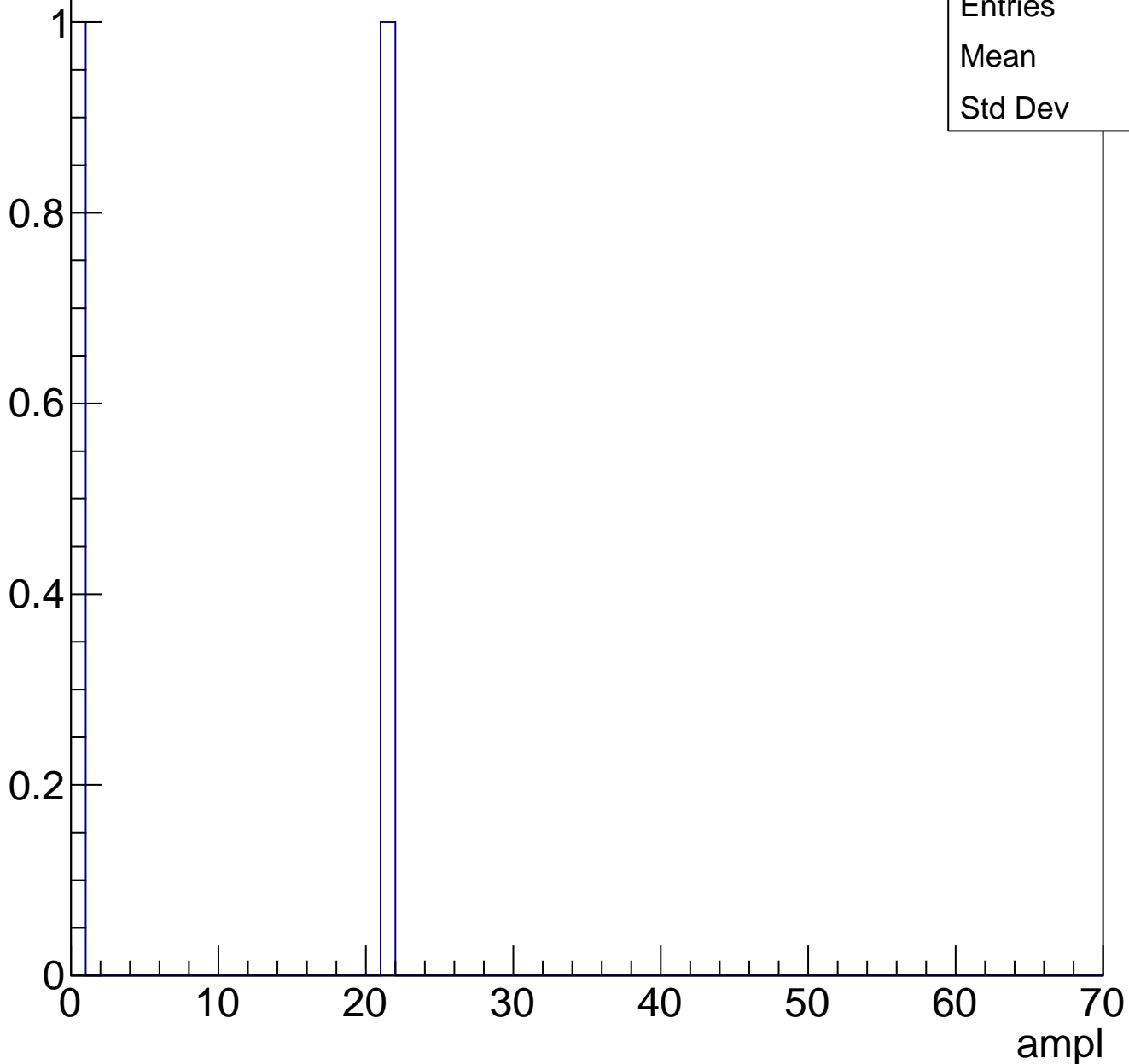
Entries	10
Mean	61
Std Dev	1.414



B1L100S, U19-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch101, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	27.02
Std Dev	6.781

Gaus mean : 28.7085

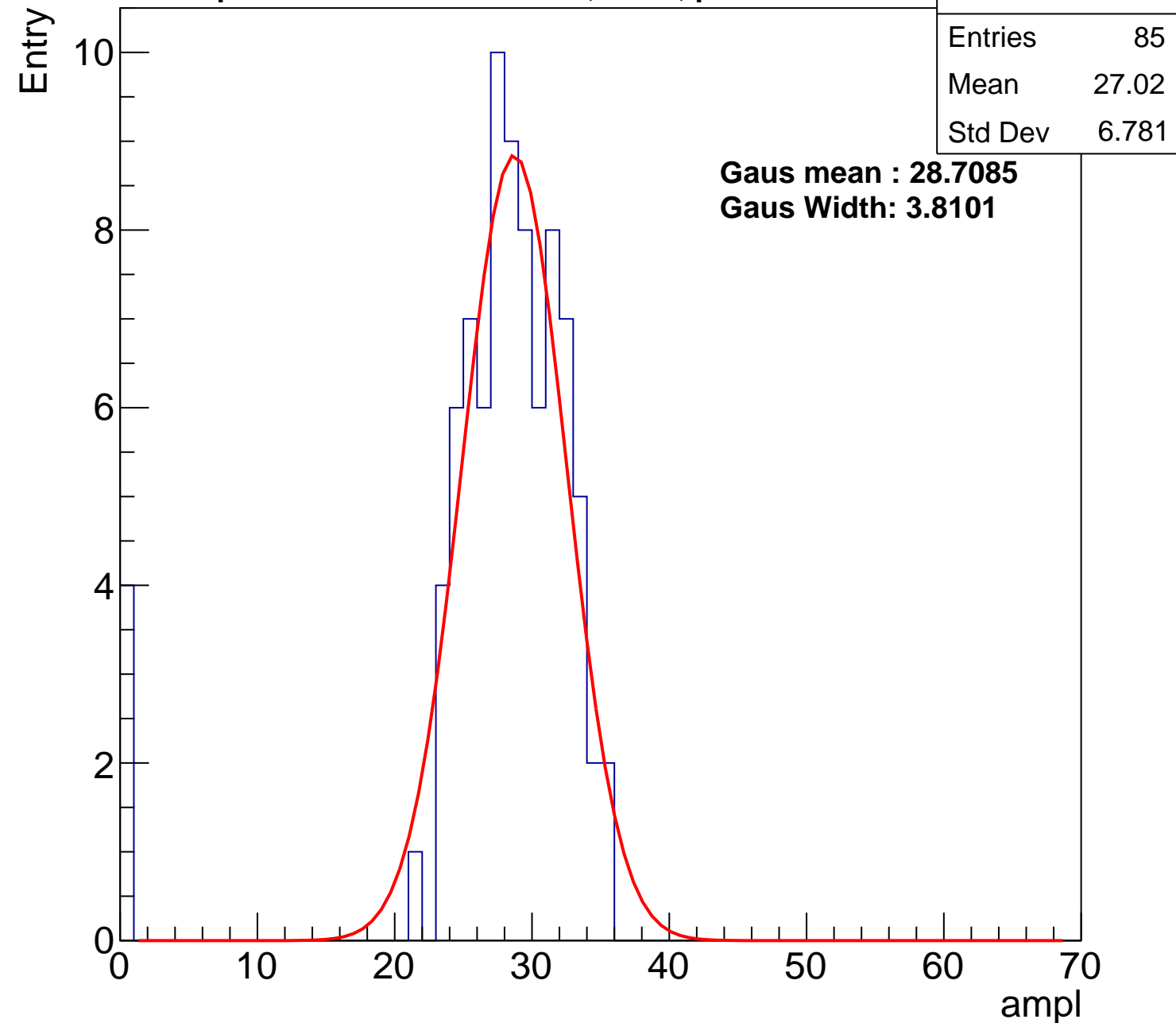
Gaus Width: 3.8101

Entry

10
8
6
4
2
0

ampl

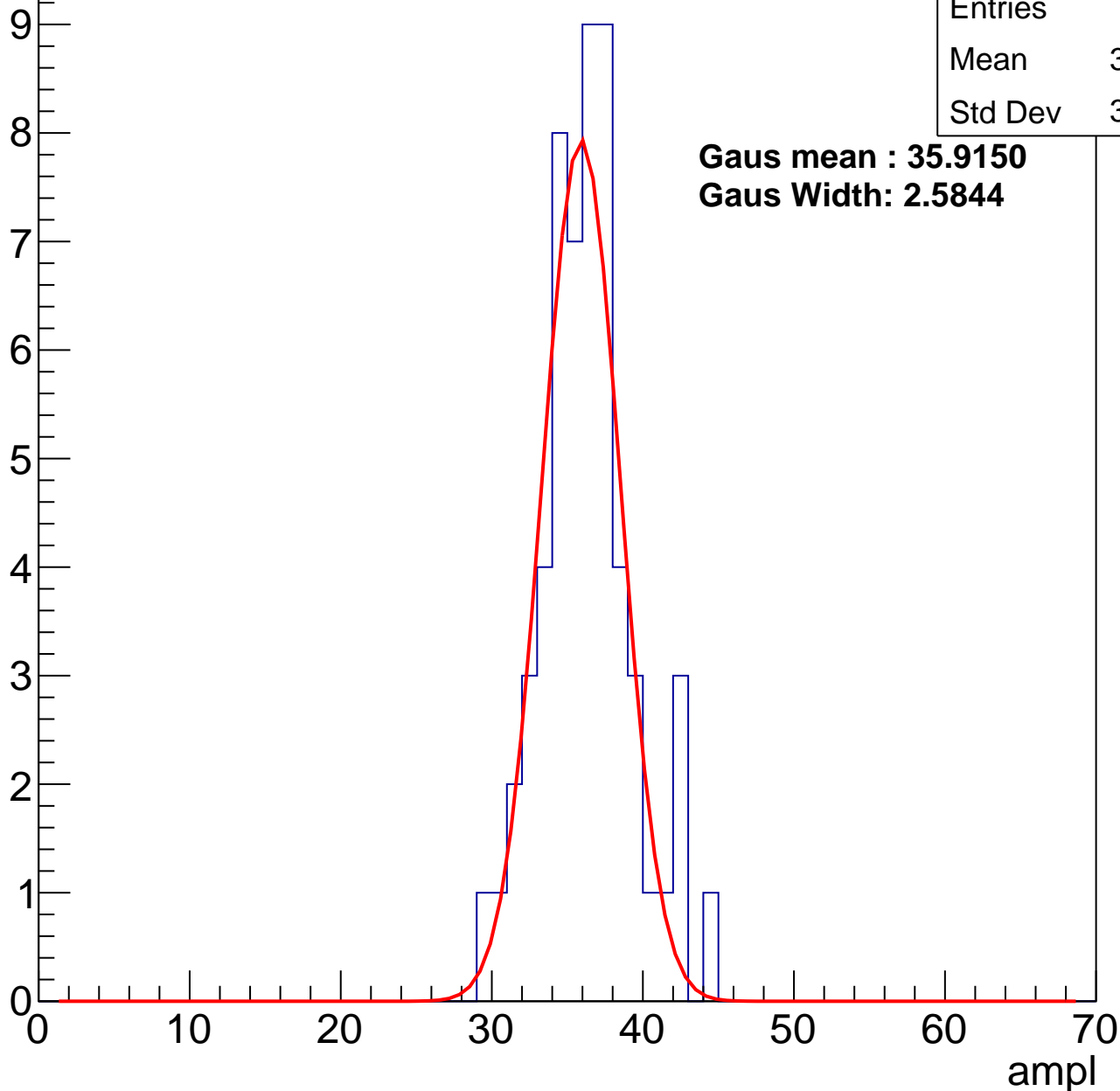
0 10 20 30 40 50 60 70



B1L100S, U19-ch101, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch101, adc2

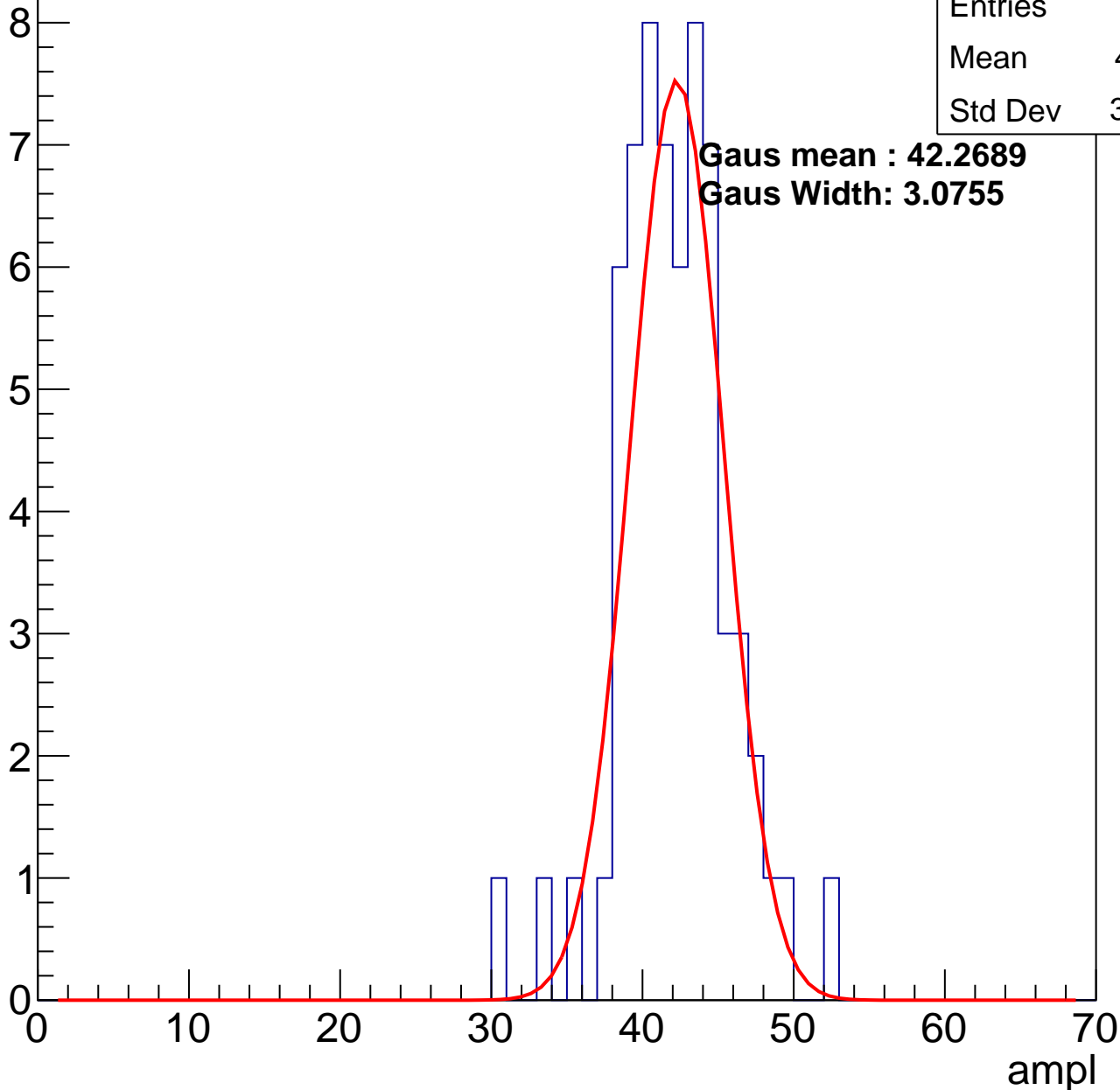
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.61
Std Dev	3.608

Gaus mean : 42.2689

Gaus Width: 3.0755

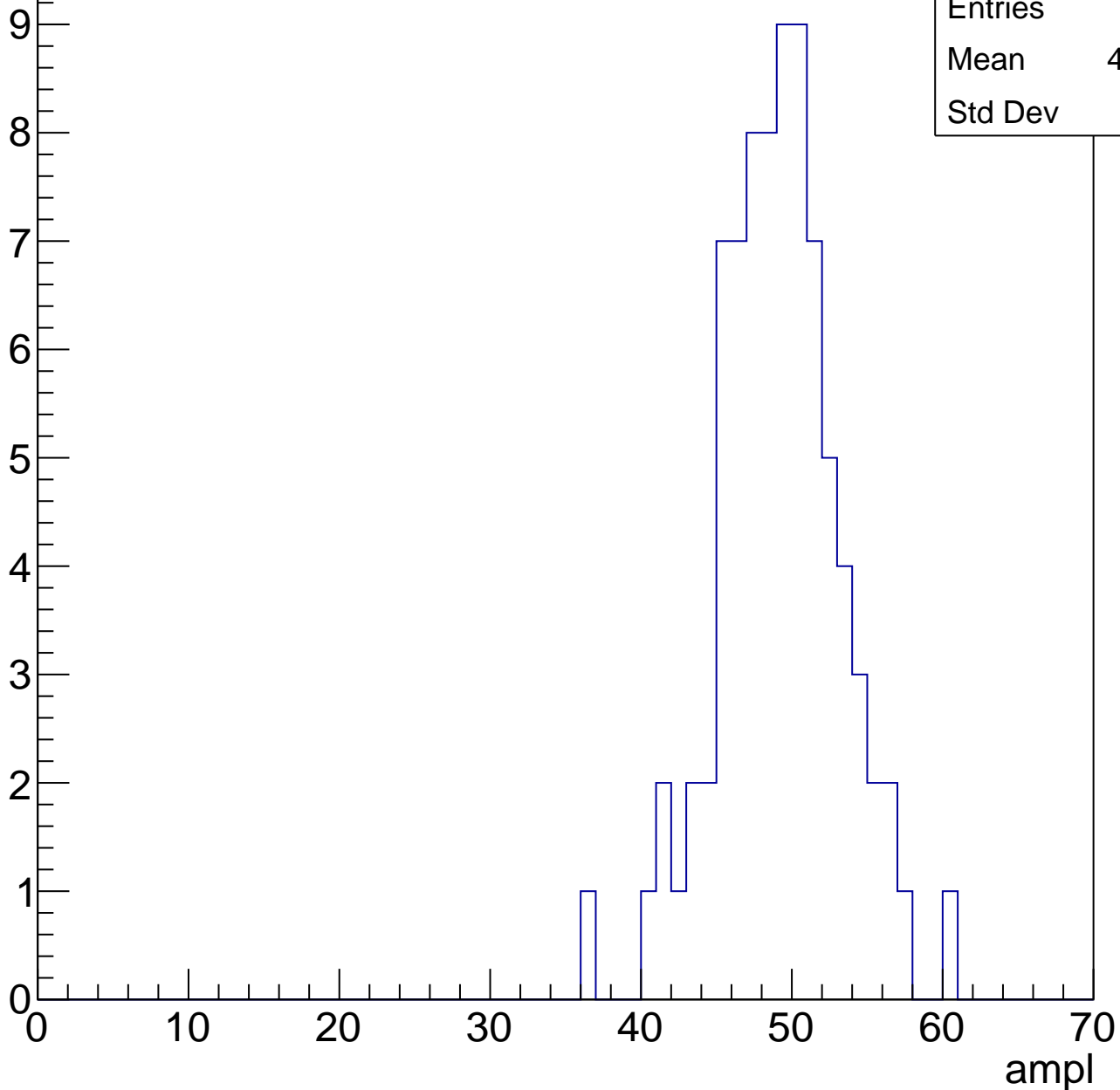


B1L100S, U19-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	48.68
Std Dev	4.03

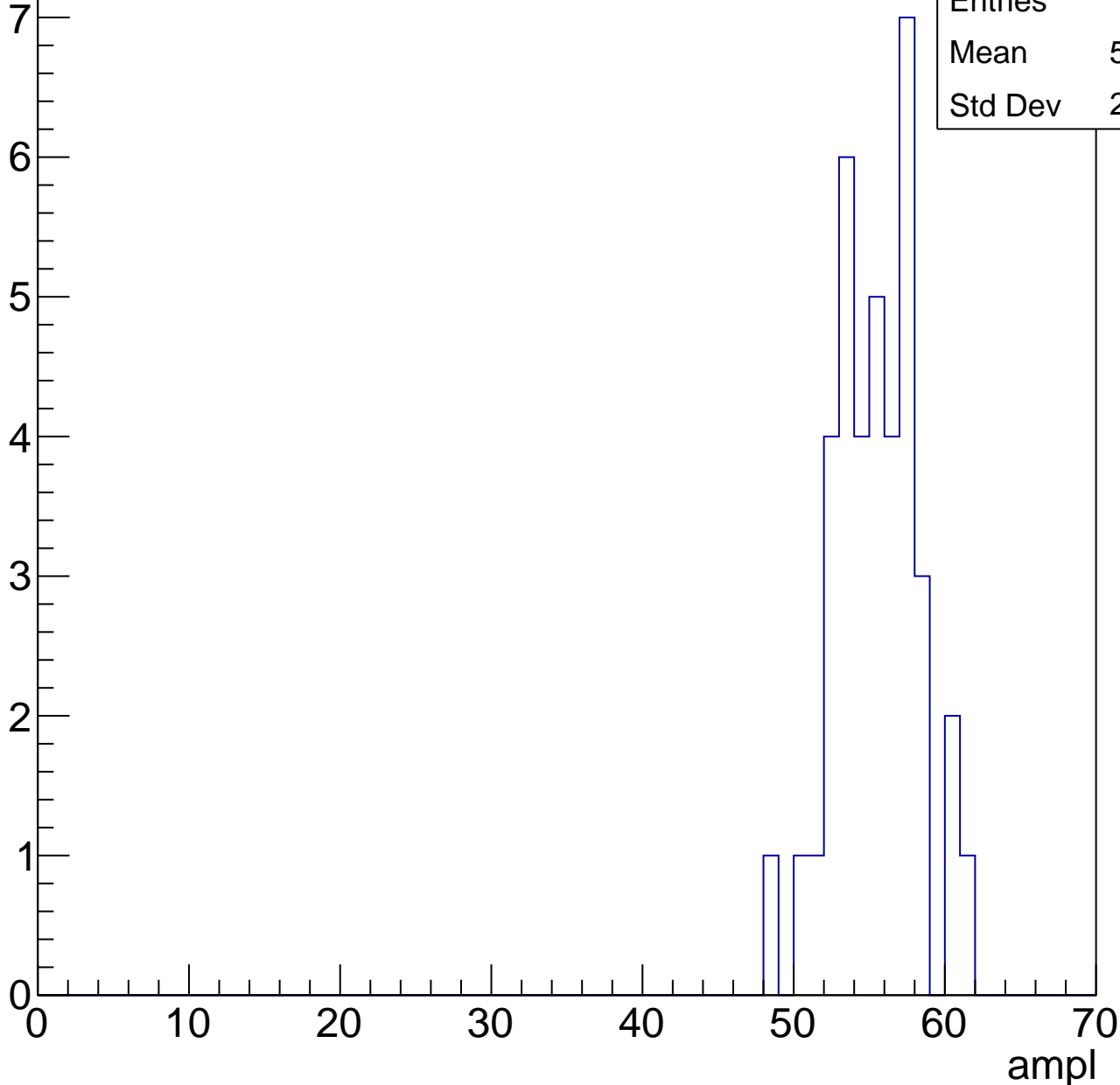


B1L100S, U19-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	54.97
Std Dev	2.769



B1L100S, U19-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	58.37
Std Dev	8.051

10

8

6

4

2

0

0

10

20

30

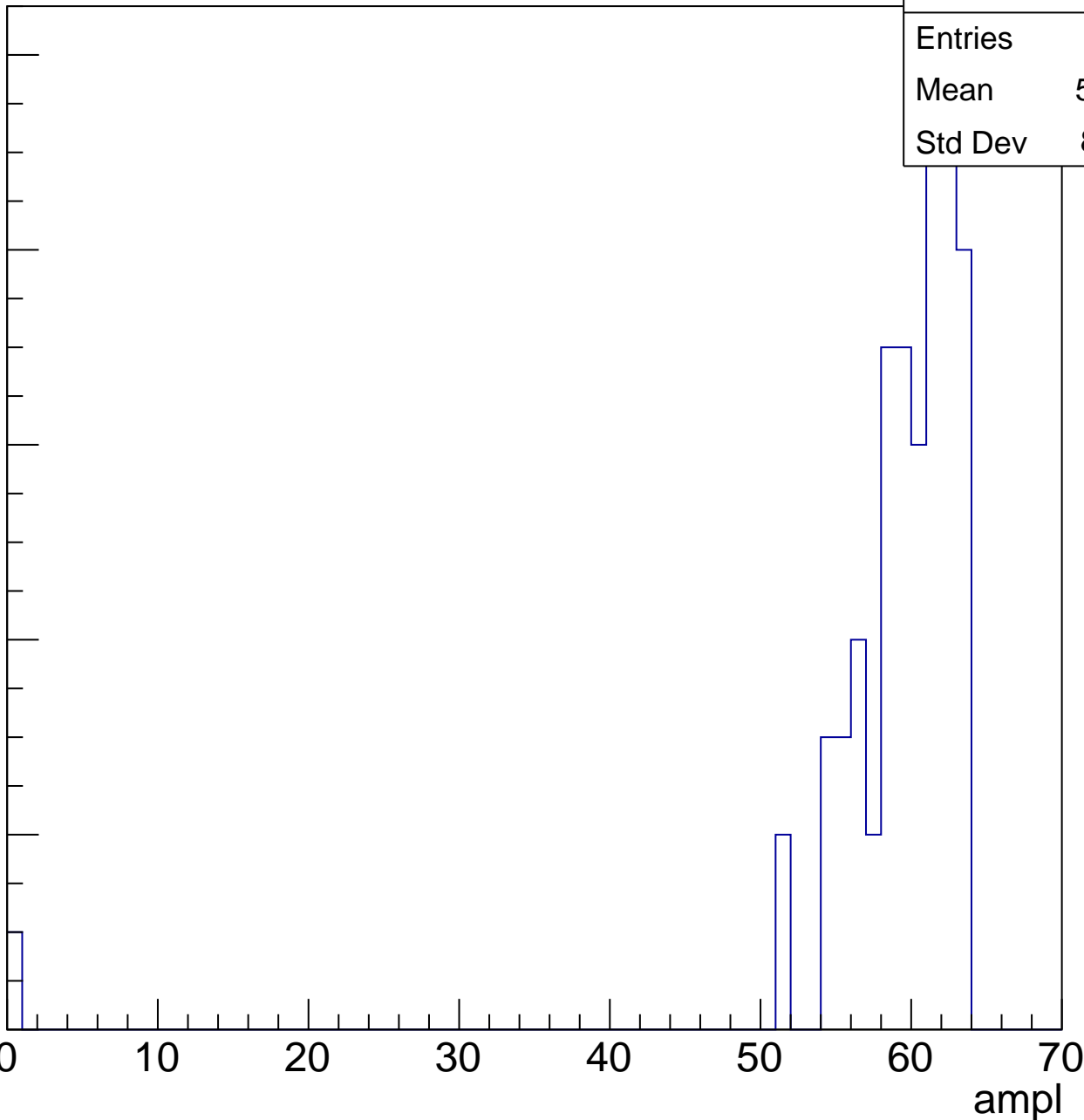
40

50

60

70

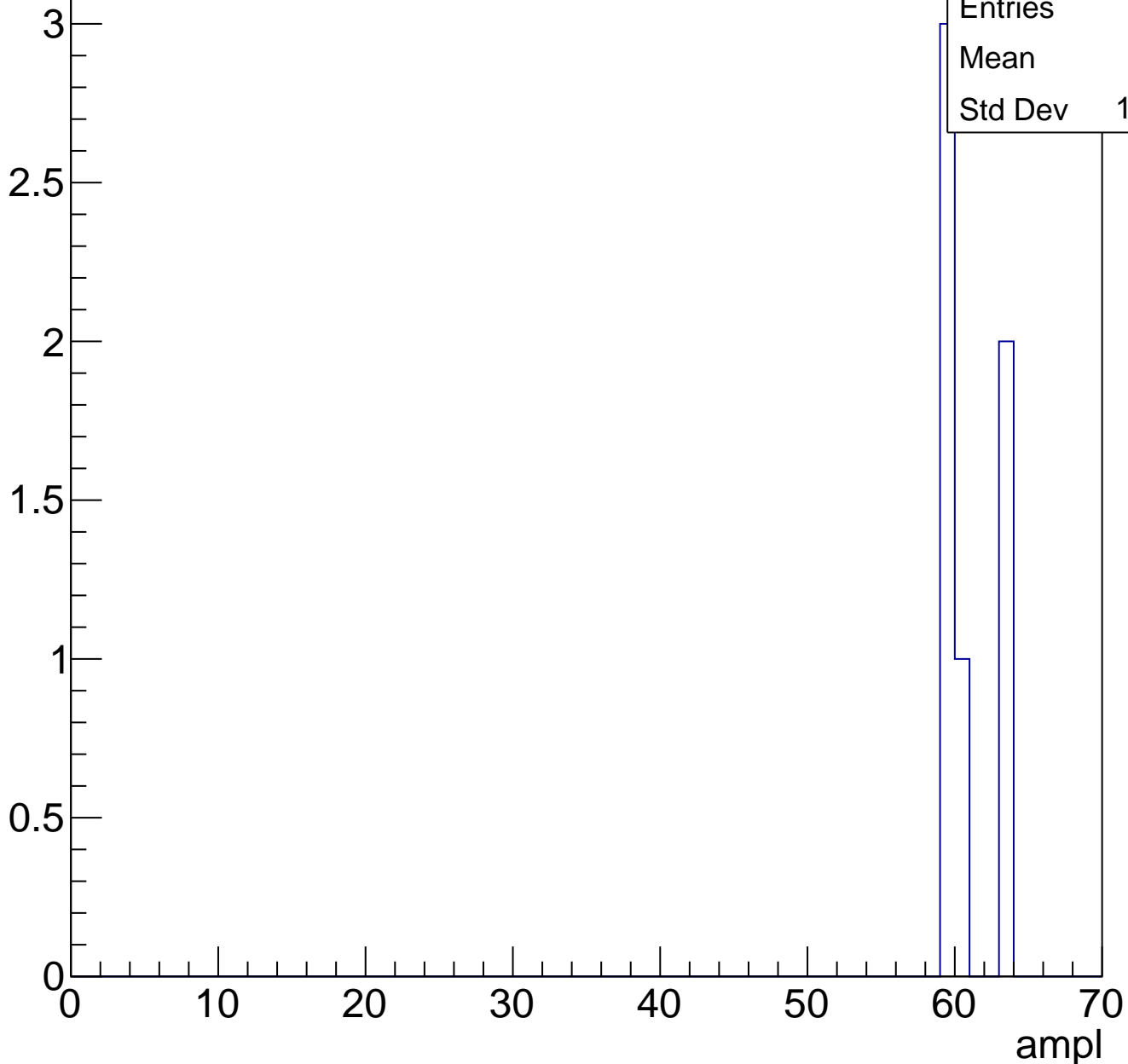
ampl



B1L100S, U19-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch102, adc0

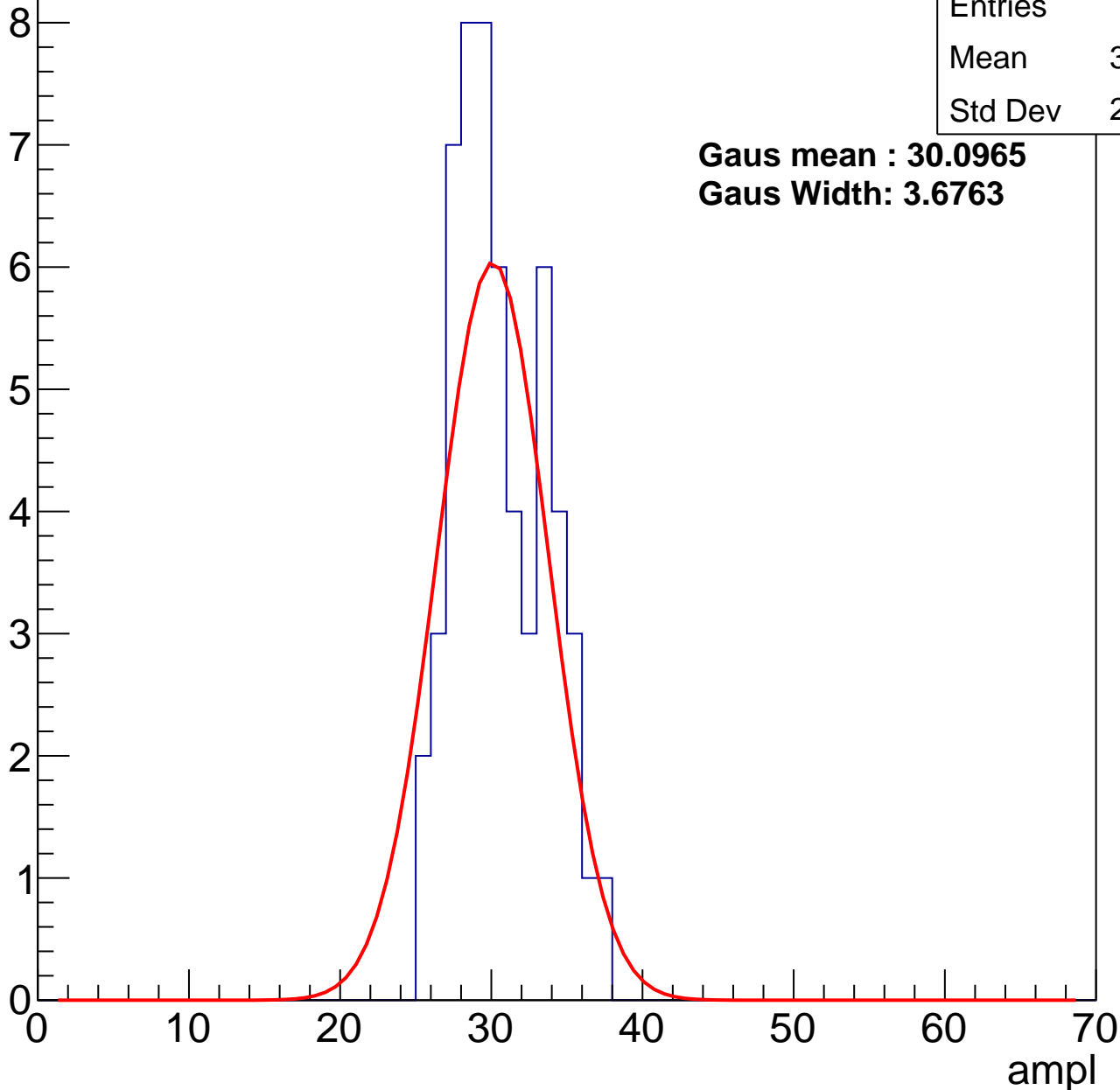
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.09
Std Dev	2.972

Gaus mean : 30.0965

Gaus Width: 3.6763



B1L100S, U19-ch102, adc1

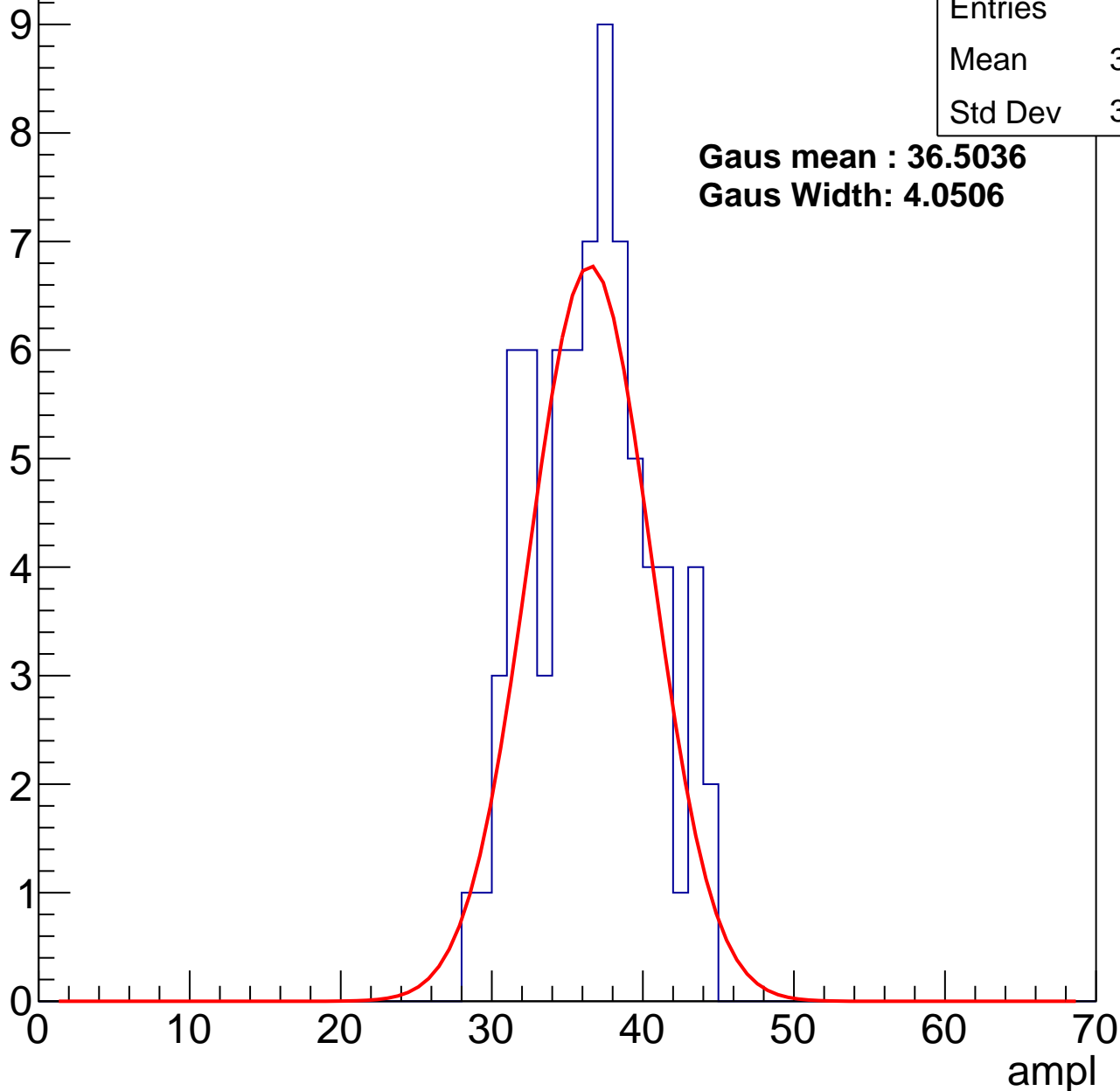
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.13
Std Dev	3.879

Gaus mean : 36.5036

Gaus Width: 4.0506



B1L100S, U19-ch102, adc2

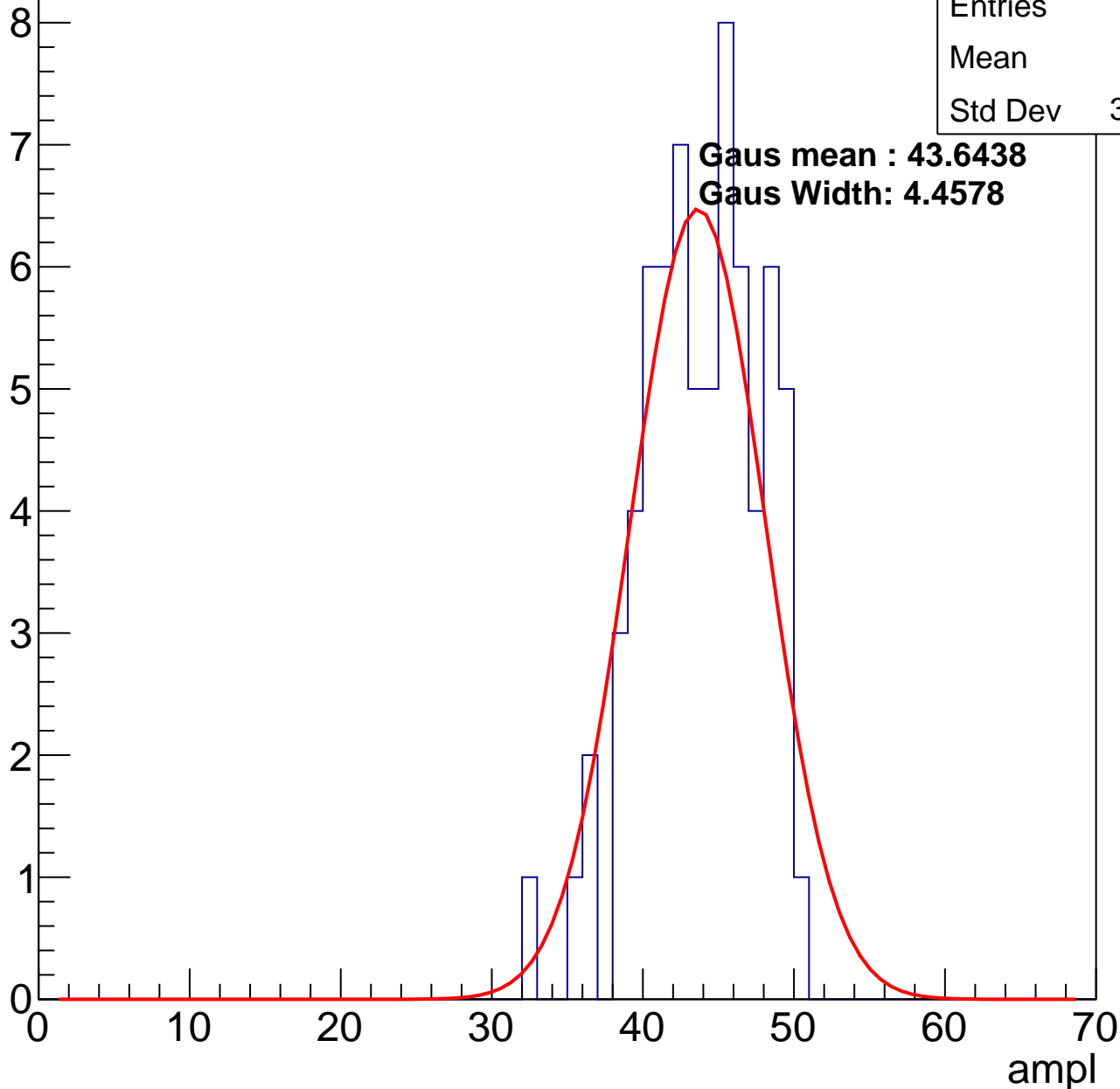
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43.3
Std Dev	3.845

Gaus mean : 43.6438

Gaus Width: 4.4578

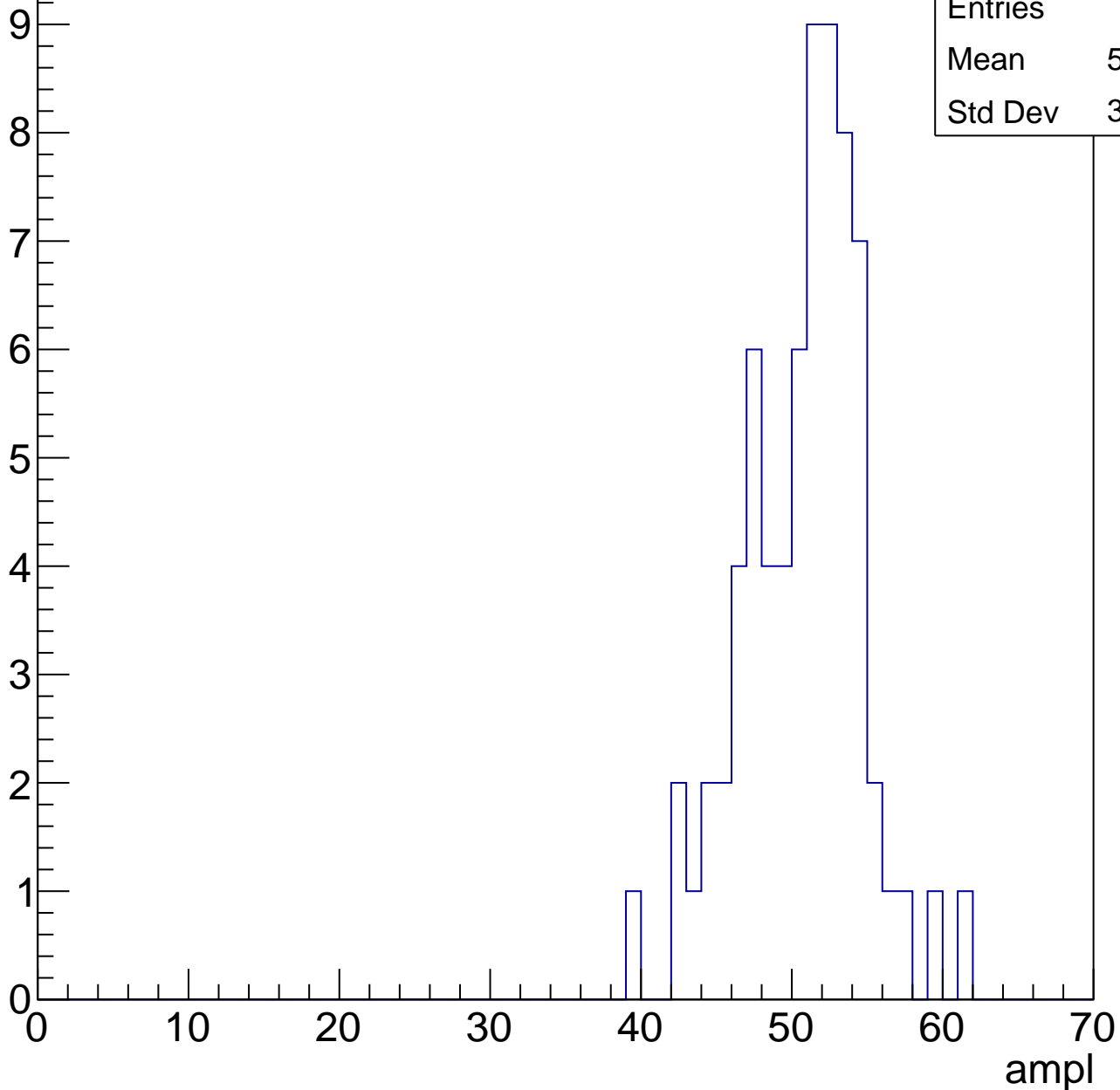


B1L100S, U19-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	50.28
Std Dev	3.969

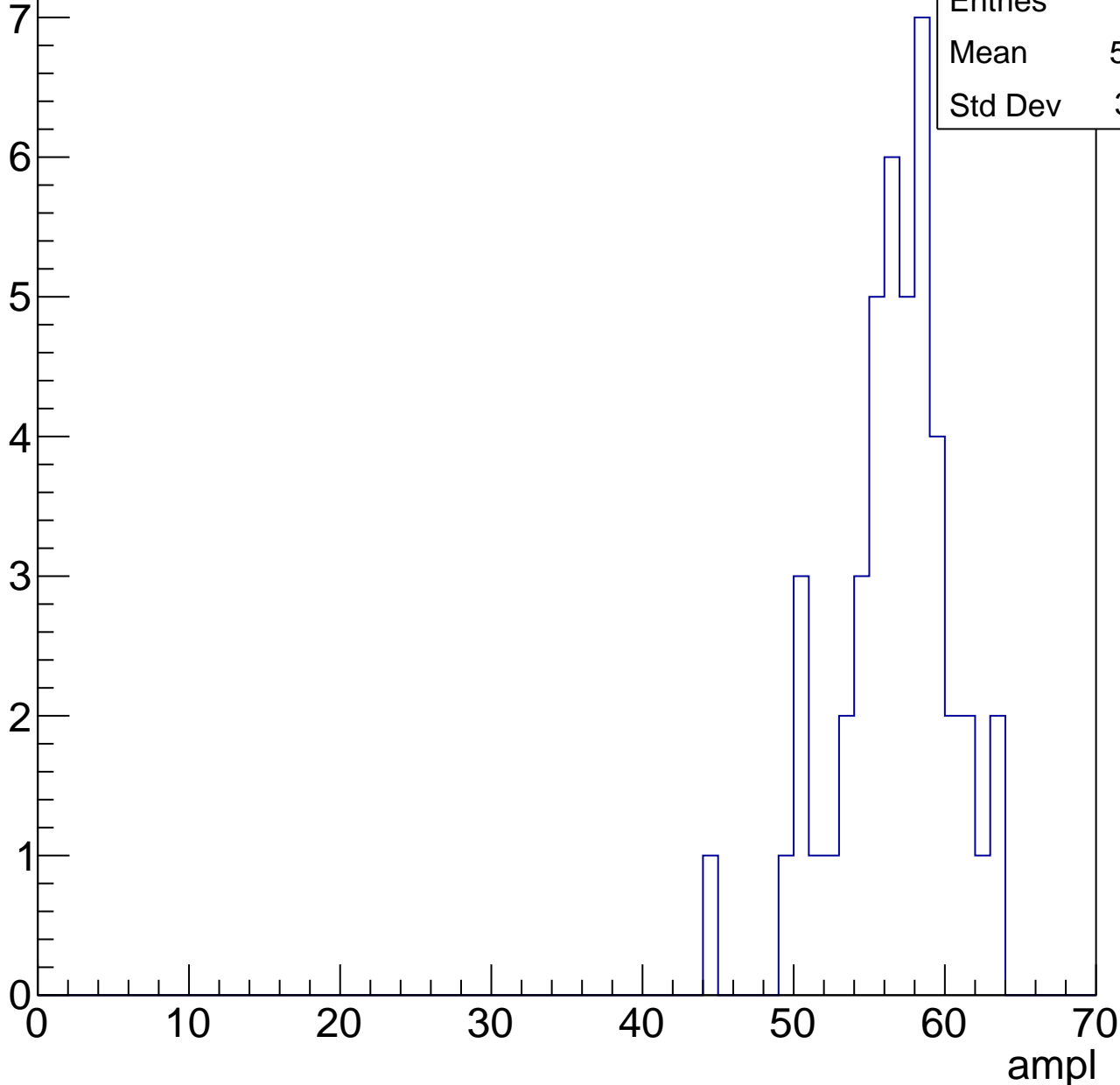


B1L100S, U19-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	56.13
Std Dev	3.791

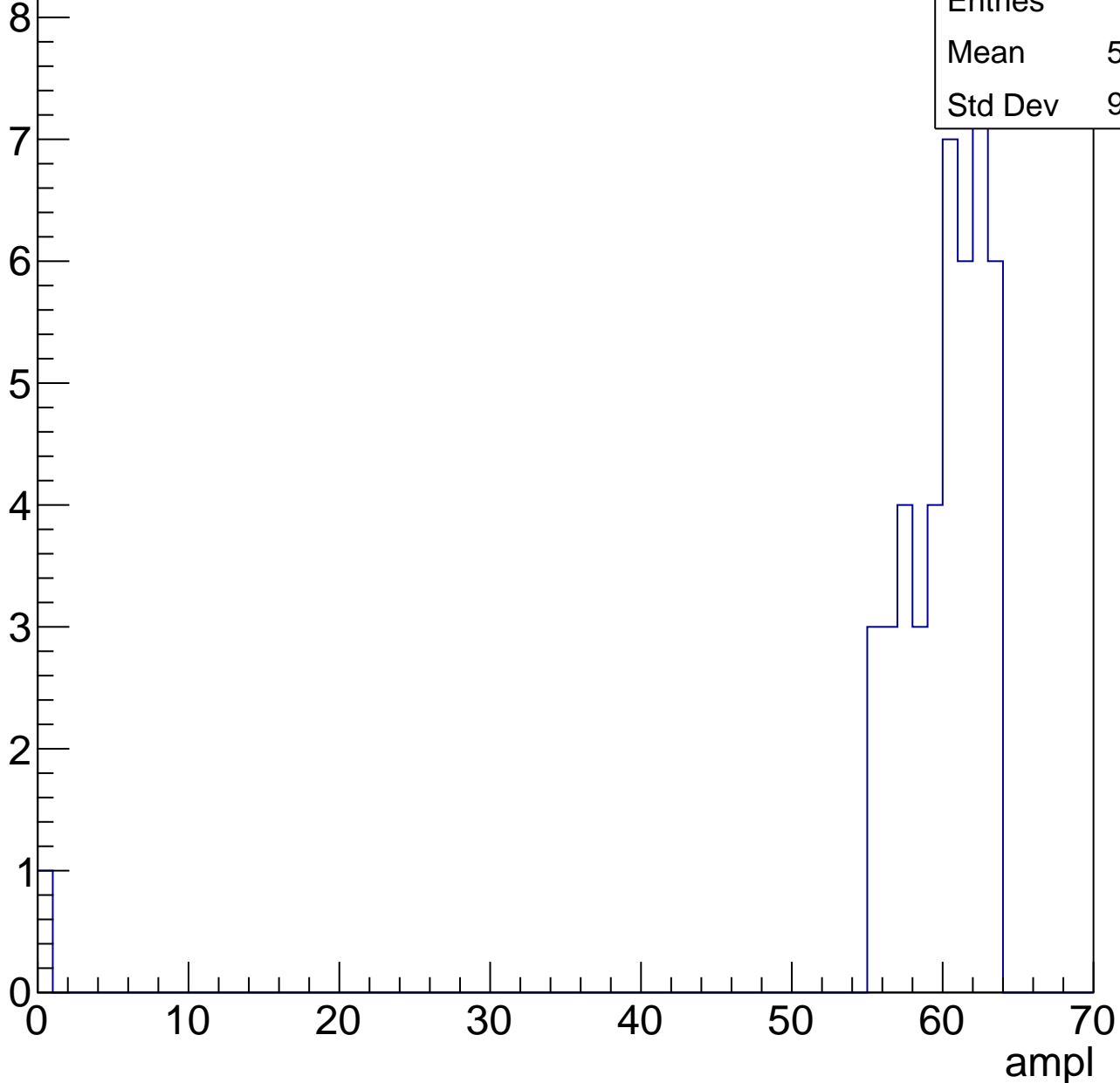


B1L100S, U19-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	58.47
Std Dev	9.142



B1L100S, U19-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

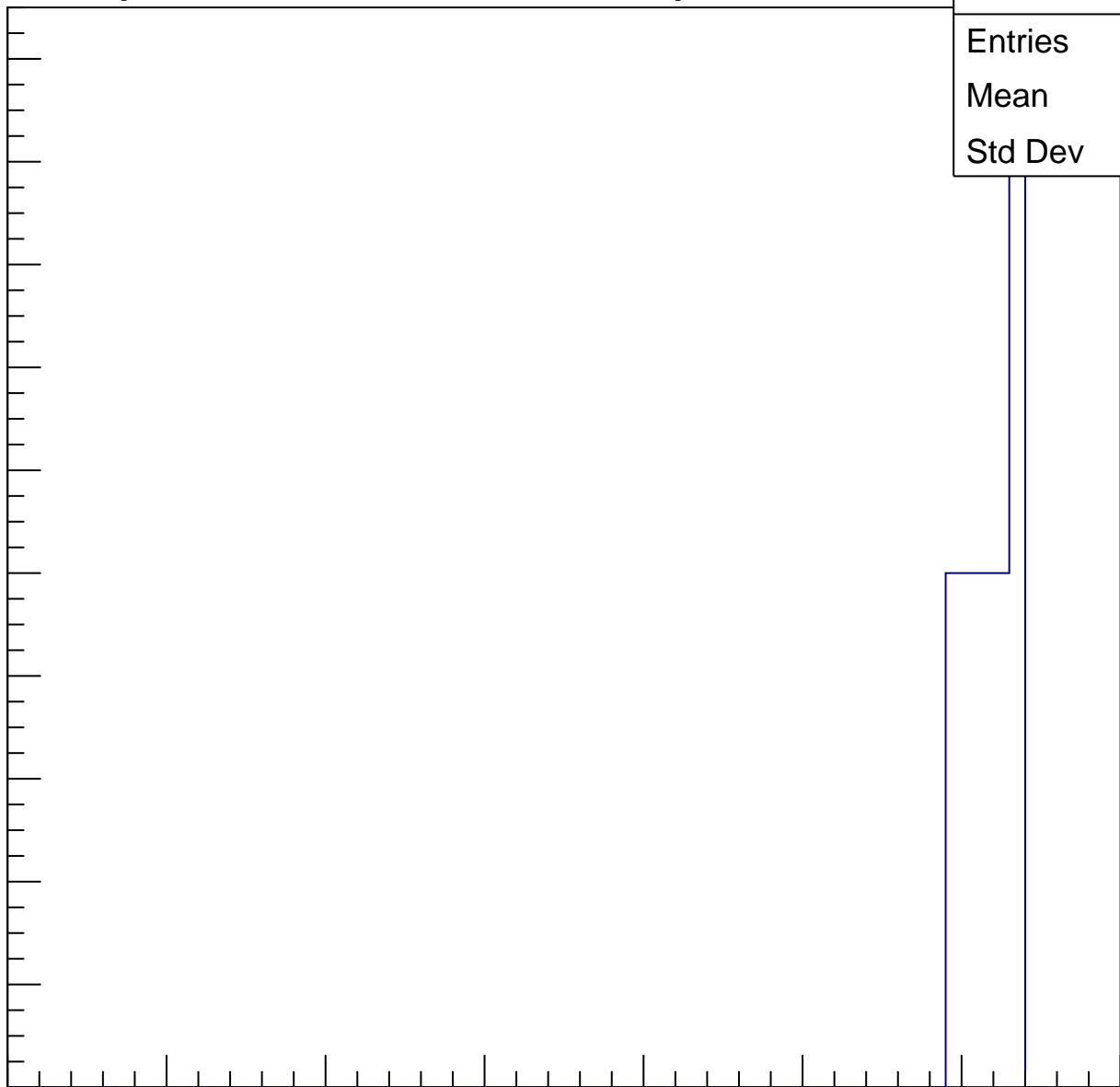
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch103, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	27.5
Std Dev	4.843

Gaus mean : 28.8601

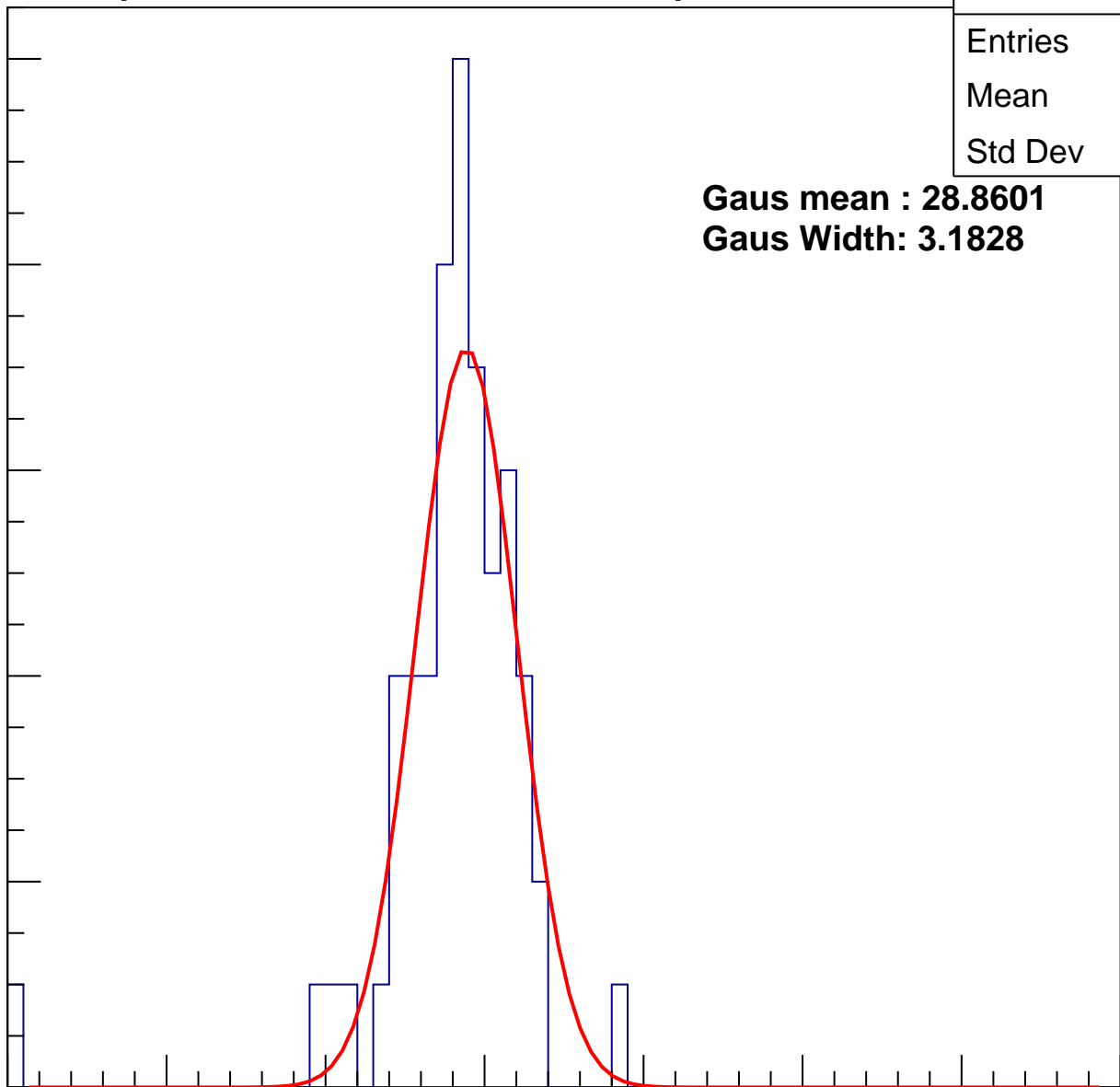
Gaus Width: 3.1828

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	35.37
Std Dev	3.309

Gaus mean : 35.5197

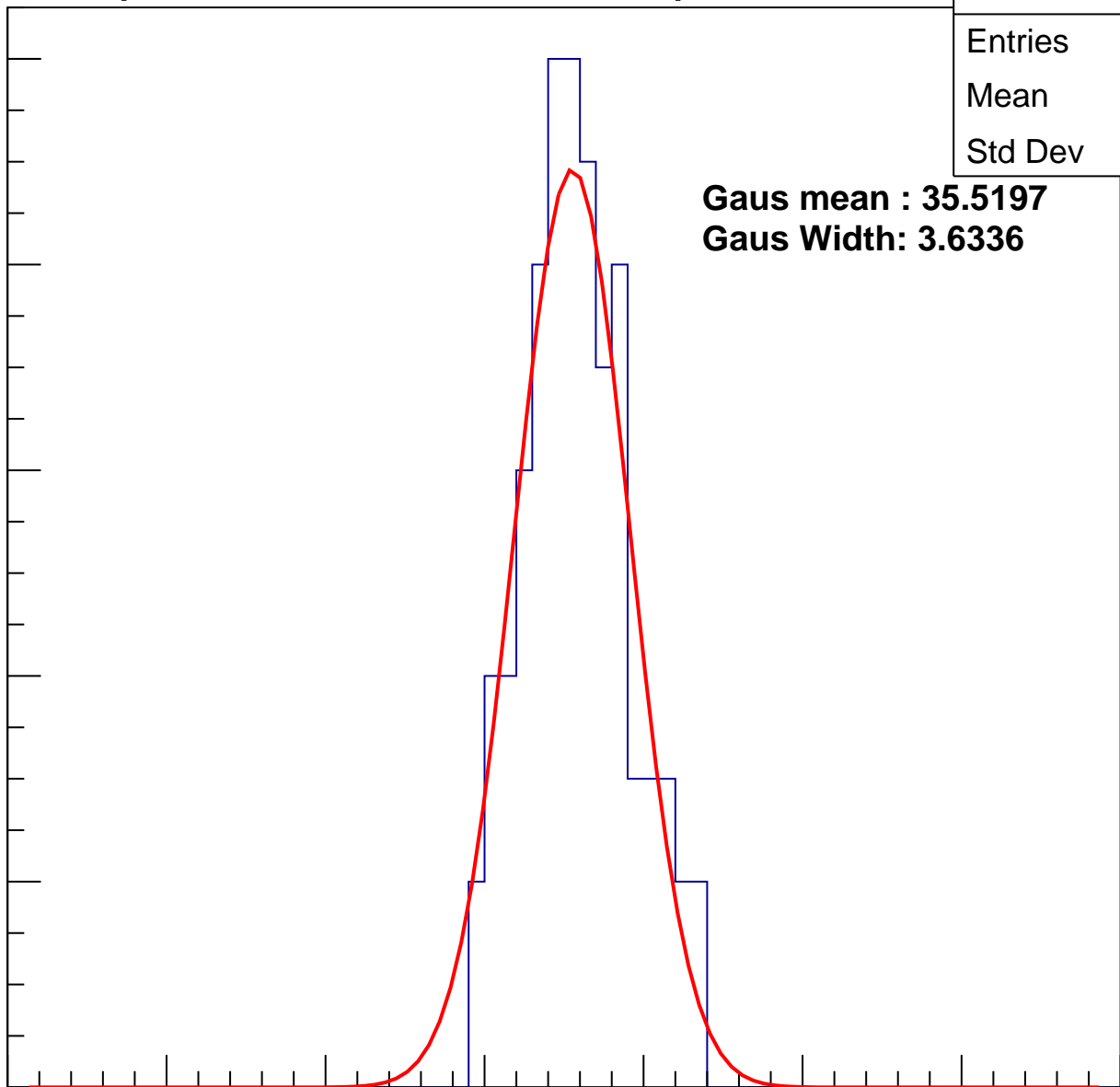
Gaus Width: 3.6336

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch103, adc2

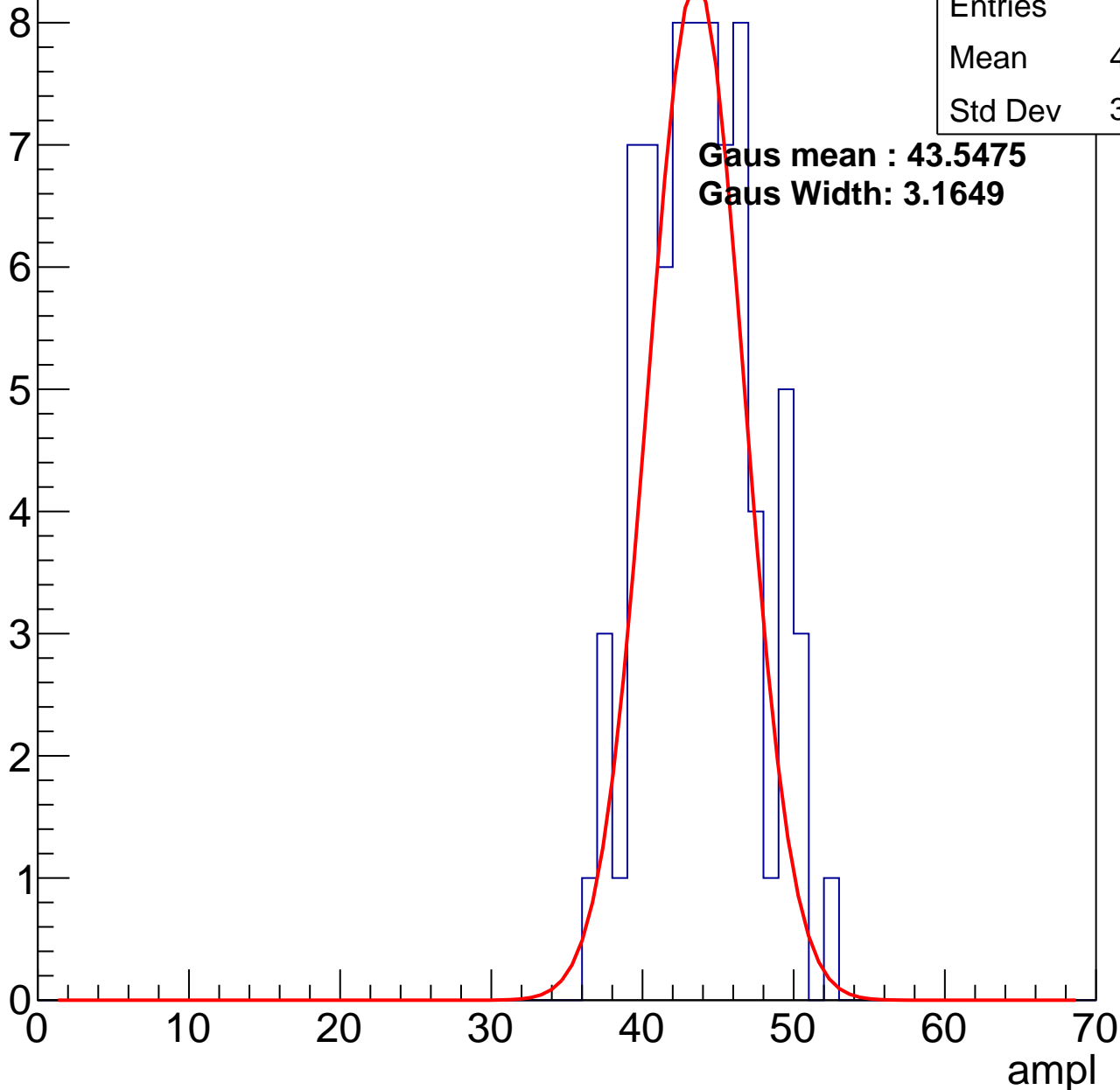
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	43.36
Std Dev	3.552

Gaus mean : 43.5475

Gaus Width: 3.1649

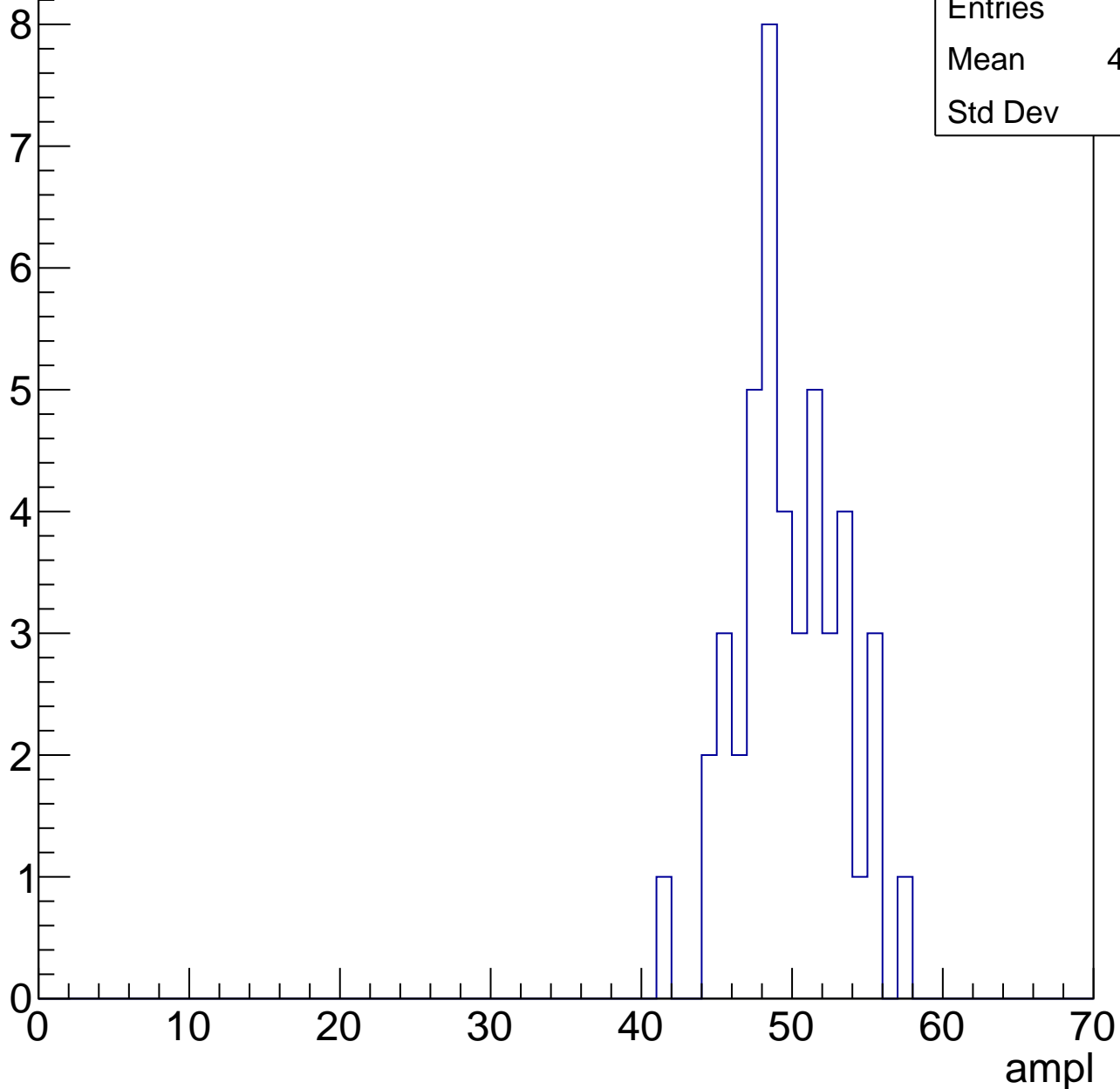


B1L100S, U19-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	49.33
Std Dev	3.38

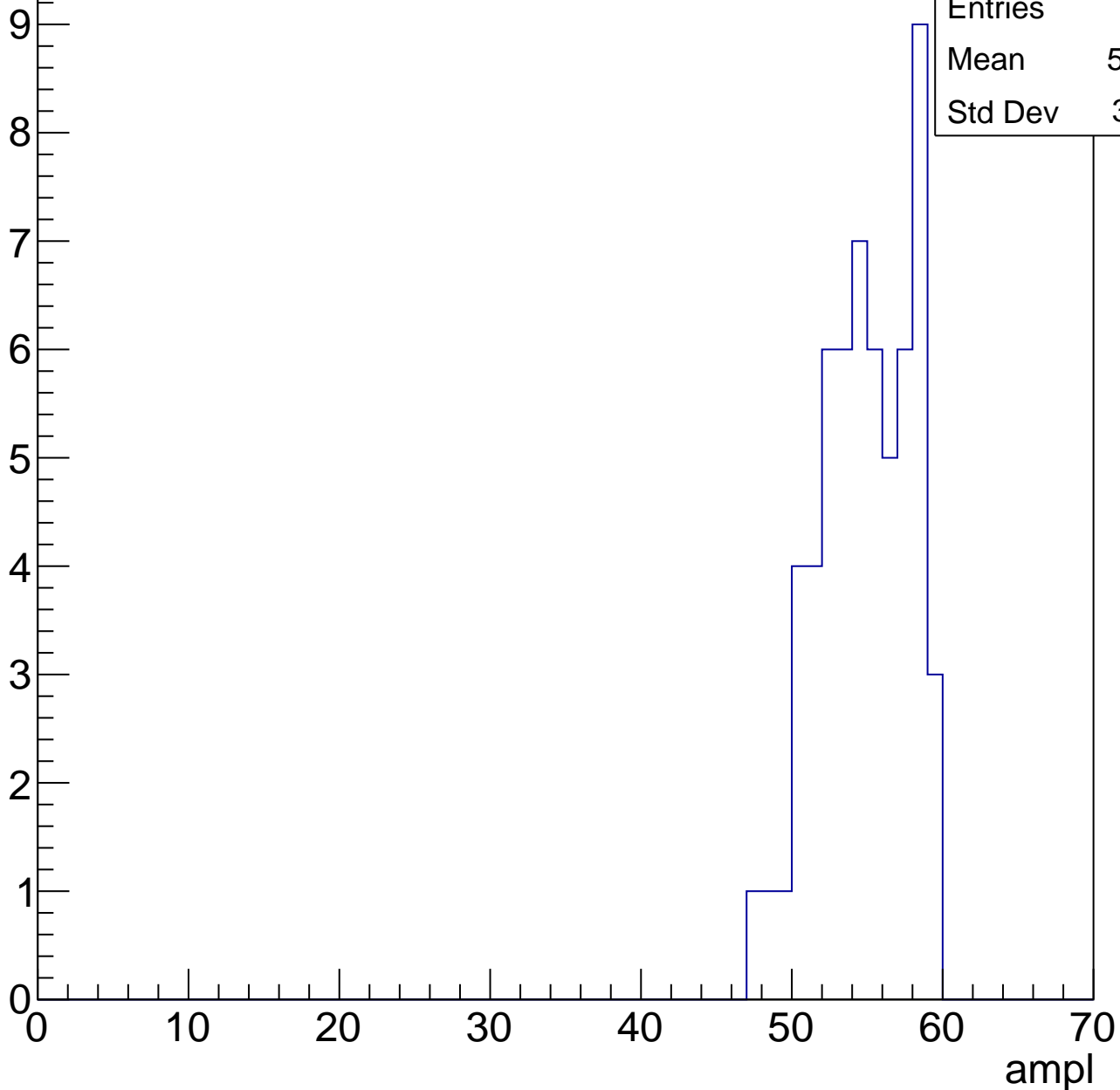


B1L100S, U19-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	54.36
Std Dev	3.001

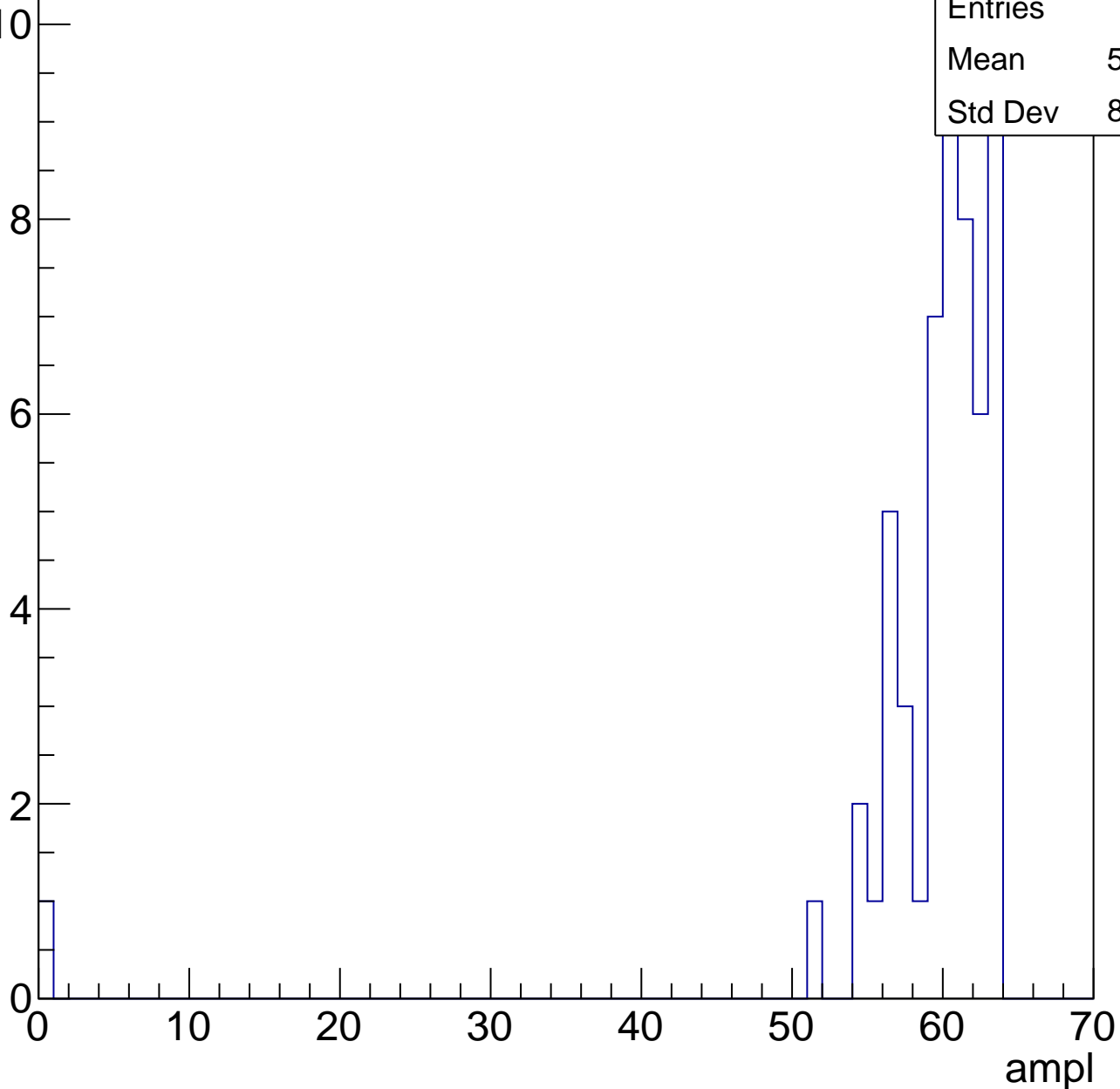


B1L100S, U19-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

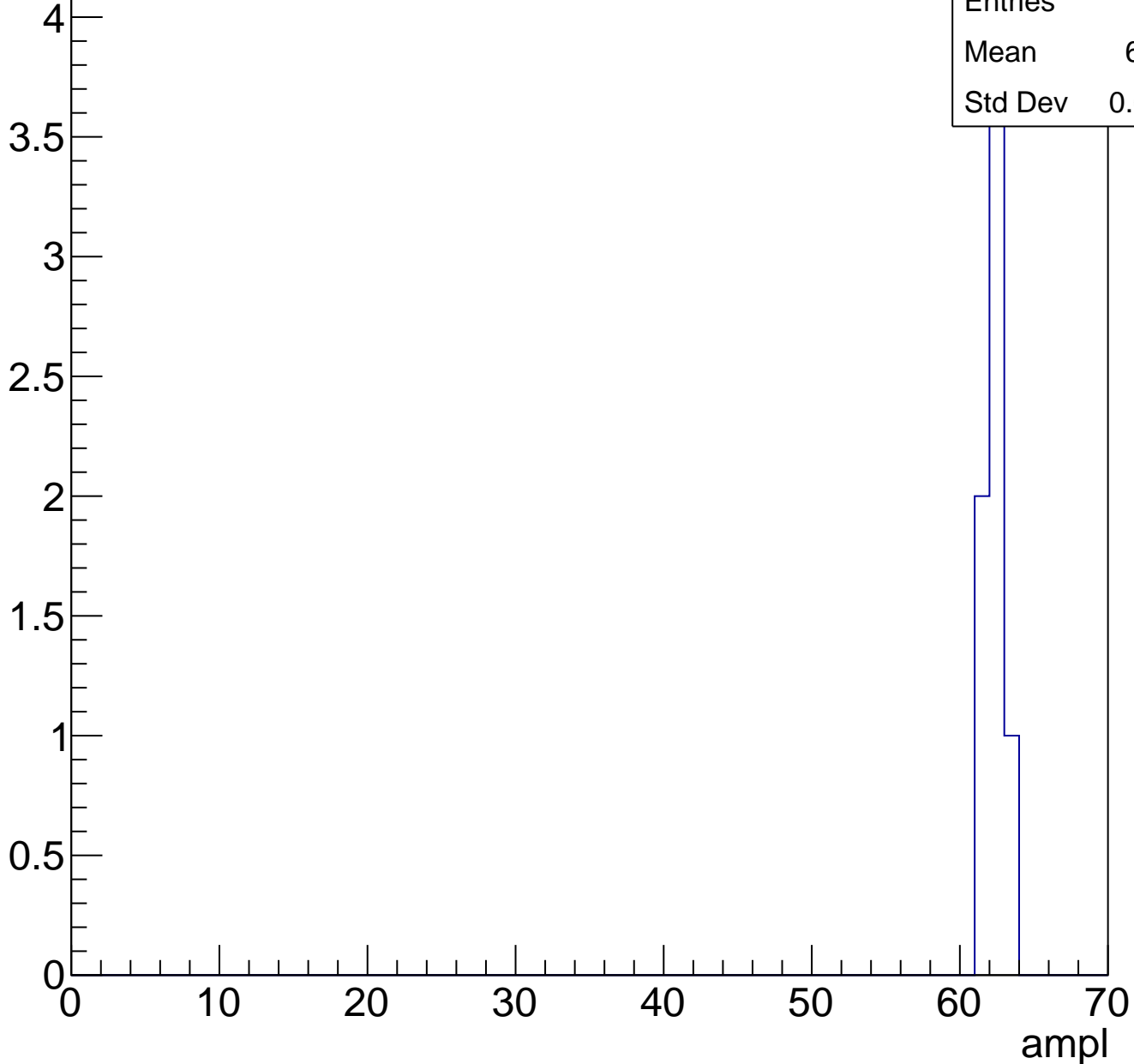
Entries	54
Mean	58.57
Std Dev	8.497



B1L100S, U19-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	7
Mean	61.86
Std Dev	0.6389

B1L100S, U19-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch104, adc0

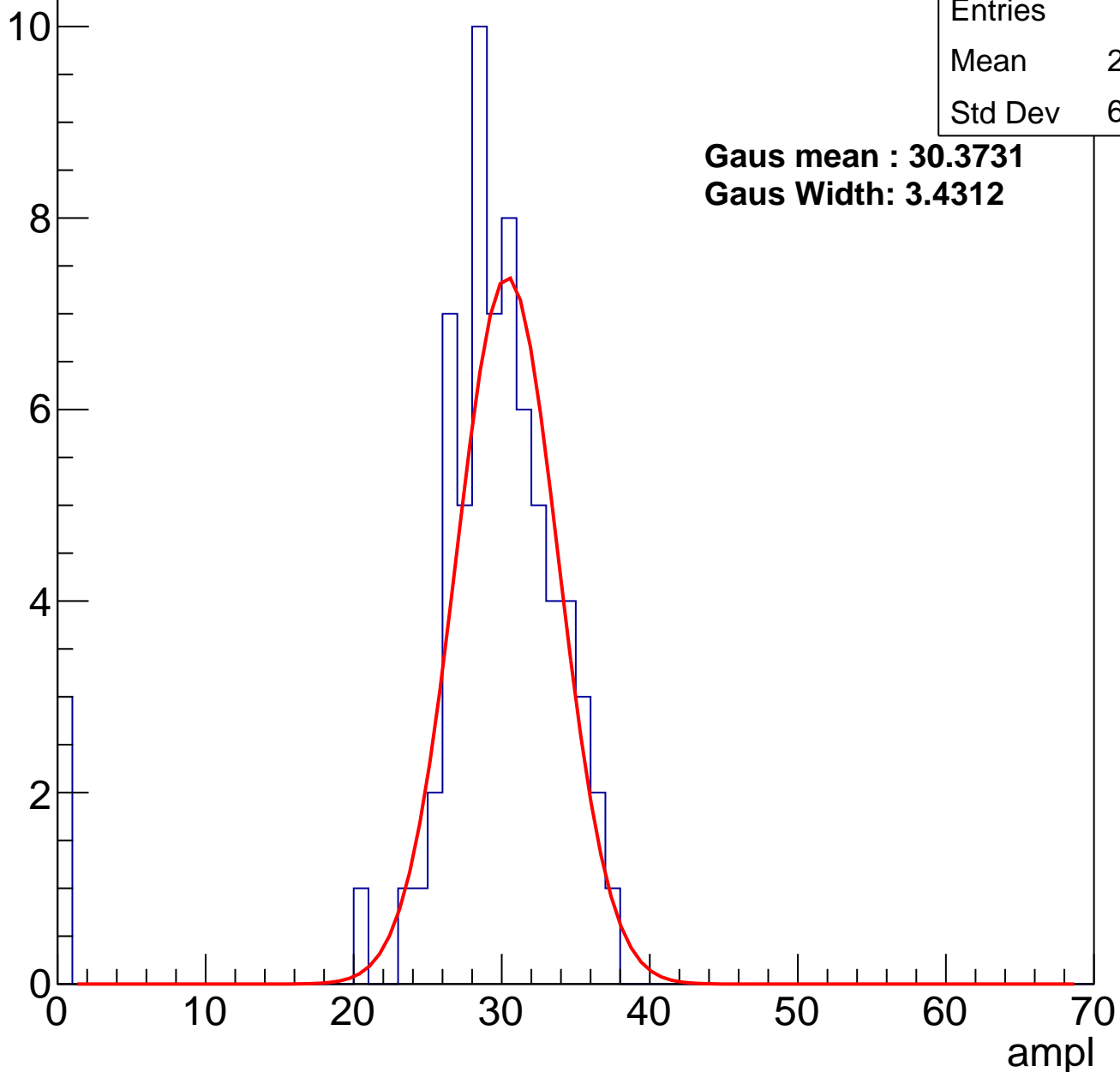
calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	28.36
Std Dev	6.837

Gaus mean : 30.3731

Gaus Width: 3.4312

Entry



B1L100S, U19-ch104, adc1

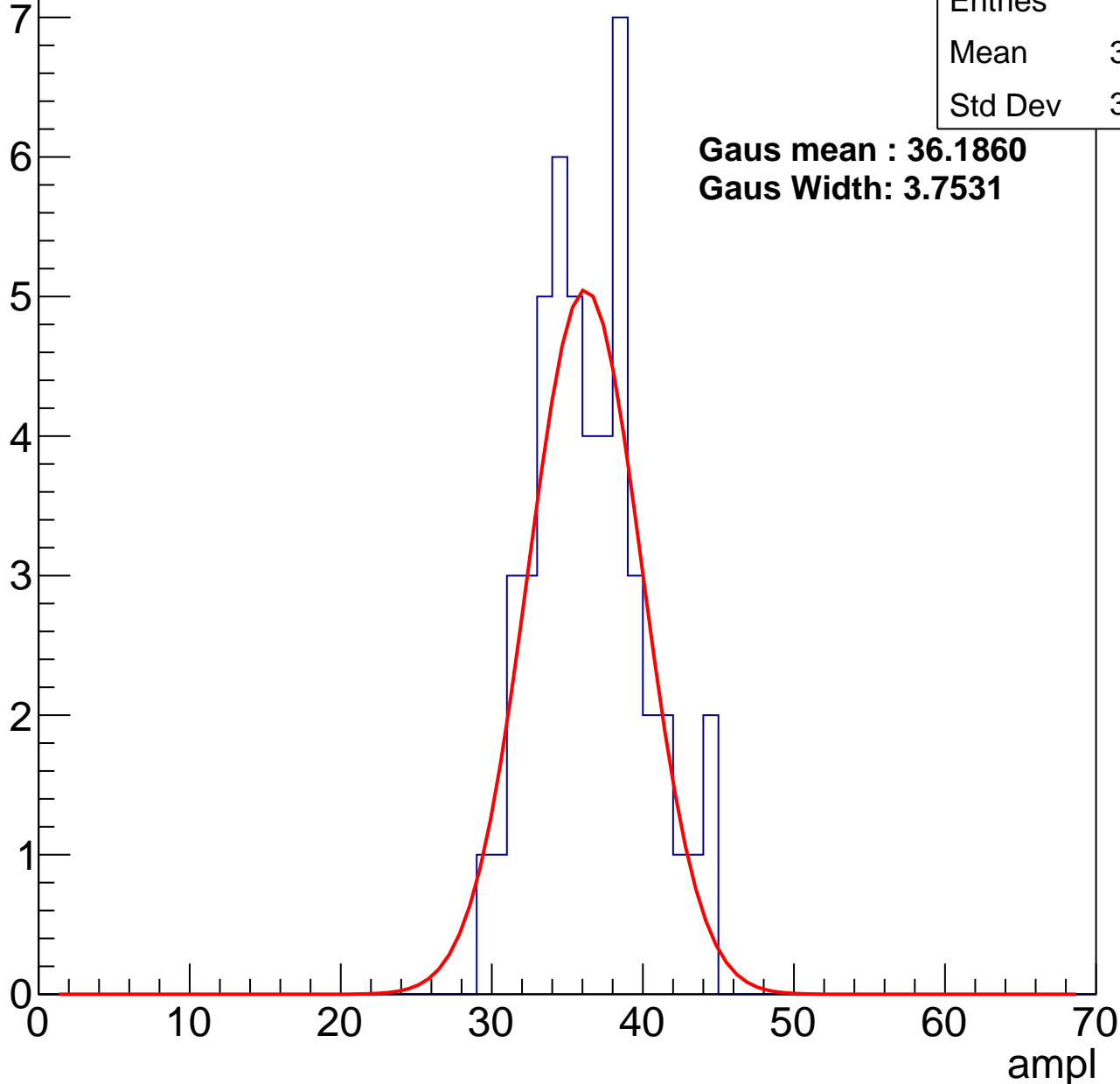
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	36.04
Std Dev	3.566

Gaus mean : 36.1860

Gaus Width: 3.7531



B1L100S, U19-ch104, adc2

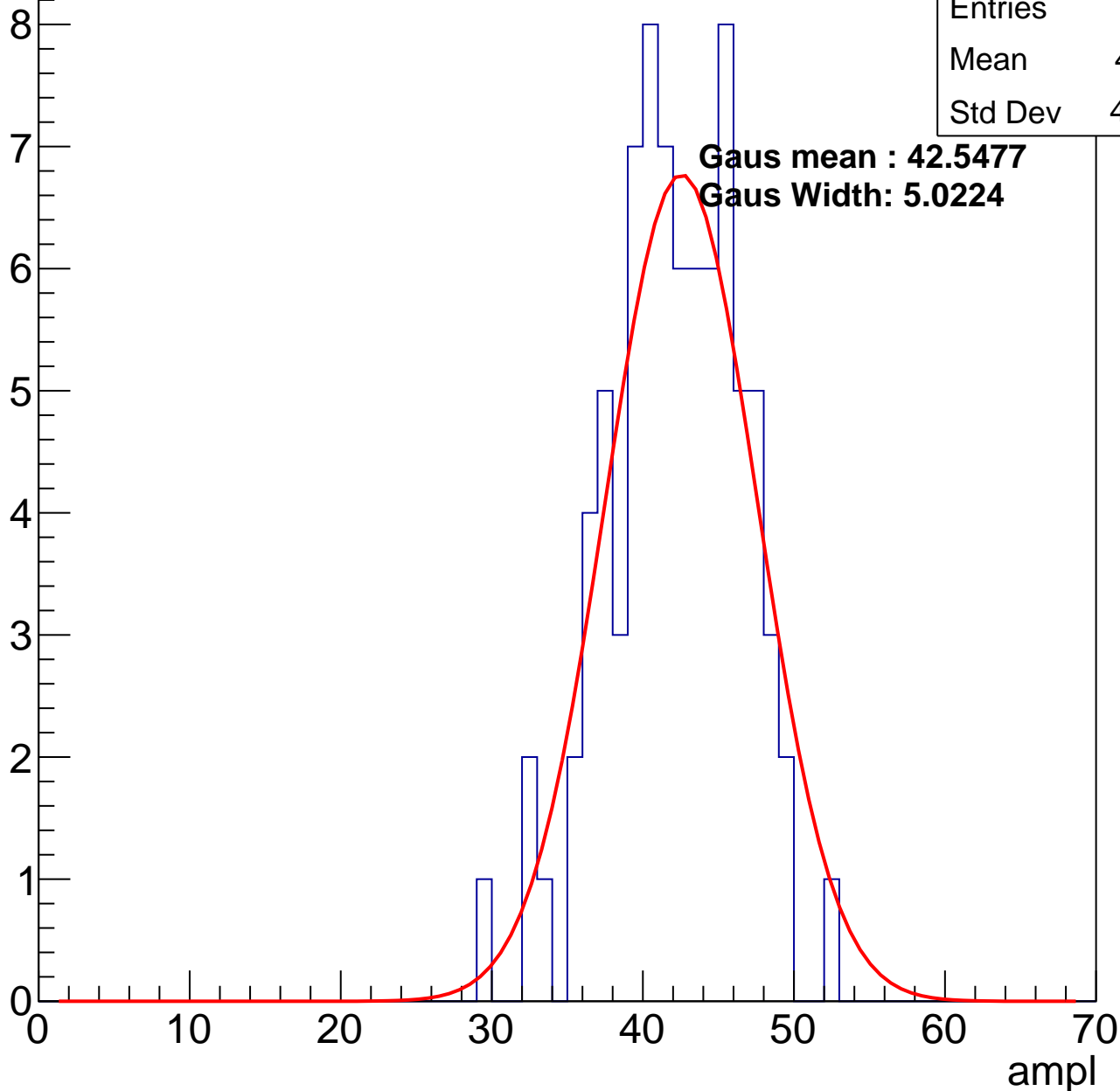
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	41.61
Std Dev	4.372

Gaus mean : 42.5477

Gaus Width: 5.0224

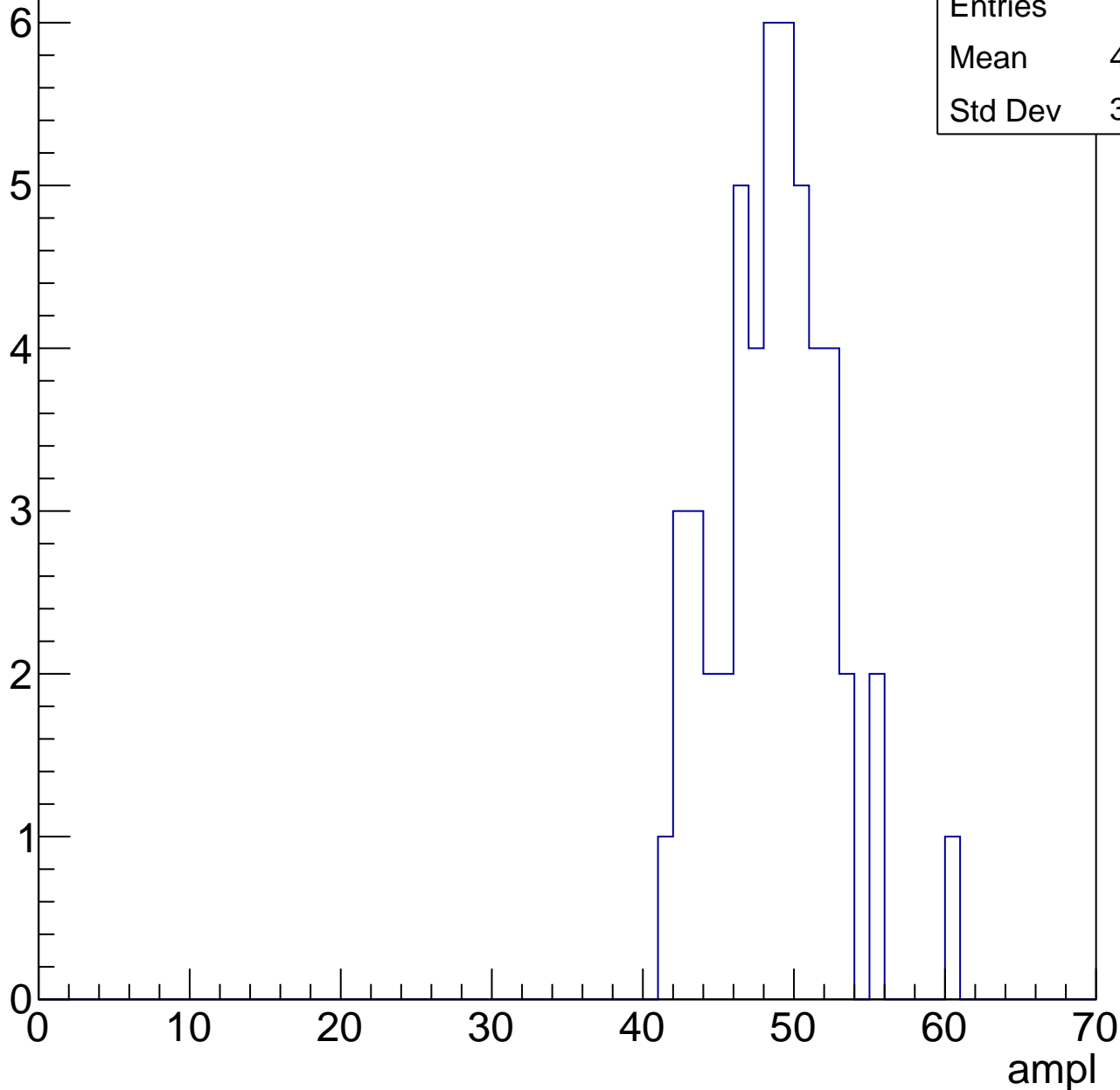


B1L100S, U19-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	48.24
Std Dev	3.798

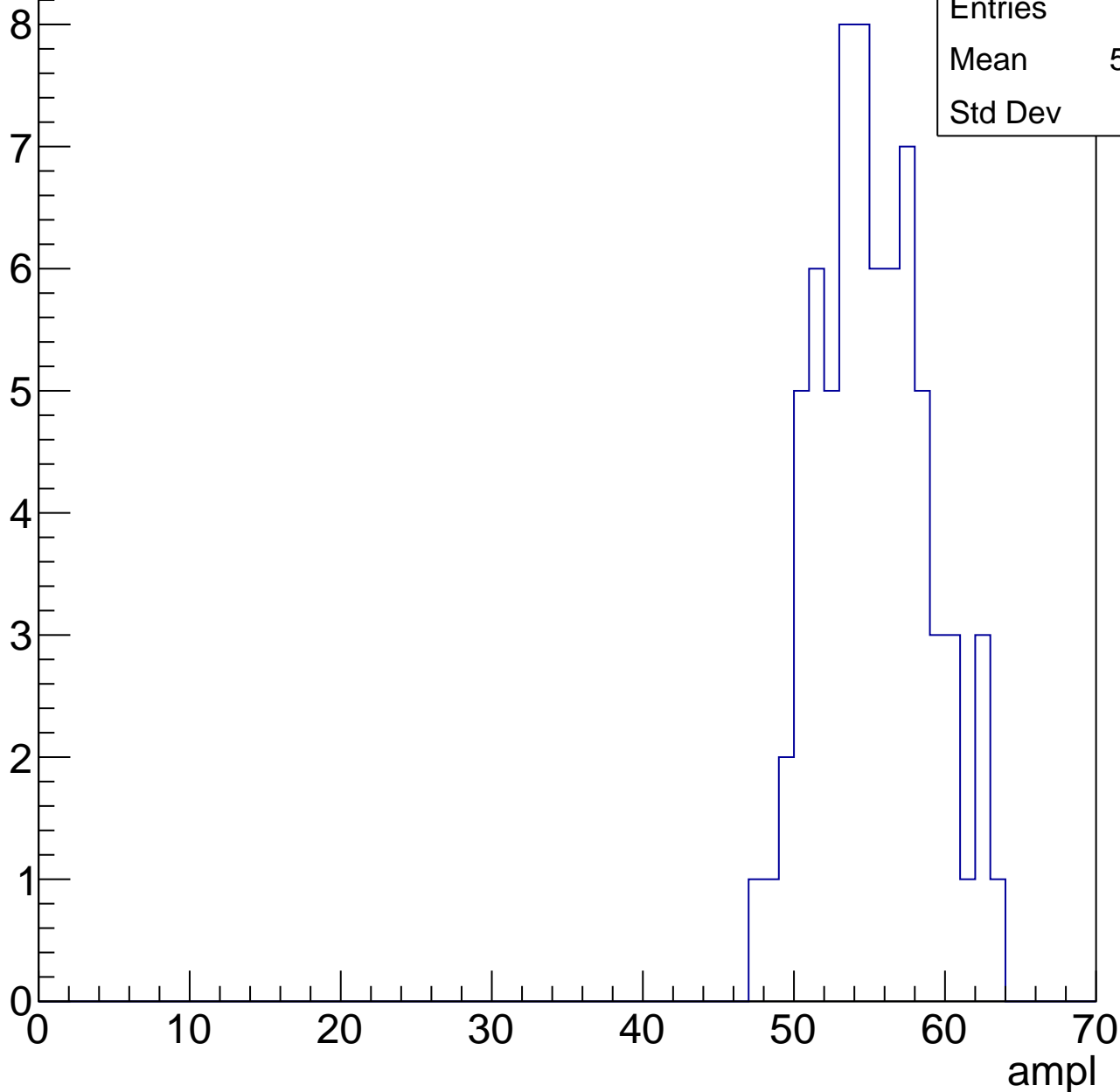


B1L100S, U19-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	54.75
Std Dev	3.63

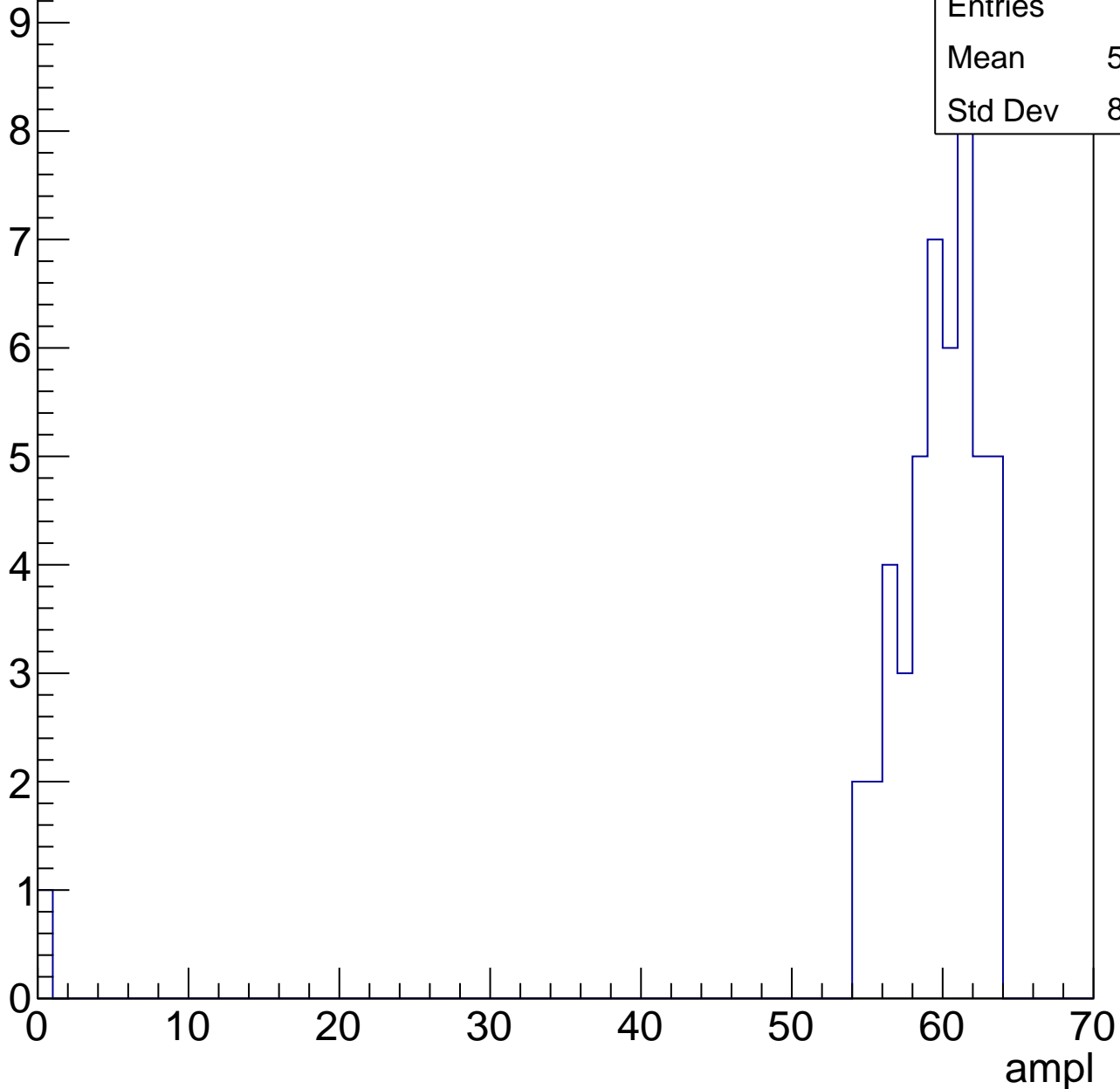


B1L100S, U19-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.16
Std Dev	8.747



B1L100S, U19-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch105, adc0

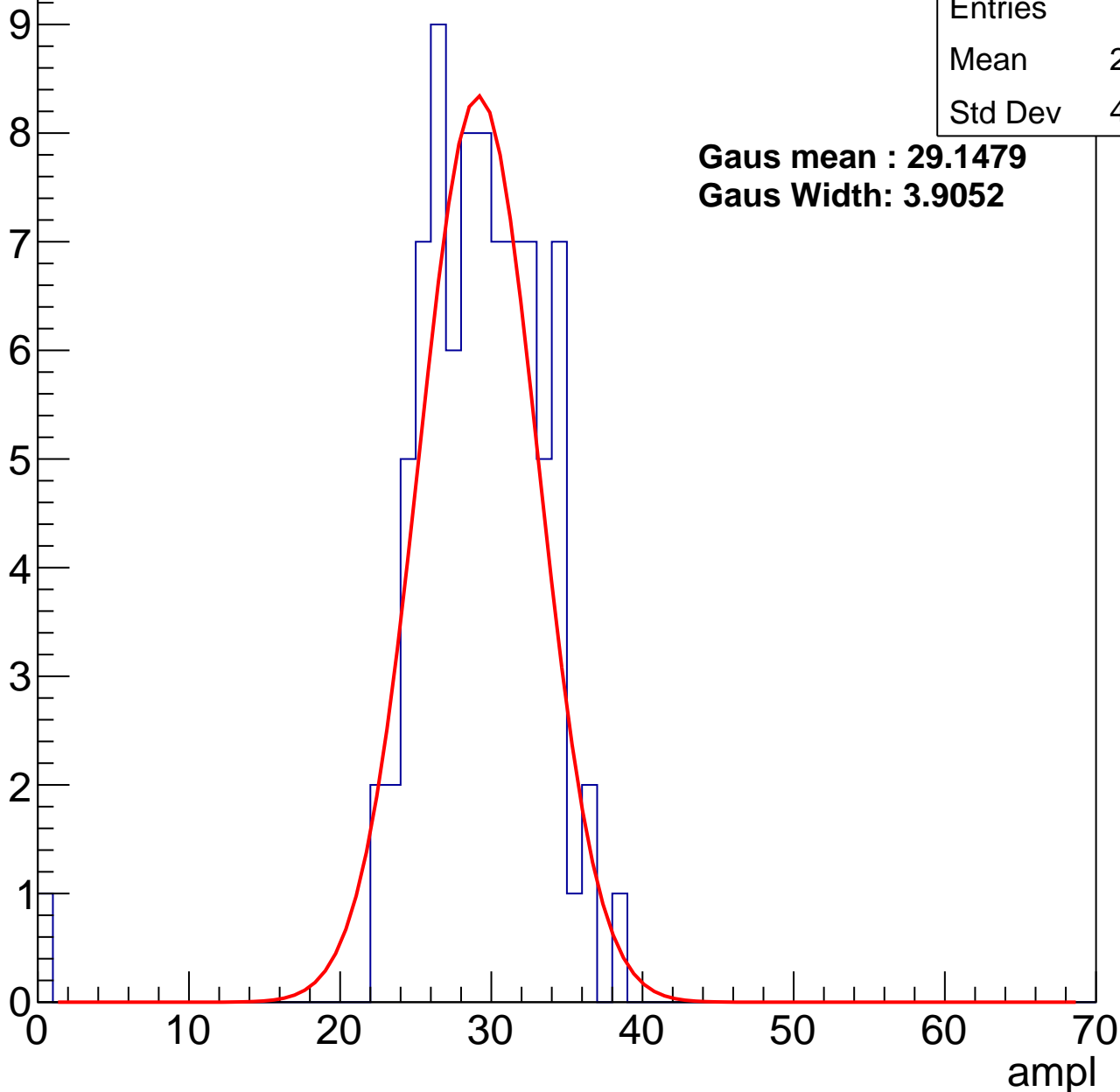
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	28.66
Std Dev	4.752

Gaus mean : 29.1479

Gaus Width: 3.9052



B1L100S, U19-ch105, adc1

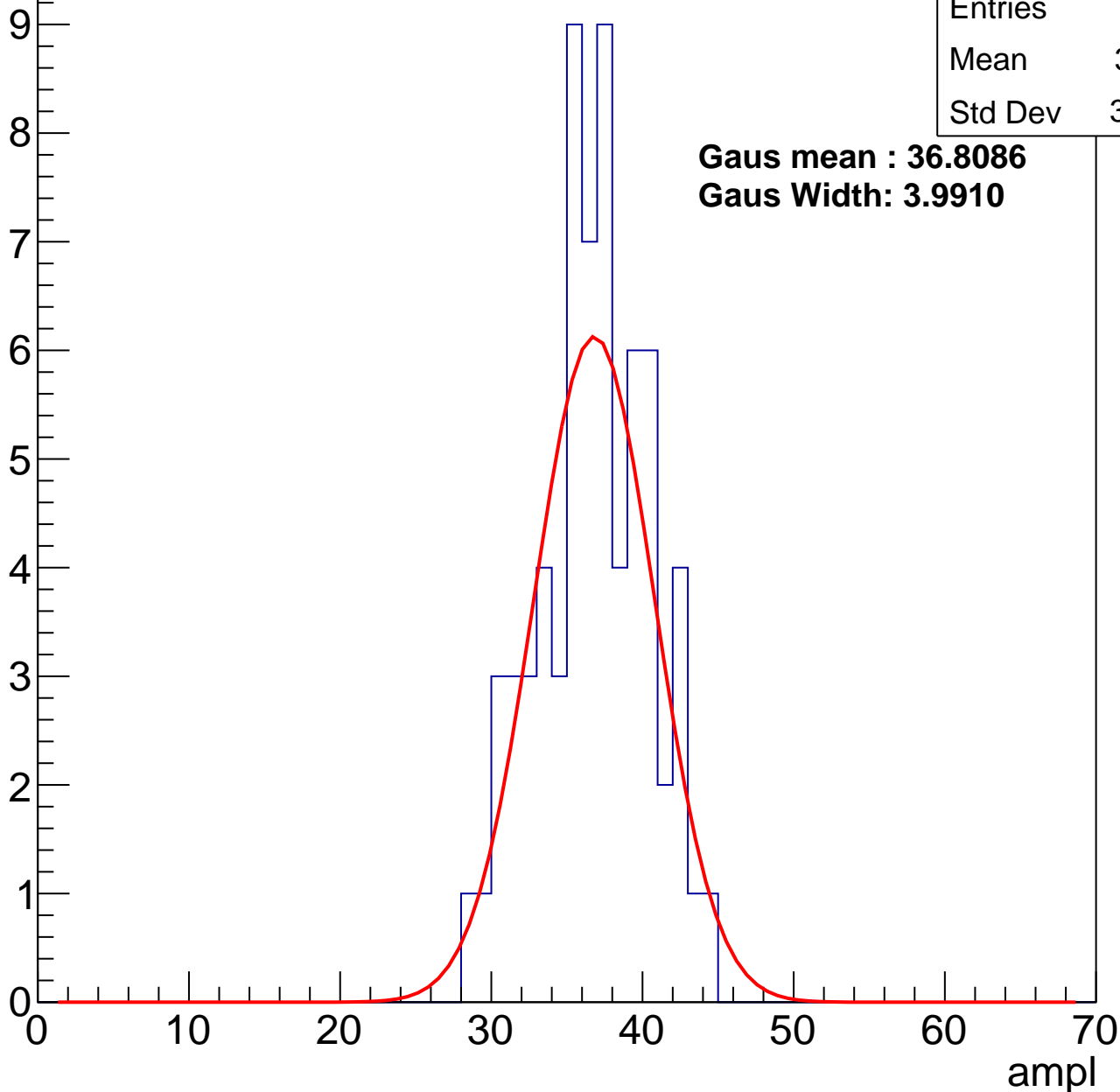
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.31
Std Dev	3.617

Gaus mean : 36.8086

Gaus Width: 3.9910

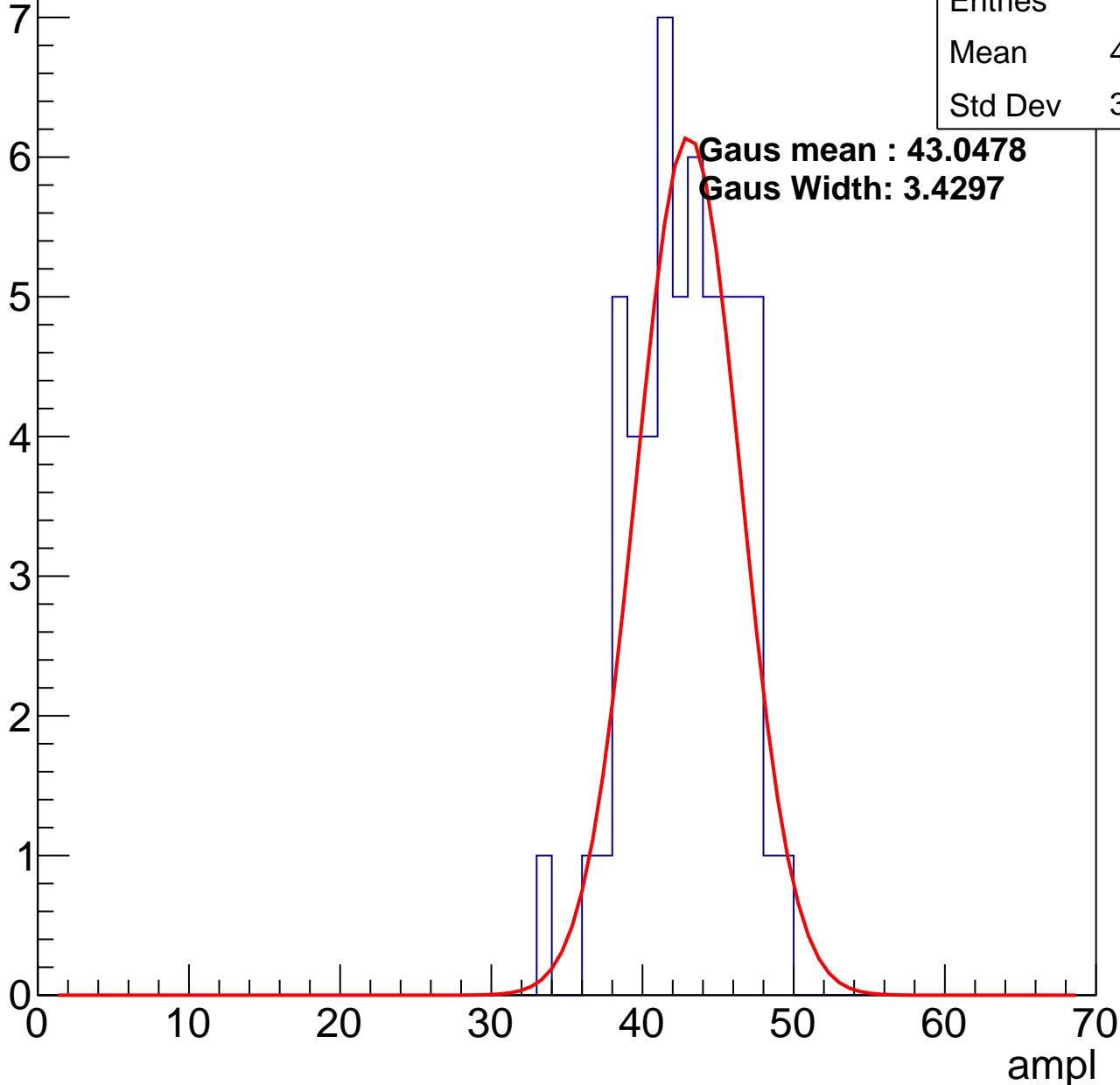


B1L100S, U19-ch105, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.39
Std Dev	3.363

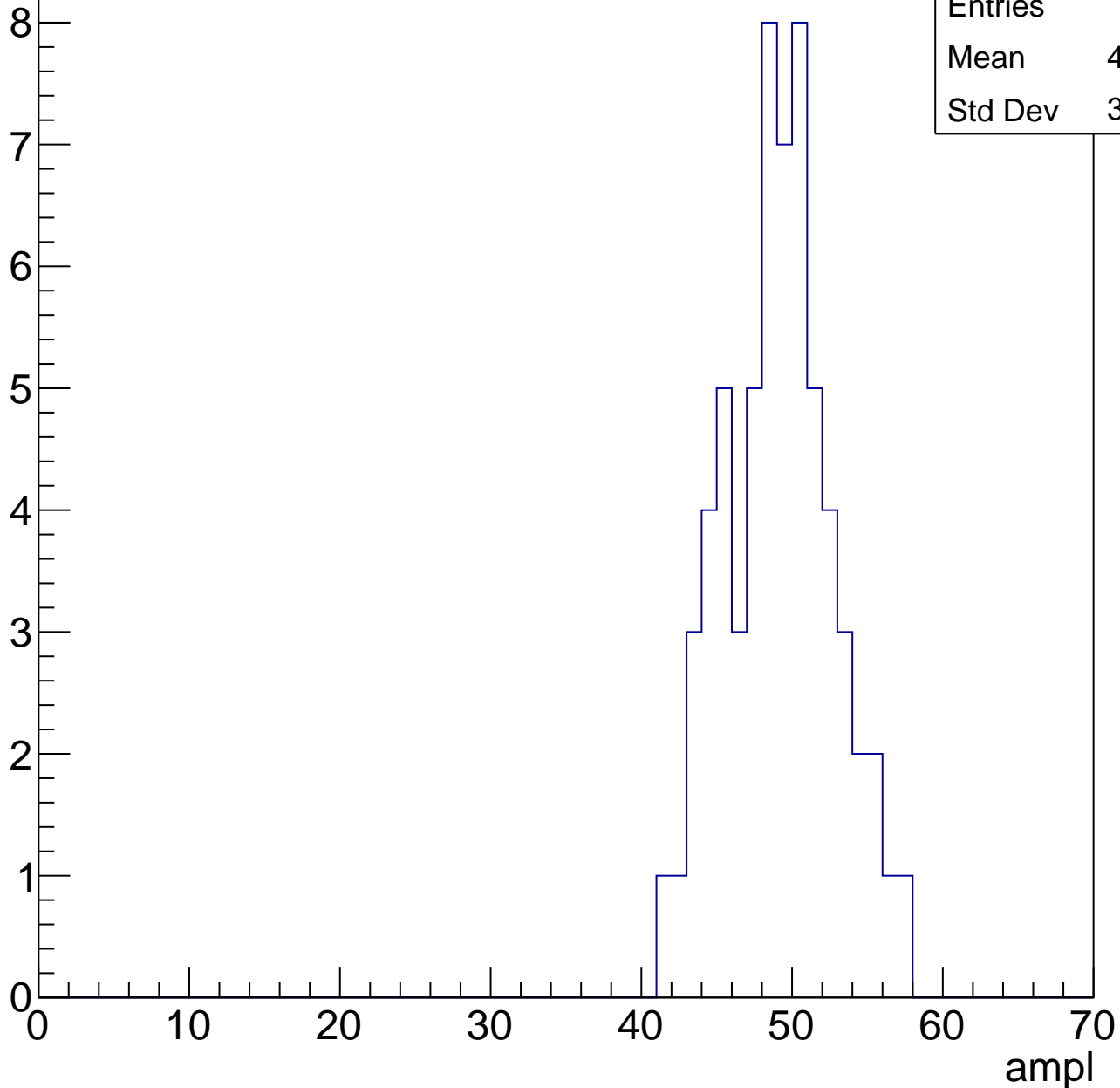


B1L100S, U19-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.67
Std Dev	3.555

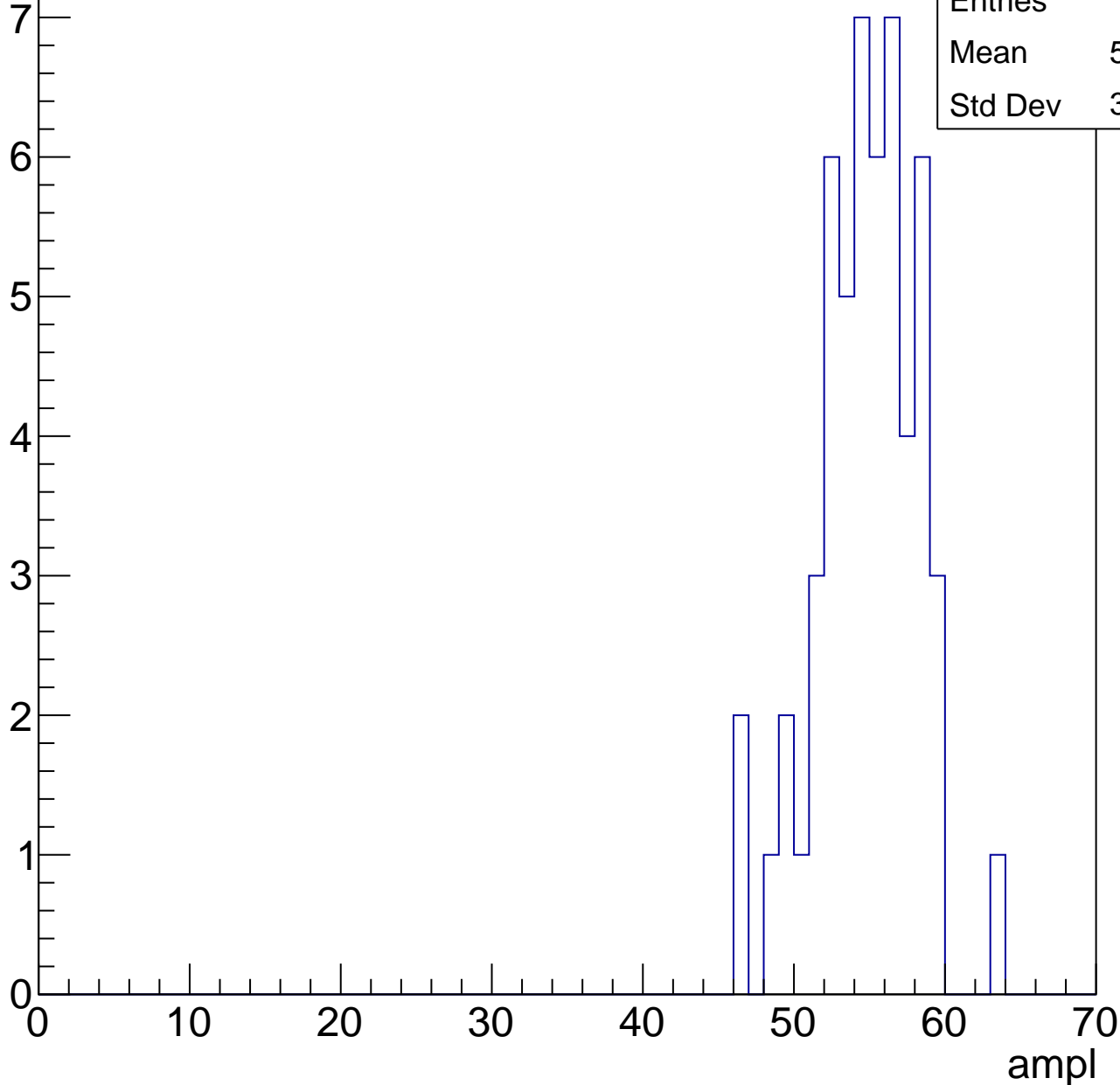


B1L100S, U19-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	54.33
Std Dev	3.339

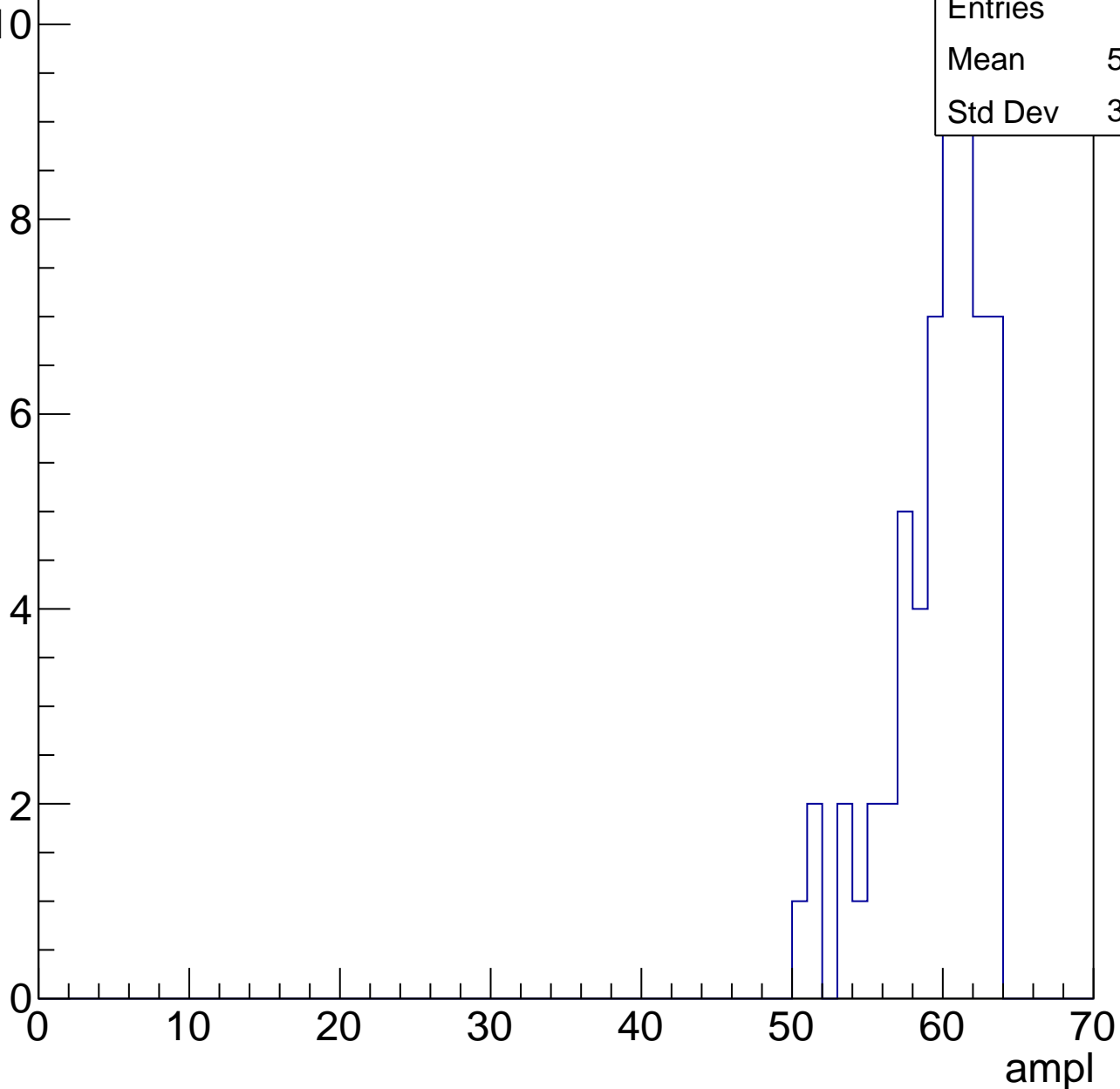


B1L100S, U19-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

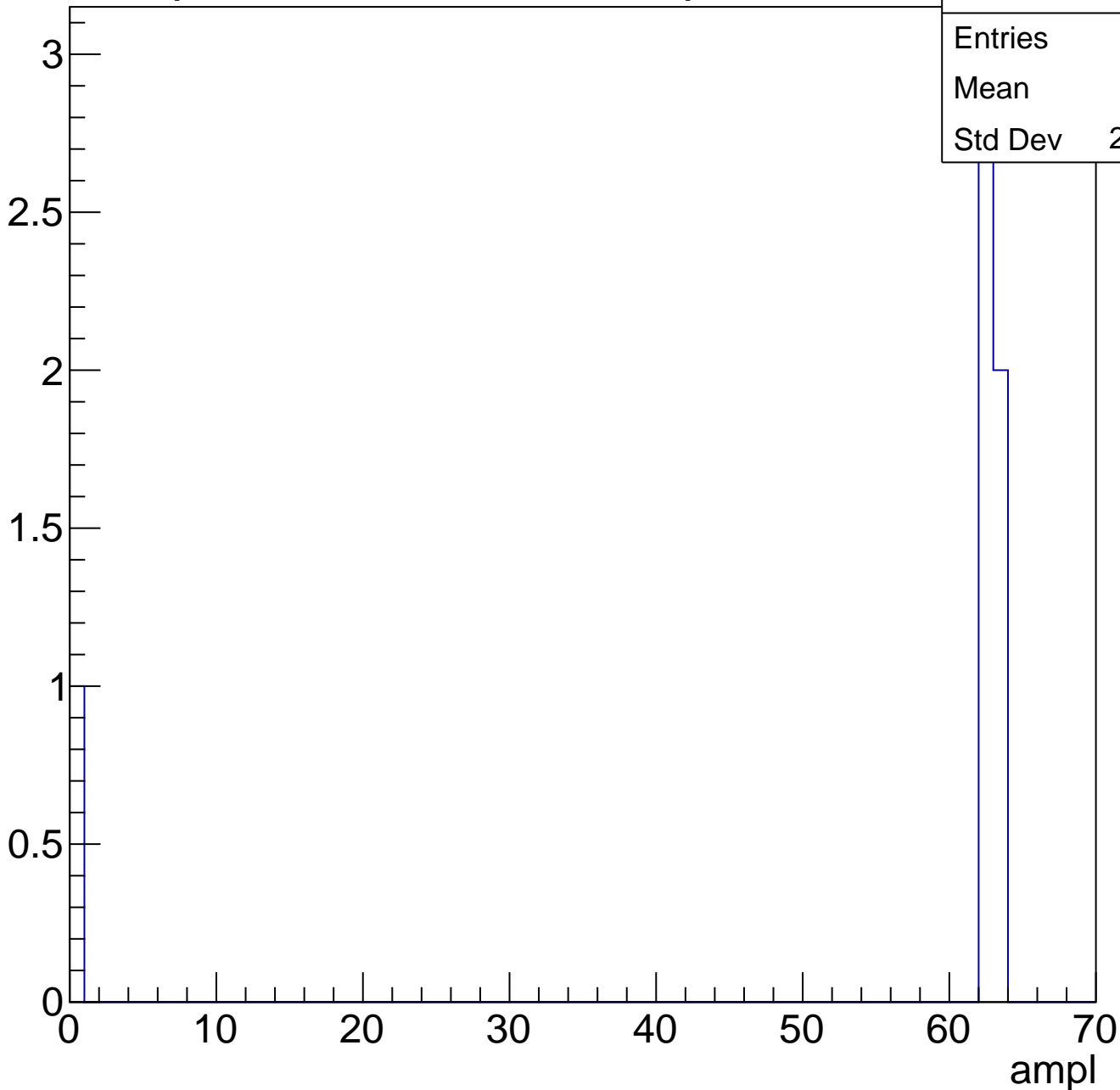
Entries	60
Mean	59.15
Std Dev	3.172



B1L100S, U19-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U19-ch106, adc0

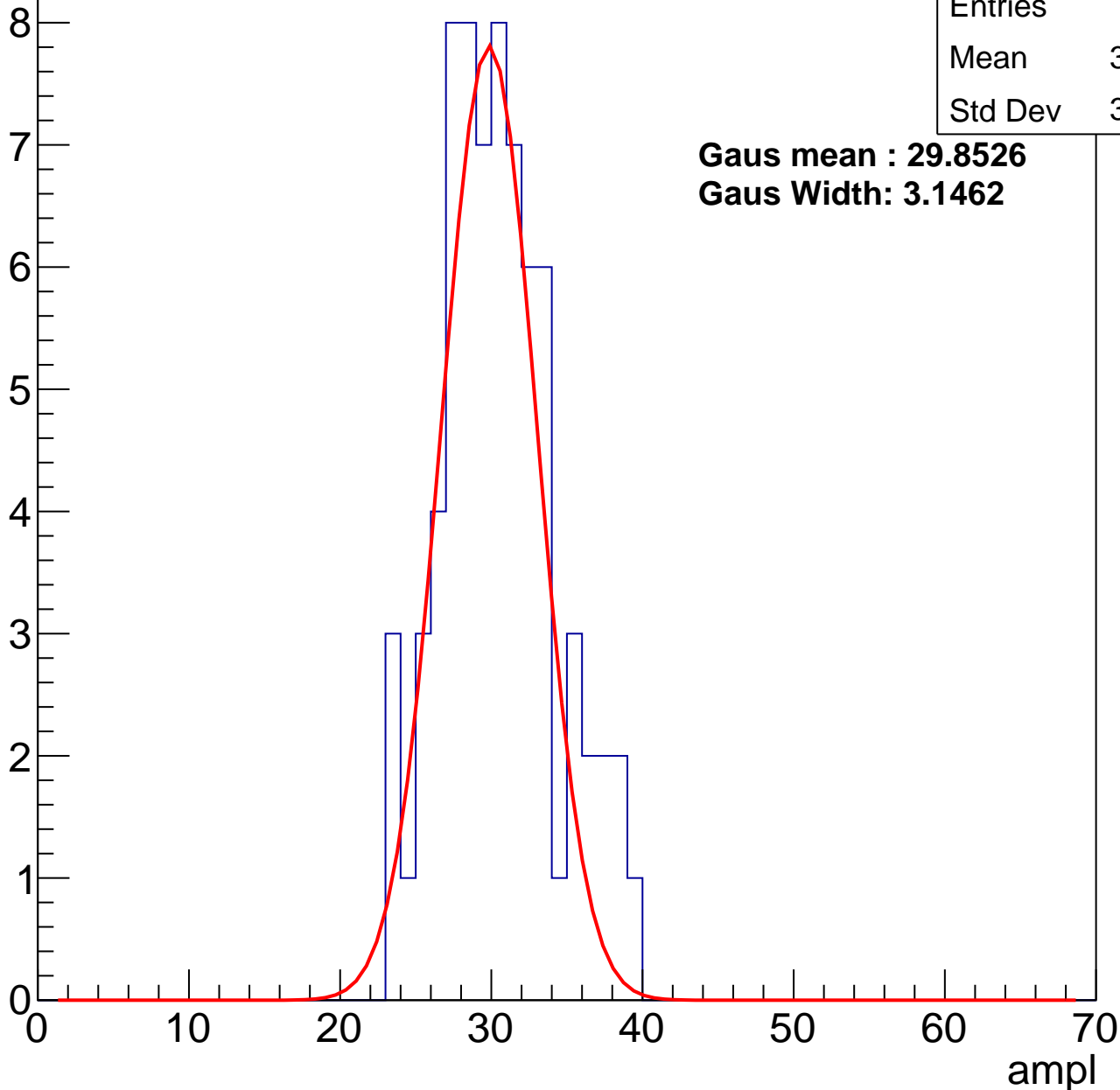
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.03
Std Dev	3.704

Gaus mean : 29.8526

Gaus Width: 3.1462



B1L100S, U19-ch106, adc1

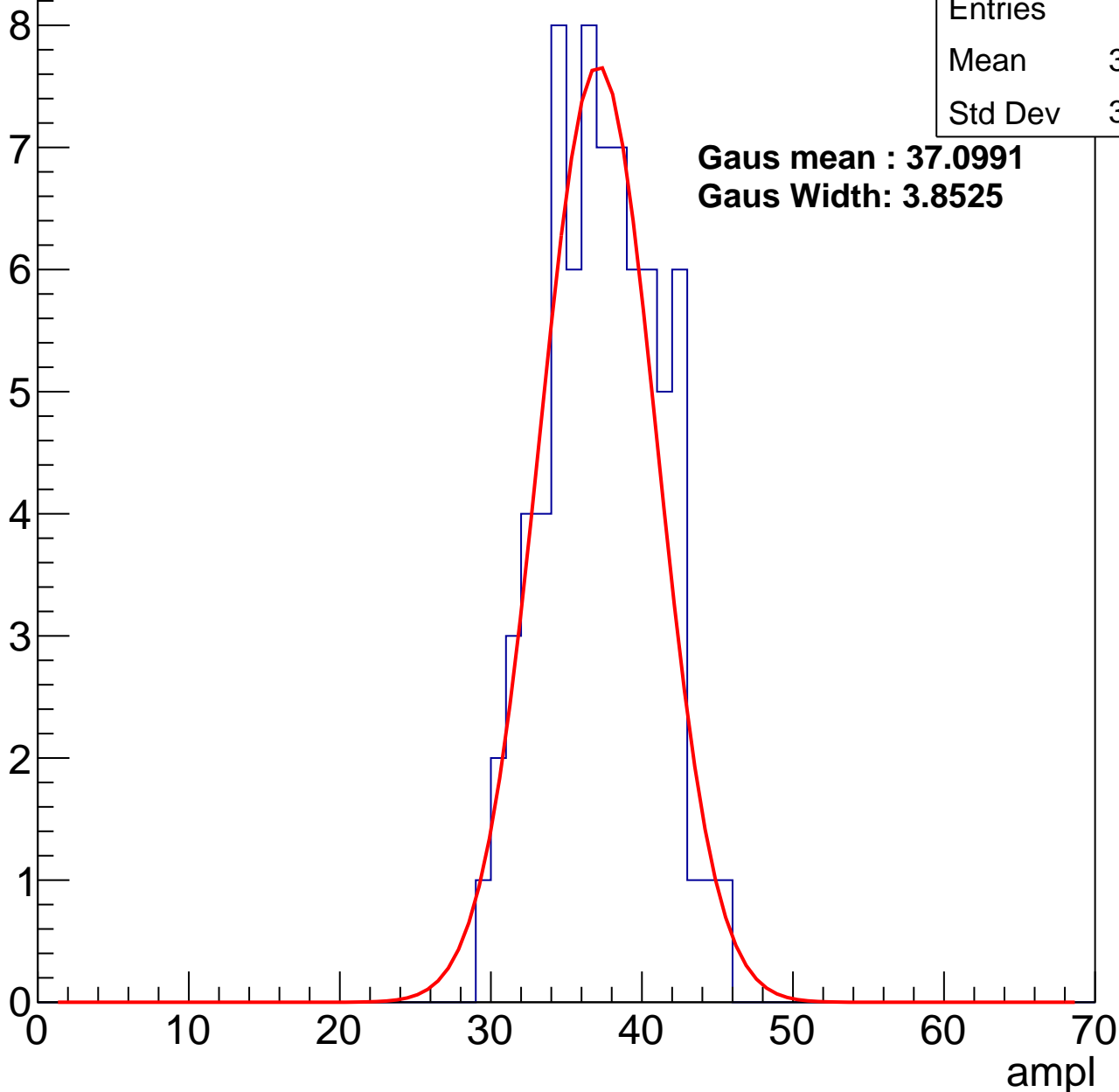
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.84
Std Dev	3.628

Gaus mean : 37.0991

Gaus Width: 3.8525



B1L100S, U19-ch106, adc2

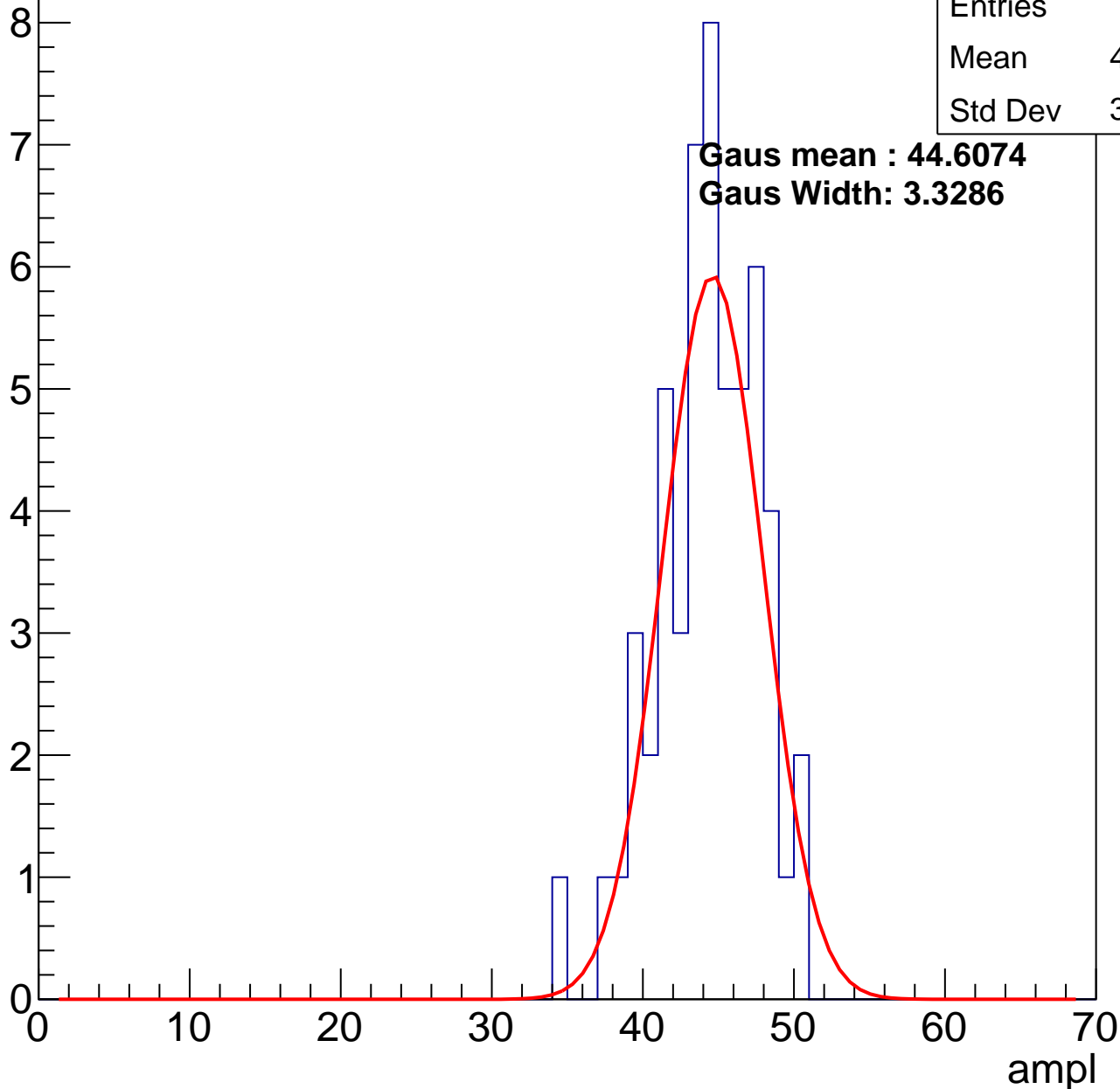
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.85
Std Dev	3.319

Gaus mean : 44.6074

Gaus Width: 3.3286

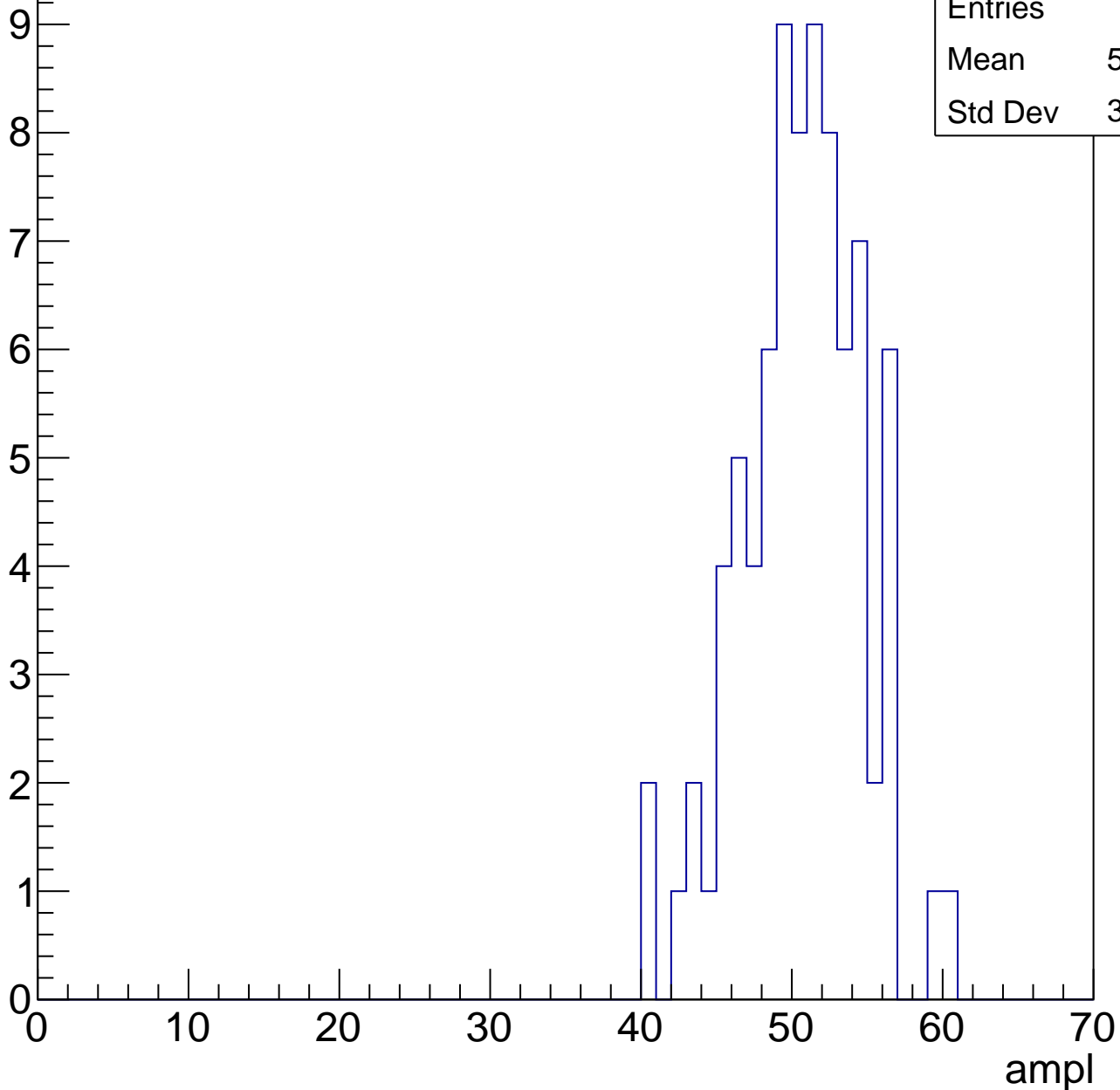


B1L100S, U19-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	50.18
Std Dev	3.979

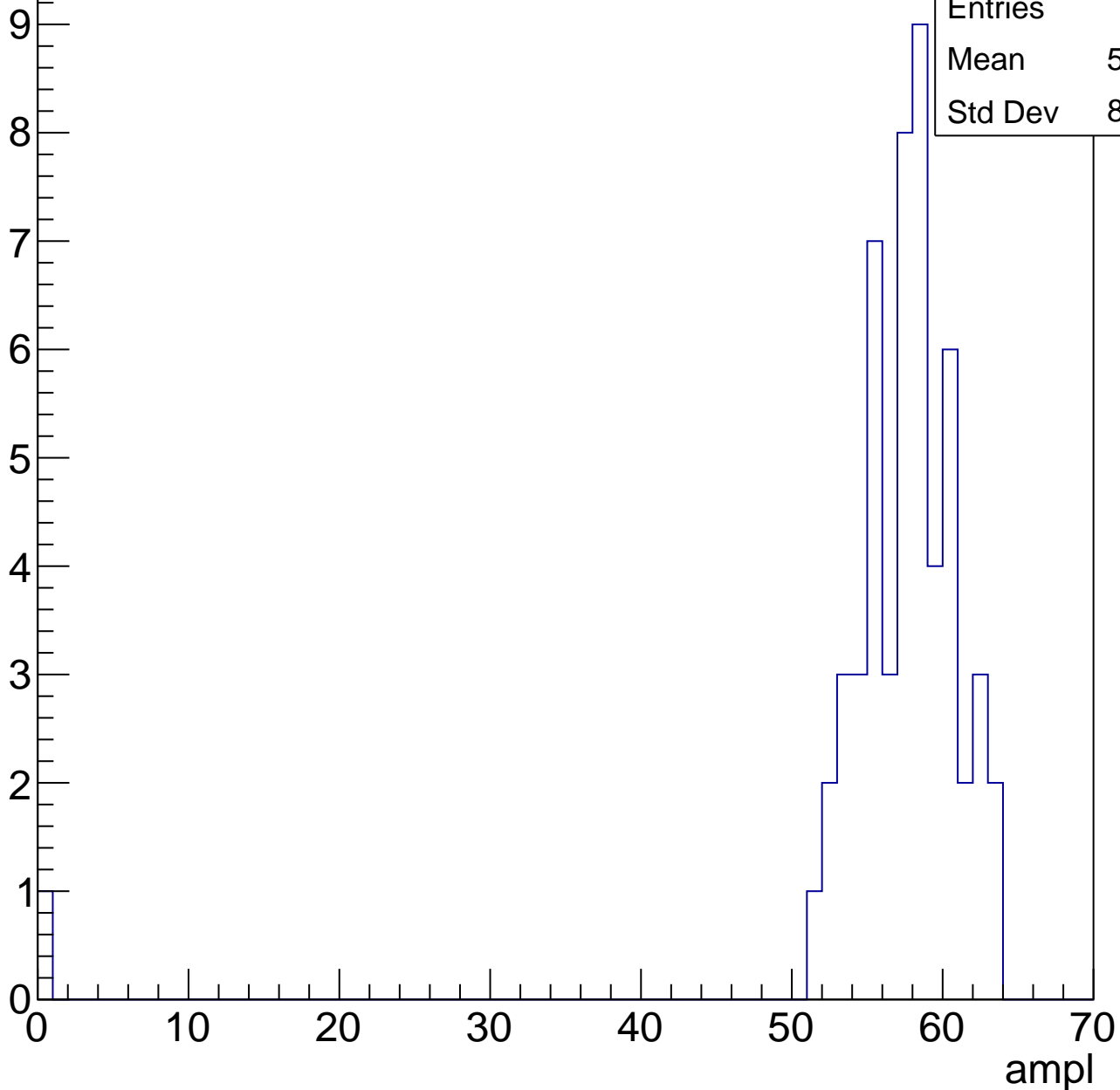


B1L100S, U19-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.24
Std Dev	8.242

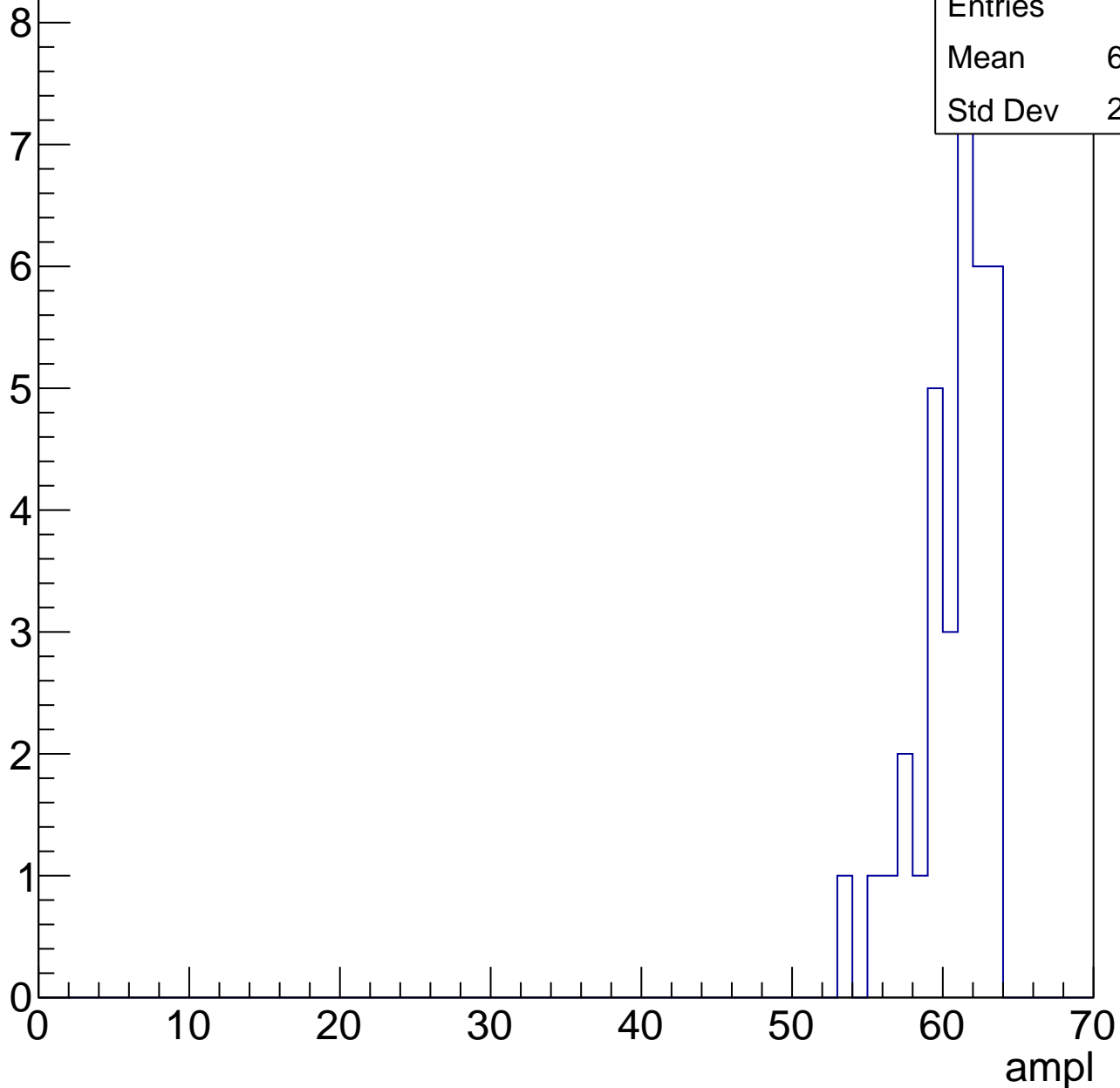


B1L100S, U19-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	60.26
Std Dev	2.429



B1L100S, U19-ch106, adc6

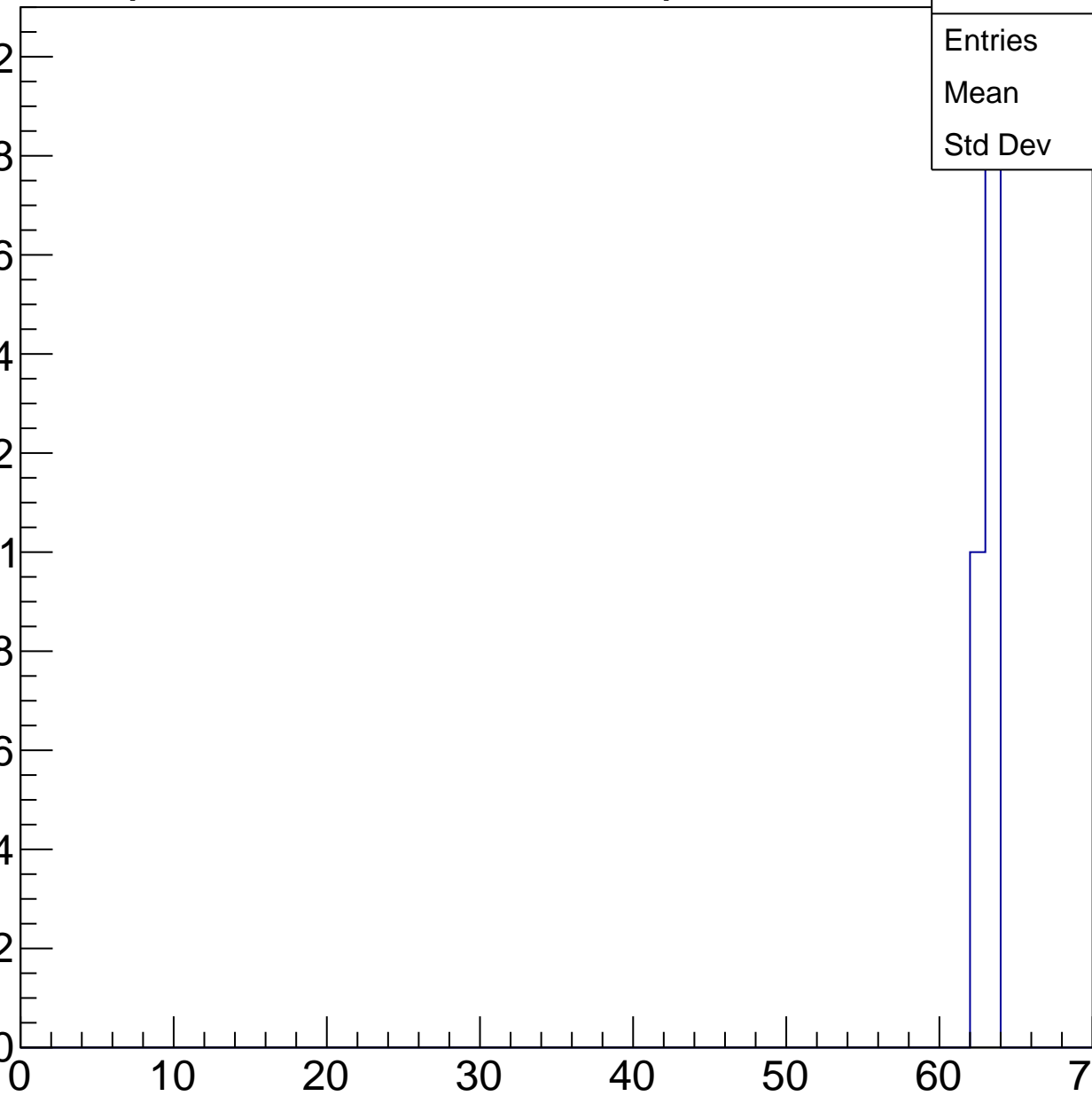
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L100S, U19-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L100S, U19-ch107, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	26.34
Std Dev	4.486

Gaus mean : 27.2211

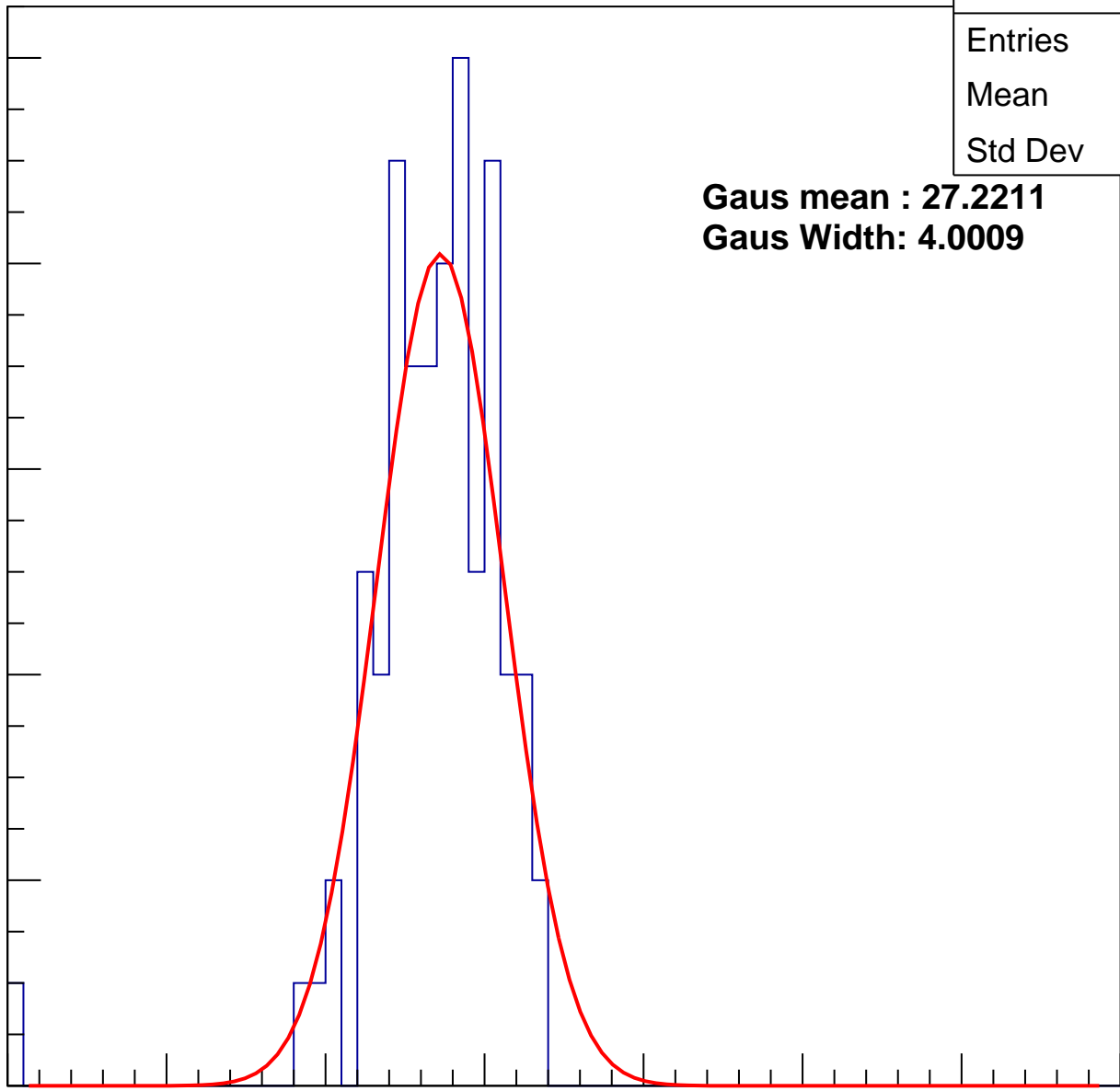
Gaus Width: 4.0009

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch107, adc1

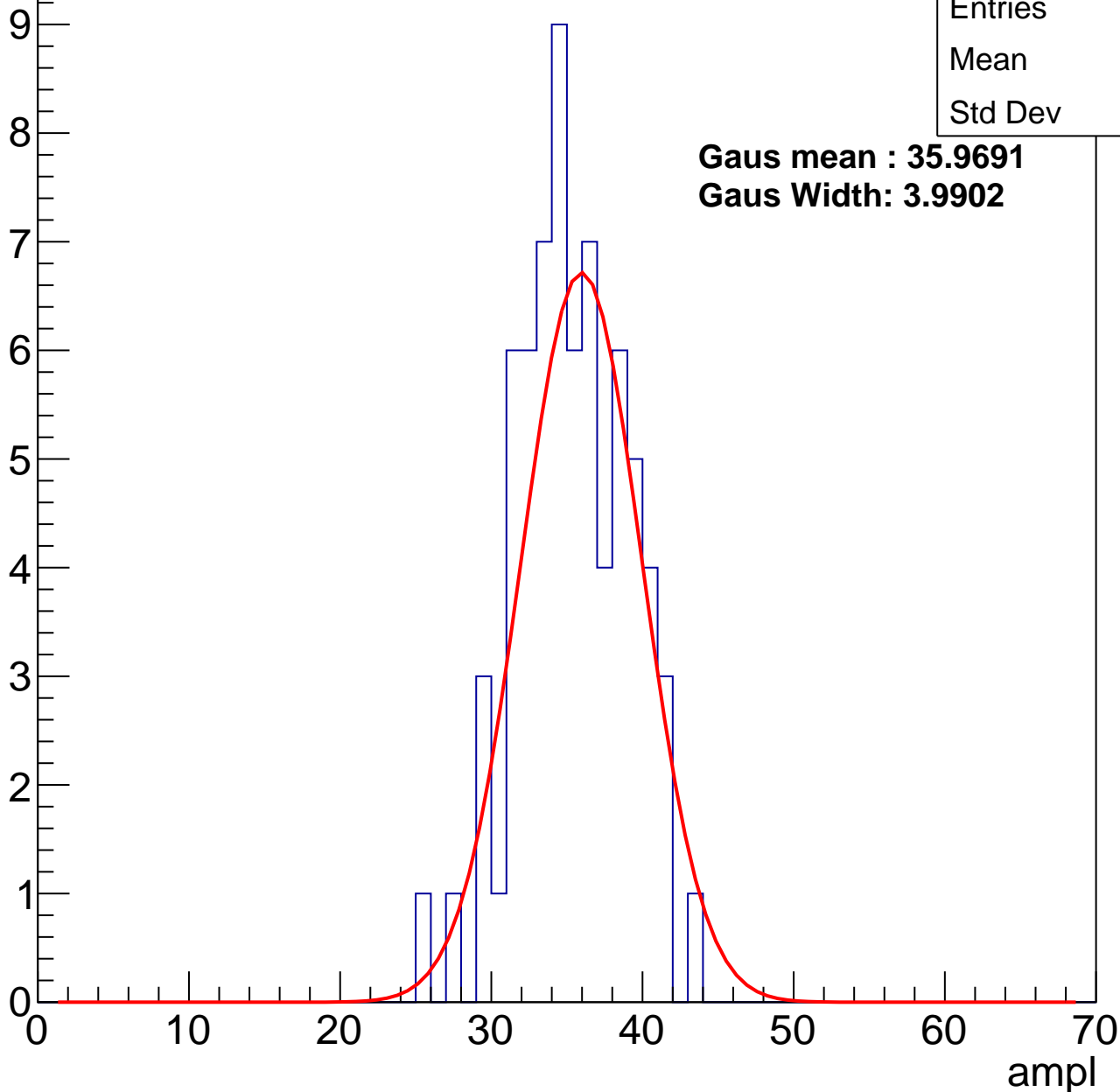
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.9
Std Dev	3.61

Gaus mean : 35.9691

Gaus Width: 3.9902



B1L100S, U19-ch107, adc2

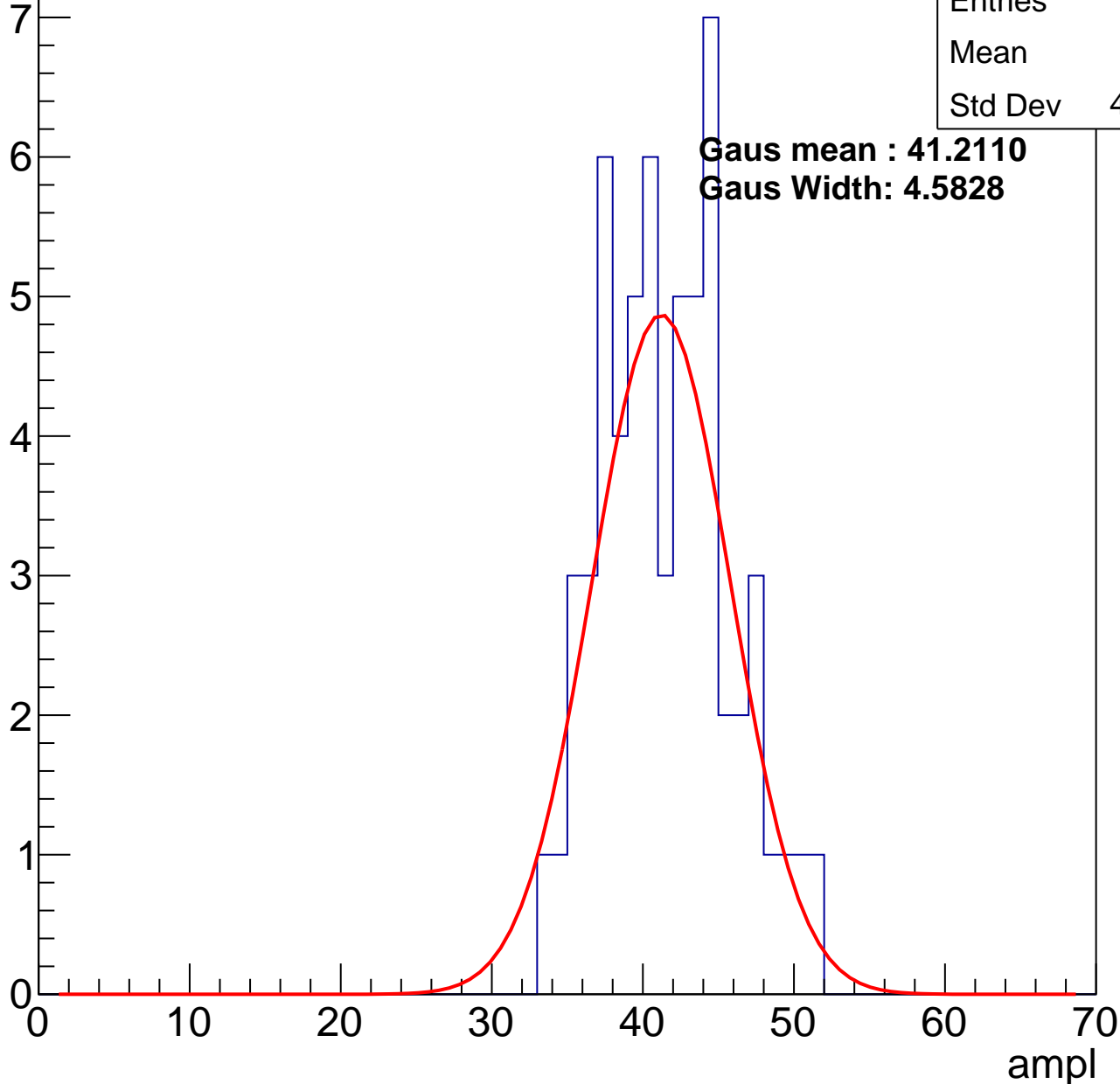
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.1
Std Dev	4.134

Gaus mean : 41.2110

Gaus Width: 4.5828

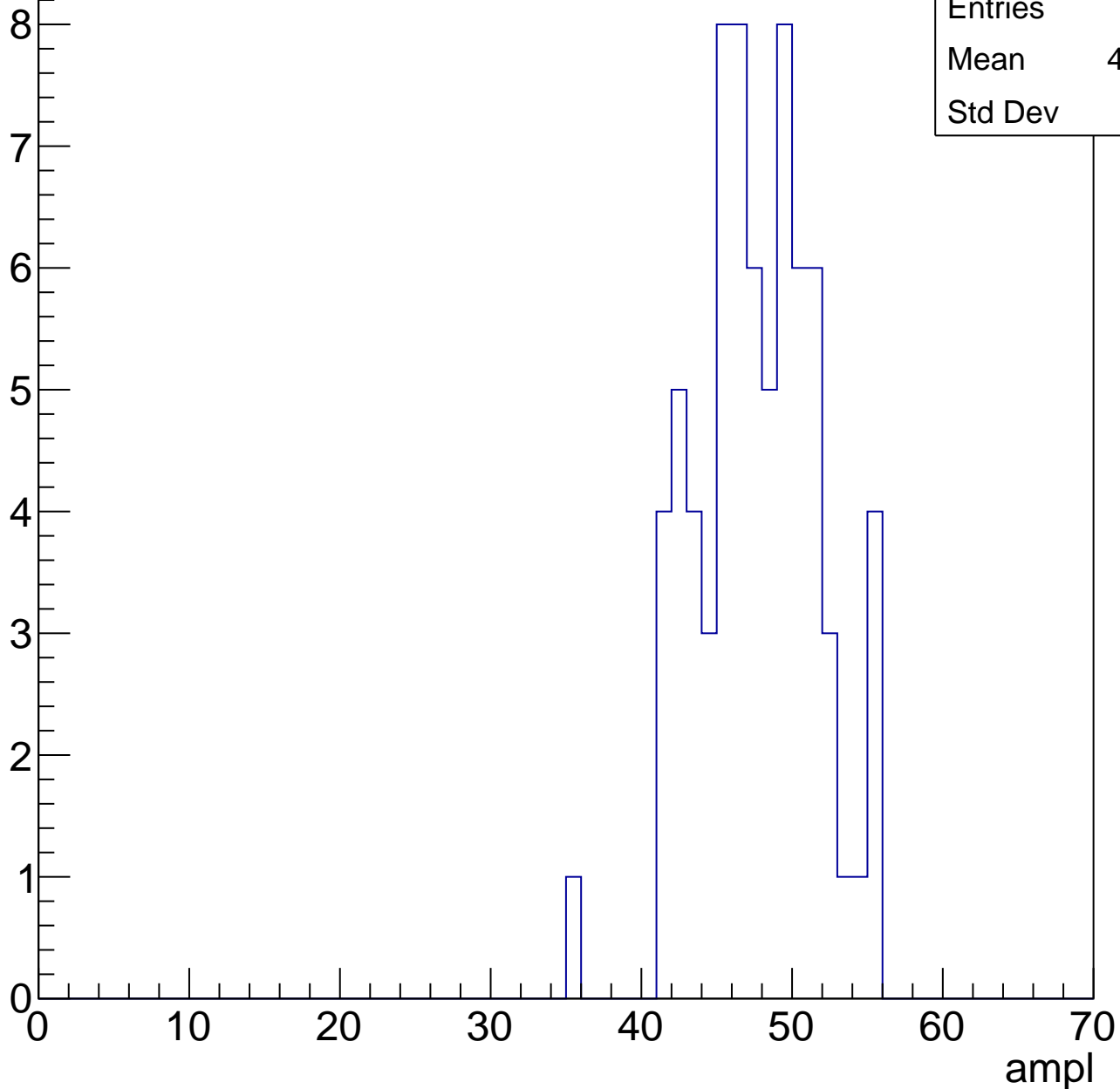


B1L100S, U19-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	47.18
Std Dev	3.96

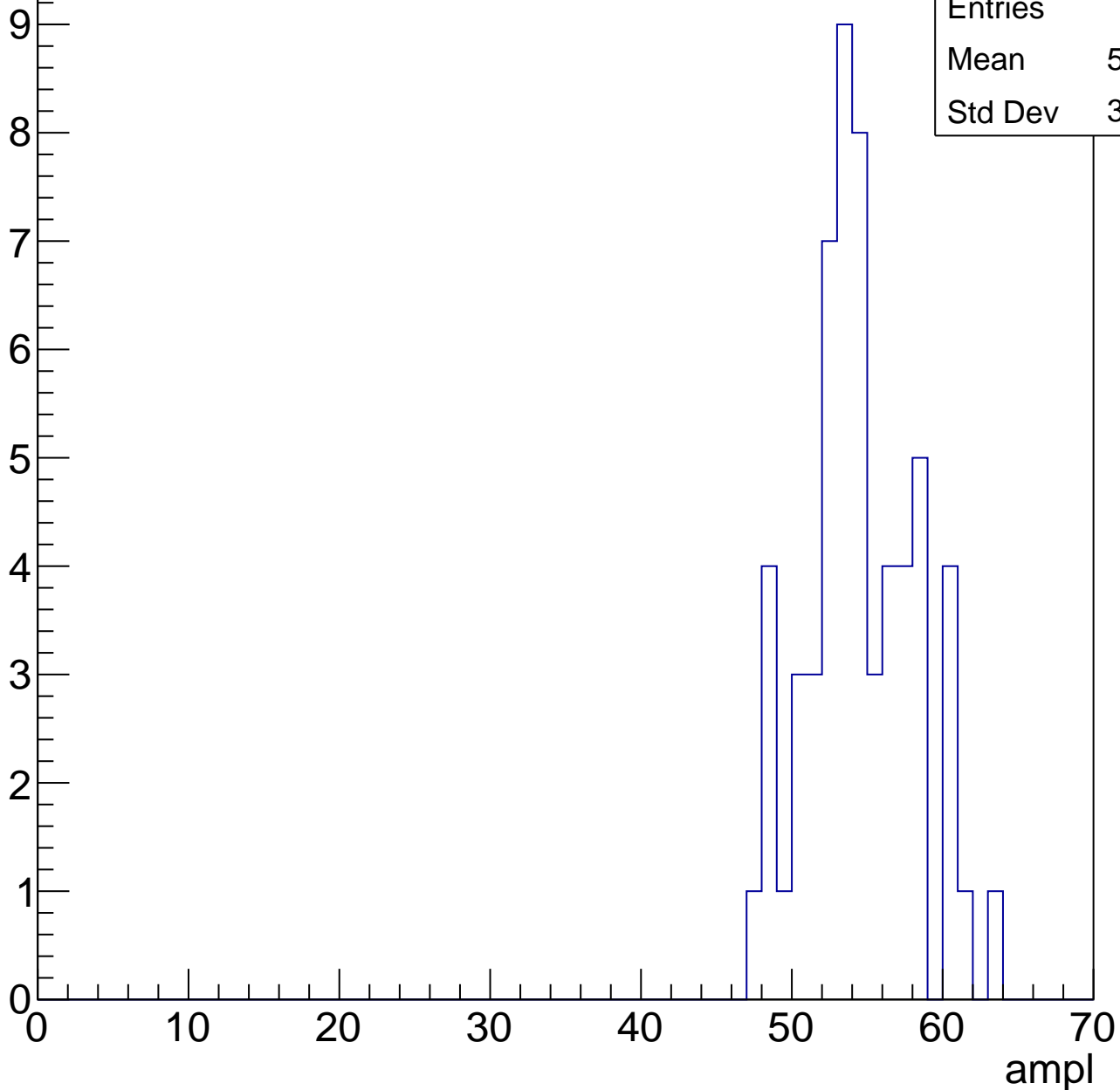


B1L100S, U19-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

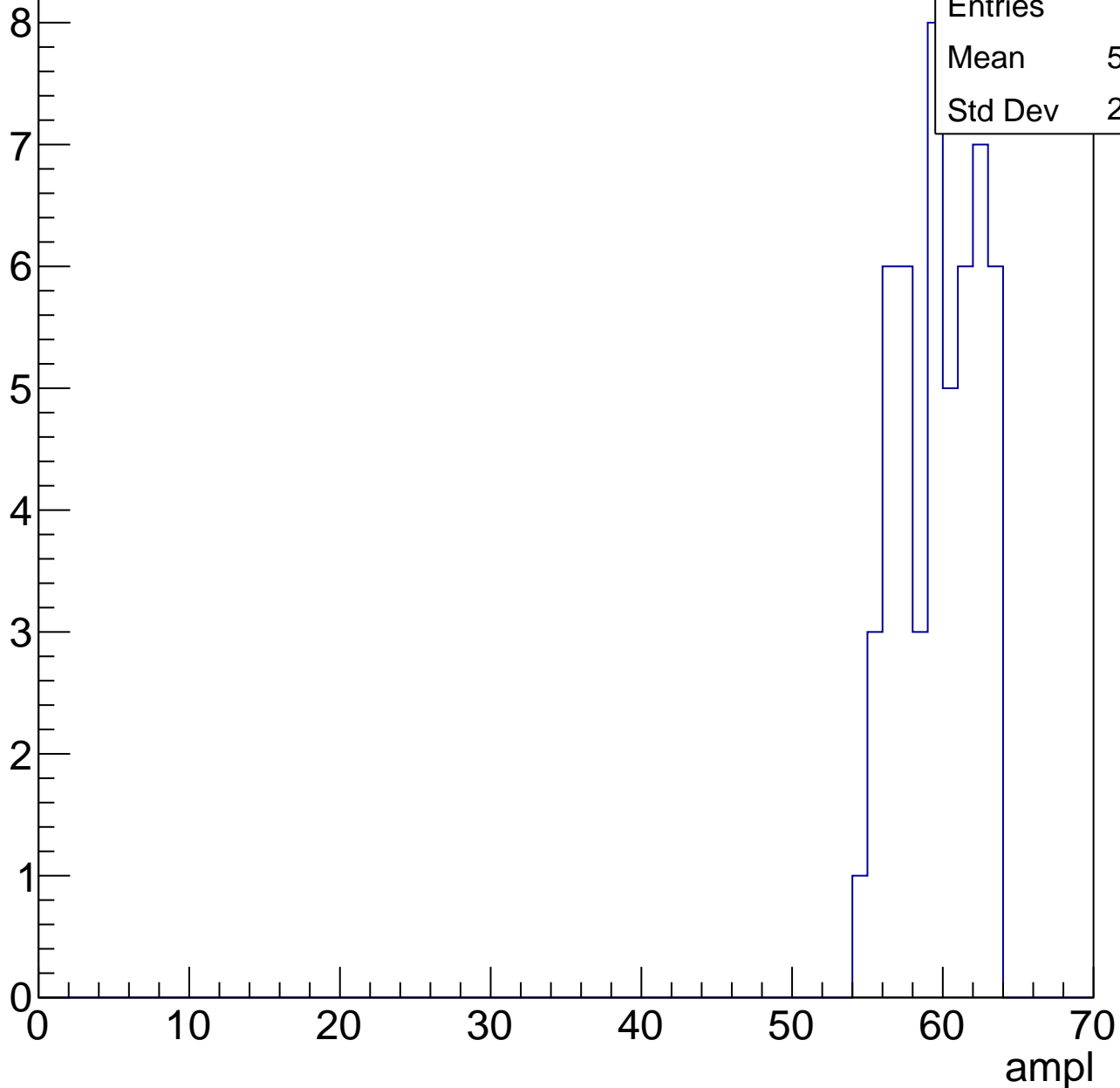
Entries	58
Mean	54.05
Std Dev	3.569



B1L100S, U19-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

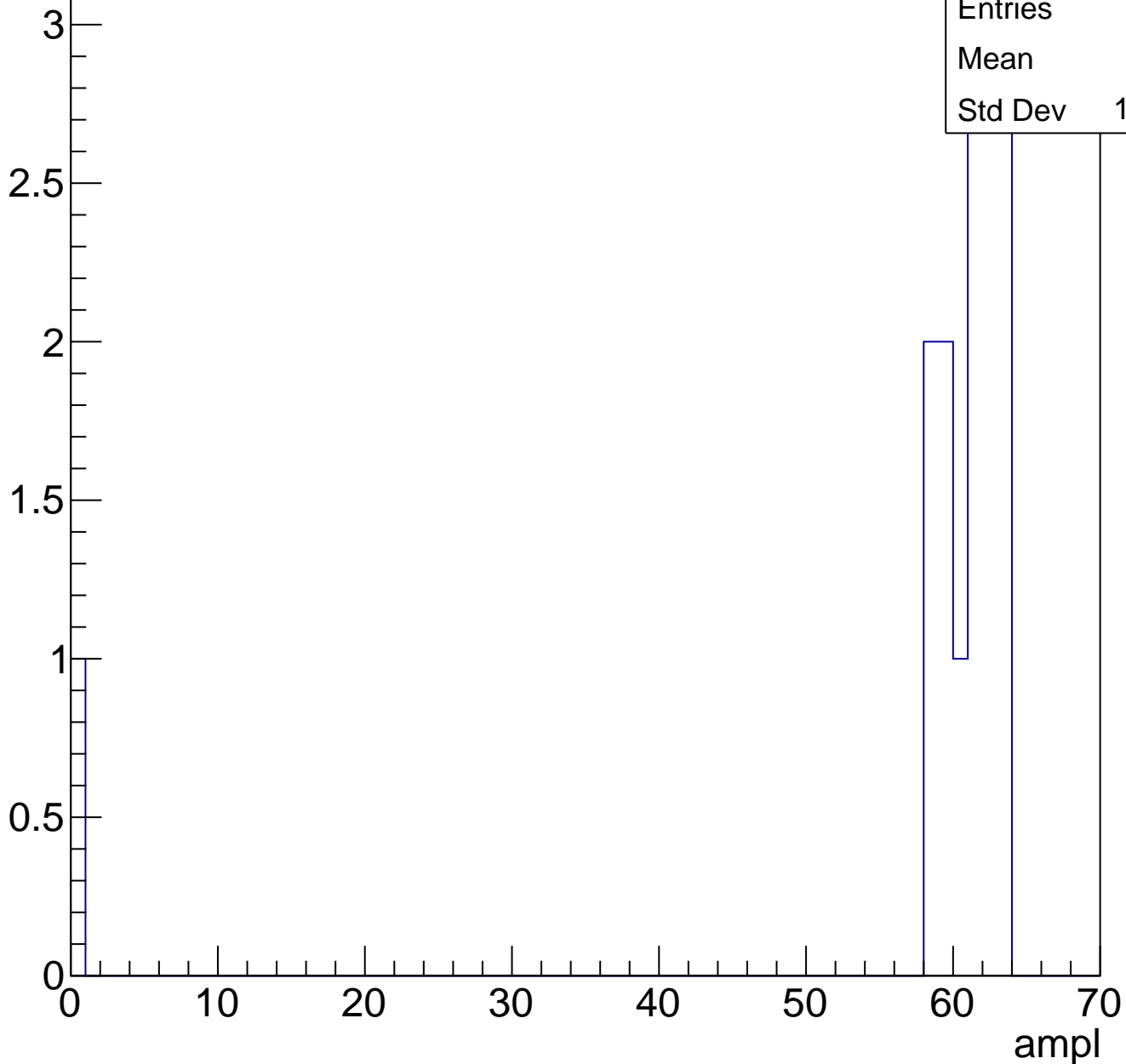
Entry



B1L100S, U19-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U19-ch108, adc0

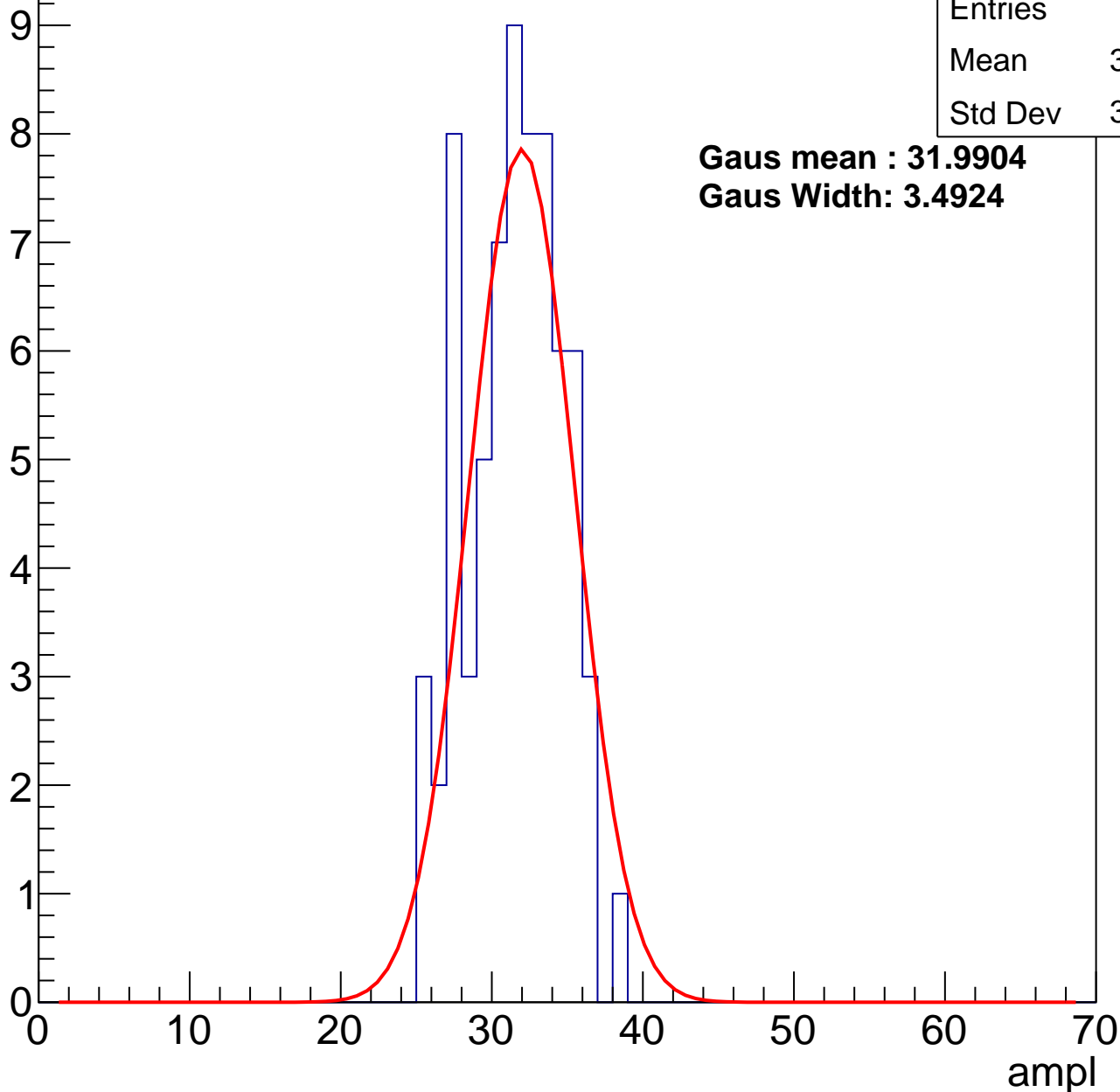
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	31.03
Std Dev	3.079

Gaus mean : 31.9904

Gaus Width: 3.4924



B1L100S, U19-ch108, adc1

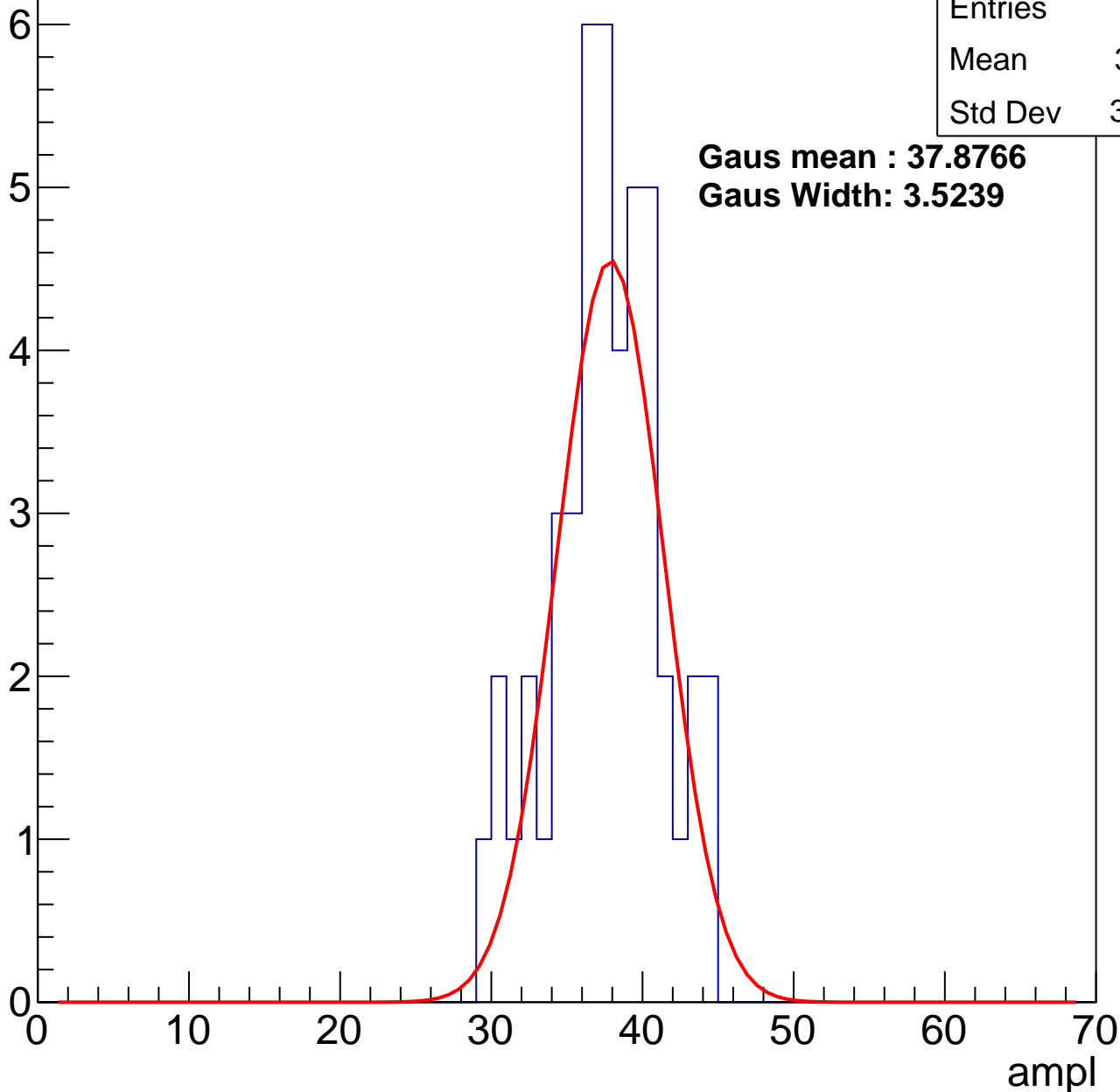
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	37.11
Std Dev	3.625

Gaus mean : 37.8766

Gaus Width: 3.5239



B1L100S, U19-ch108, adc2

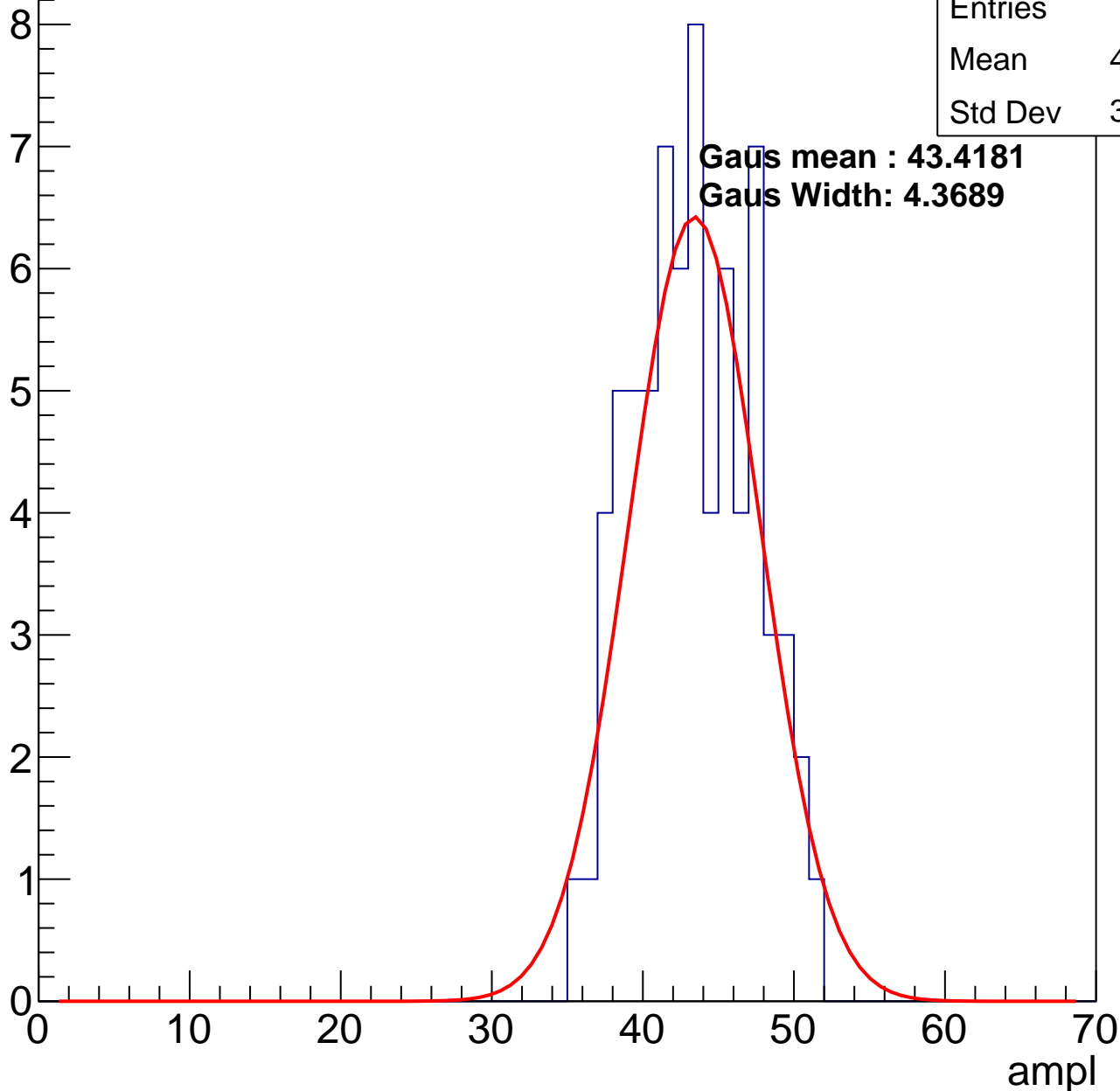
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.89
Std Dev	3.839

Gaus mean : 43.4181

Gaus Width: 4.3689

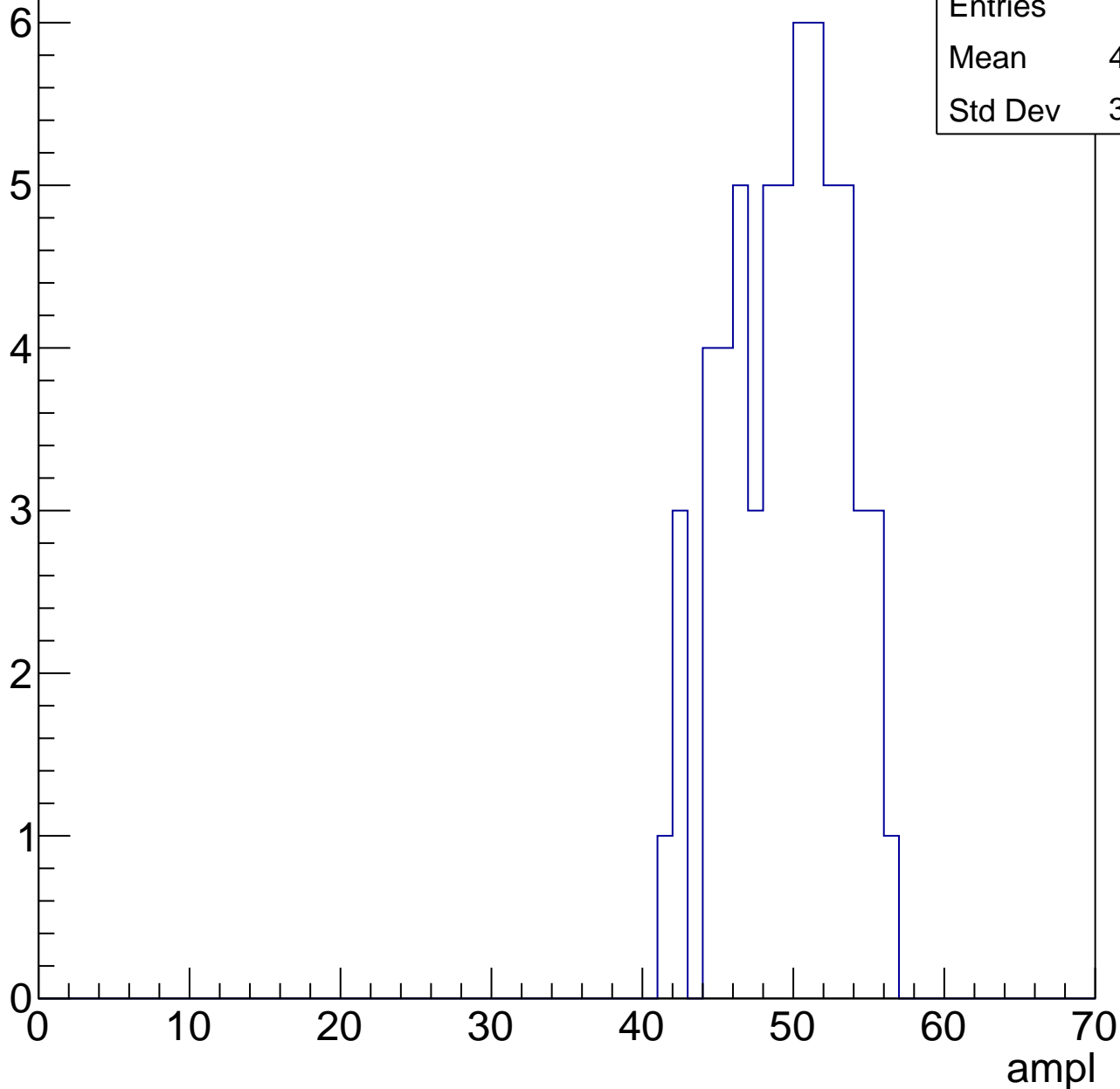


B1L100S, U19-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.03
Std Dev	3.737

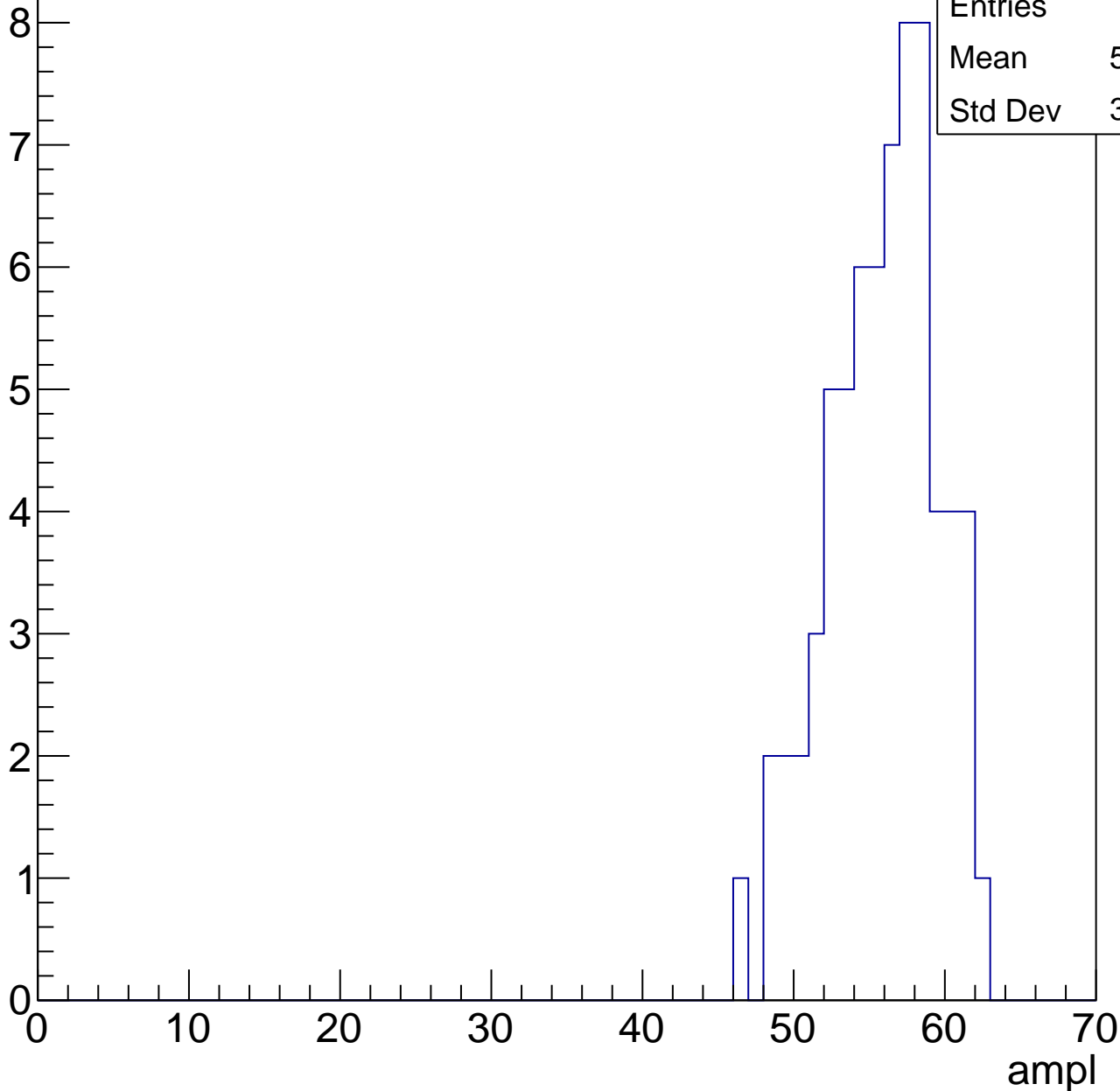


B1L100S, U19-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	55.38
Std Dev	3.593

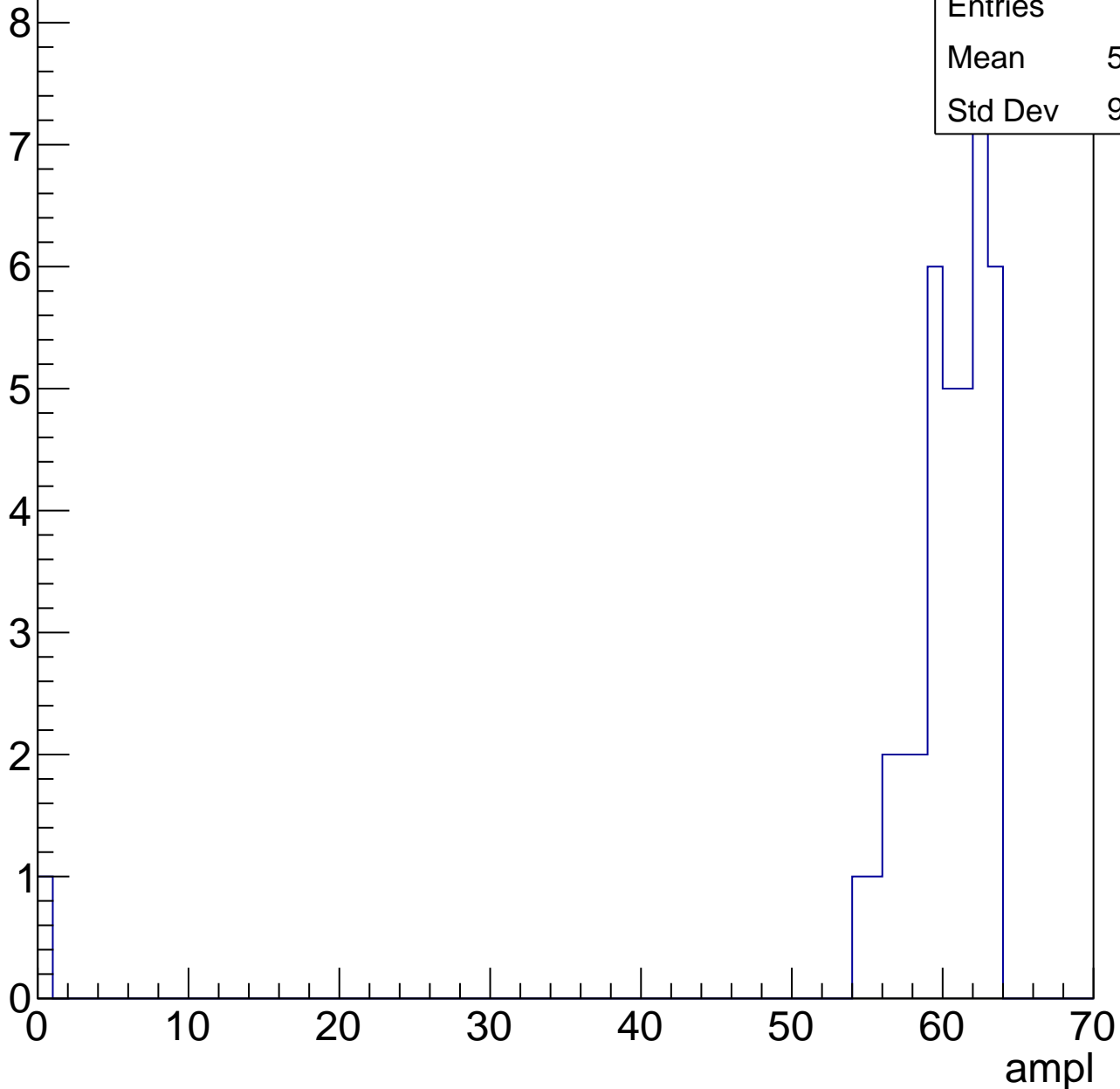


B1L100S, U19-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	58.56
Std Dev	9.787



B1L100S, U19-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch109, adc0

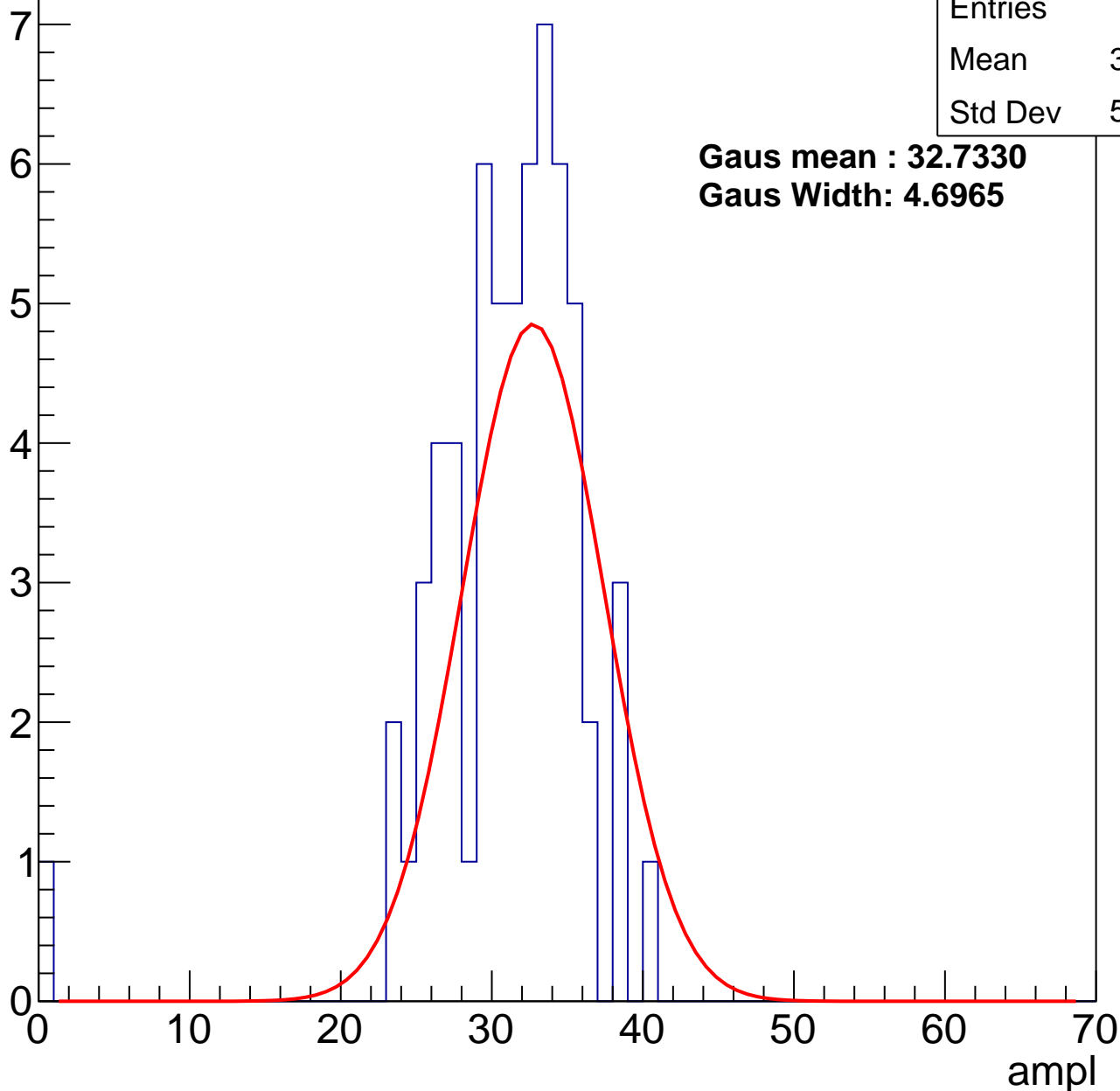
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.52
Std Dev	5.506

Gaus mean : 32.7330

Gaus Width: 4.6965



B1L100S, U19-ch109, adc1

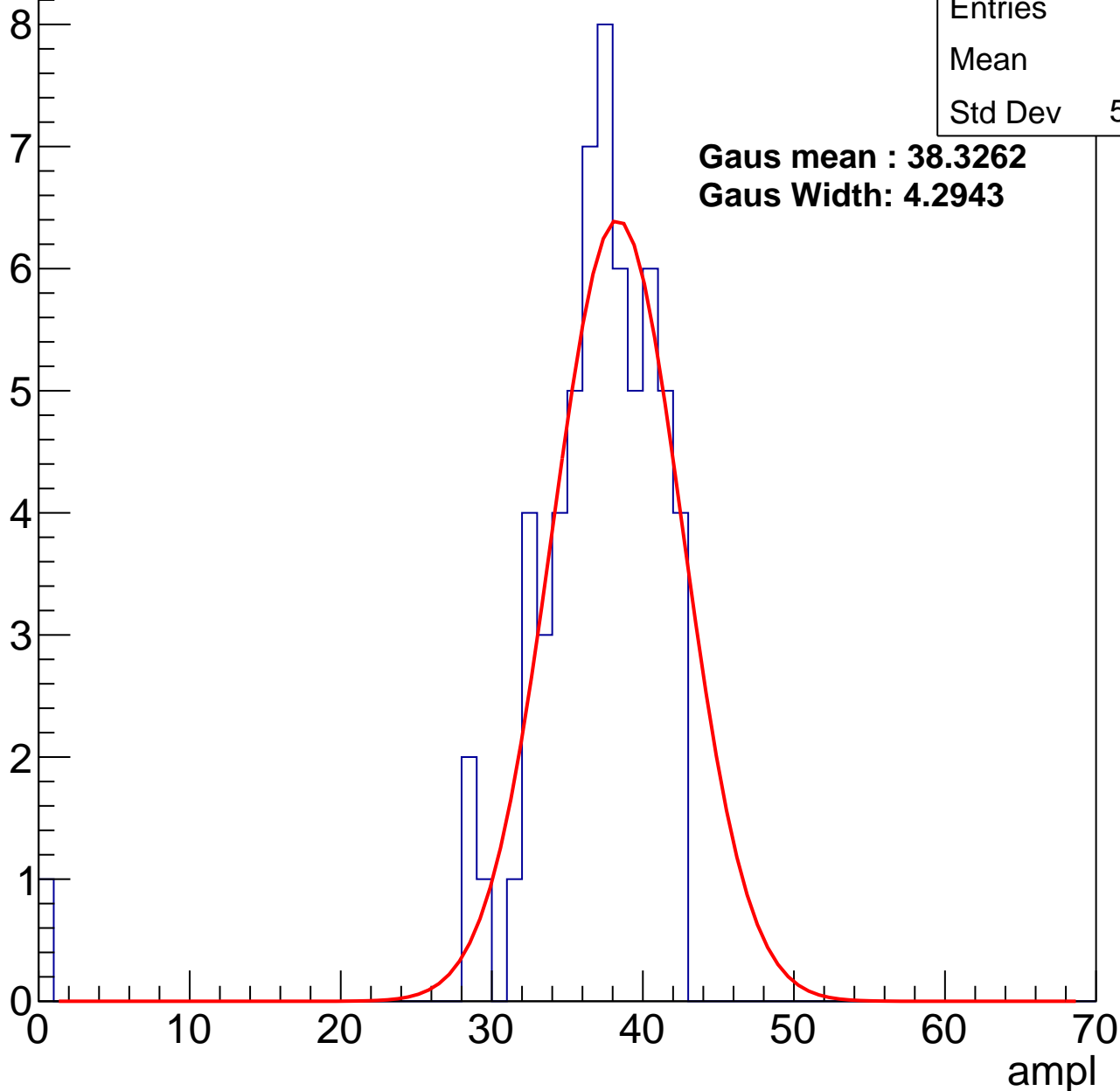
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.1
Std Dev	5.752

Gaus mean : 38.3262

Gaus Width: 4.2943



B1L100S, U19-ch109, adc2

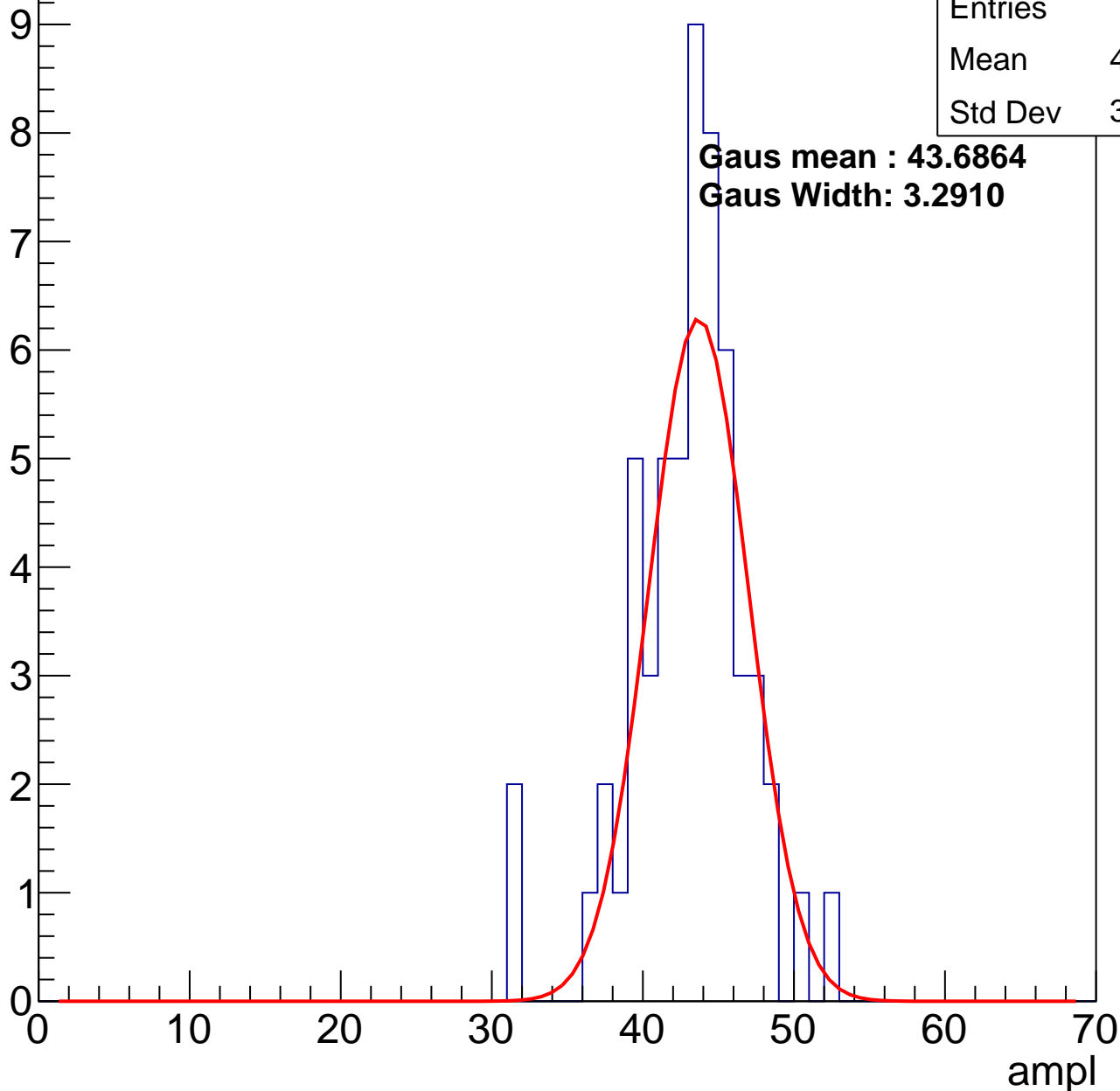
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.56
Std Dev	3.844

Gaus mean : 43.6864

Gaus Width: 3.2910



B1L100S, U19-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	49.34
Std Dev	3.301

Entry

10

8

6

4

2

0

0

10

20

30

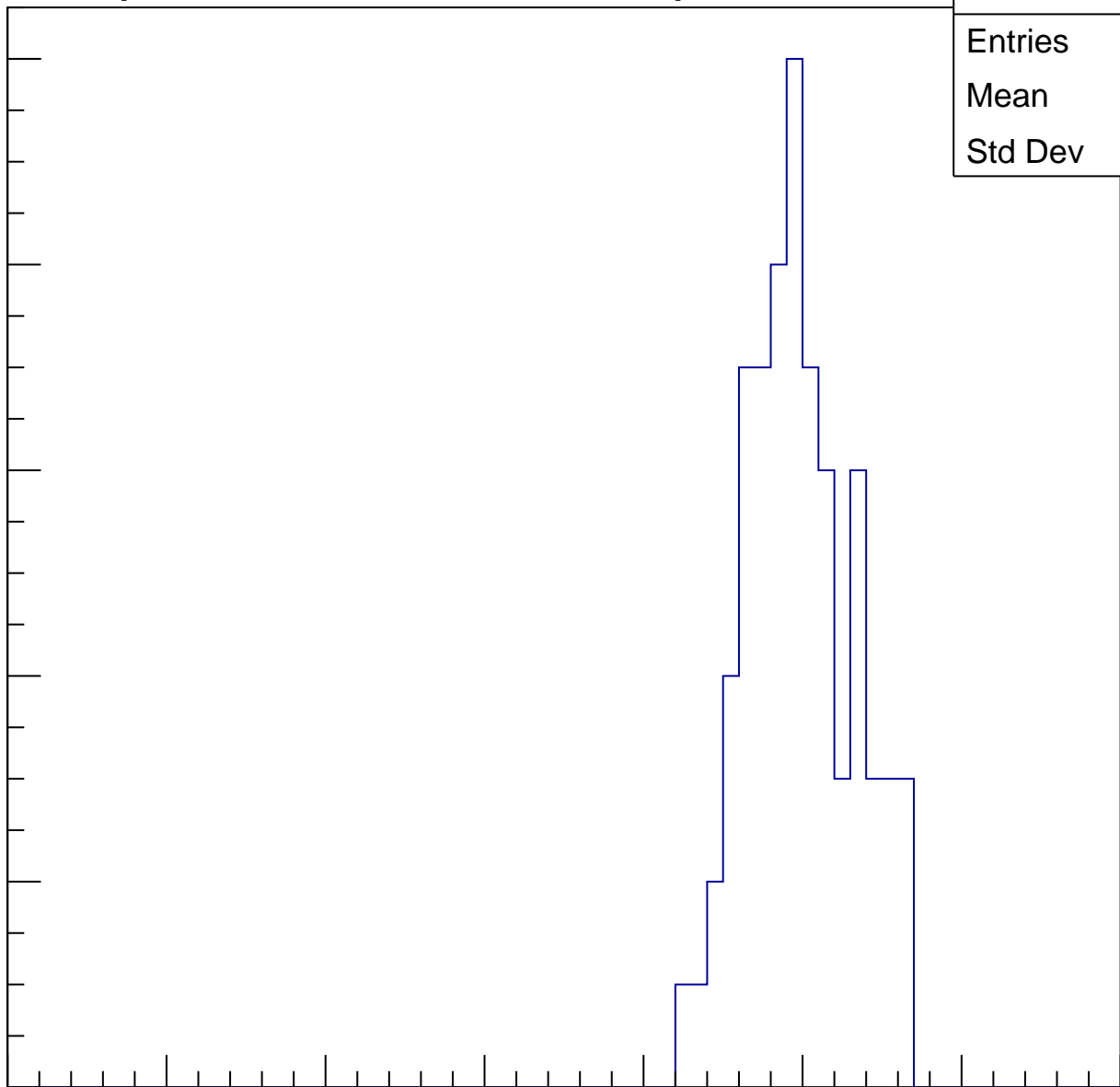
40

50

60

70

ampl

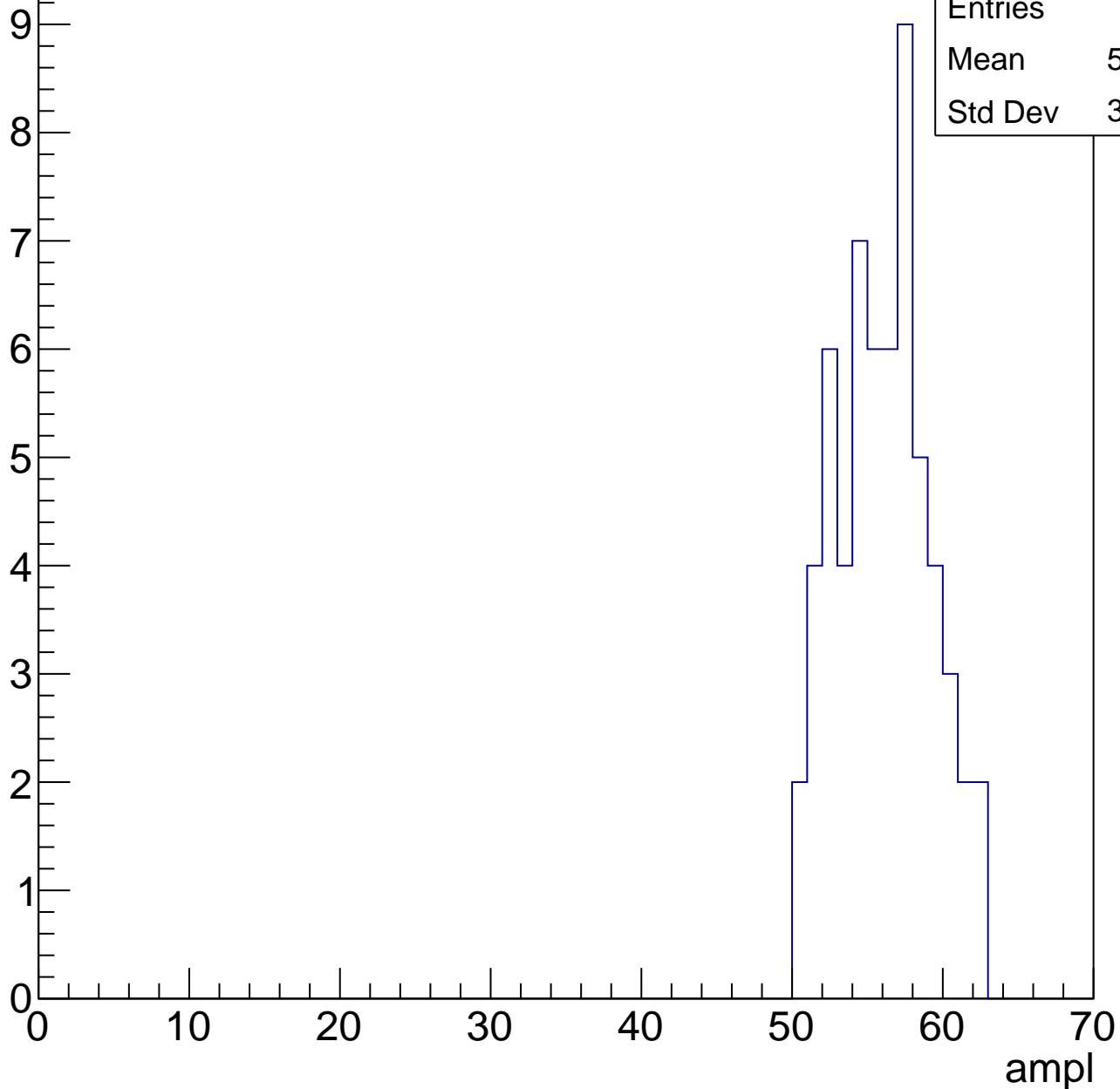


B1L100S, U19-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

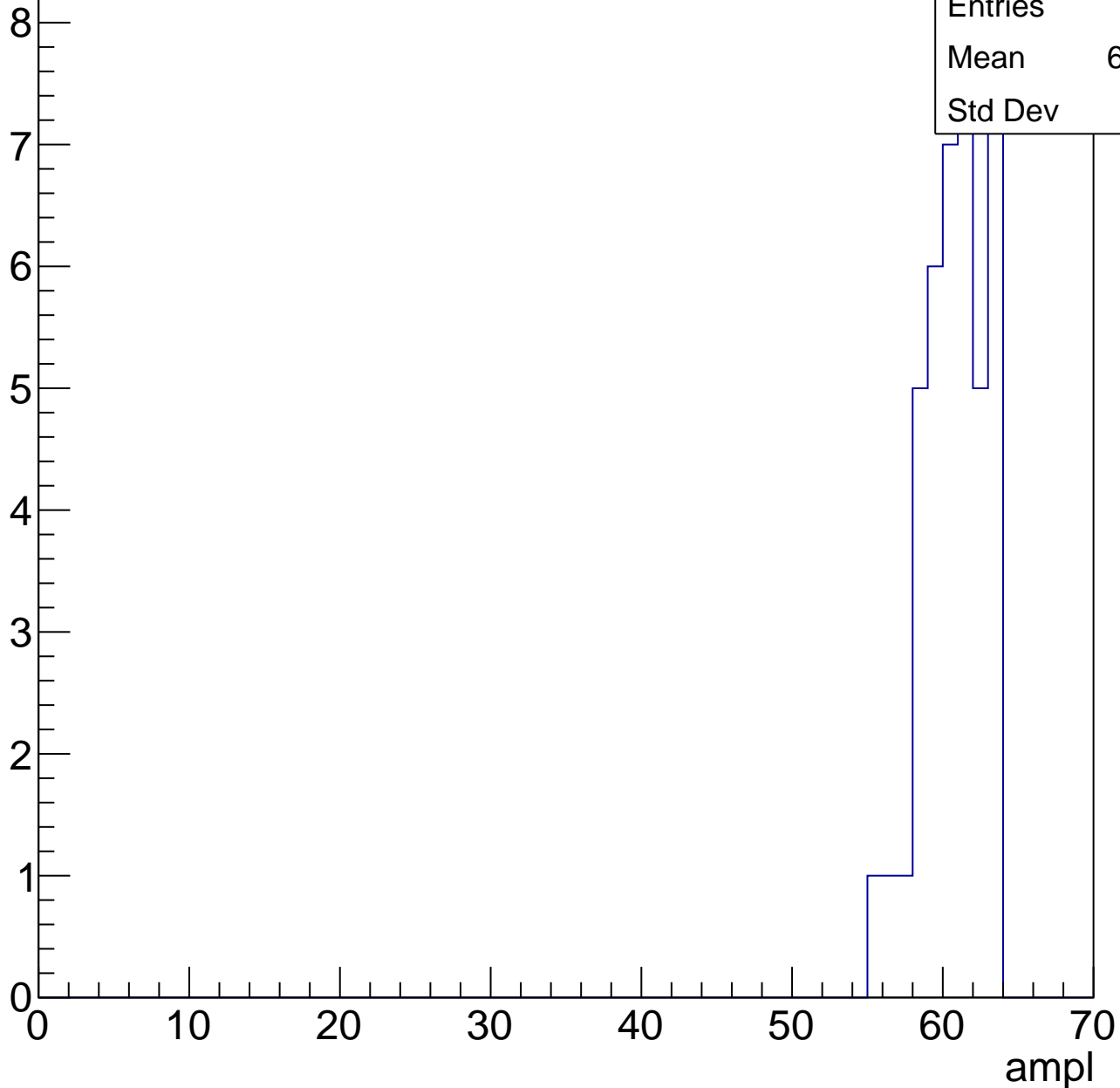
Entries	60
Mean	55.62
Std Dev	3.066



B1L100S, U19-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

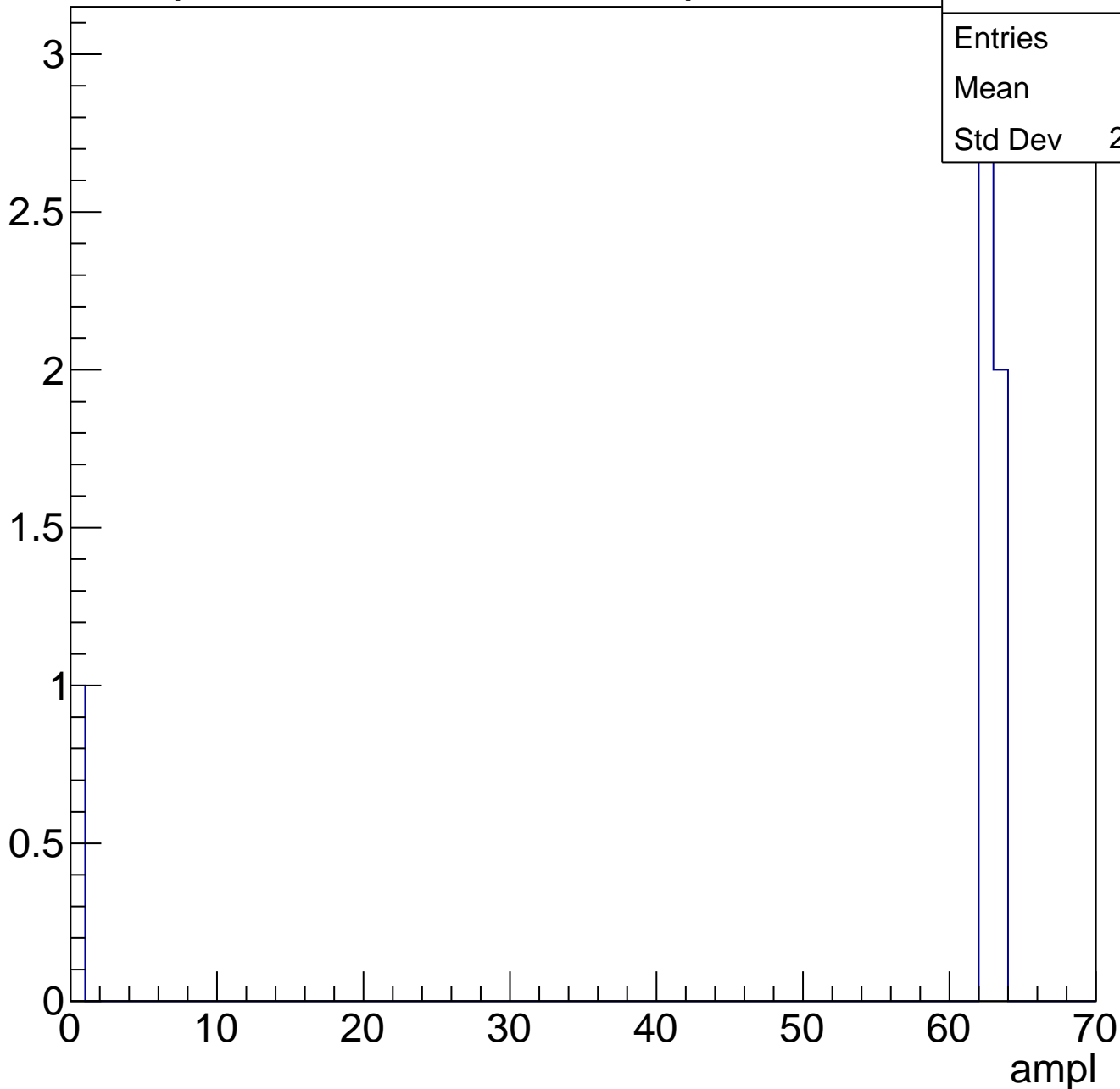
Entry



B1L100S, U19-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch110, adc0

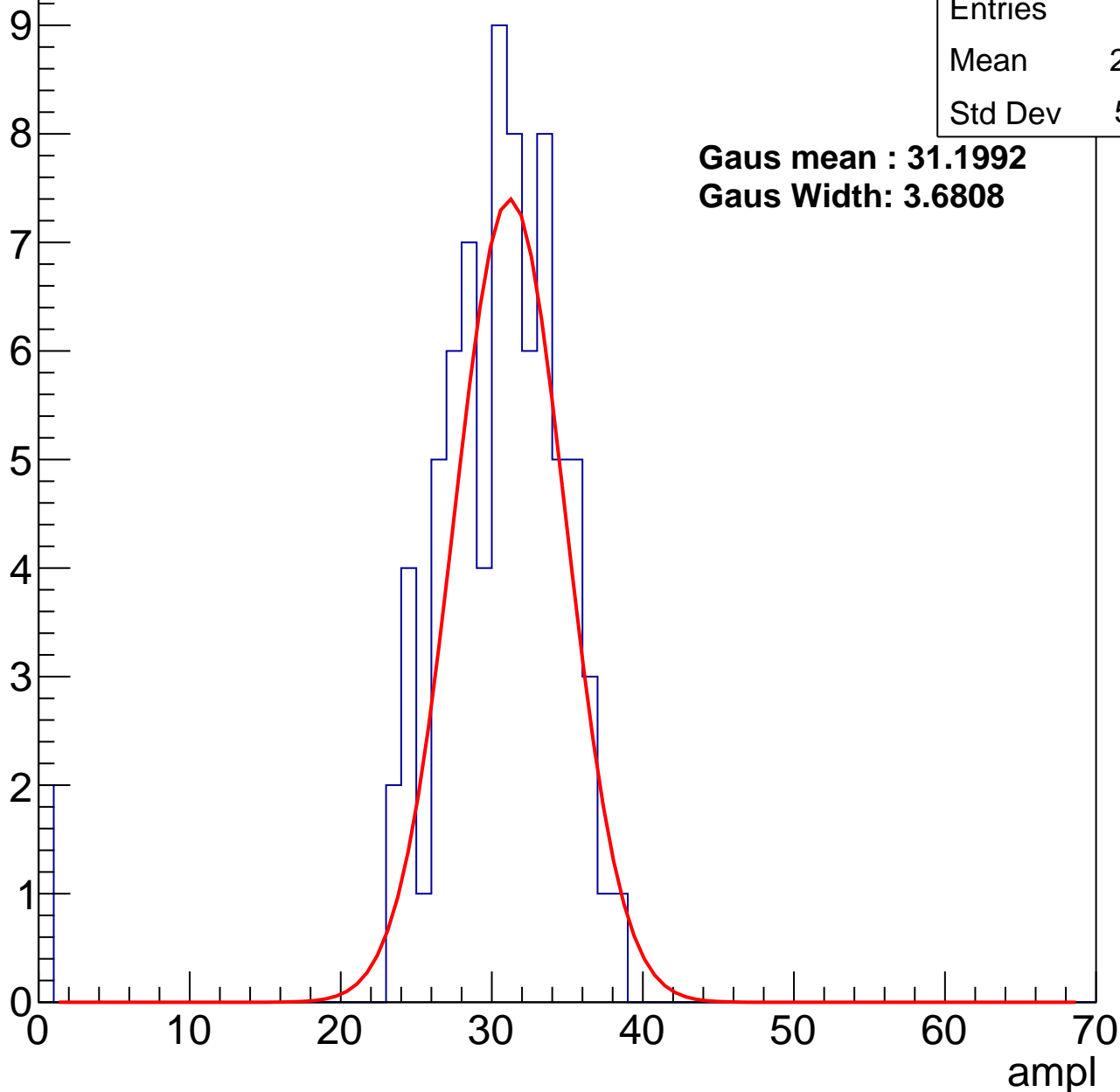
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.52
Std Dev	5.971

Gaus mean : 31.1992

Gaus Width: 3.6808



B1L100S, U19-ch110, adc1

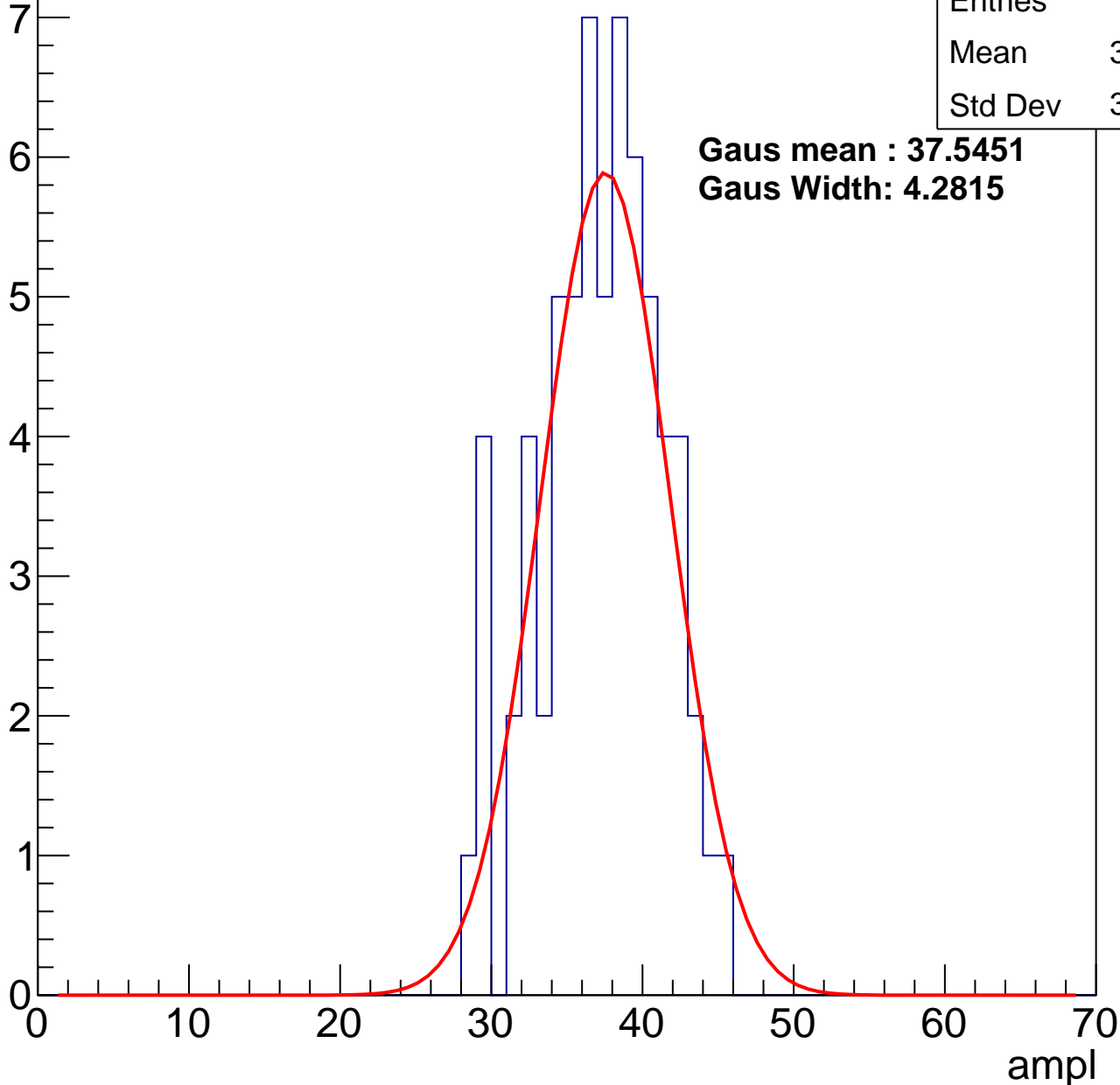
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.75
Std Dev	3.989

Gaus mean : 37.5451

Gaus Width: 4.2815



B1L100S, U19-ch110, adc2

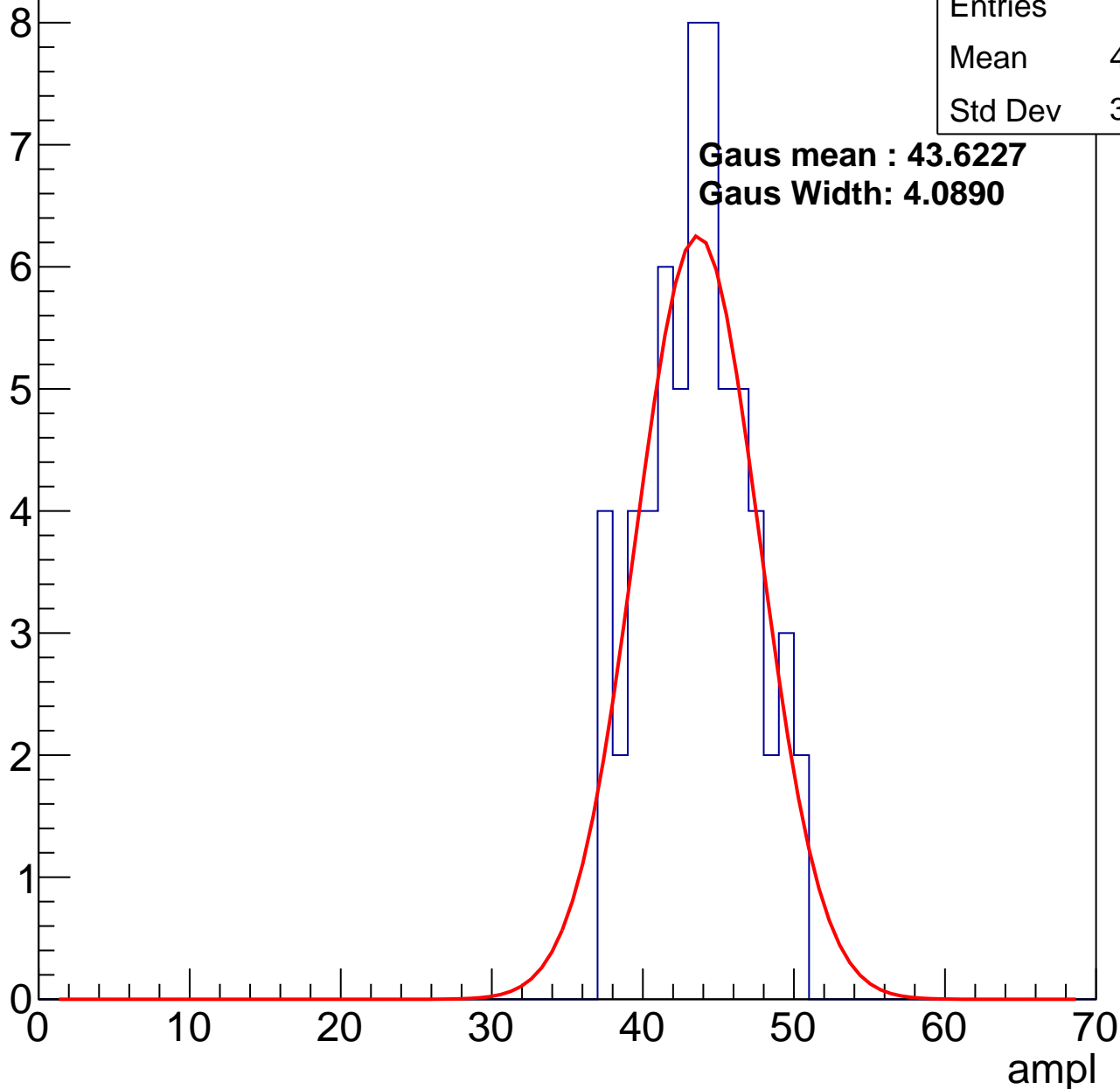
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.19
Std Dev	3.393

Gaus mean : 43.6227

Gaus Width: 4.0890

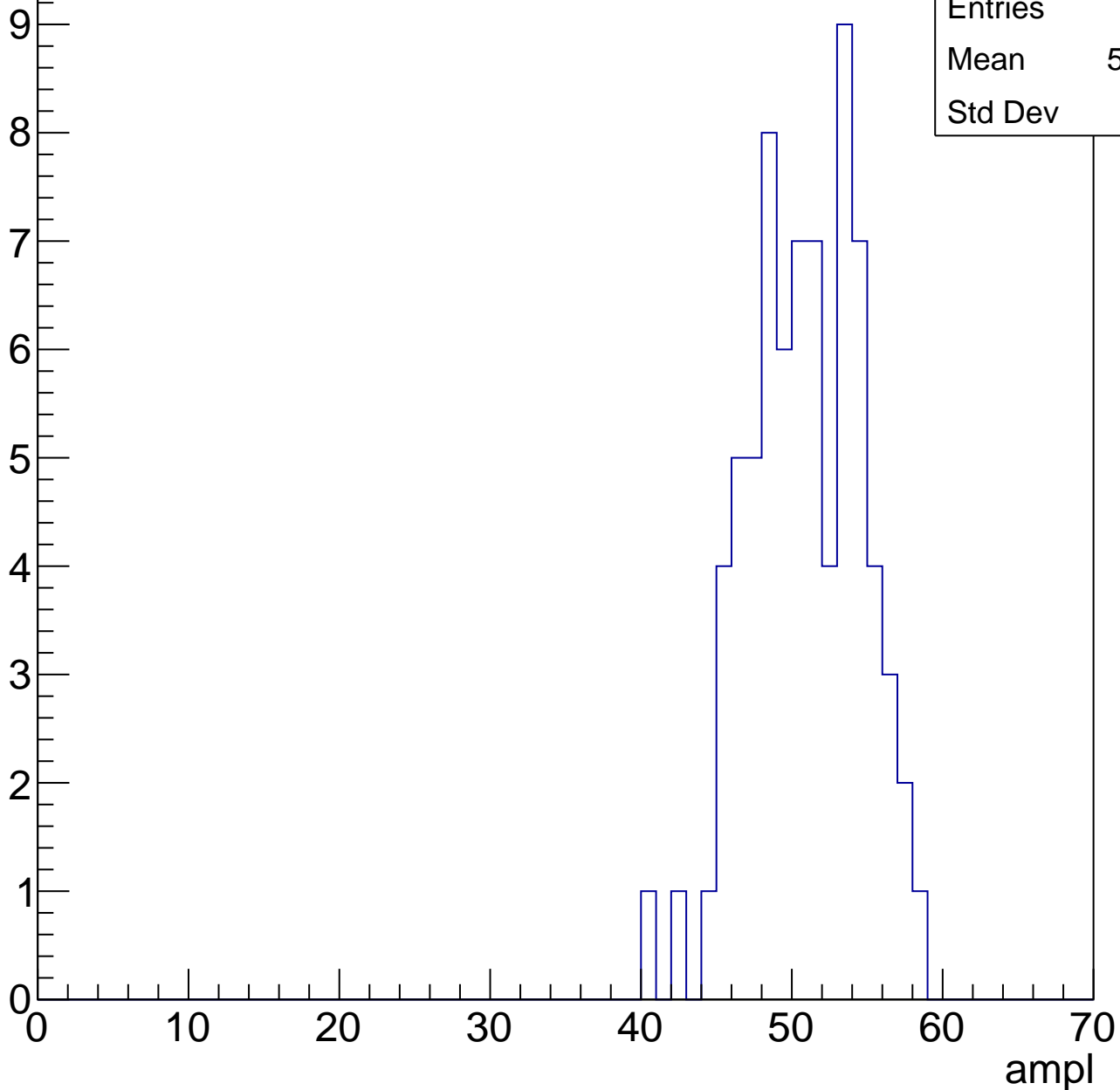


B1L100S, U19-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	50.39
Std Dev	3.72

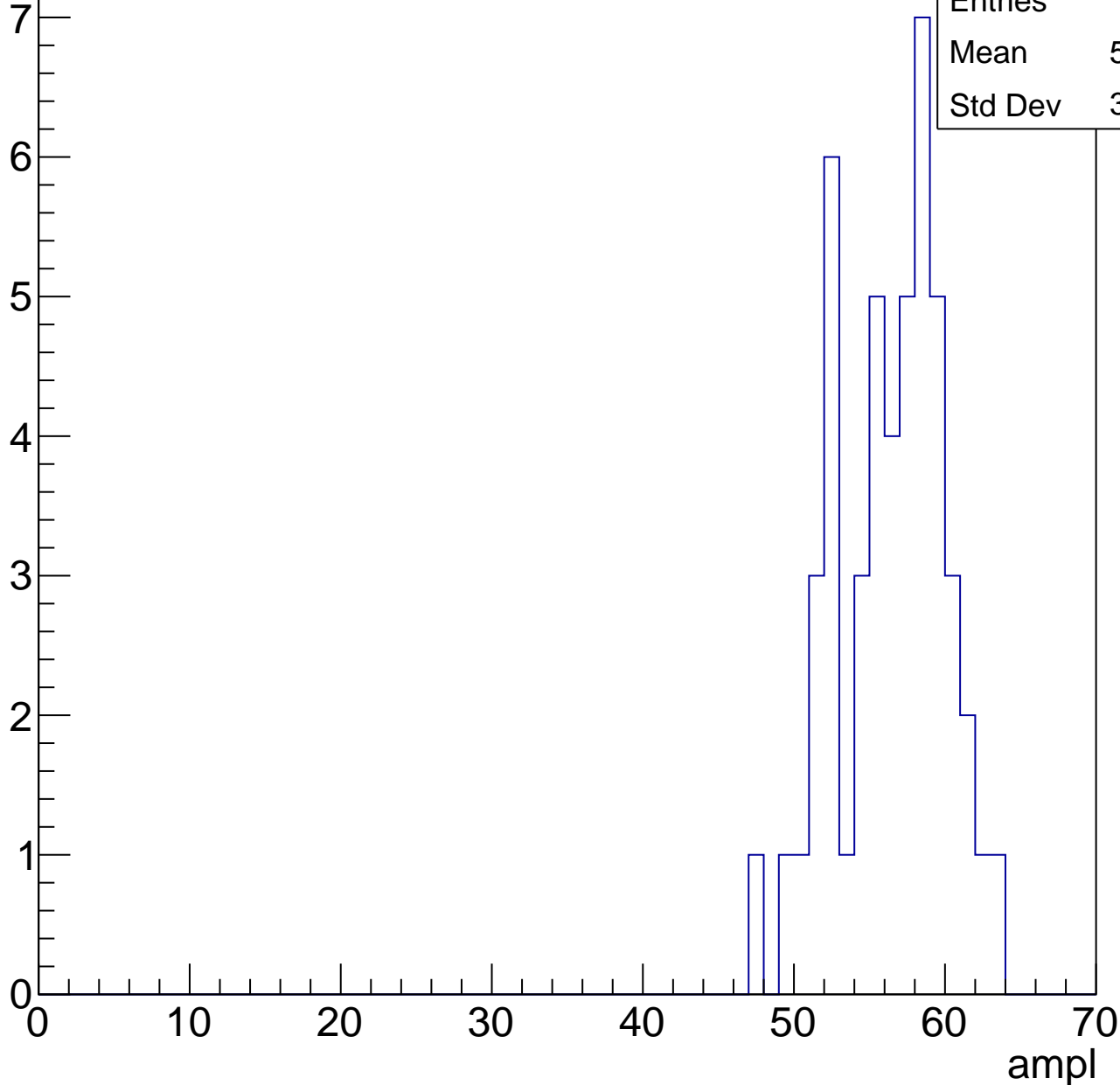


B1L100S, U19-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	55.88
Std Dev	3.566

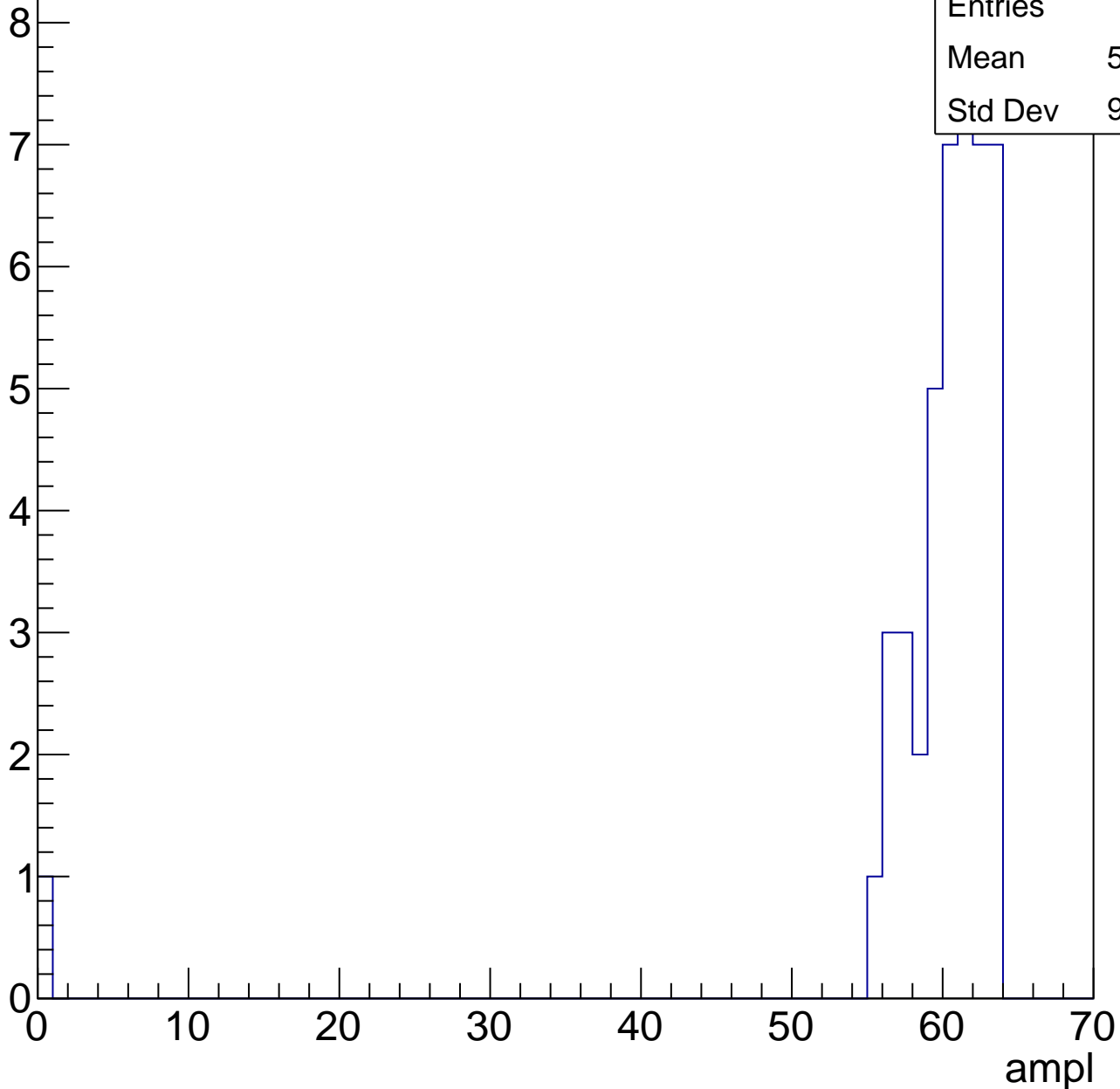


B1L100S, U19-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.82
Std Dev	9.233



B1L100S, U19-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch111, adc0

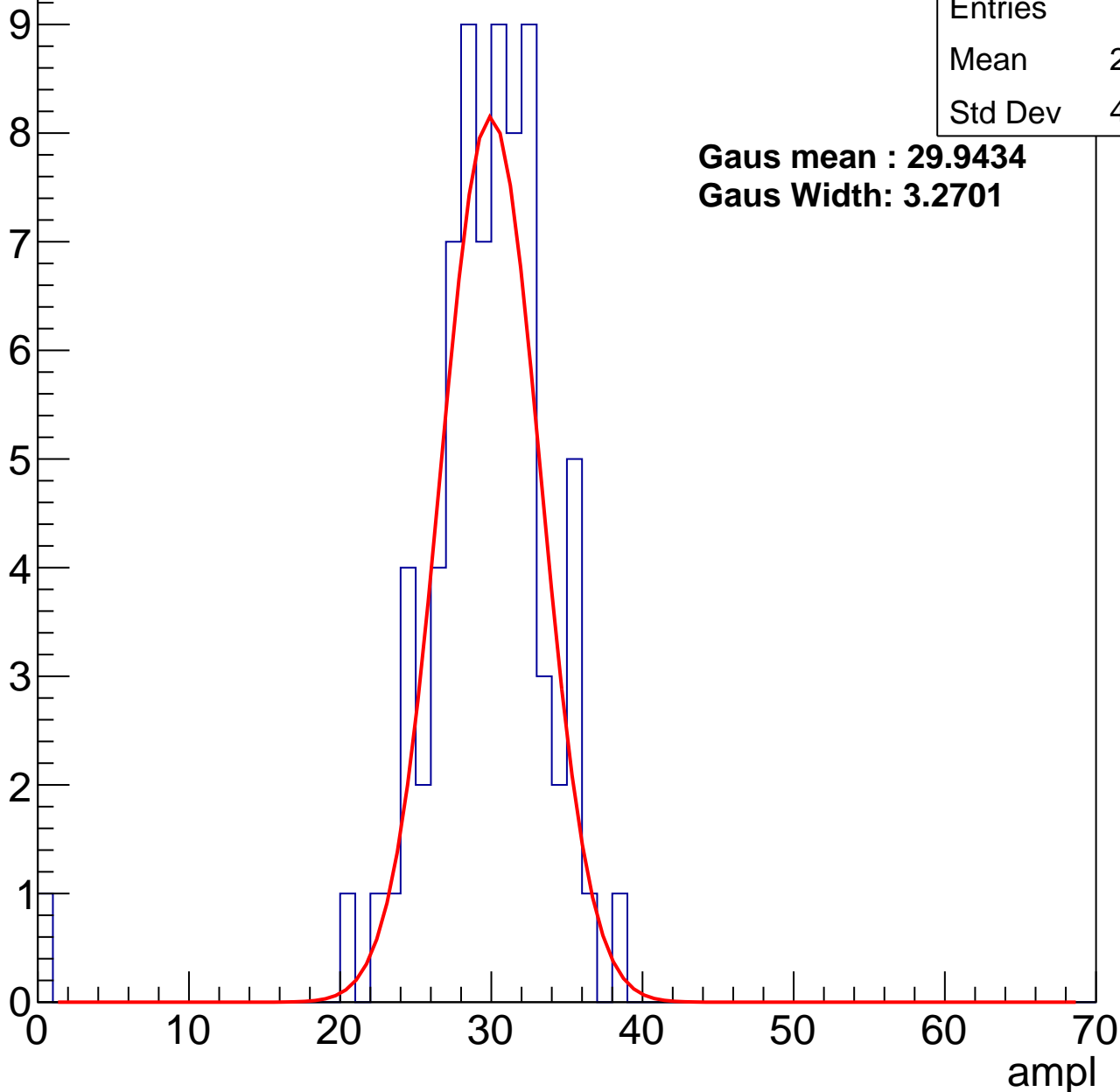
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.08
Std Dev	4.819

Gaus mean : 29.9434

Gaus Width: 3.2701



B1L100S, U19-ch111, adc1

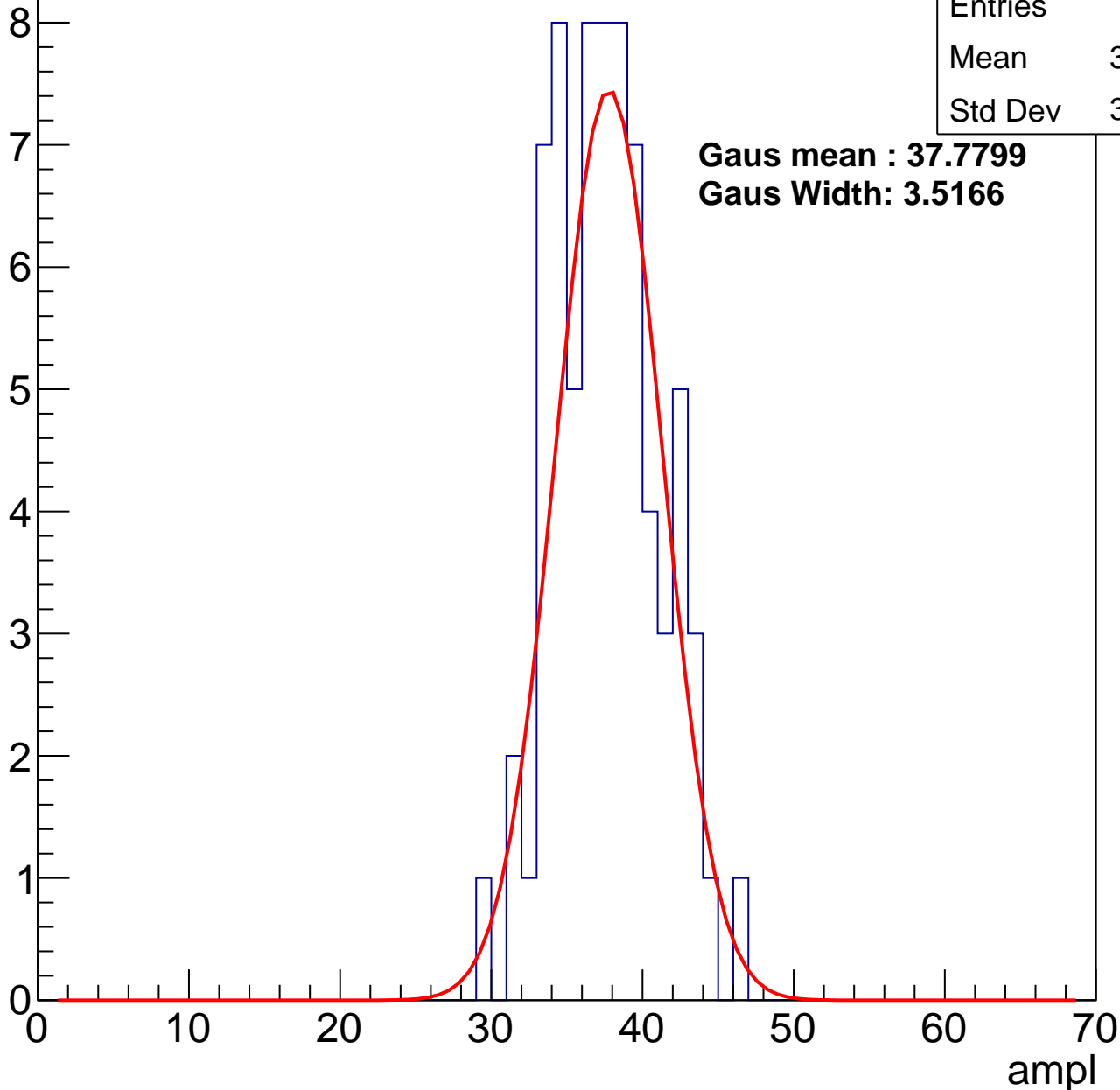
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.14
Std Dev	3.445

Gaus mean : 37.7799

Gaus Width: 3.5166



B1L100S, U19-ch111, adc2

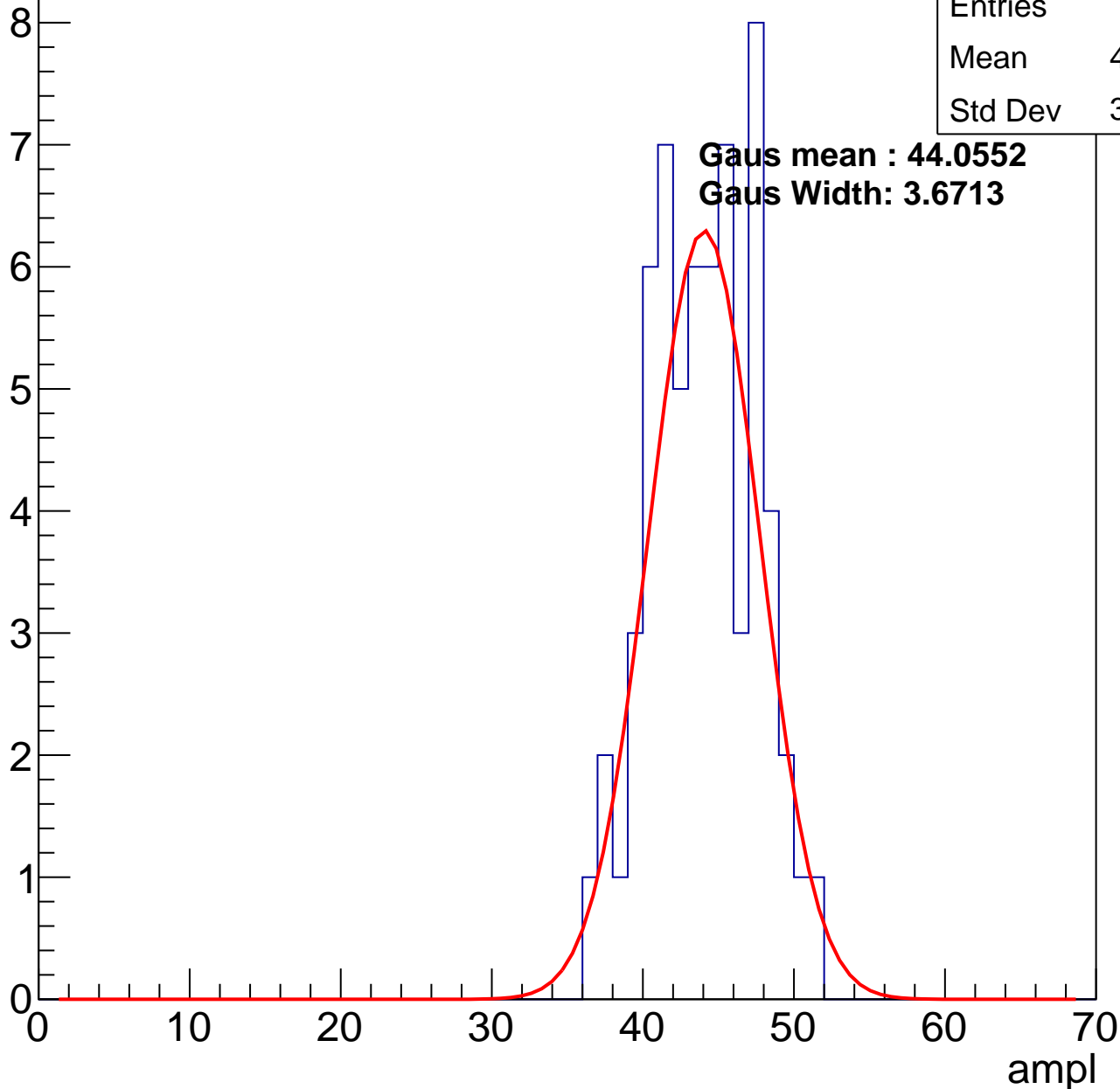
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.56
Std Dev	3.417

Gaus mean : 44.0552

Gaus Width: 3.6713

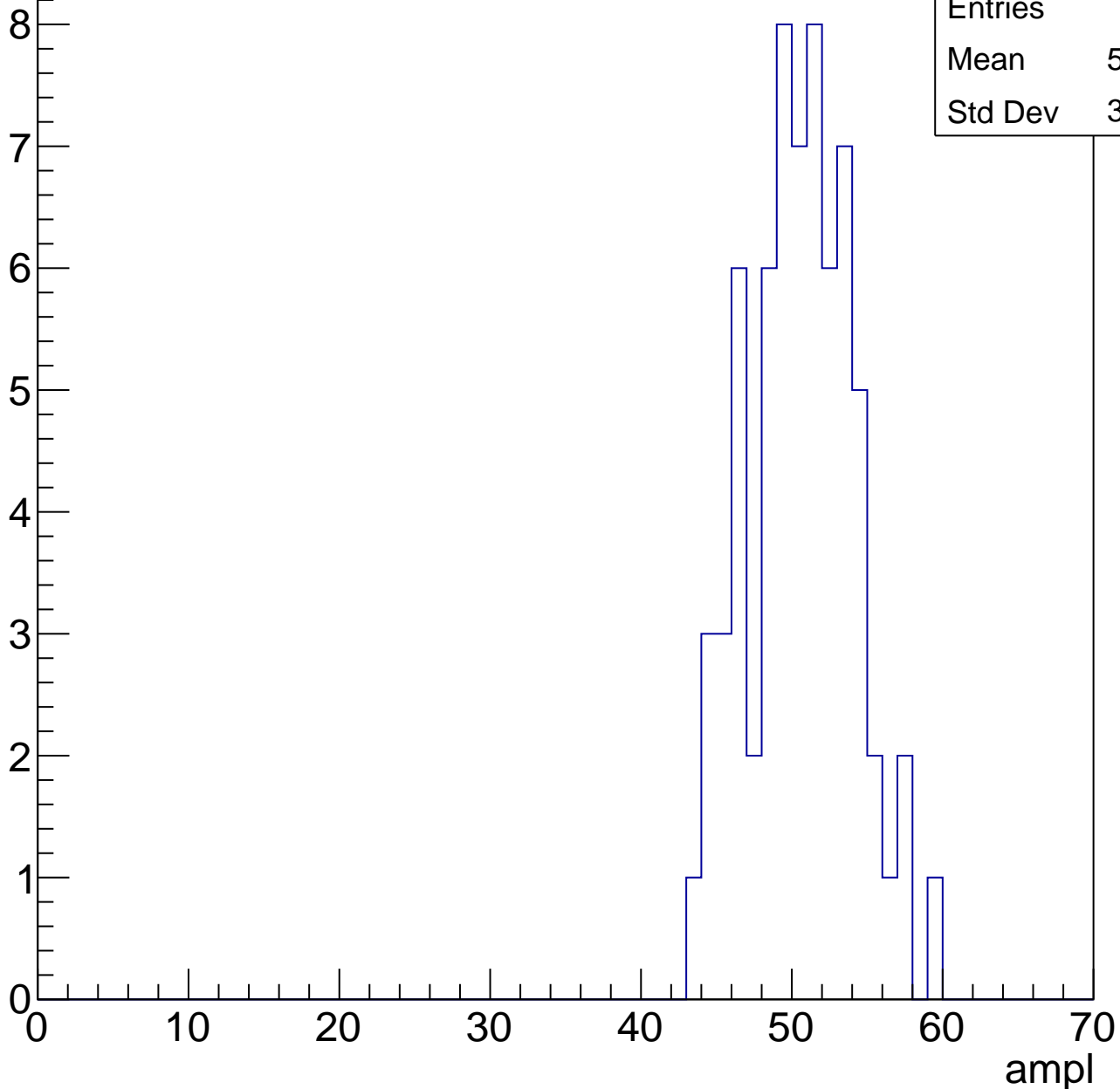


B1L100S, U19-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.15
Std Dev	3.465

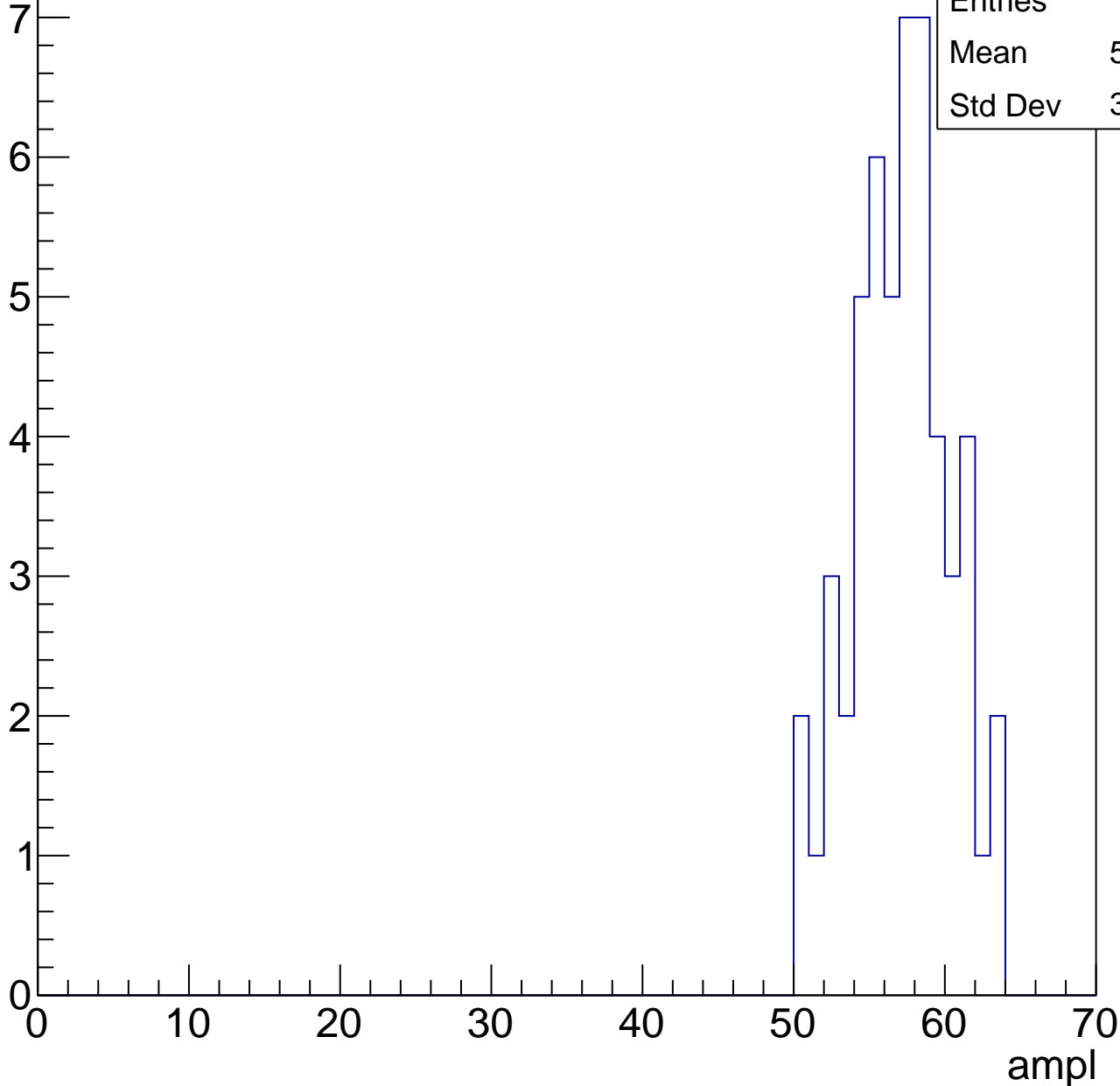


B1L100S, U19-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.65
Std Dev	3.162

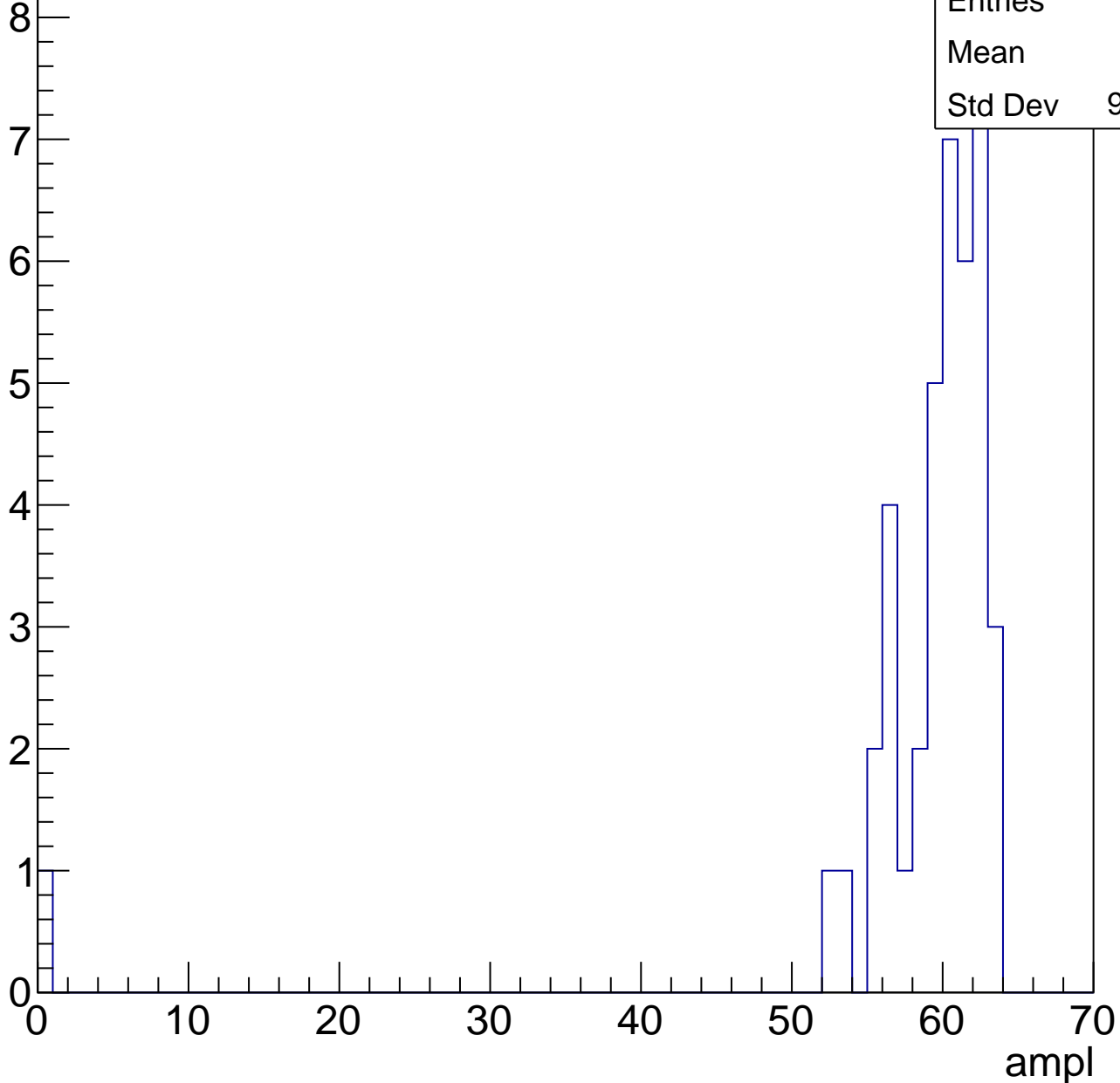


B1L100S, U19-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	58
Std Dev	9.564

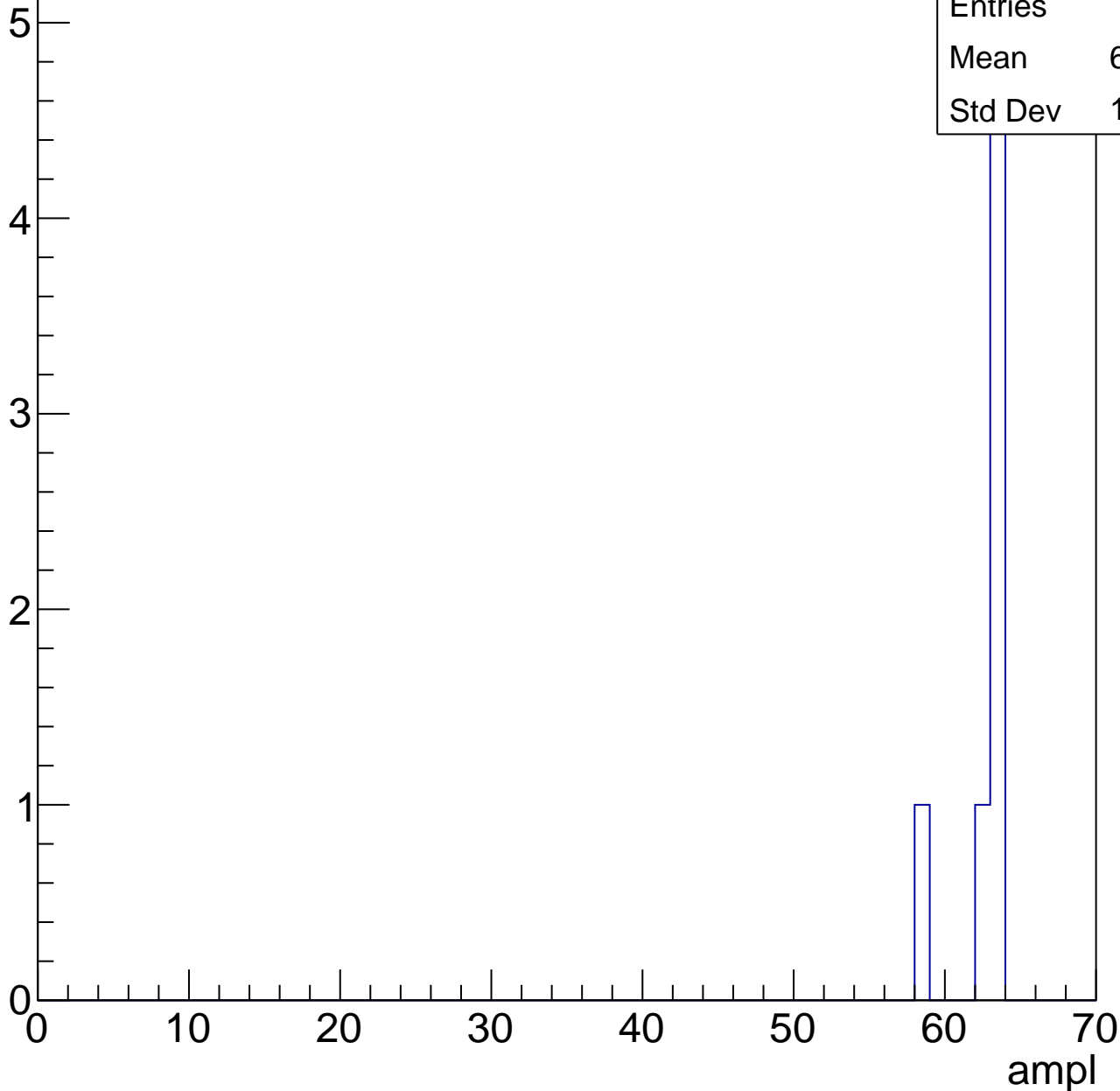


B1L100S, U19-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	62.14
Std Dev	1.726



B1L100S, U19-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch112, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	28.94
Std Dev	5.811

Gaus mean : 29.7838

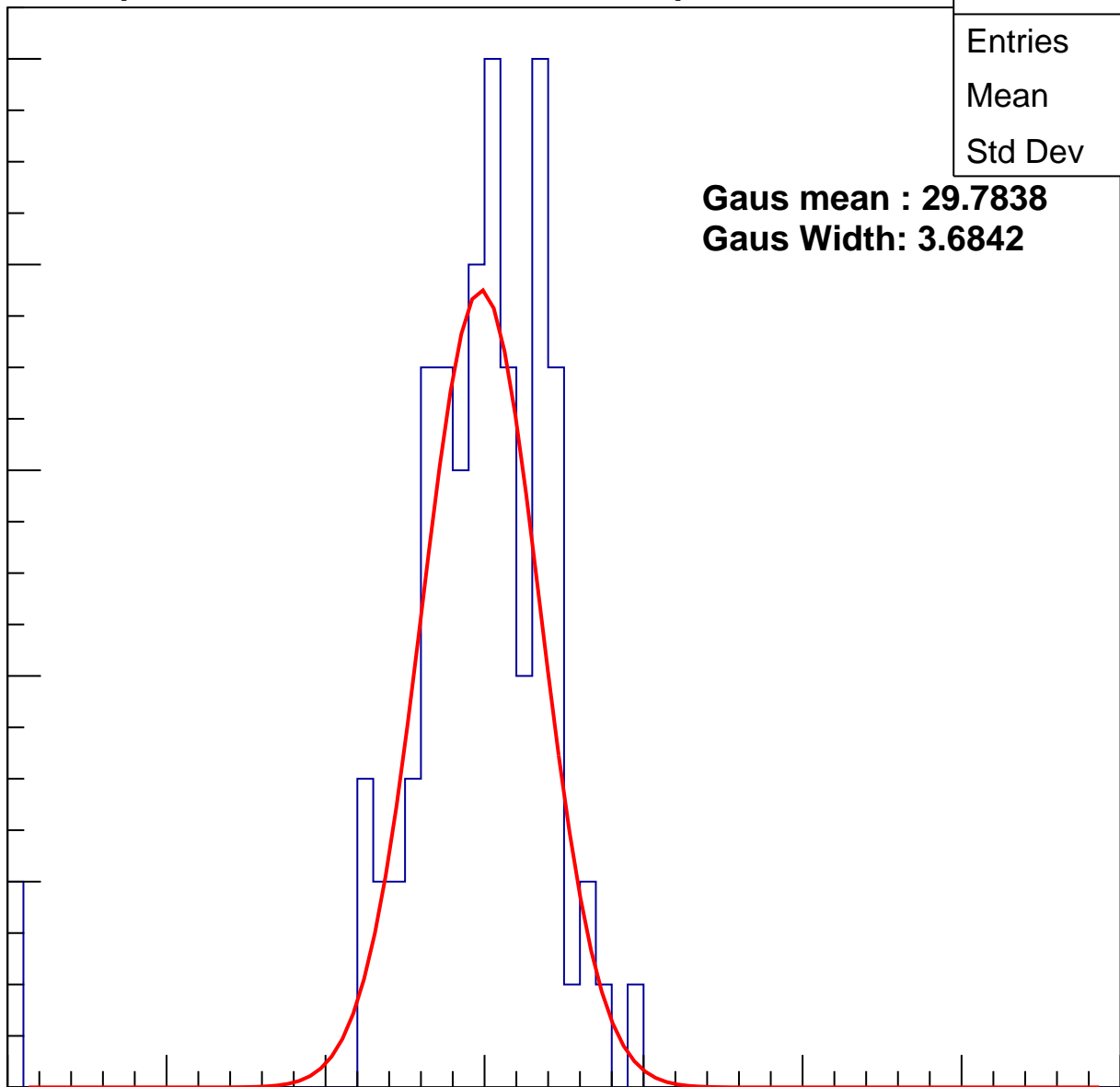
Gaus Width: 3.6842

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch112, adc1

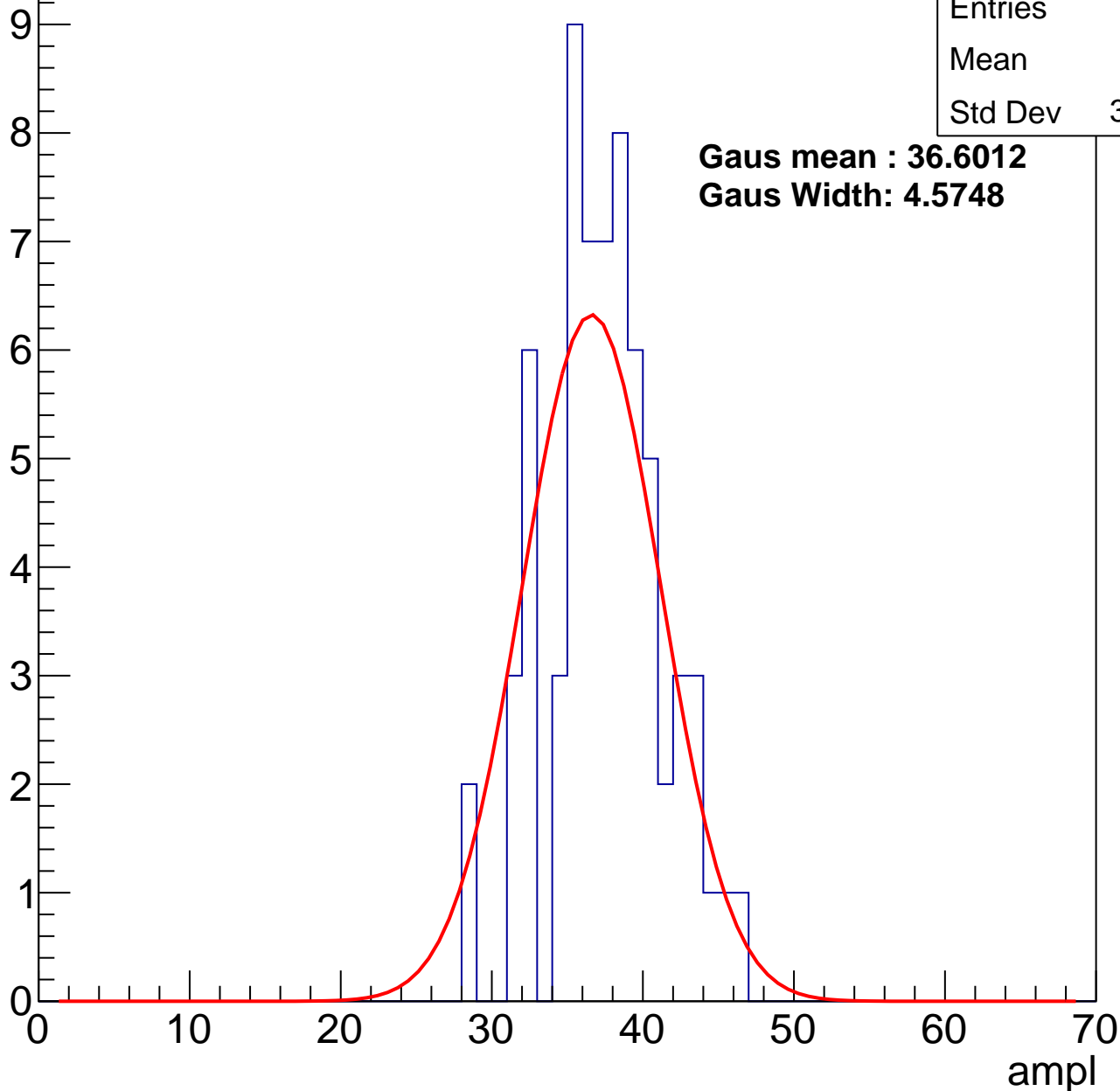
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37
Std Dev	3.817

Gaus mean : 36.6012

Gaus Width: 4.5748



B1L100S, U19-ch112, adc2

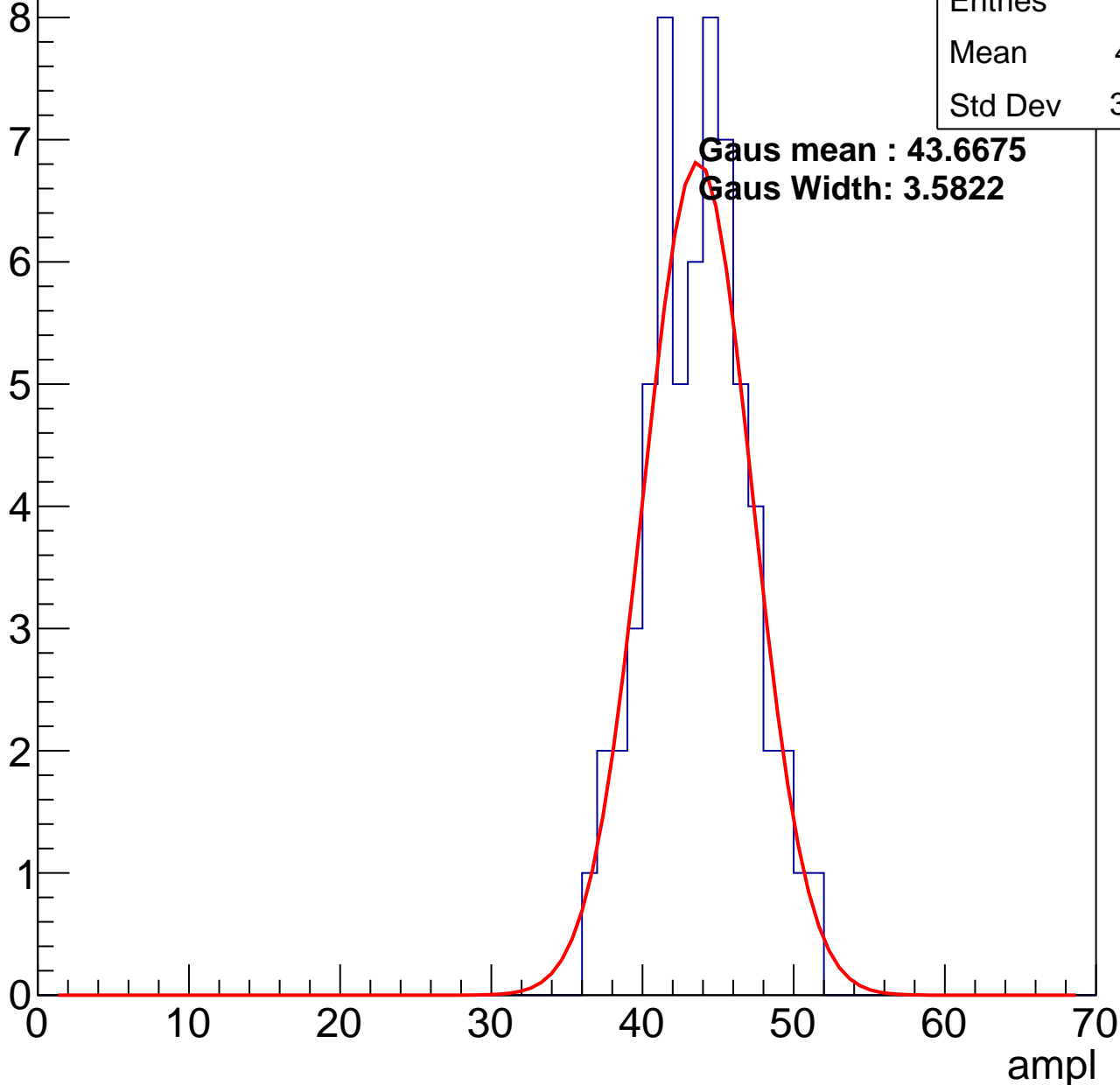
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.21
Std Dev	3.308

Gaus mean : 43.6675

Gaus Width: 3.5822

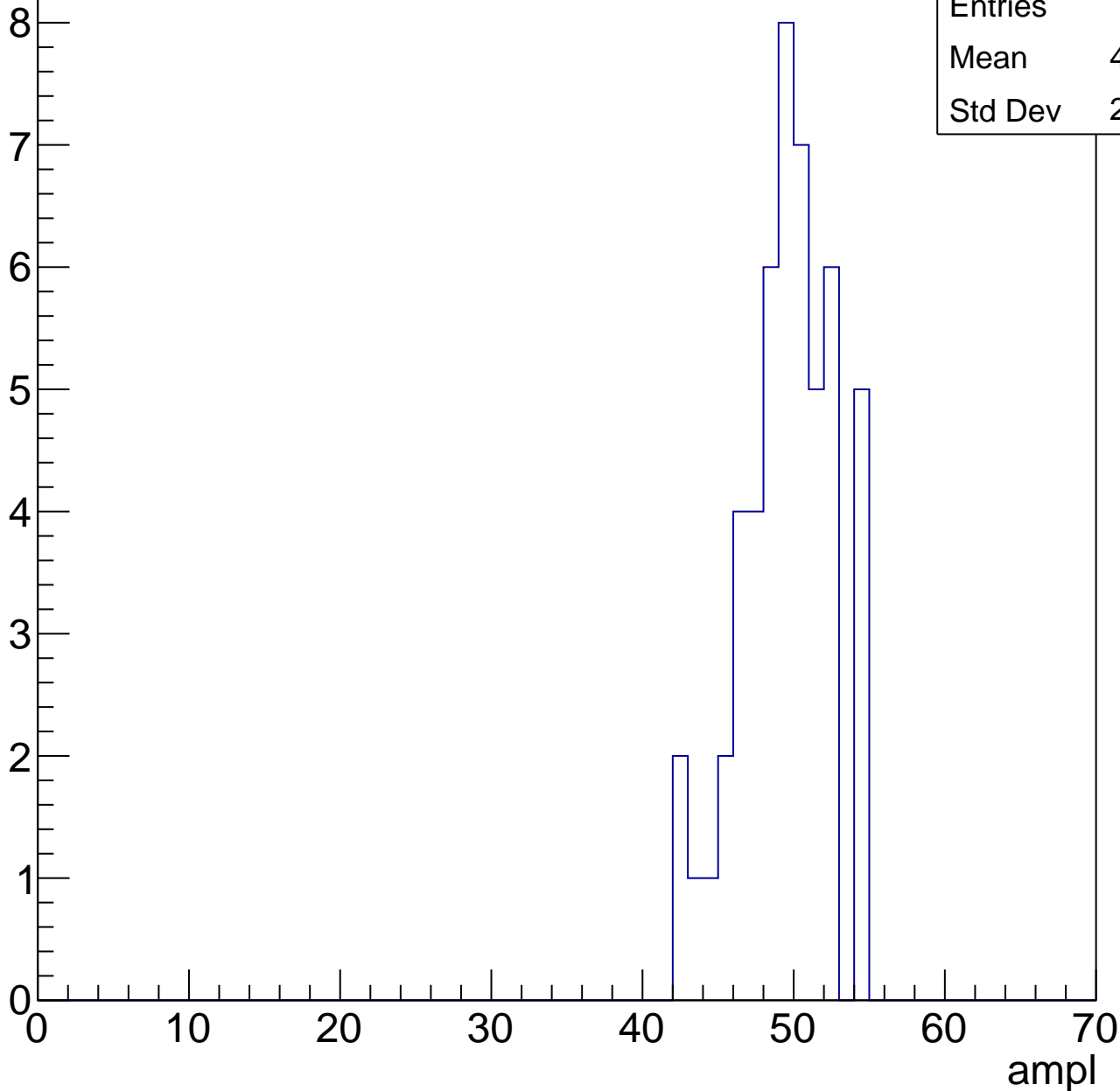


B1L100S, U19-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	49.02
Std Dev	2.987

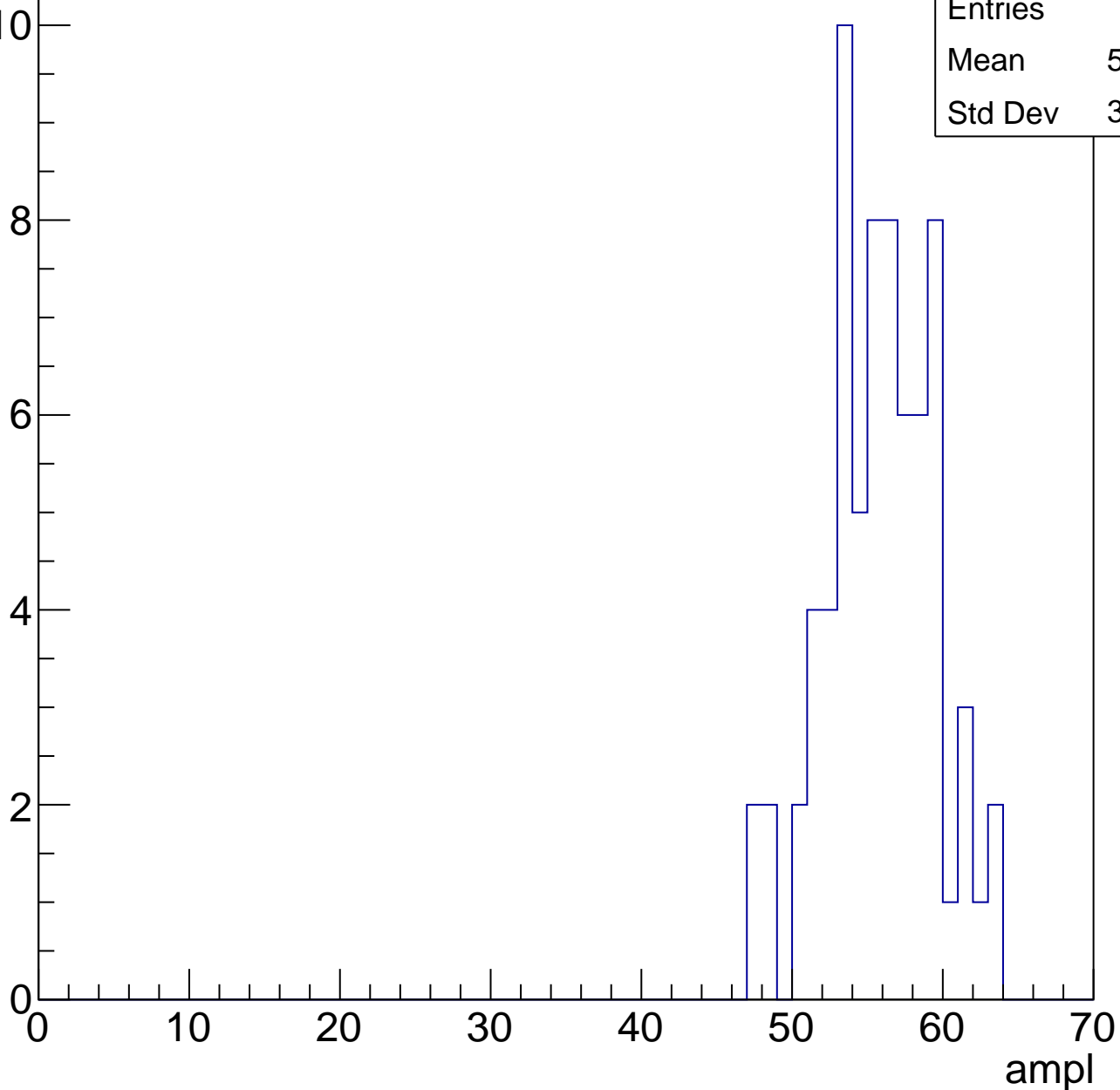


B1L100S, U19-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	55.32
Std Dev	3.609

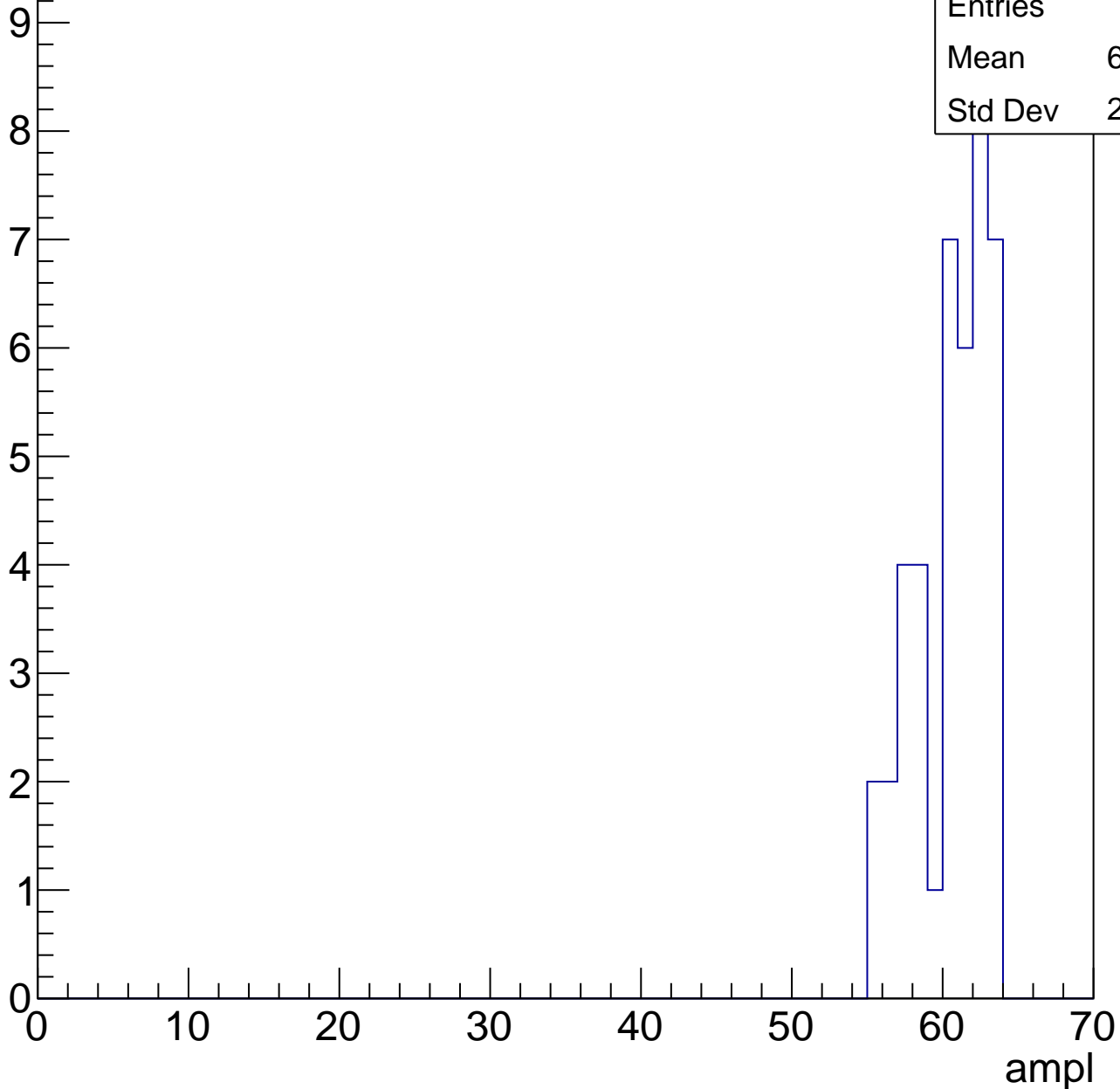


B1L100S, U19-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	60.14
Std Dev	2.386



B1L100S, U19-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L100S, U19-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch113, adc0

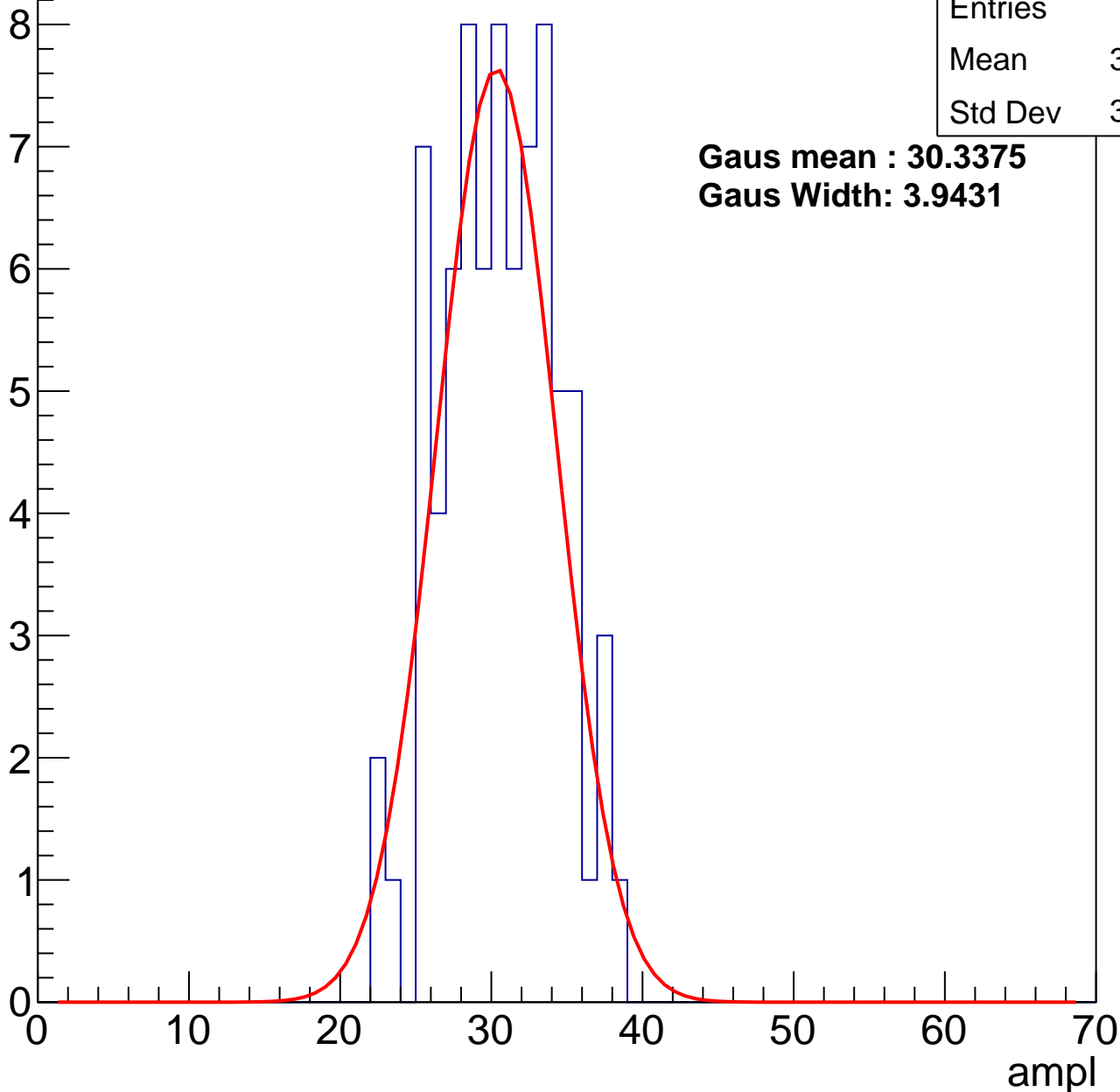
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.13
Std Dev	3.695

Gaus mean : 30.3375

Gaus Width: 3.9431



B1L100S, U19-ch113, adc1

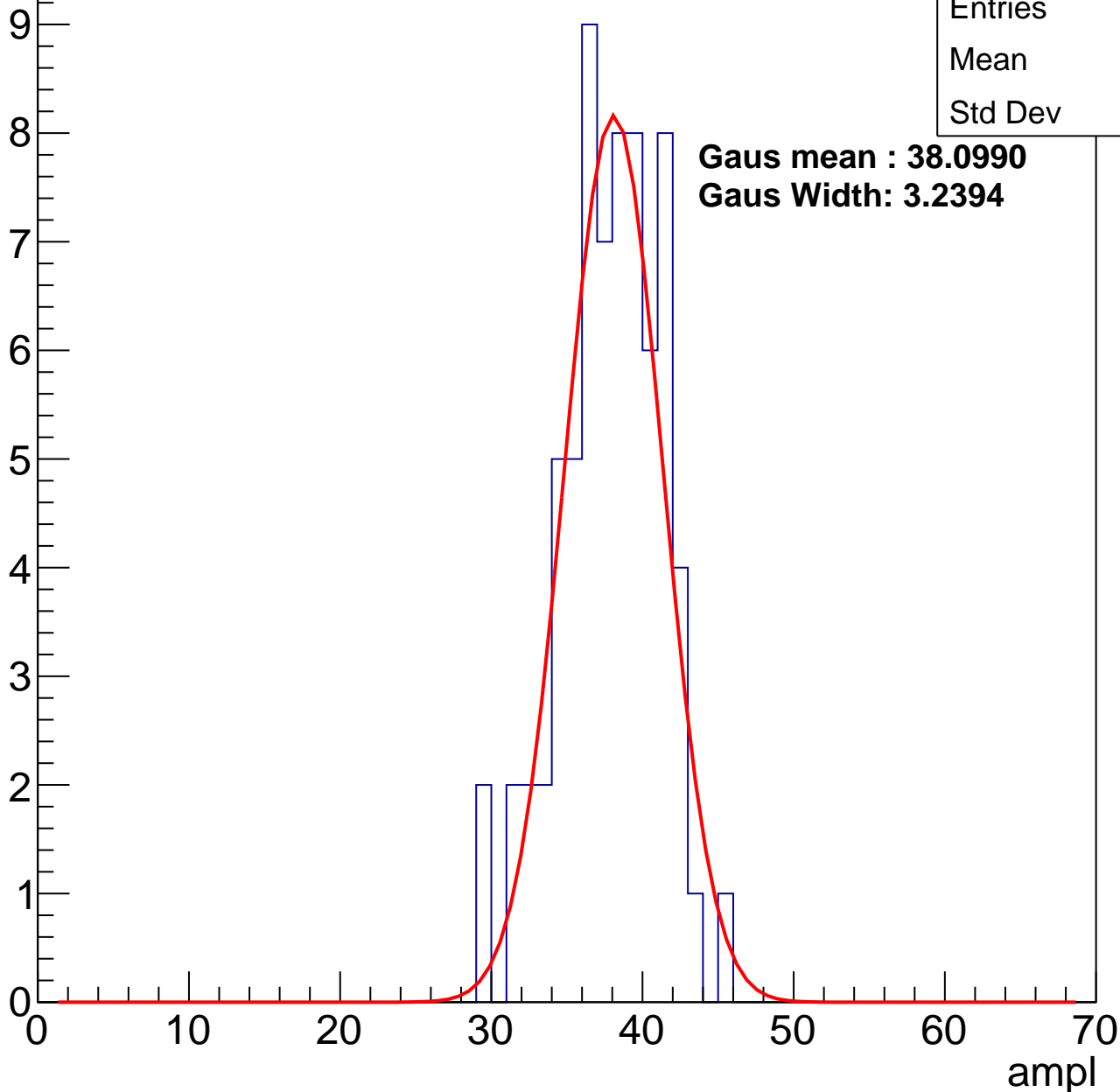
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.4
Std Dev	3.31

Gaus mean : 38.0990

Gaus Width: 3.2394



B1L100S, U19-ch113, adc2

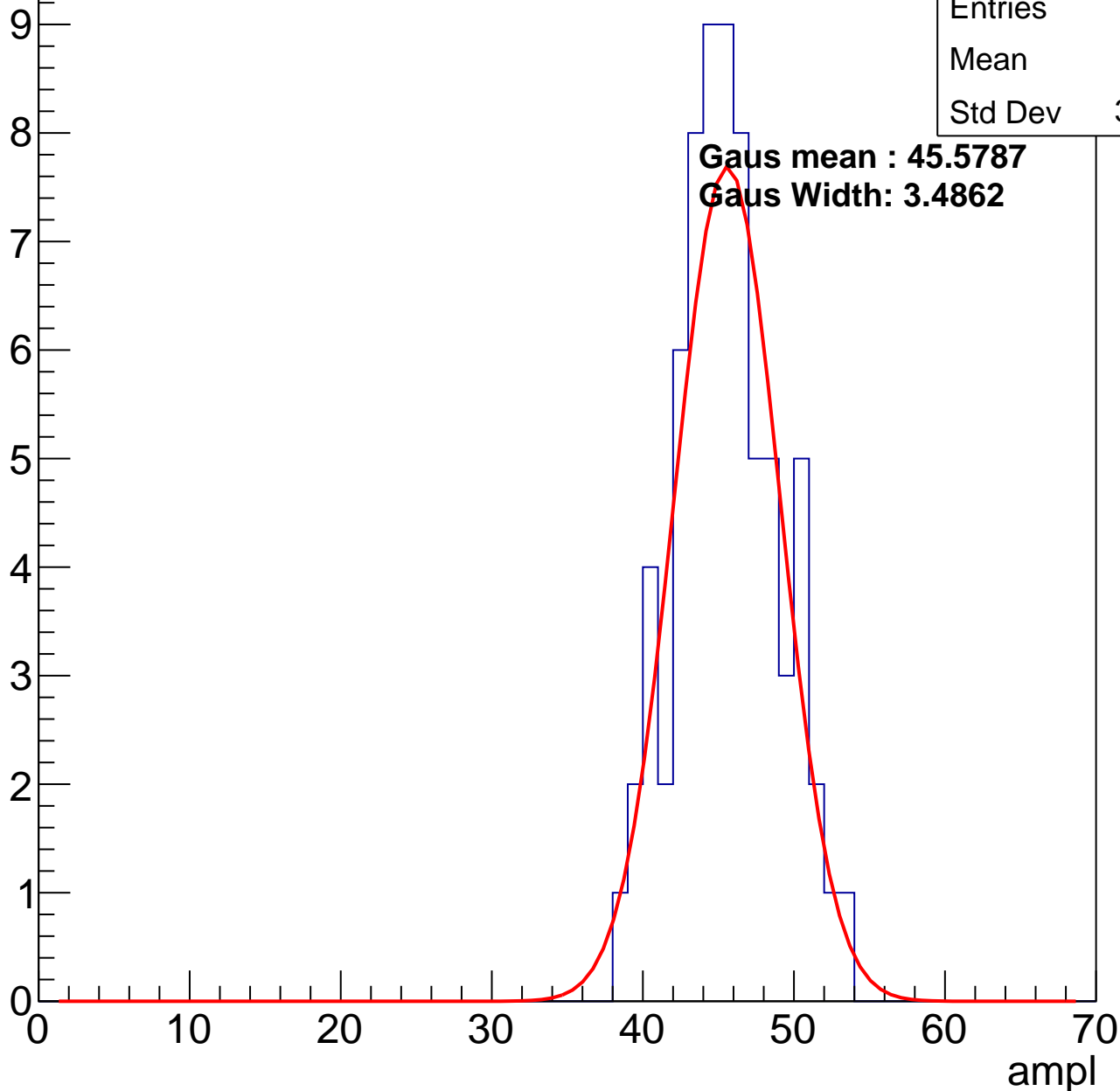
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	45.1
Std Dev	3.311

Gaus mean : 45.5787

Gaus Width: 3.4862

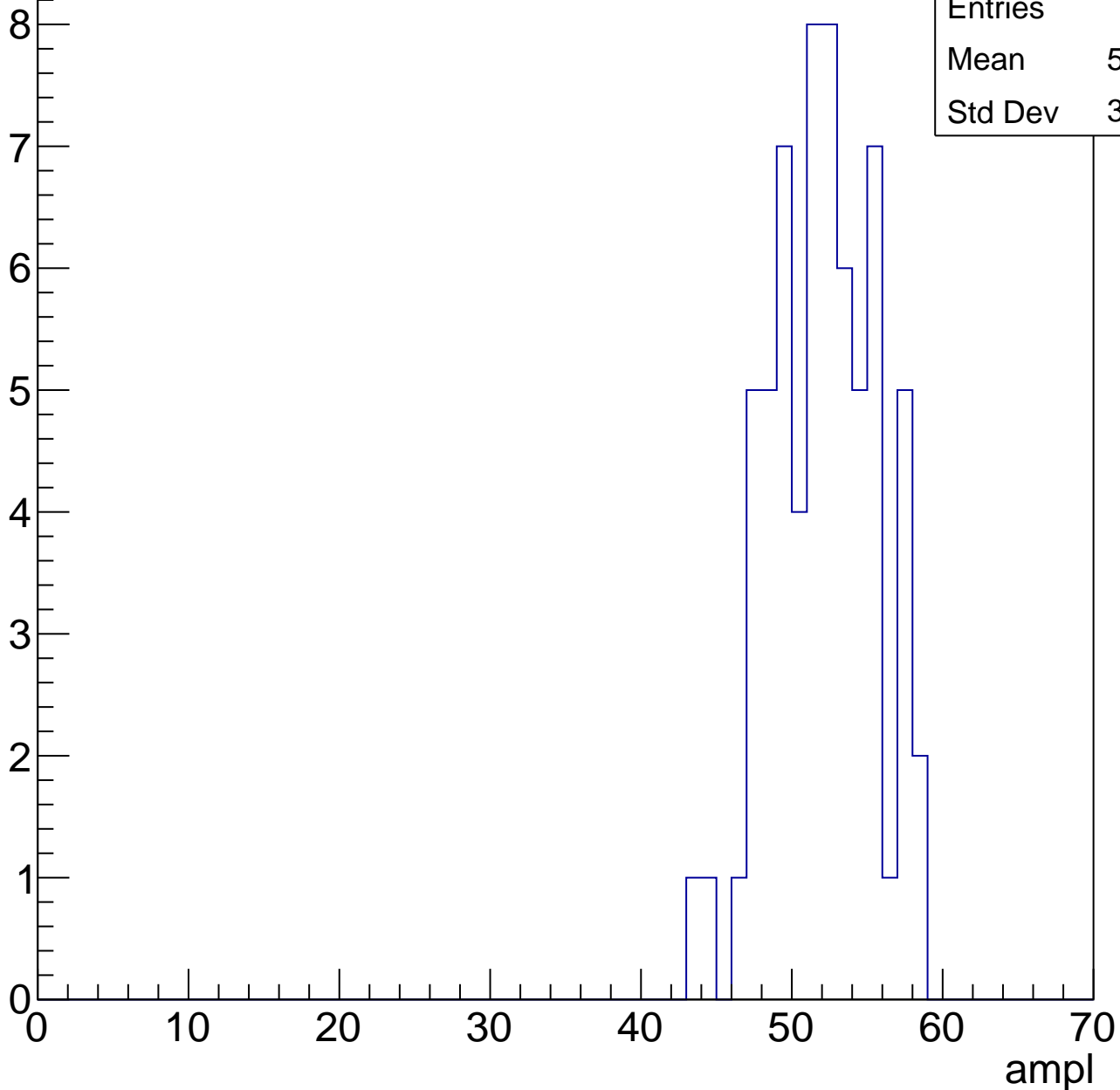


B1L100S, U19-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	51.59
Std Dev	3.402

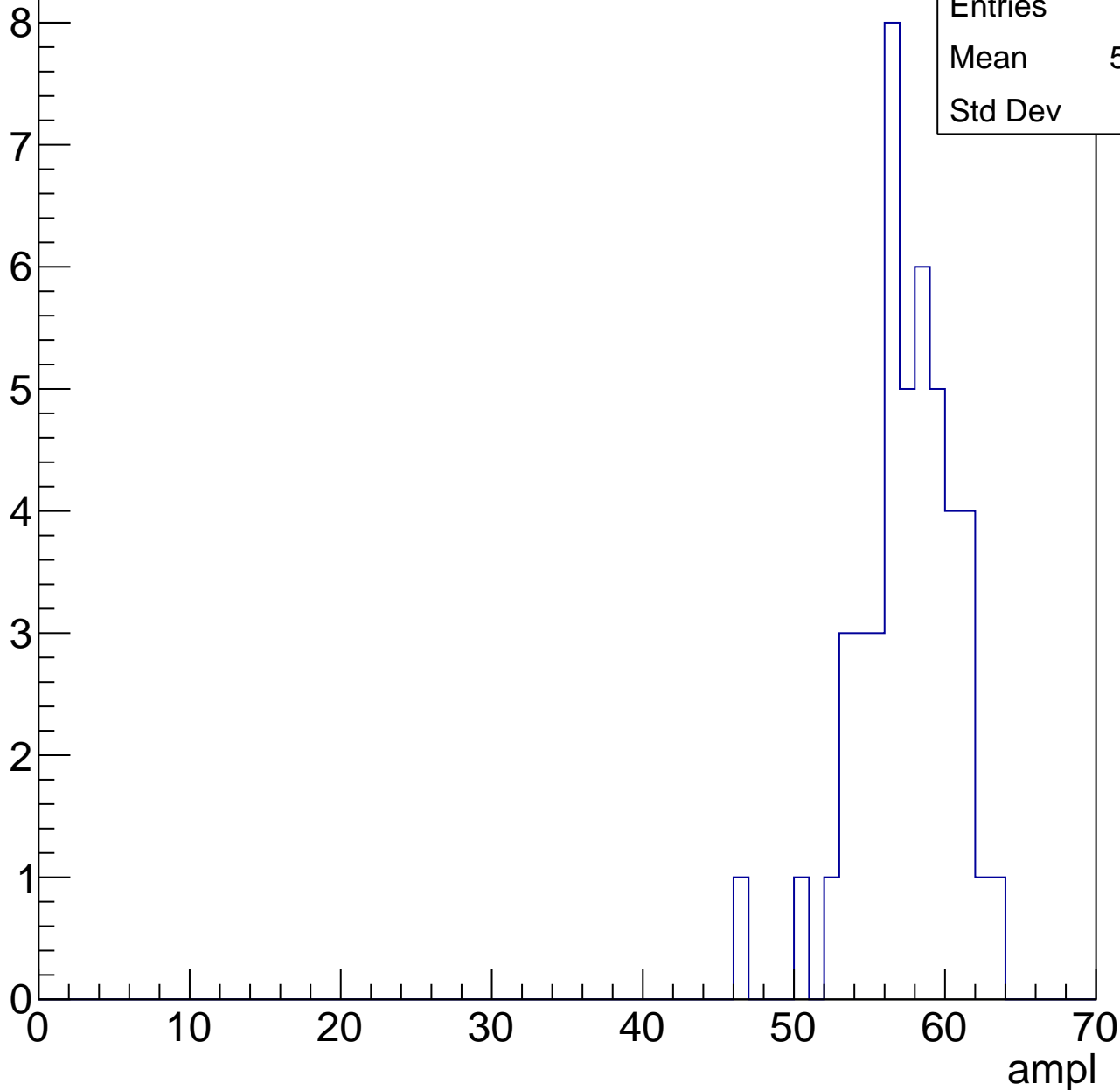


B1L100S, U19-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	56.93
Std Dev	3.22

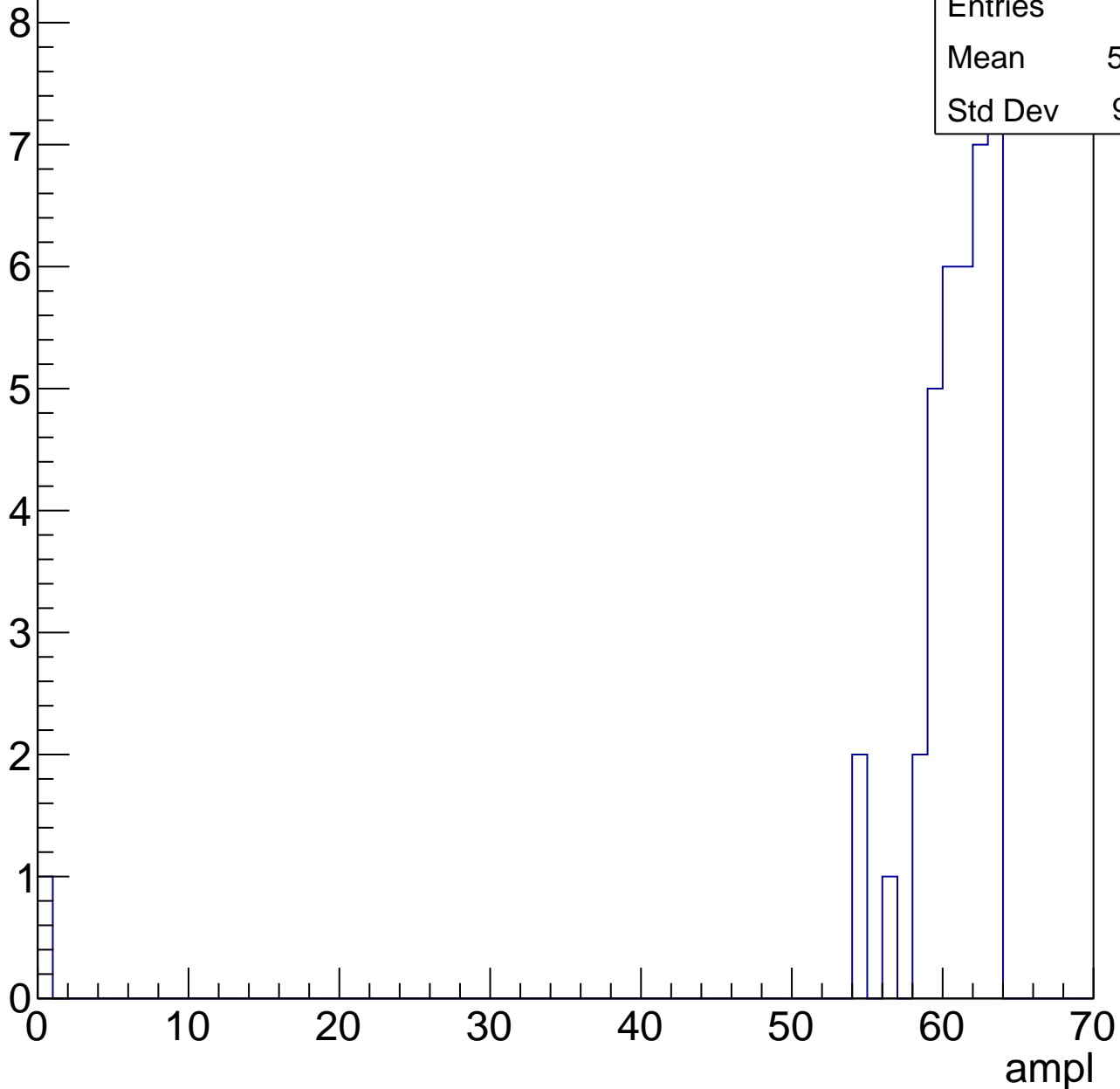


B1L100S, U19-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.92
Std Dev	9.951



B1L100S, U19-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L100S, U19-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch114, adc0

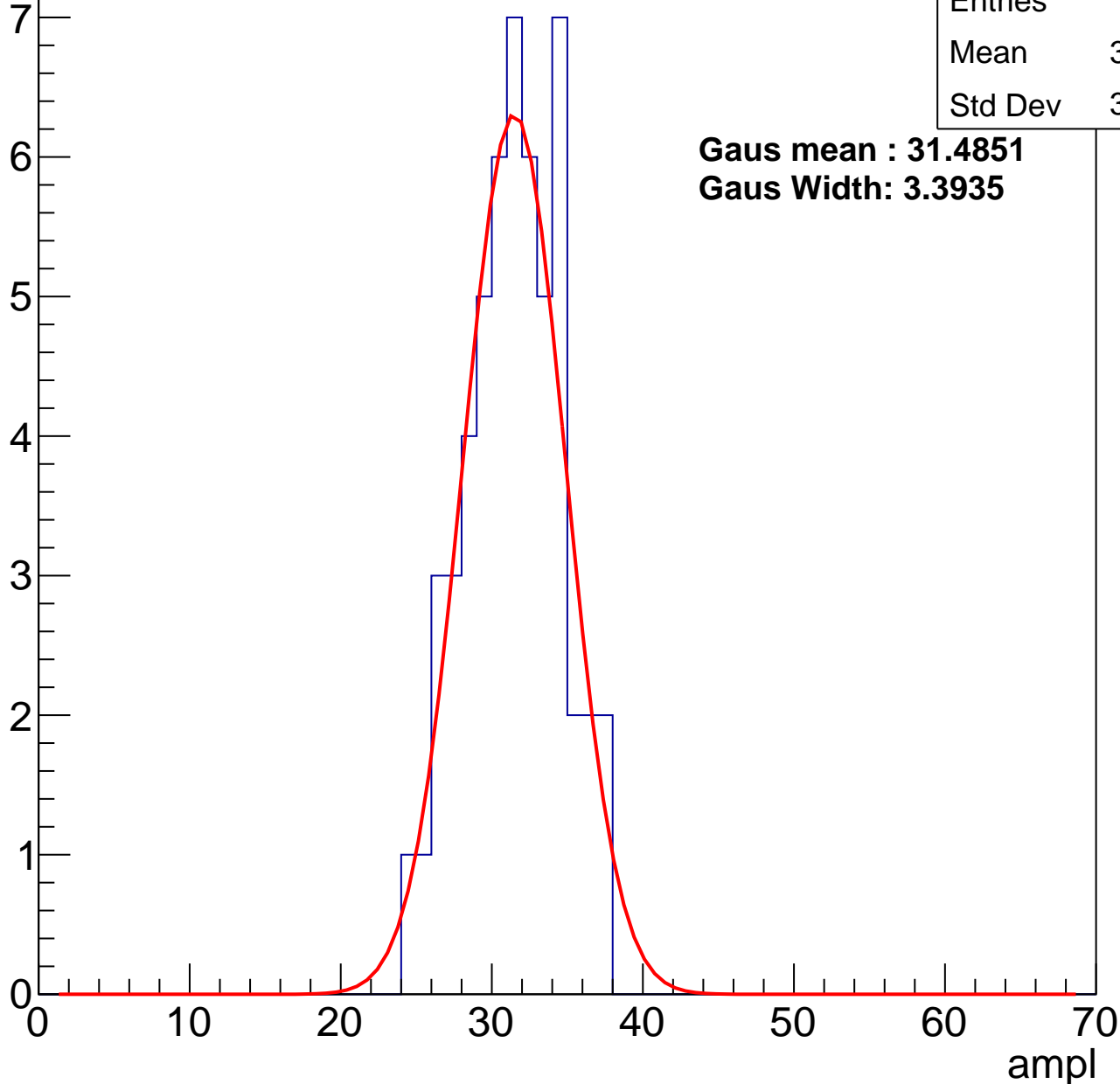
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	30.98
Std Dev	3.082

Gaus mean : 31.4851

Gaus Width: 3.3935



B1L100S, U19-ch114, adc1

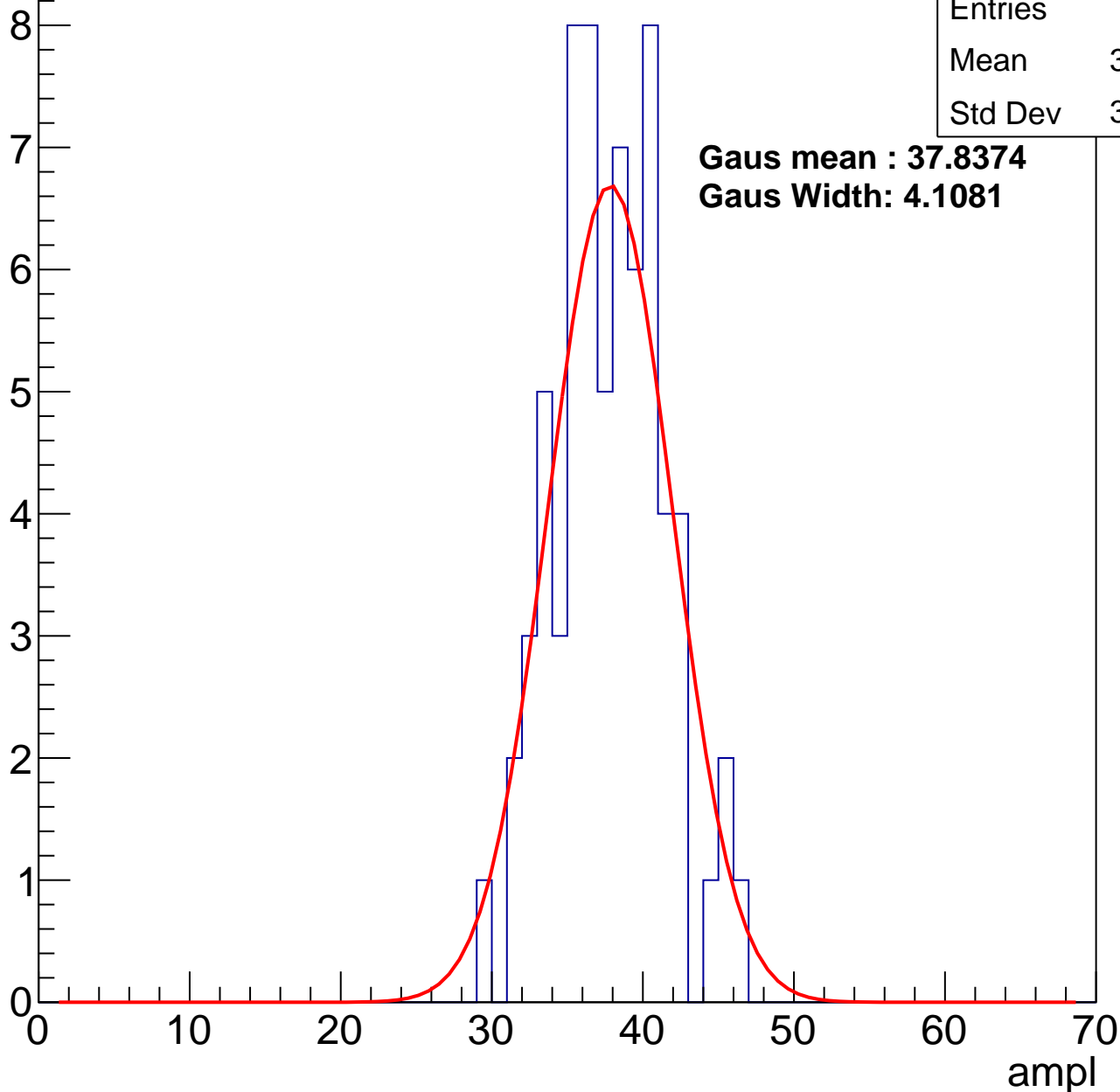
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.34
Std Dev	3.588

Gaus mean : 37.8374

Gaus Width: 4.1081



B1L100S, U19-ch114, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	43.91
Std Dev	3.507

Gaus mean : 44.5282

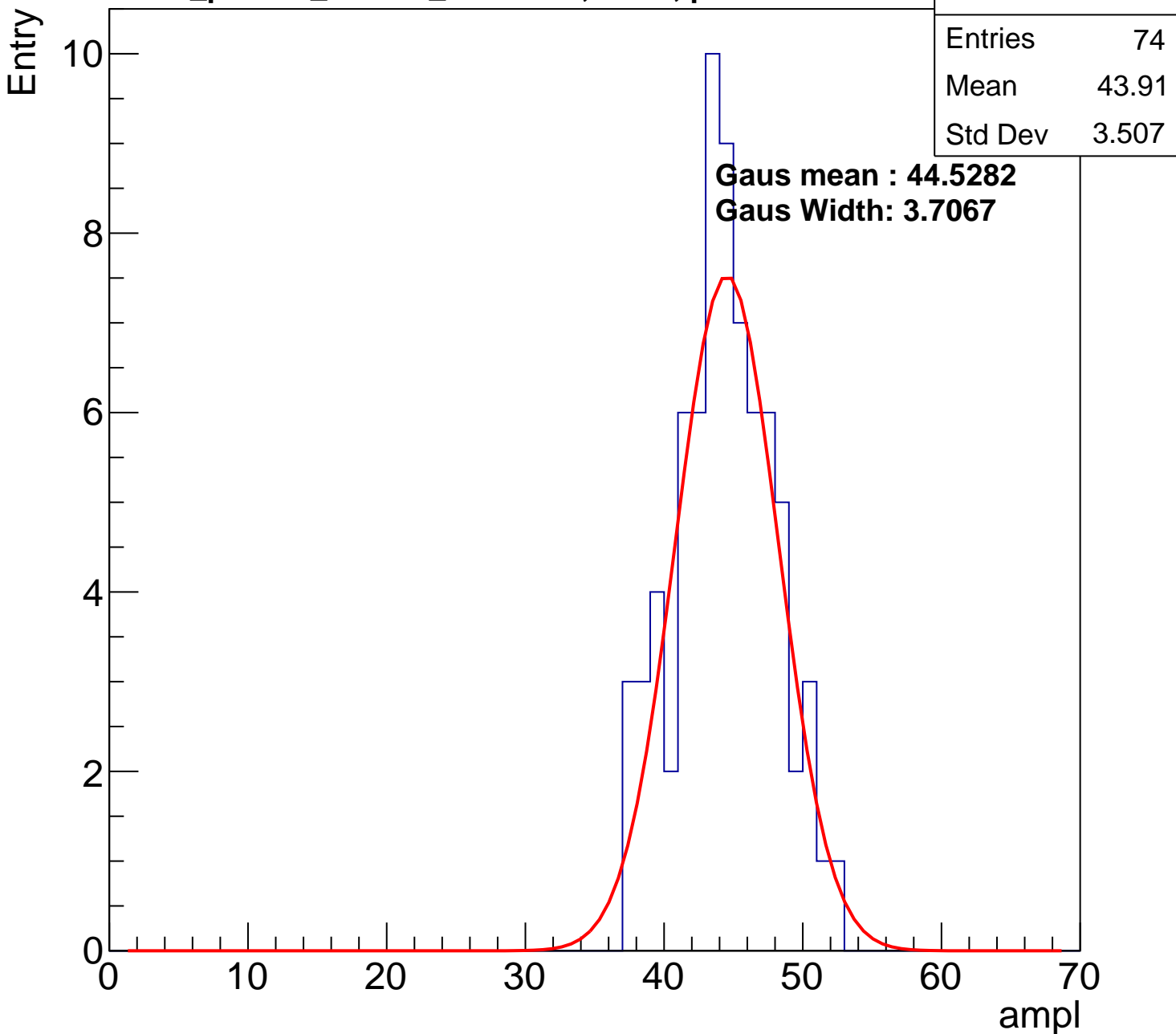
Gaus Width: 3.7067

Entry

10
8
6
4
2
0

ampl

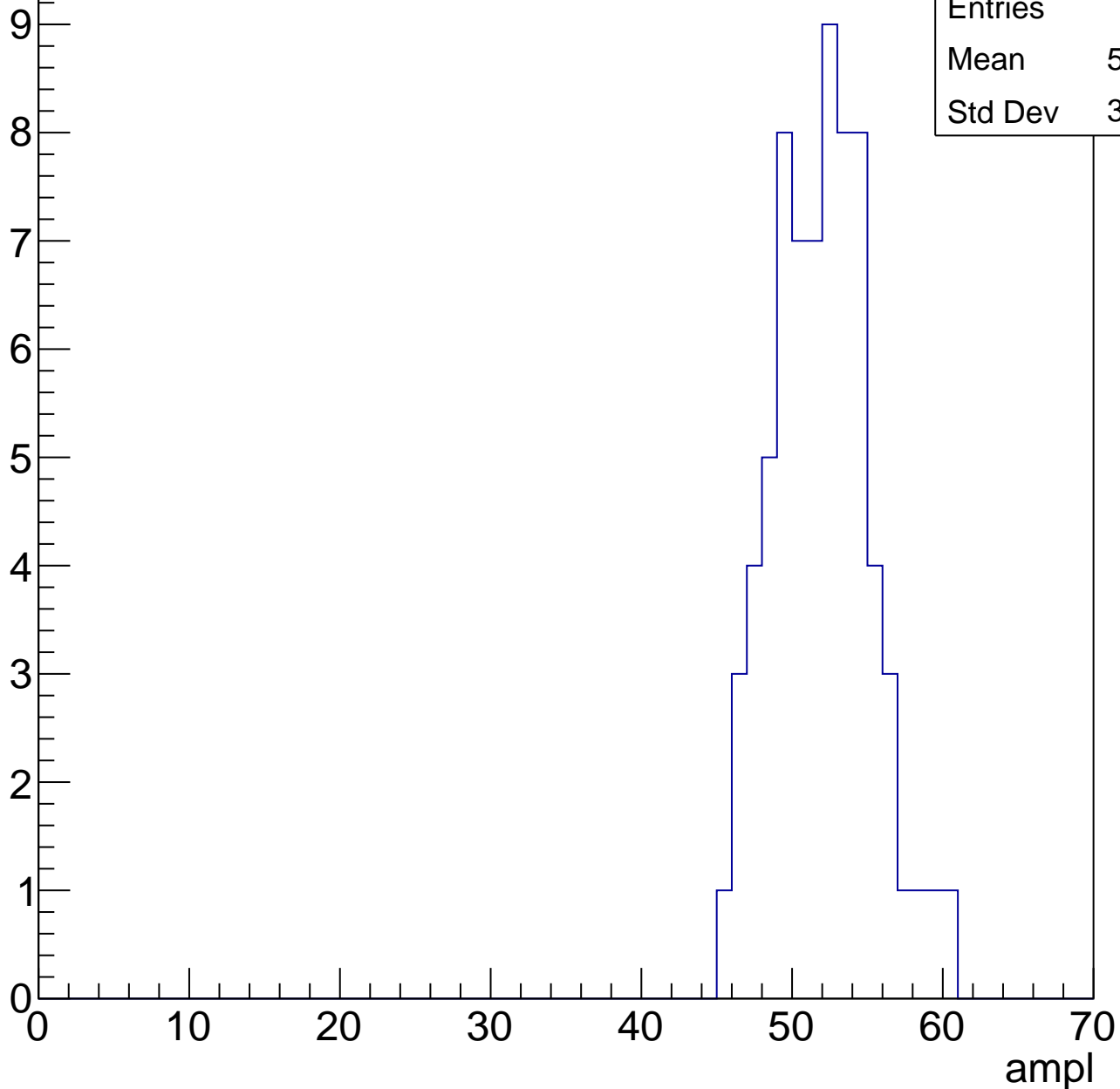
0 10 20 30 40 50 60 70



B1L100S, U19-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

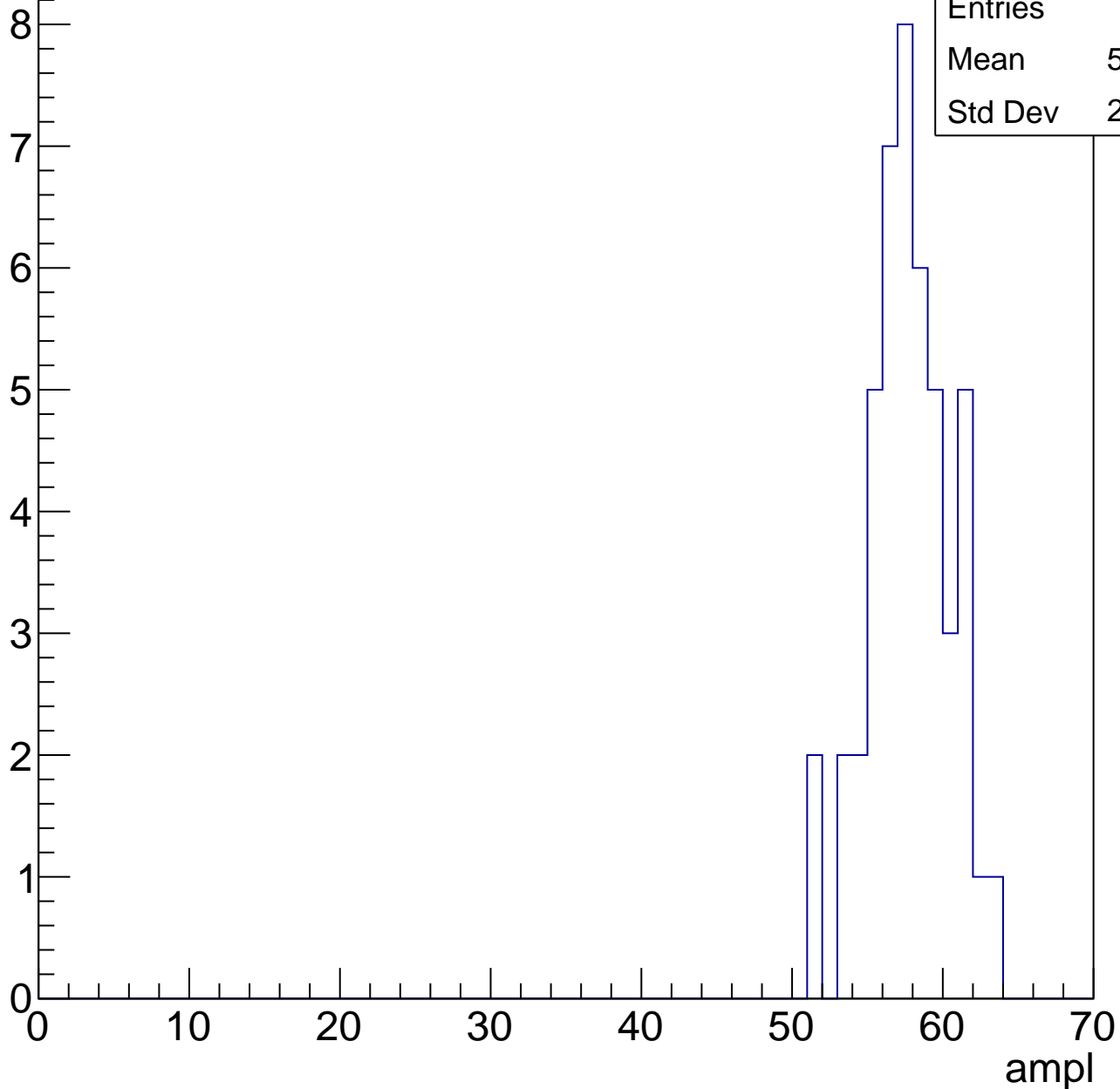


Entries	71
Mean	51.49
Std Dev	3.193

B1L100S, U19-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



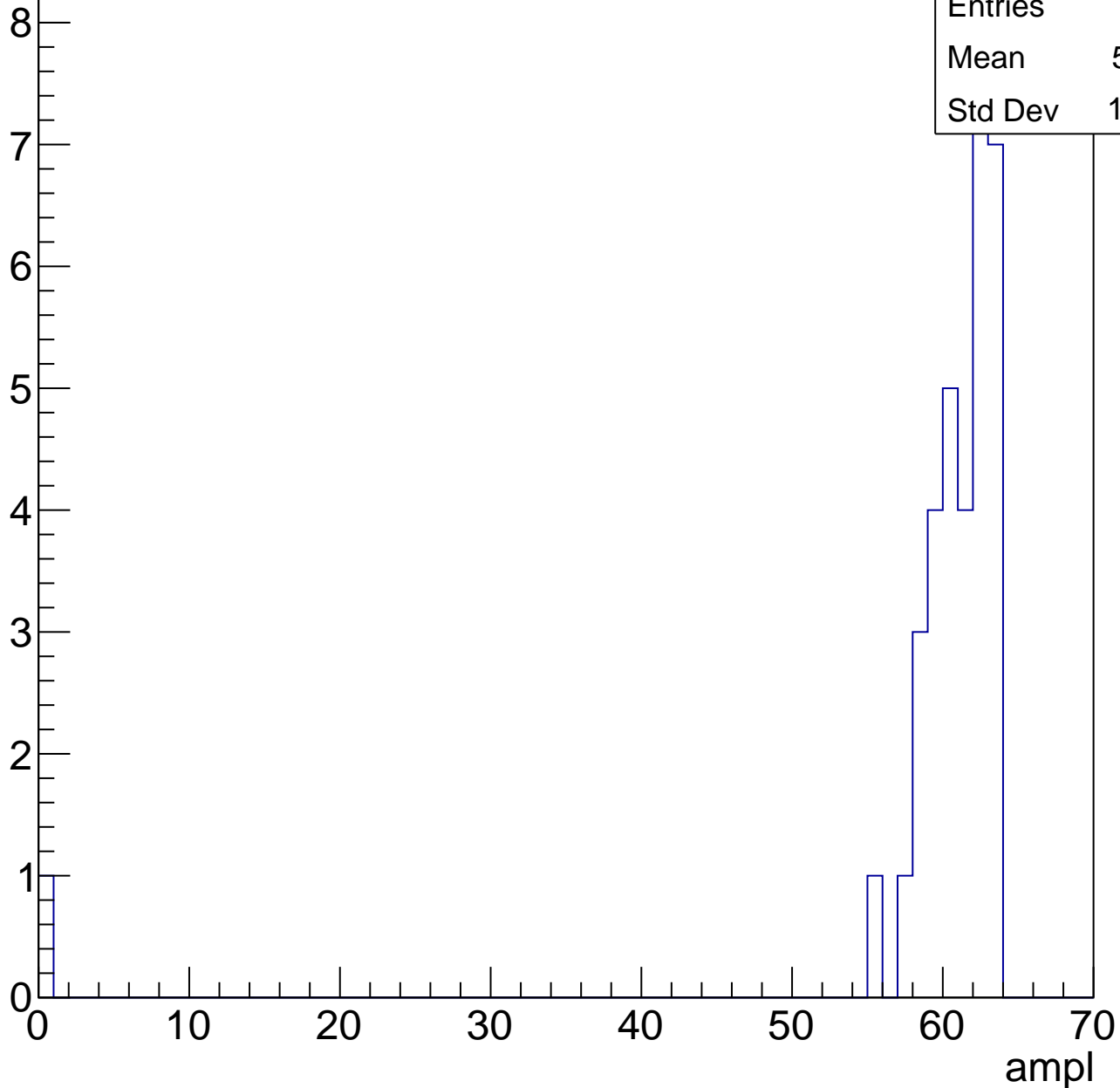
Entries	47
Mean	57.28
Std Dev	2.687

B1L100S, U19-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.91
Std Dev	10.44



B1L100S, U19-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	61.8
Std Dev	1.166

B1L100S, U19-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch115, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	27.86
Std Dev	5.822

Gaus mean : 29.0155

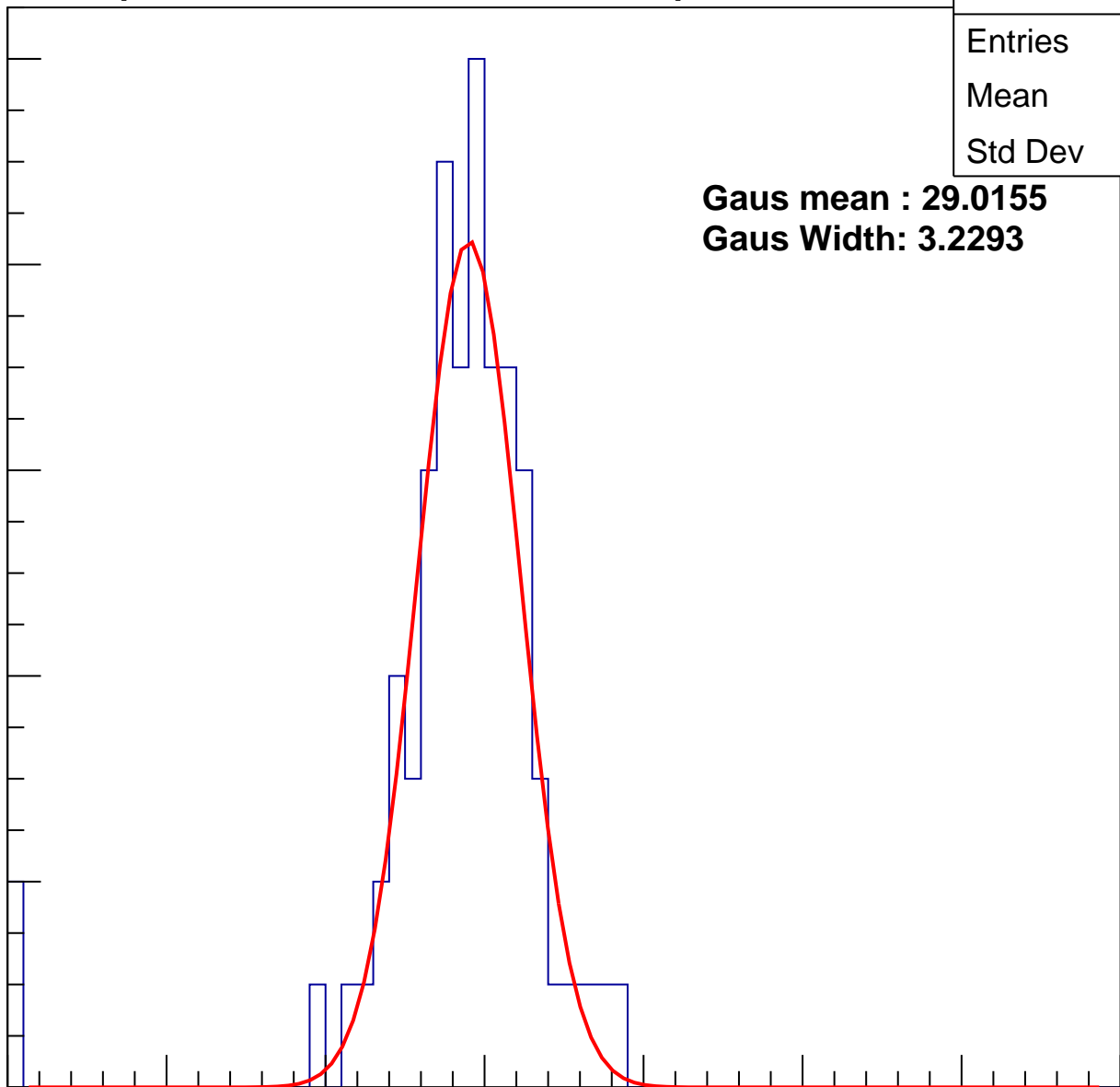
Gaus Width: 3.2293

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch115, adc1

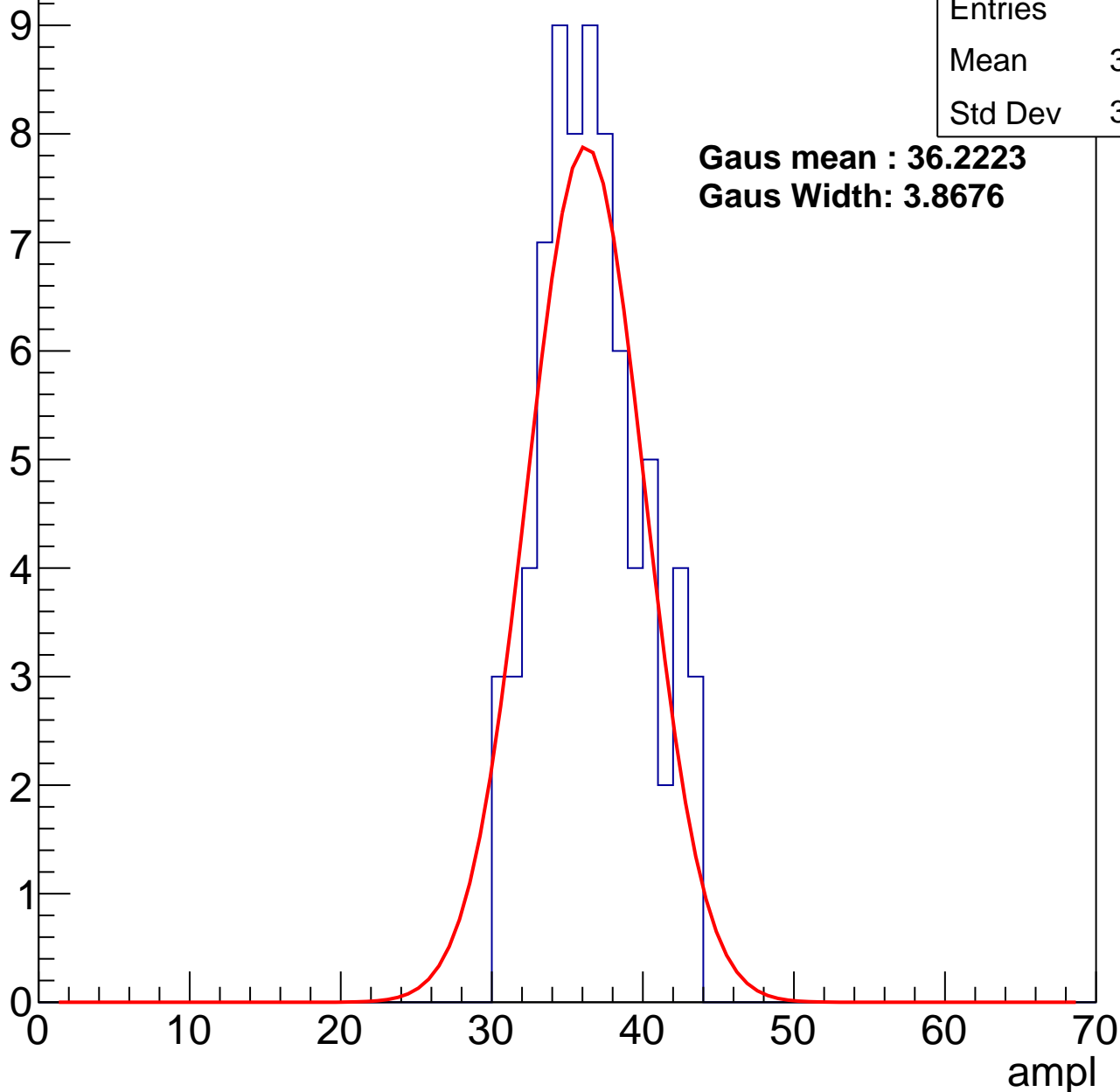
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.15
Std Dev	3.349

Gaus mean : 36.2223

Gaus Width: 3.8676



B1L100S, U19-ch115, adc2

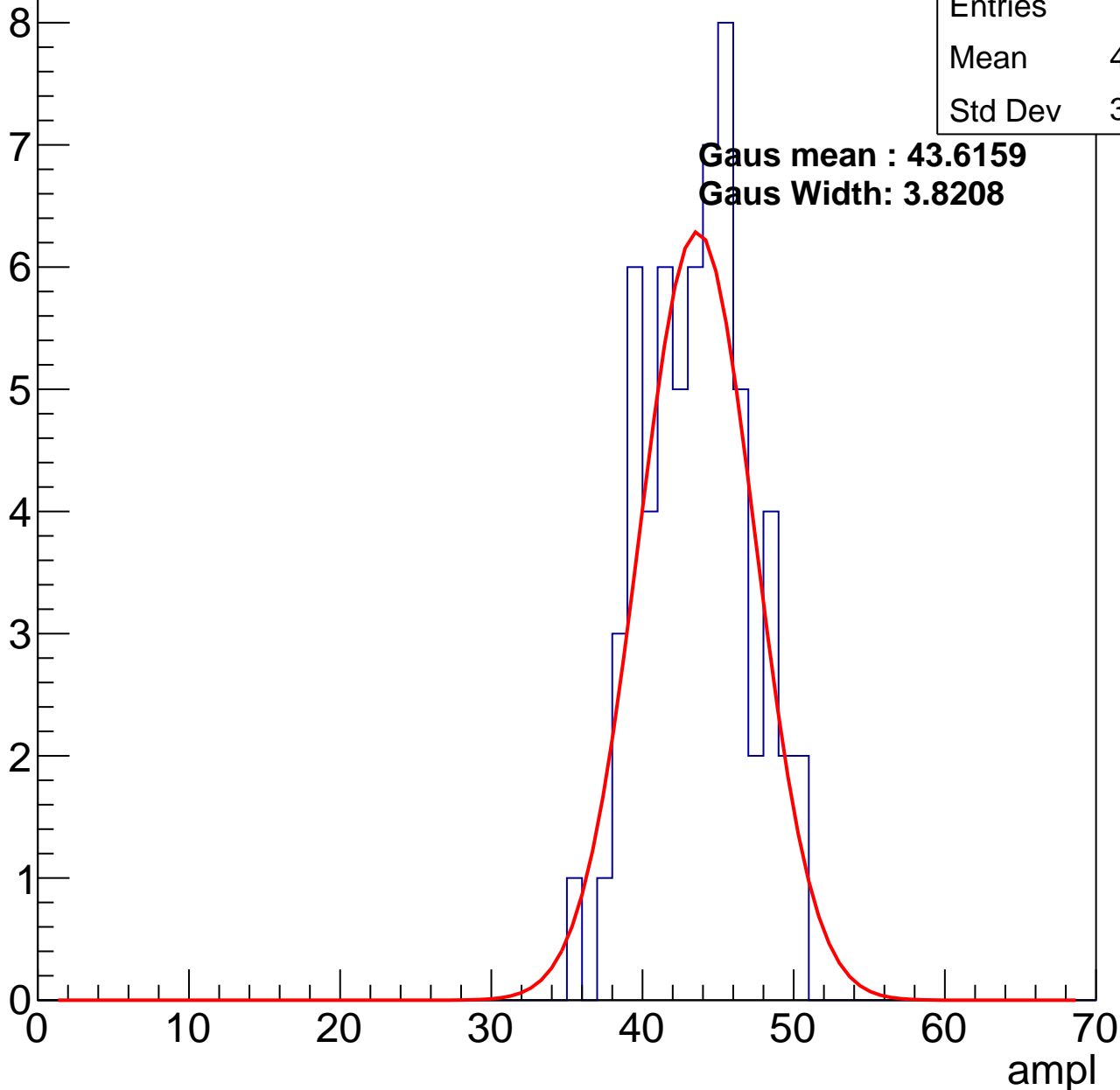
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.16
Std Dev	3.409

Gaus mean : 43.6159

Gaus Width: 3.8208

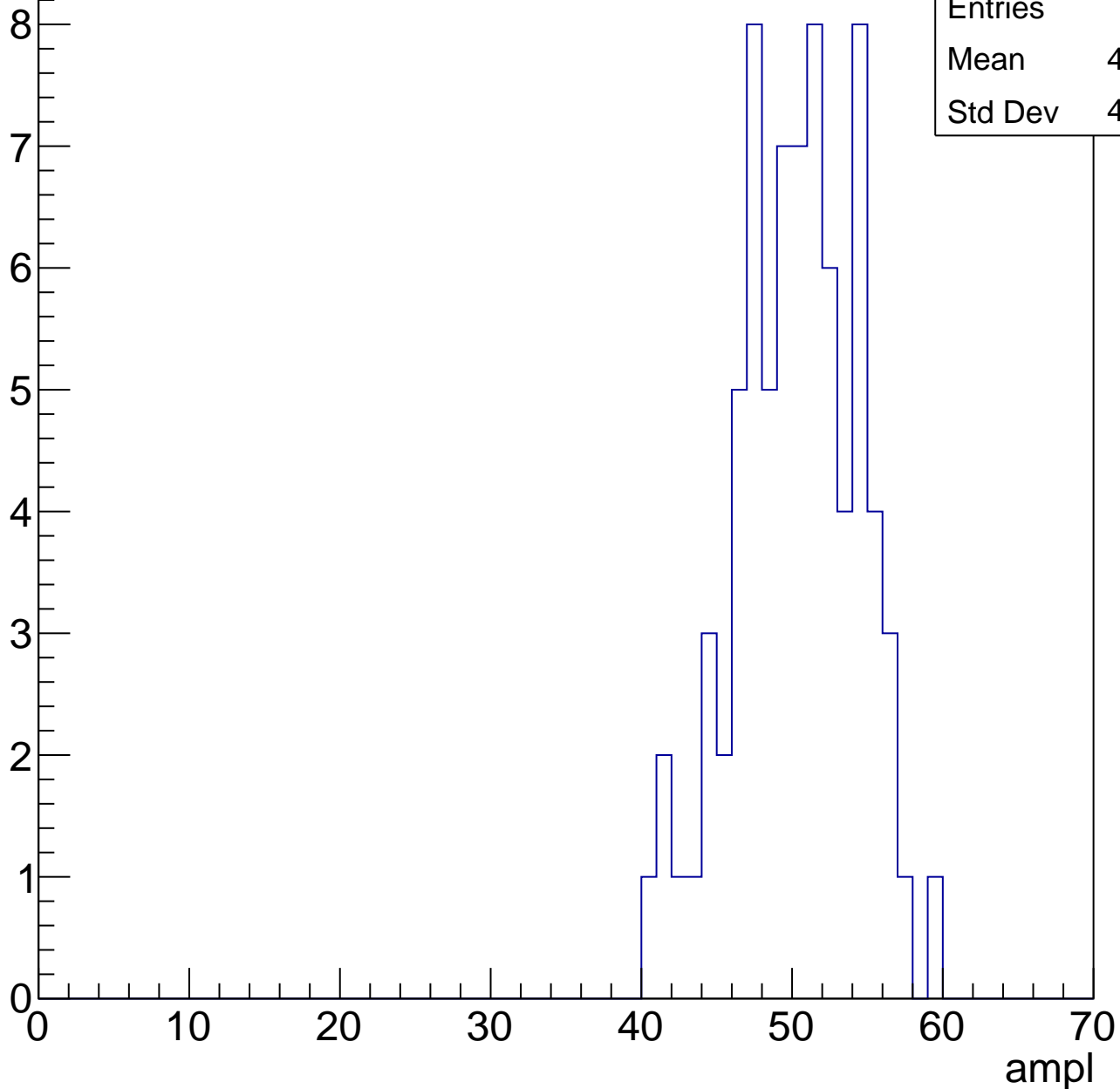


B1L100S, U19-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	49.82
Std Dev	4.019

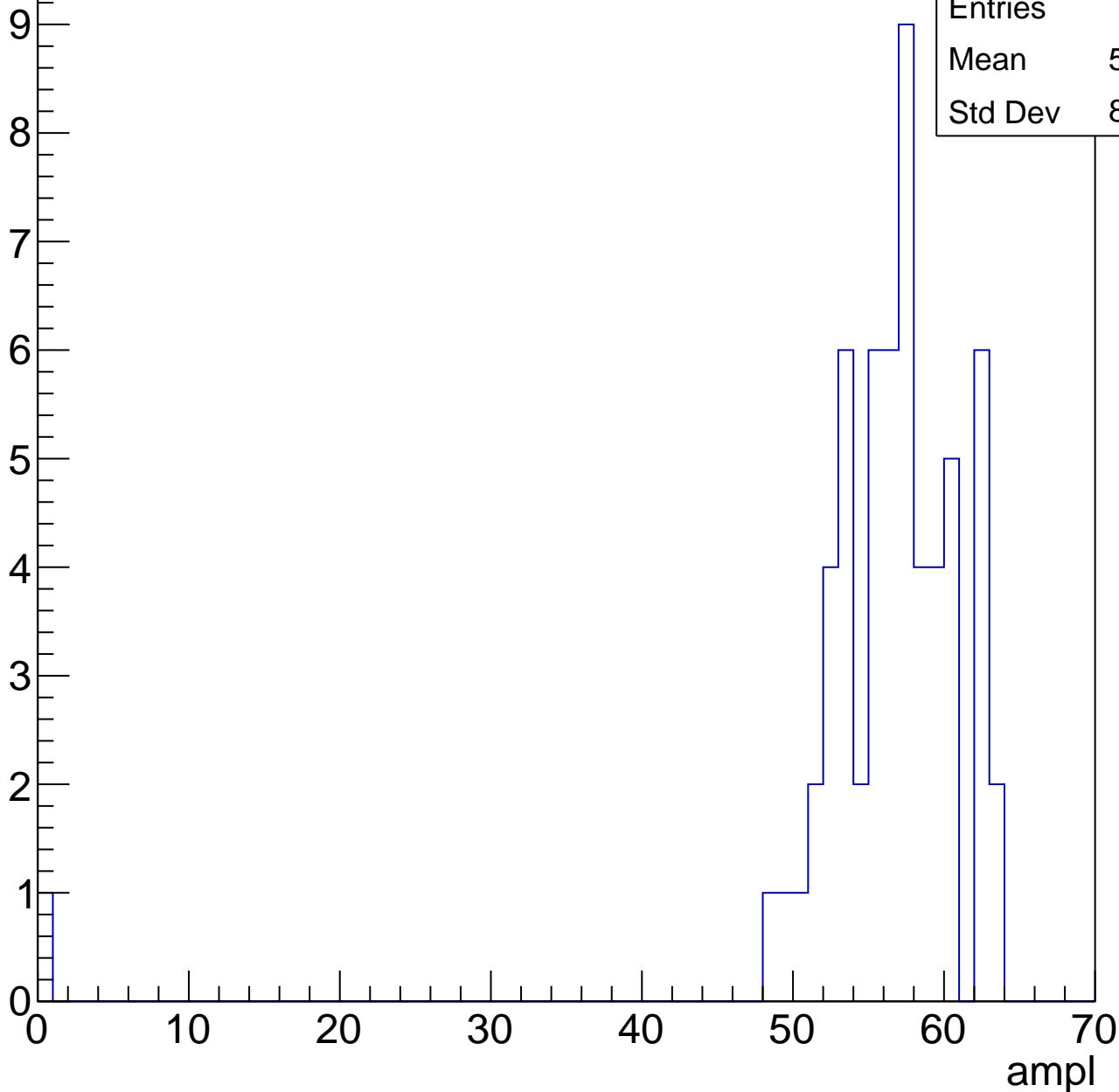


B1L100S, U19-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	55.47
Std Dev	8.067

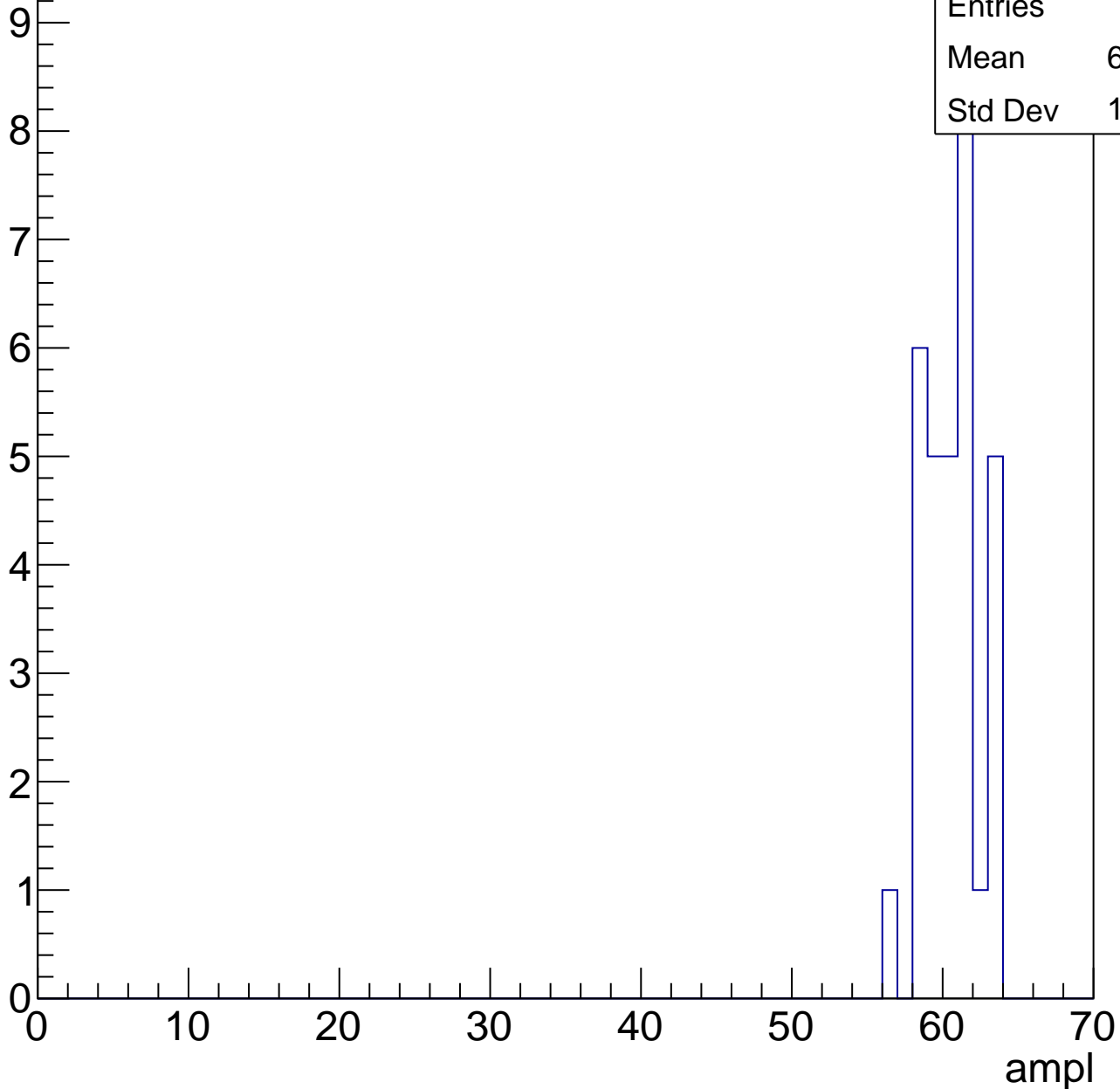


B1L100S, U19-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	60.16
Std Dev	1.787



B1L100S, U19-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U19-ch116, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	30.4
Std Dev	3.355

Gaus mean : 31.2957

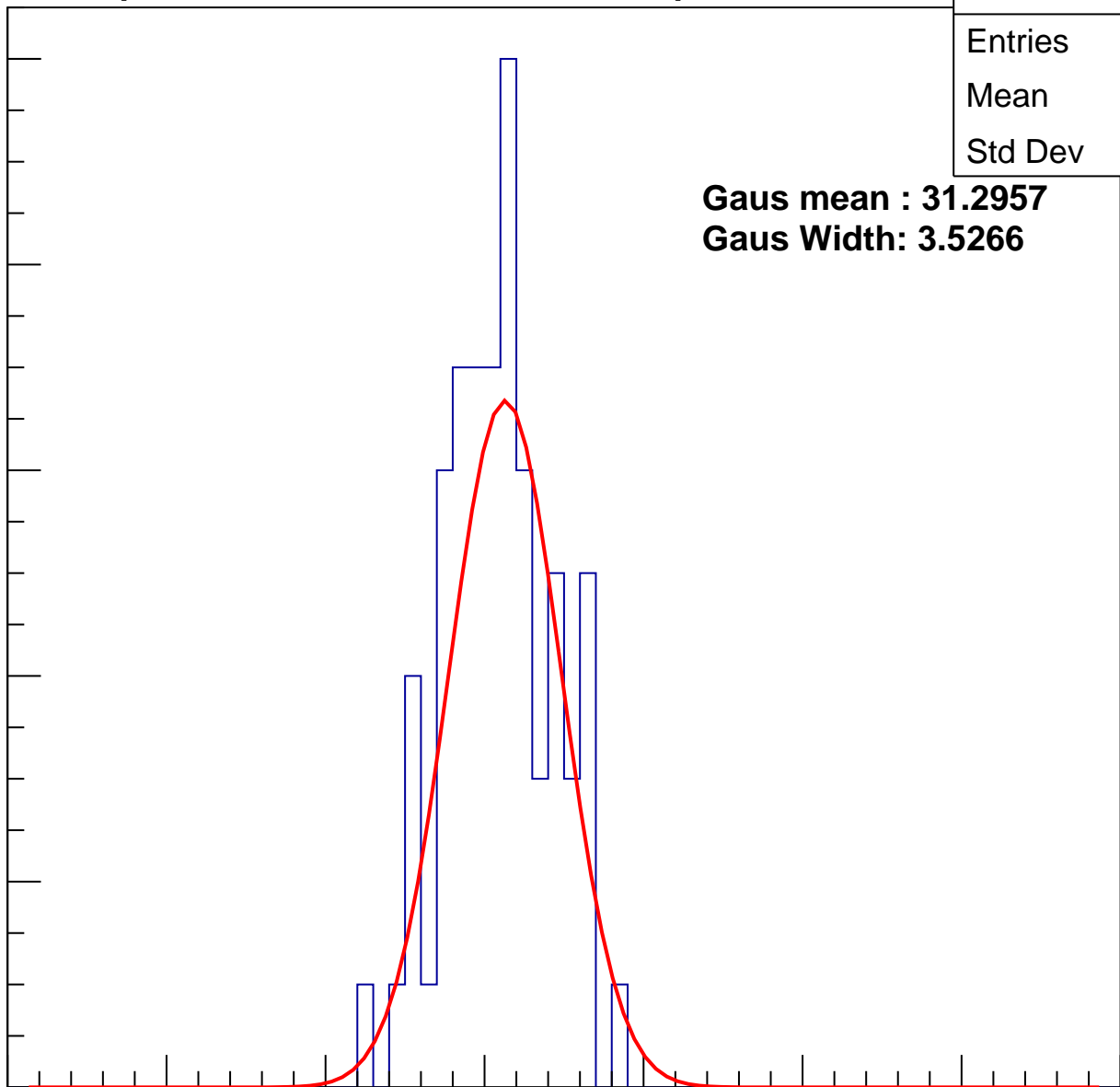
Gaus Width: 3.5266

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch116, adc1

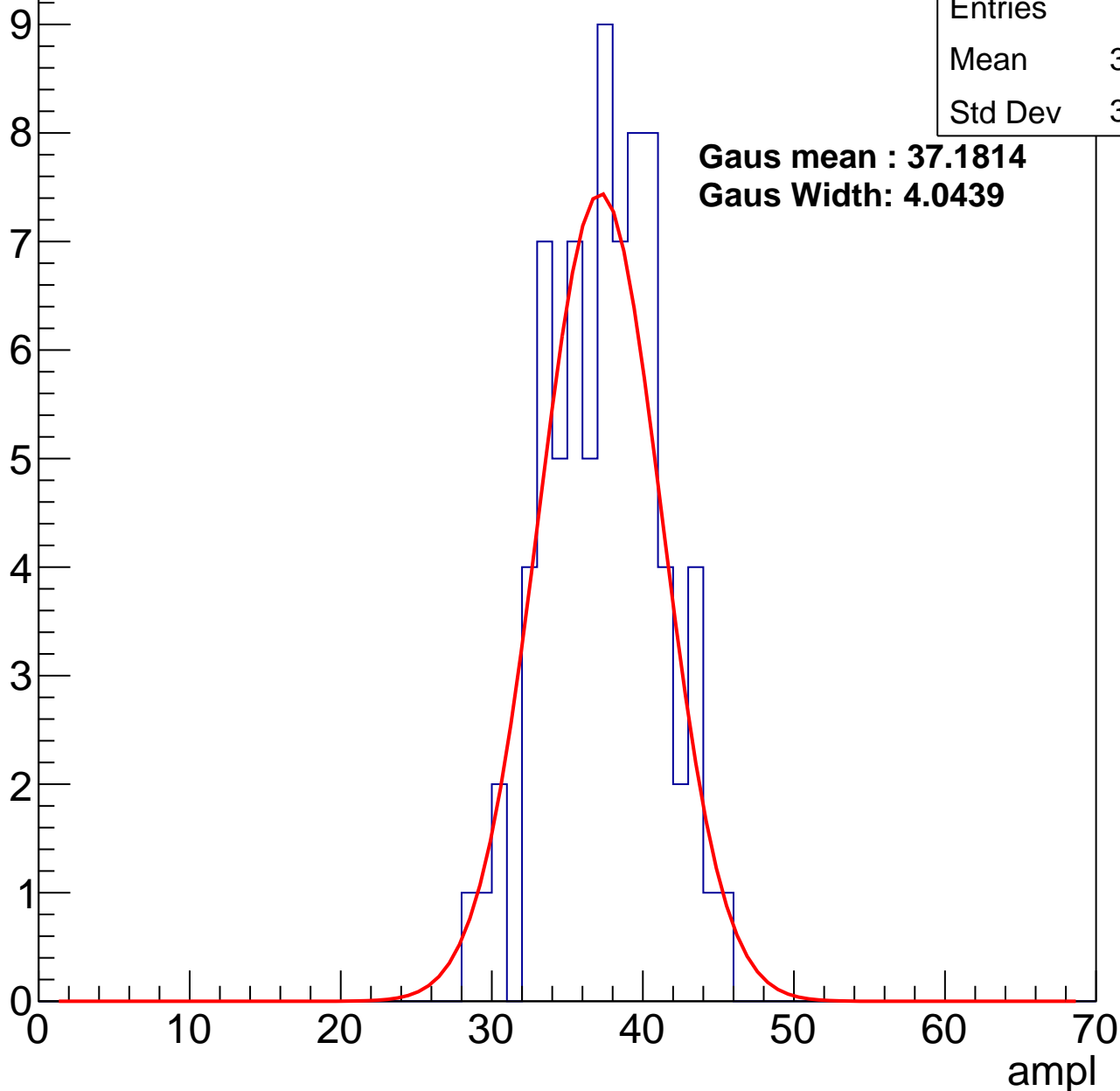
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.99
Std Dev	3.654

Gaus mean : 37.1814

Gaus Width: 4.0439



B1L100S, U19-ch116, adc2

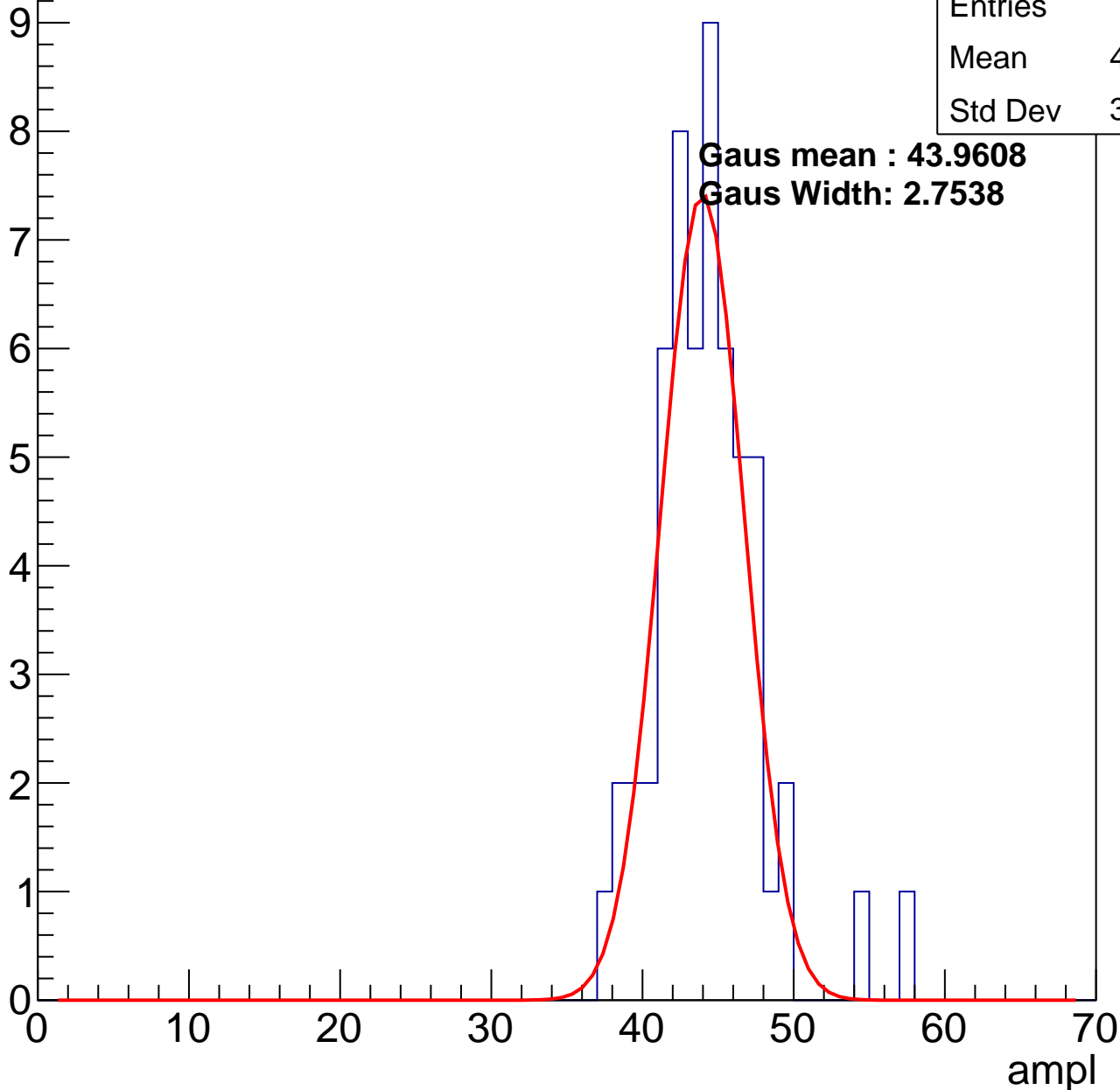
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	43.84
Std Dev	3.503

Gaus mean : 43.9608

Gaus Width: 2.7538

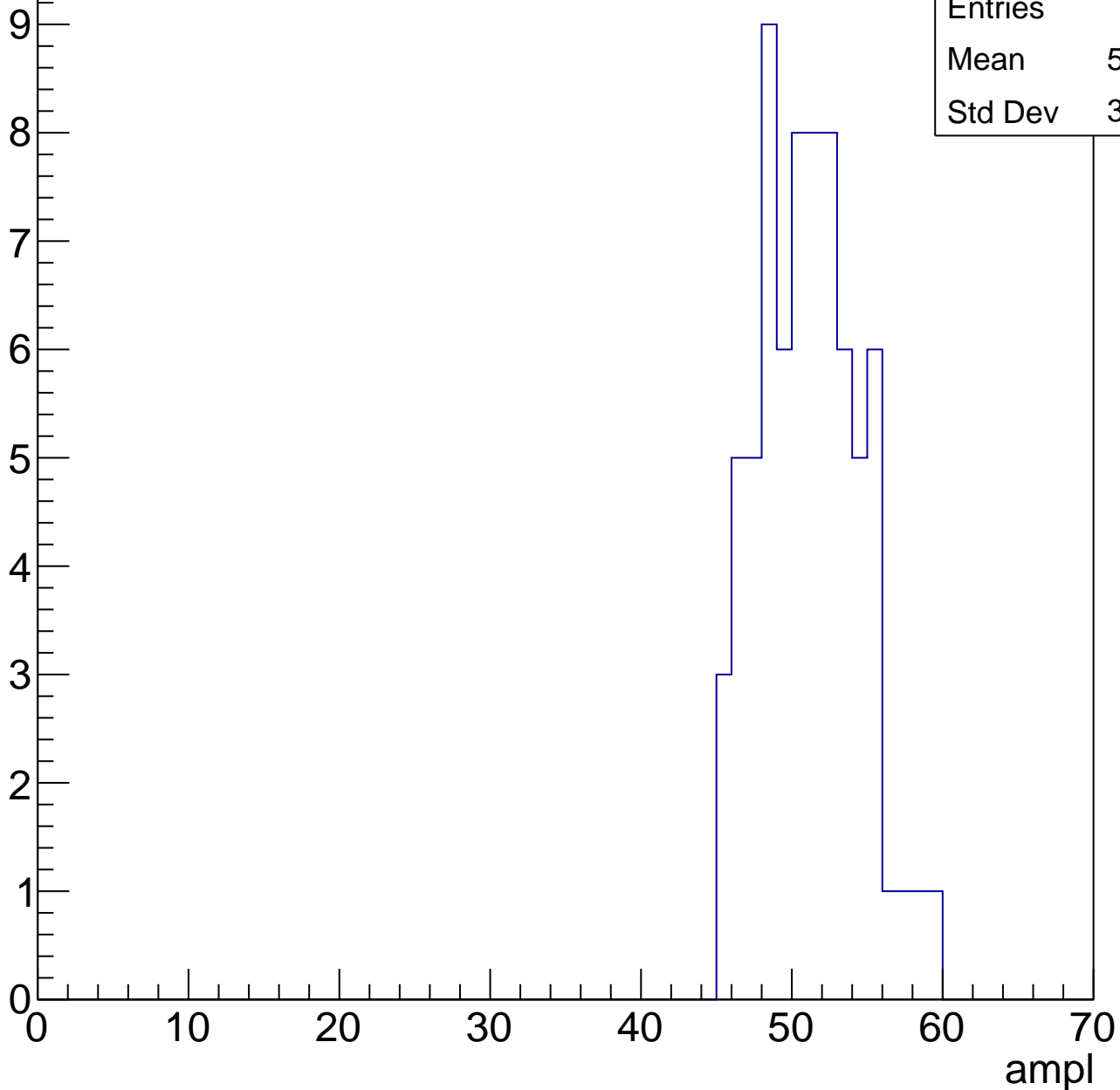


B1L100S, U19-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

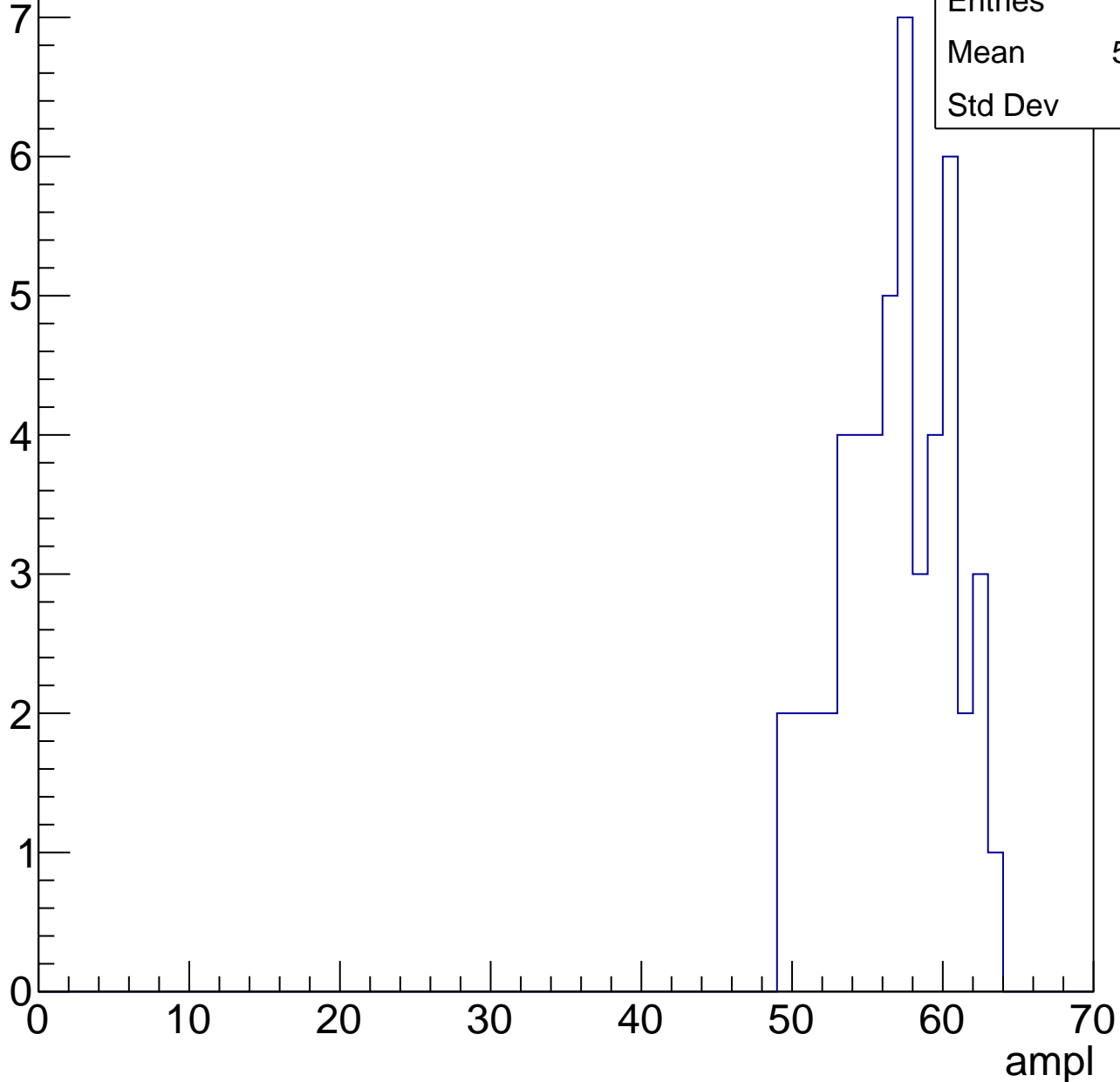
Entries	73
Mean	50.66
Std Dev	3.236



B1L100S, U19-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



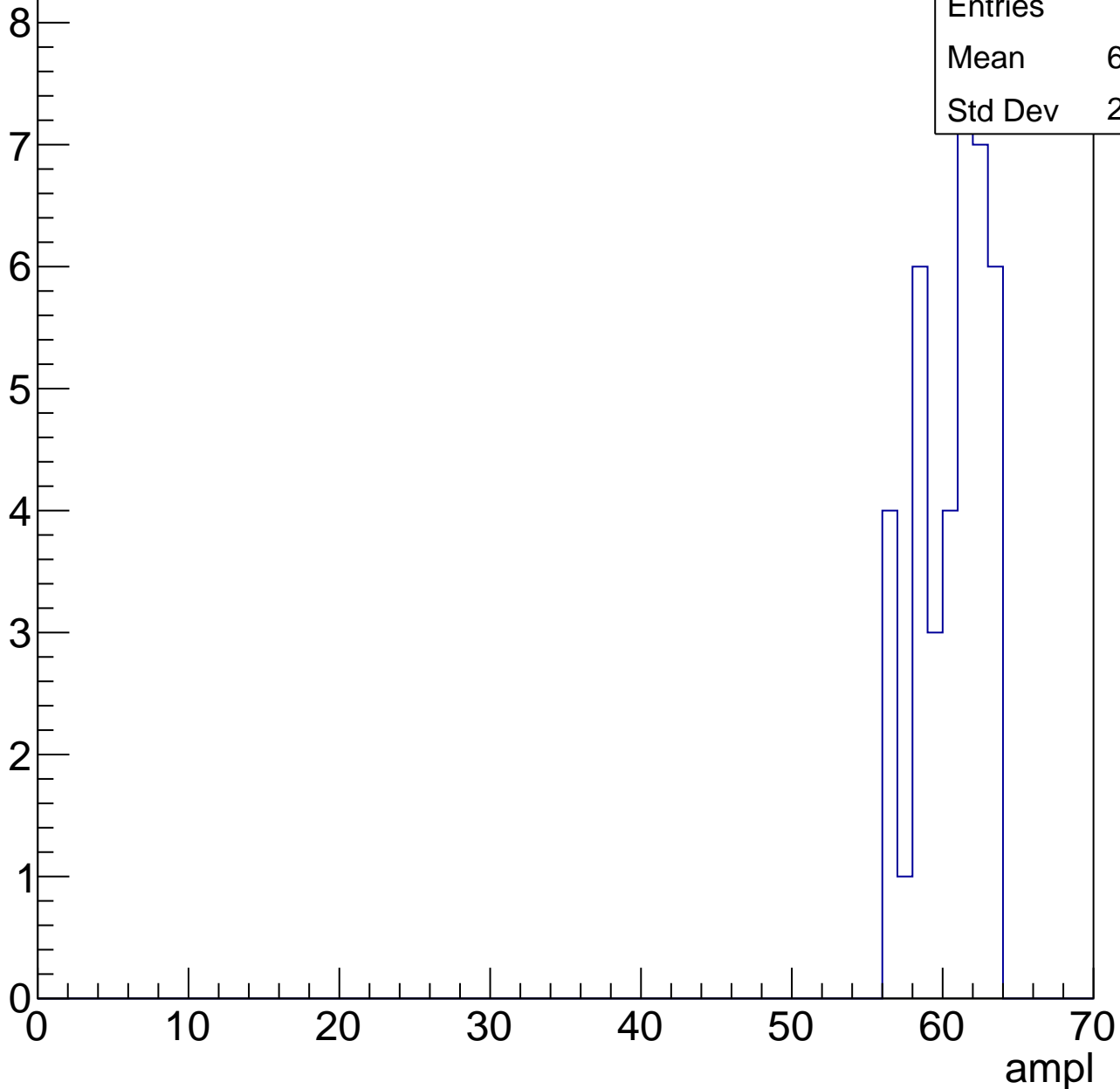
Entries	51
Mean	56.31
Std Dev	3.6

B1L100S, U19-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

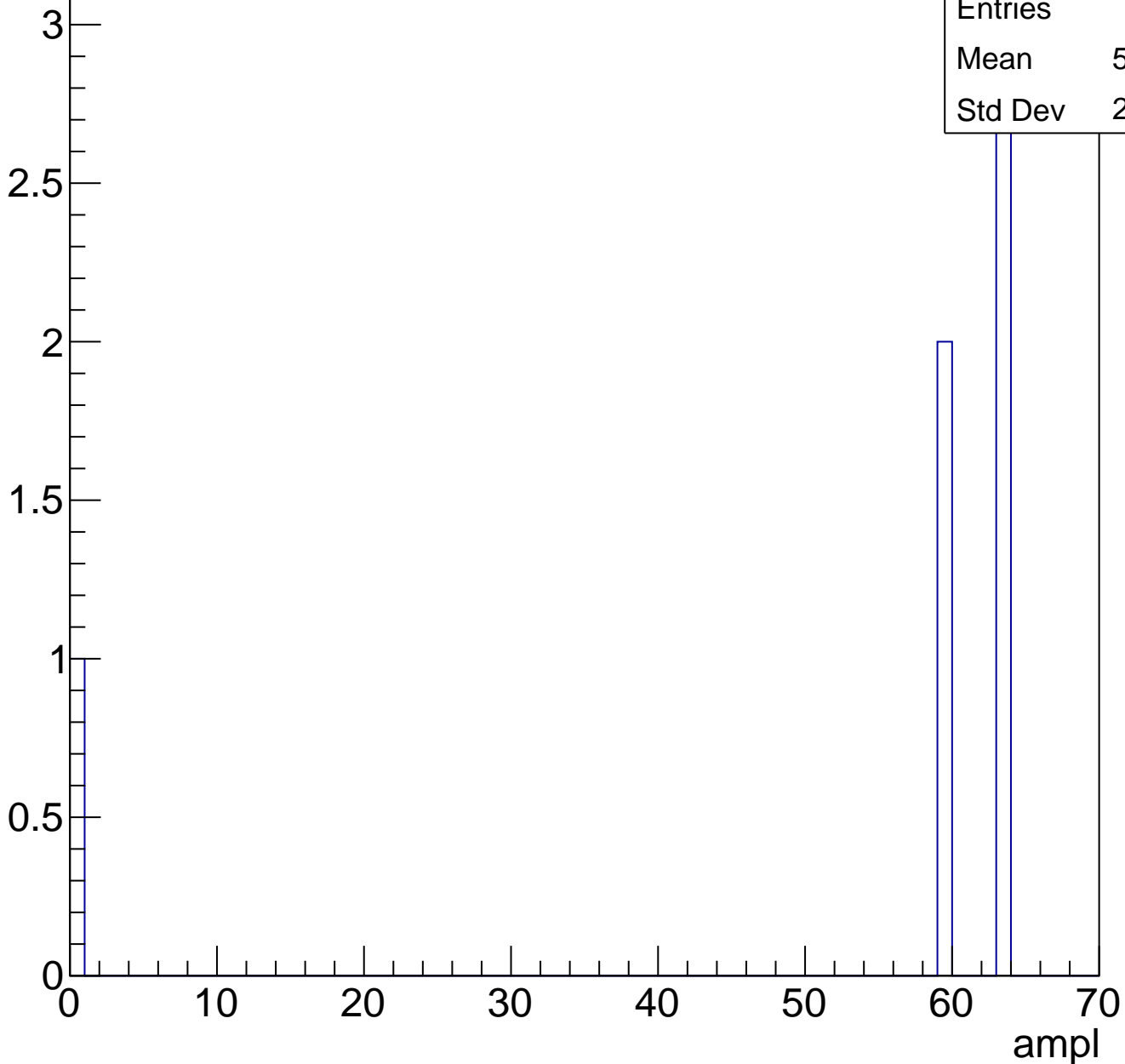
Entries	39
Mean	60.15
Std Dev	2.202



B1L100S, U19-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

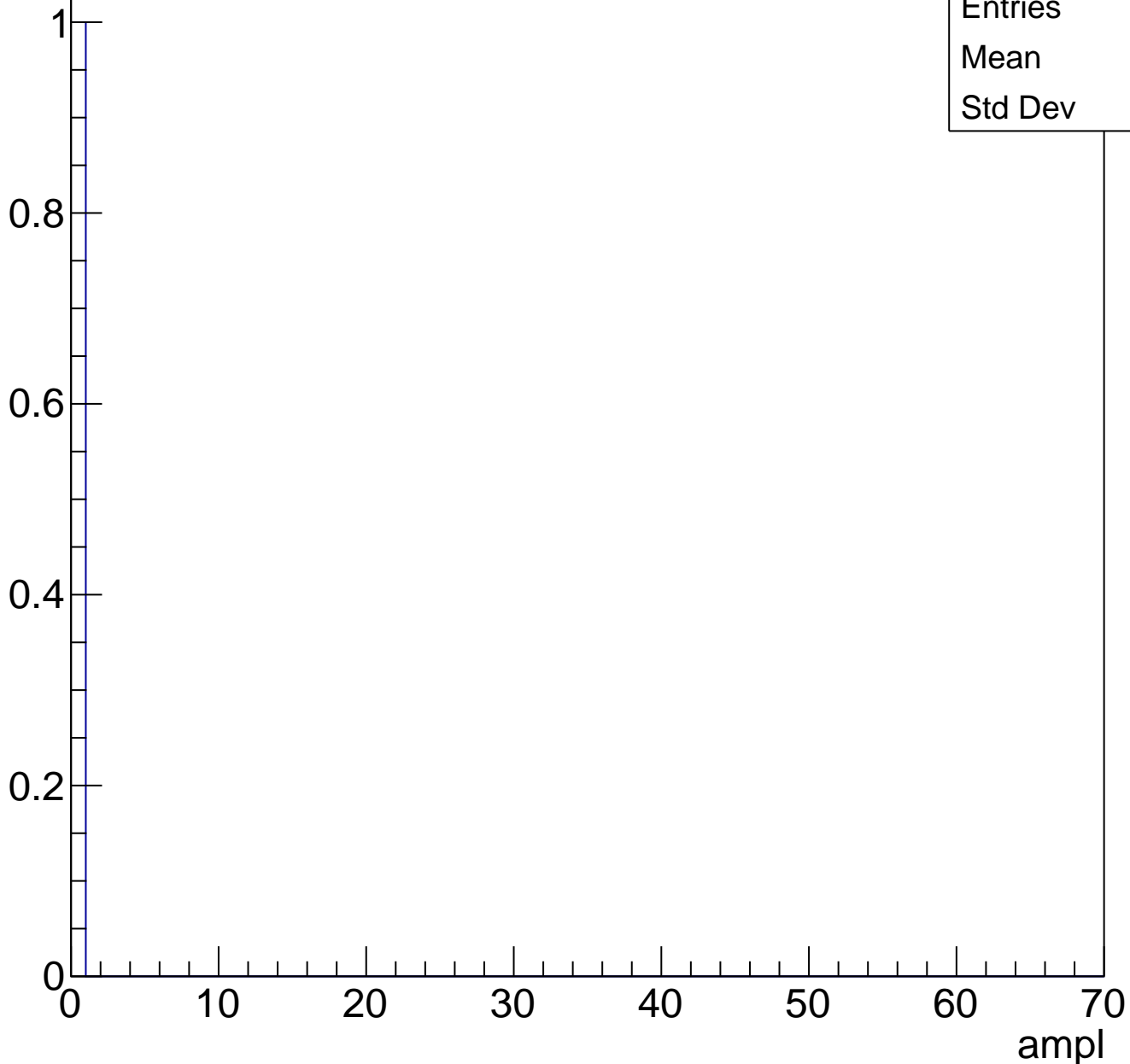
Entry



B1L100S, U19-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch117, adc0

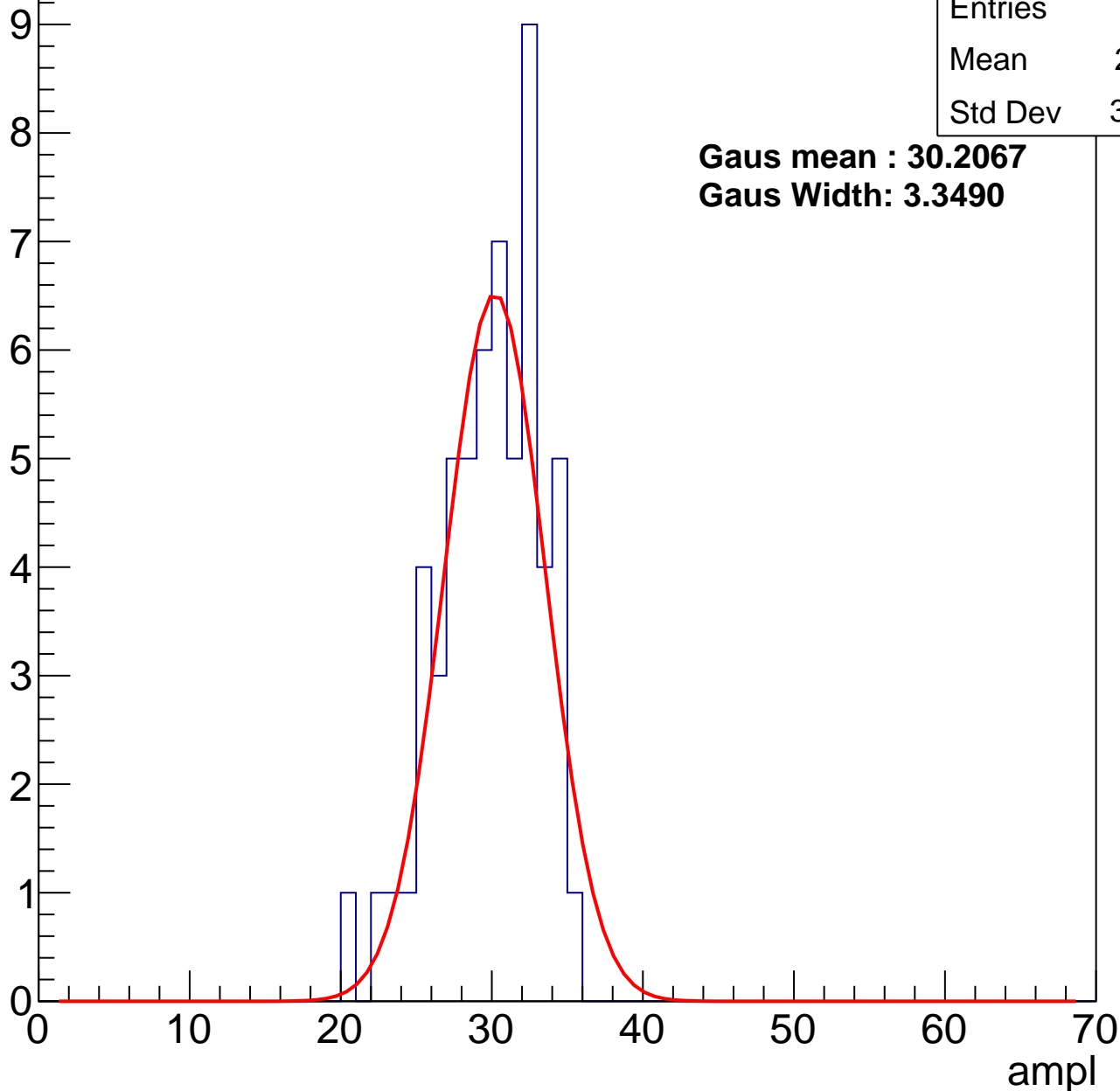
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.41
Std Dev	3.296

Gaus mean : 30.2067

Gaus Width: 3.3490



B1L100S, U19-ch117, adc1

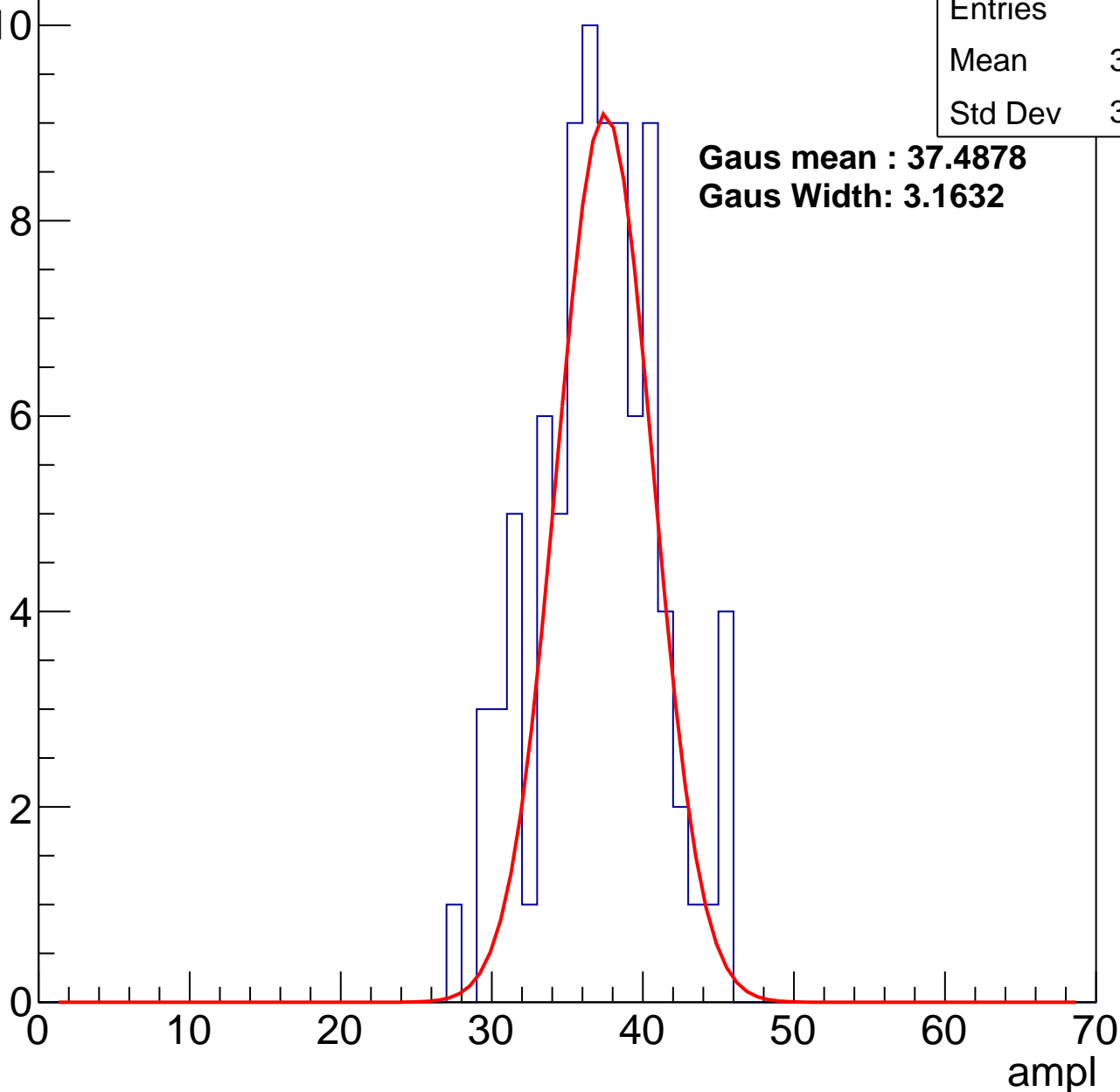
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	36.57
Std Dev	3.965

Gaus mean : 37.4878

Gaus Width: 3.1632



B1L100S, U19-ch117, adc2

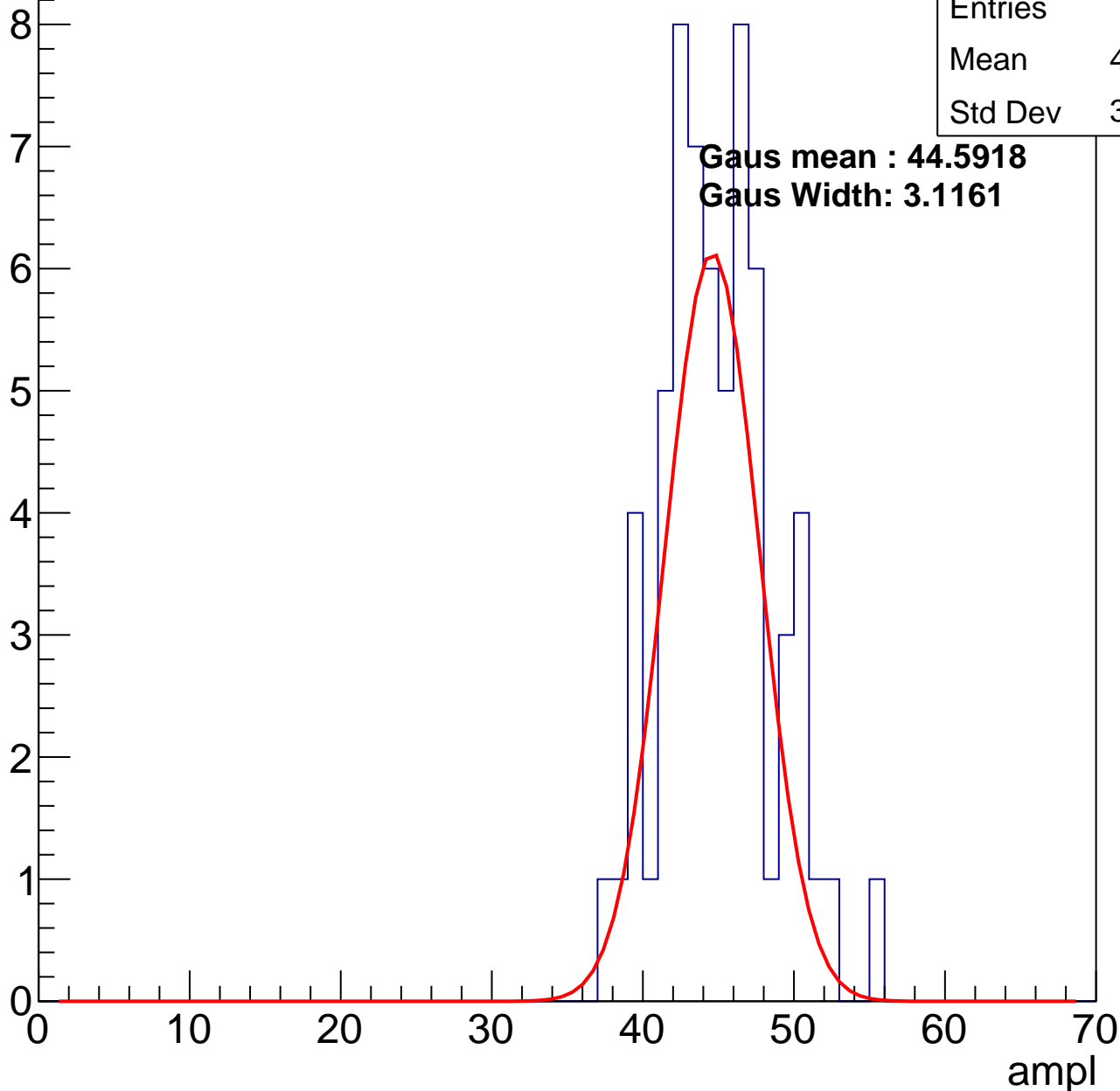
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.52
Std Dev	3.625

Gaus mean : 44.5918

Gaus Width: 3.1161

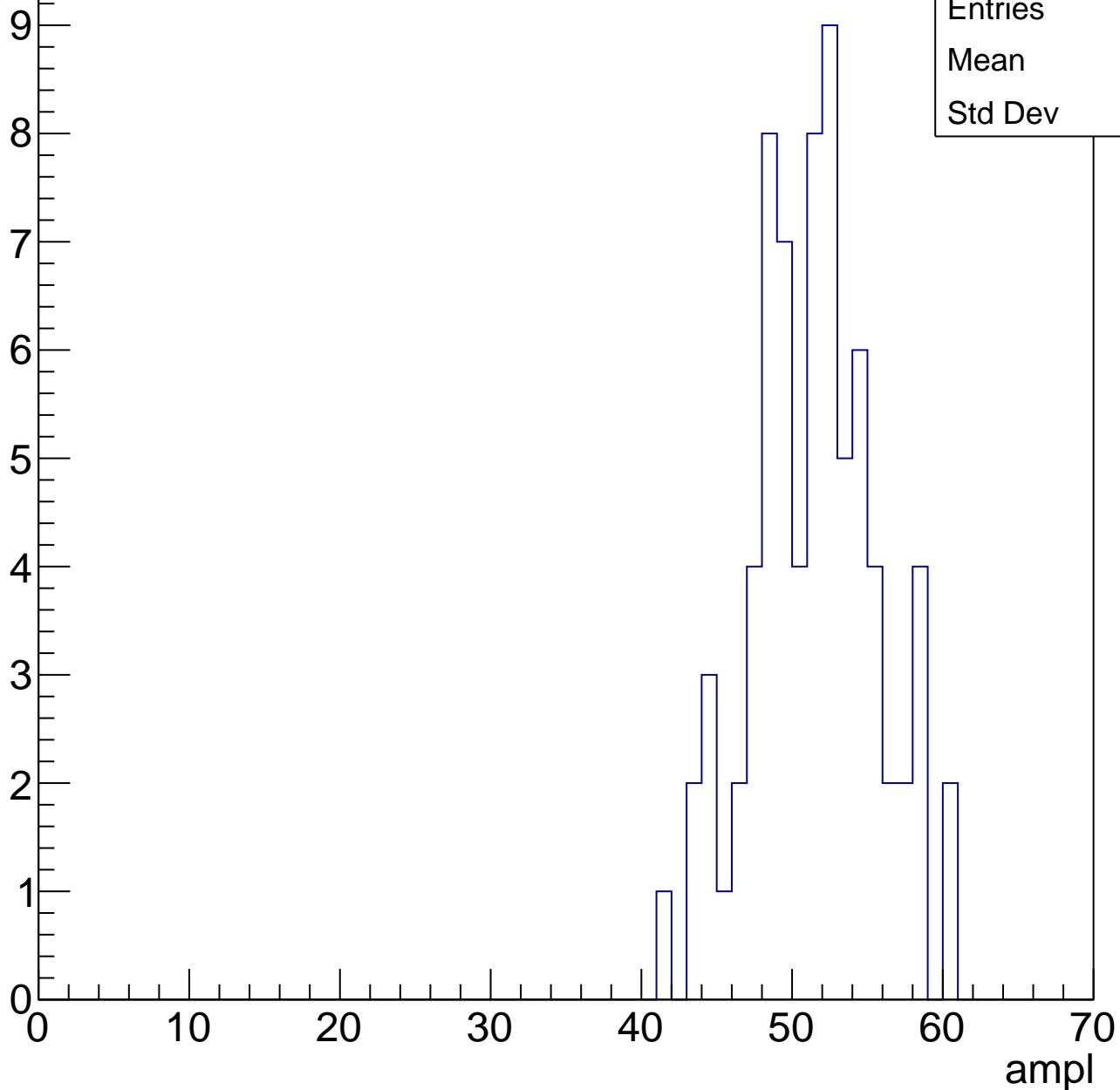


B1L100S, U19-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	51
Std Dev	4.12

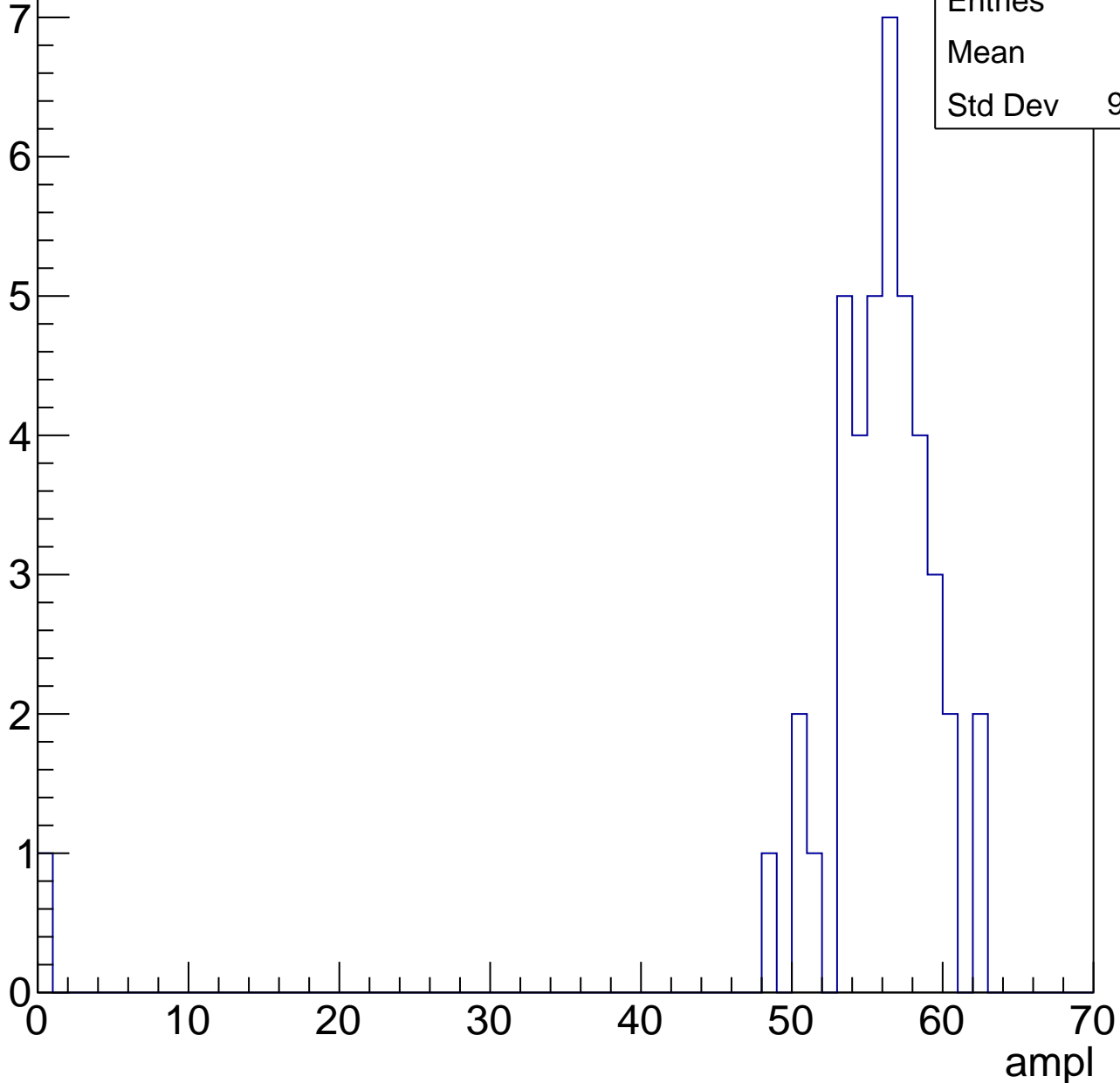


B1L100S, U19-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

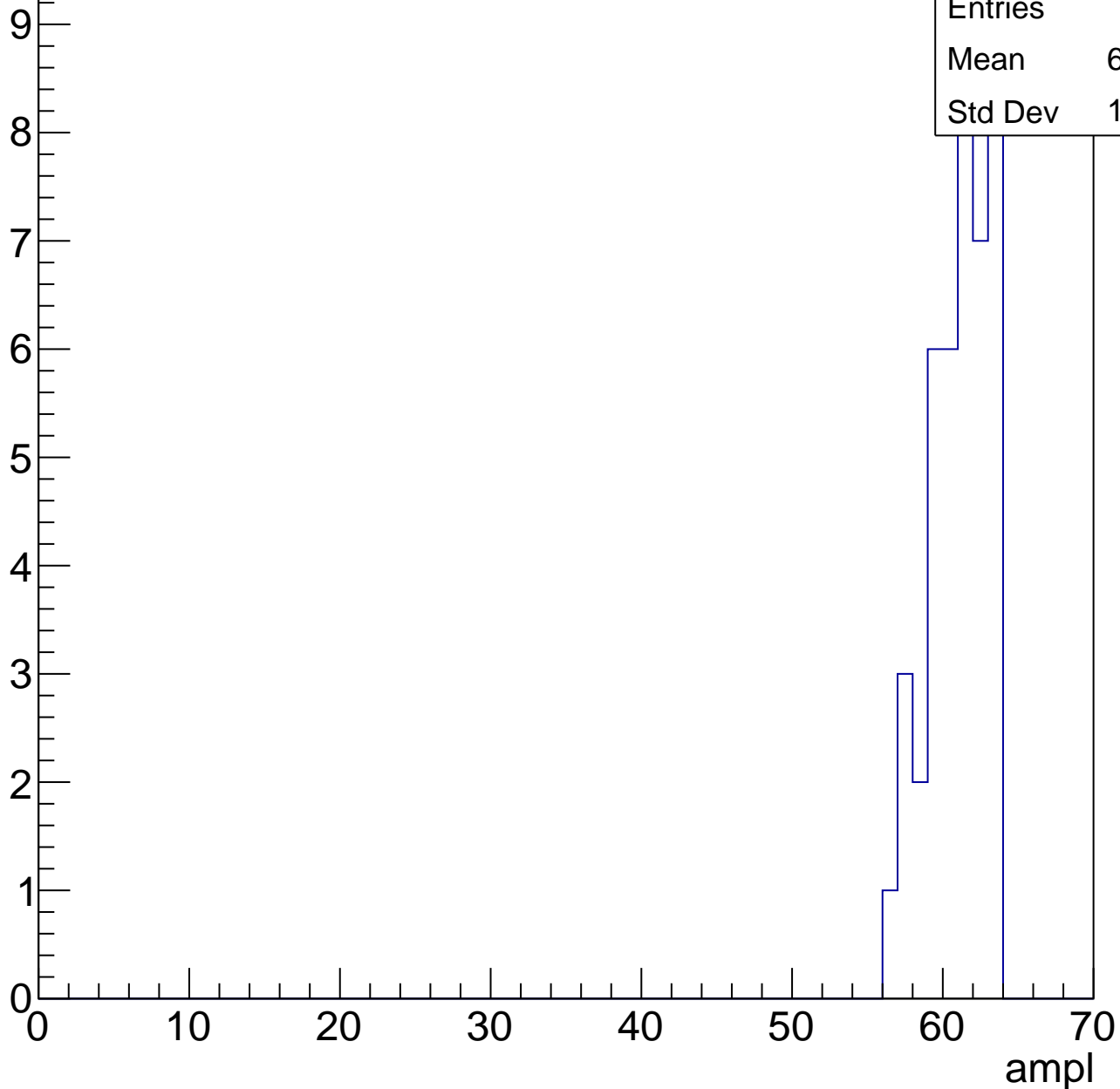
Entries	42
Mean	54.4
Std Dev	9.008



B1L100S, U19-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	42
Mean	60.57
Std Dev	1.904

B1L100S, U19-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

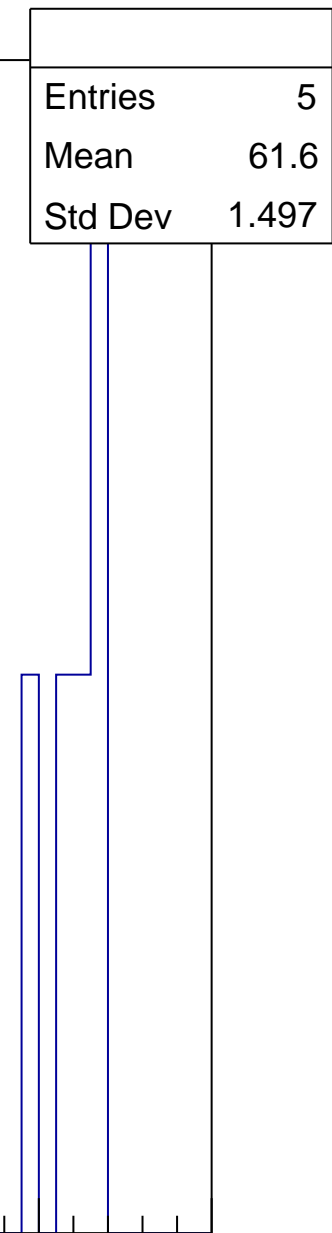
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.497

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U19-ch118, adc0

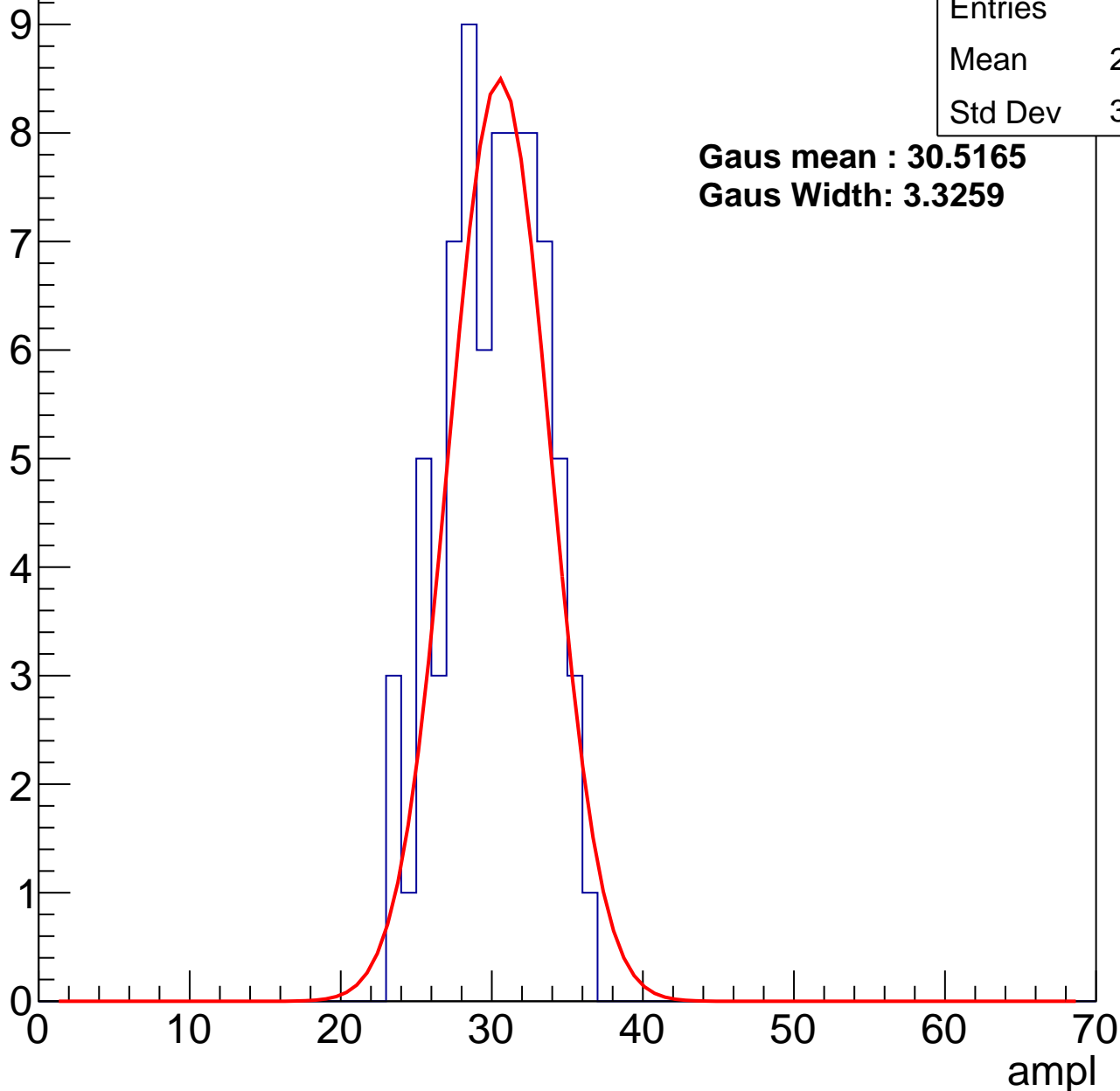
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.69
Std Dev	3.179

Gaus mean : 30.5165

Gaus Width: 3.3259



B1L100S, U19-ch118, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	36.9
Std Dev	3.666

Gaus mean : 37.7450

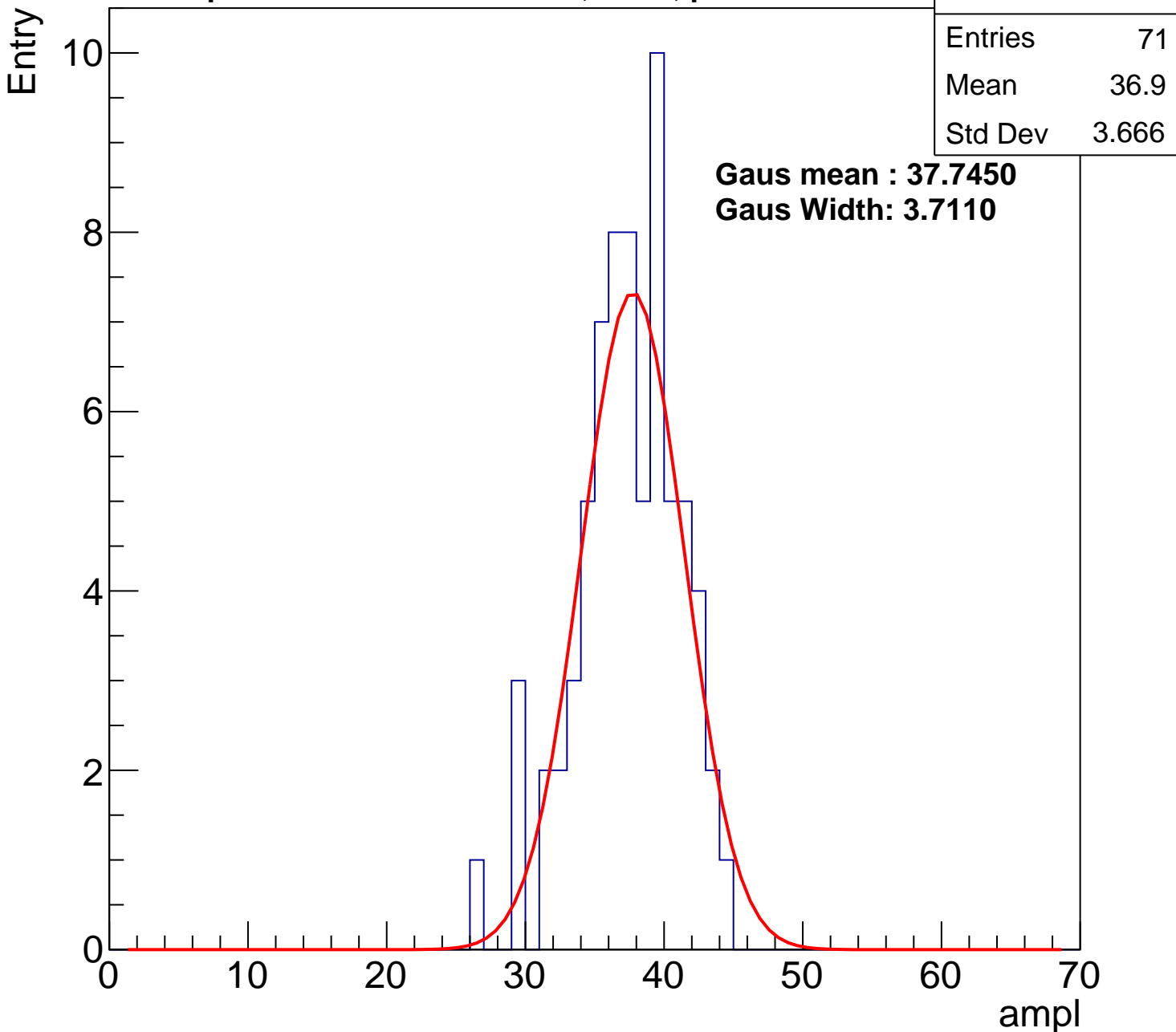
Gaus Width: 3.7110

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch118, adc2

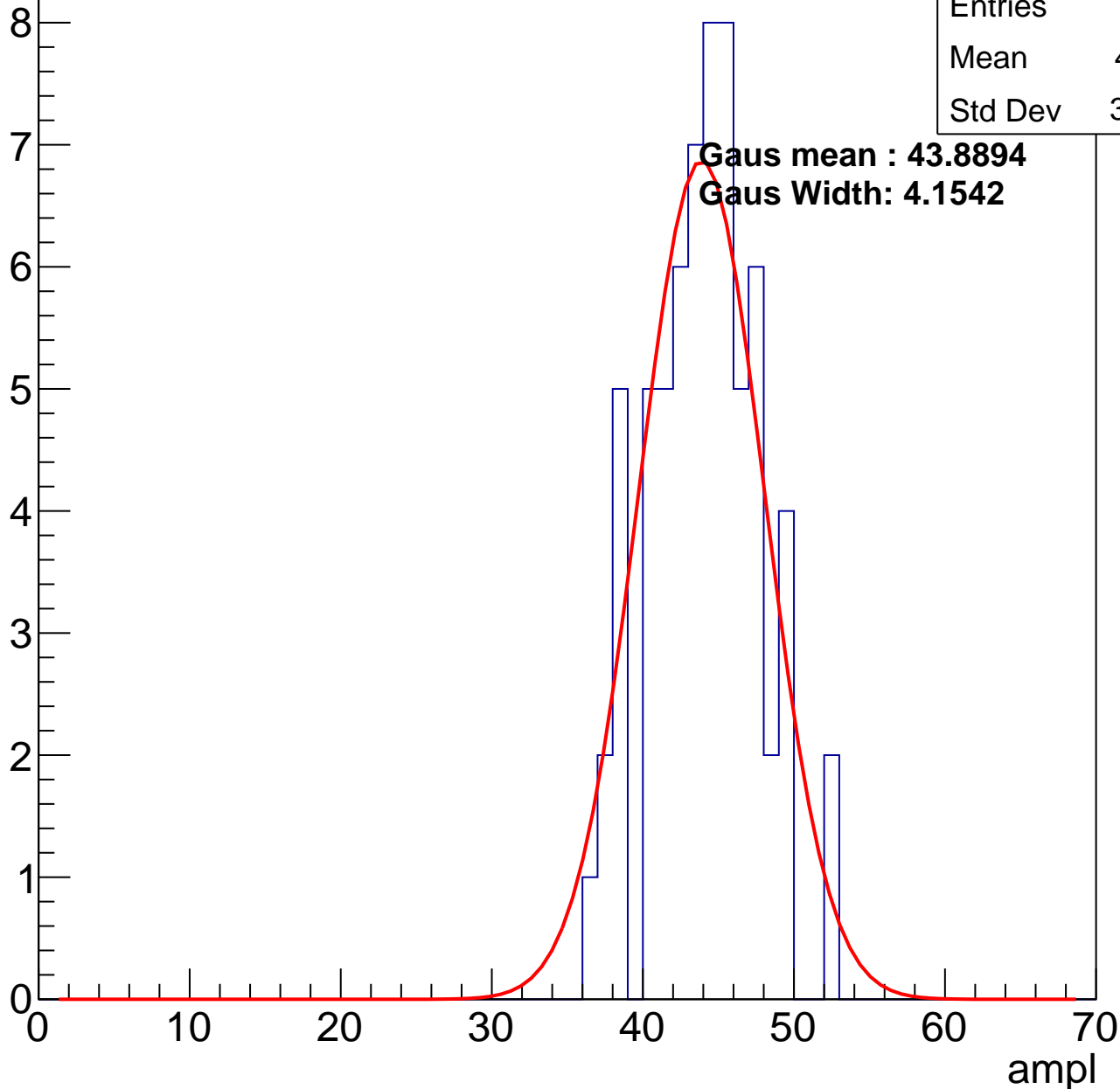
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.61
Std Dev	3.546

Gaus mean : 43.8894

Gaus Width: 4.1542

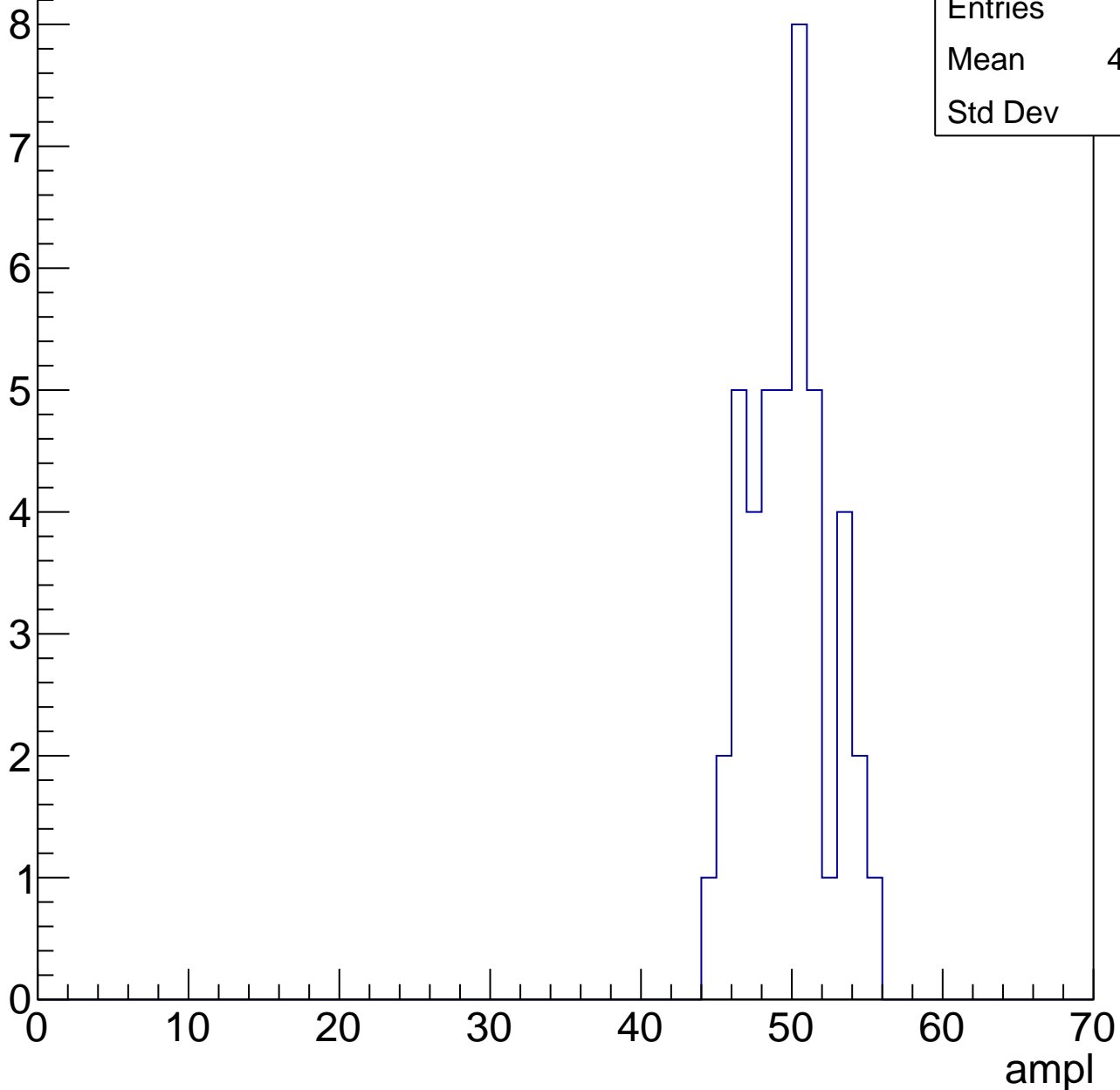


B1L100S, U19-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	49.28
Std Dev	2.67

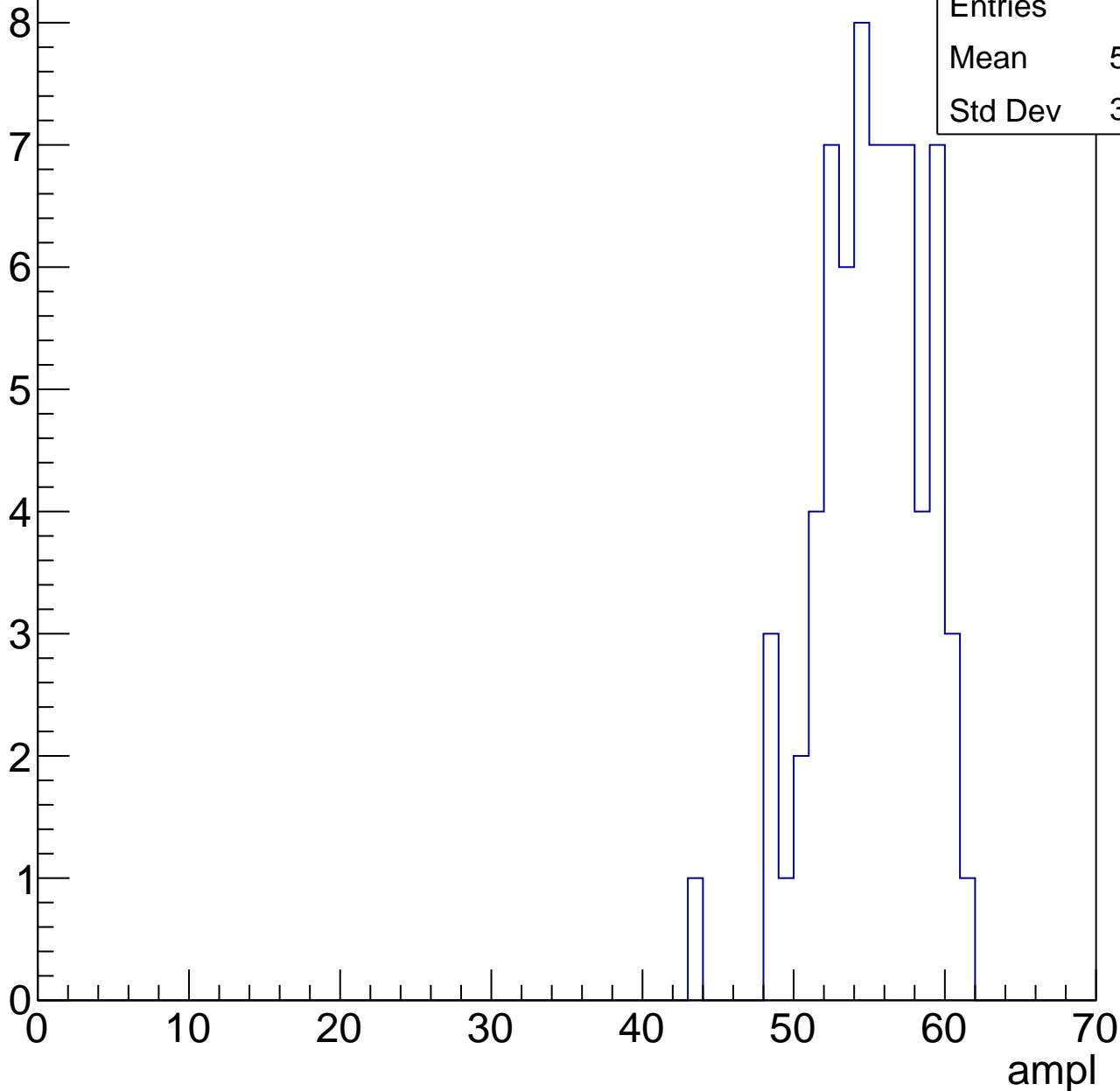


B1L100S, U19-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	54.65
Std Dev	3.476

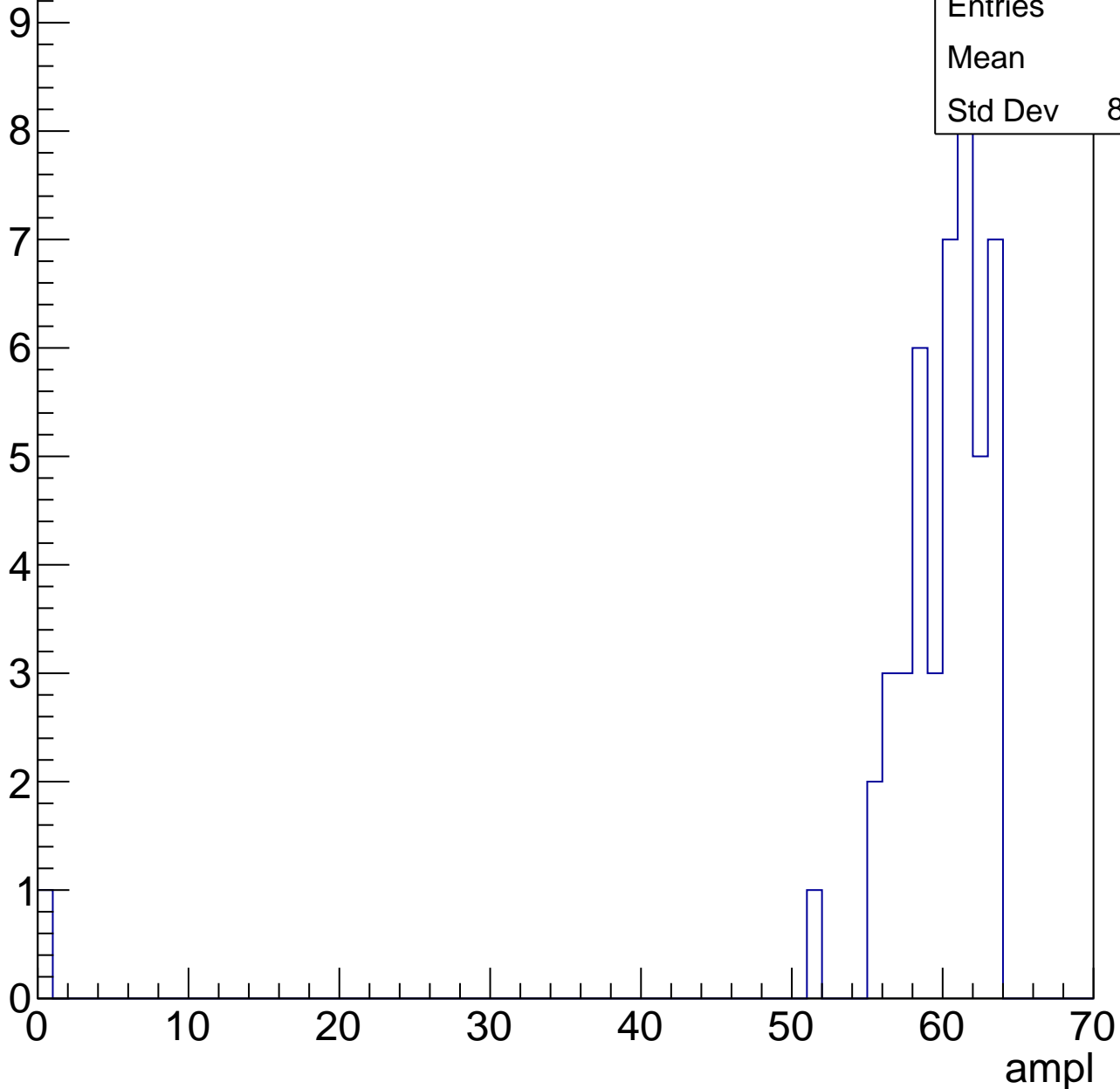


B1L100S, U19-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.4
Std Dev	8.998

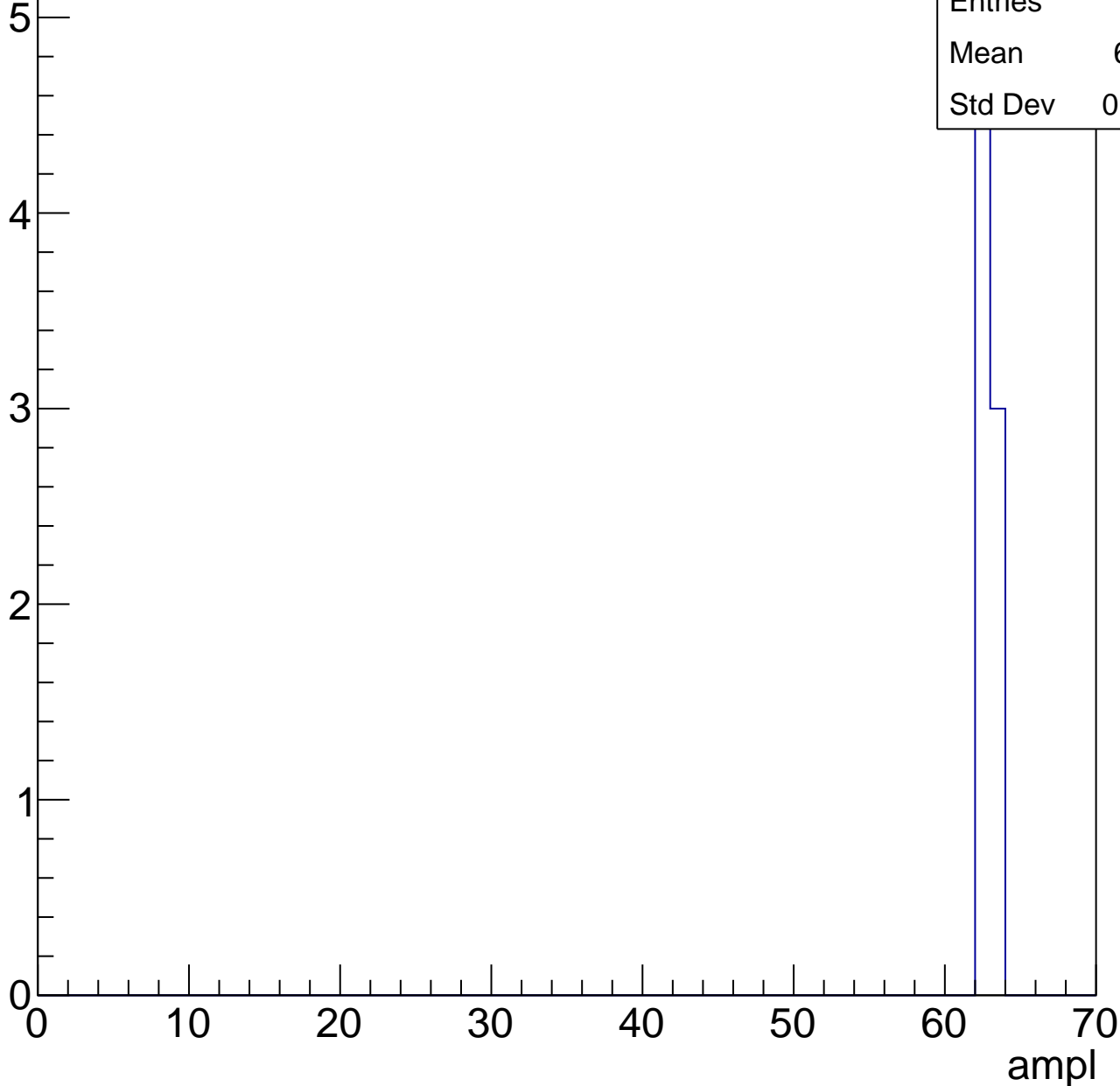


B1L100S, U19-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	8
Mean	62.38
Std Dev	0.4841



B1L100S, U19-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L100S, U19-ch119, adc0

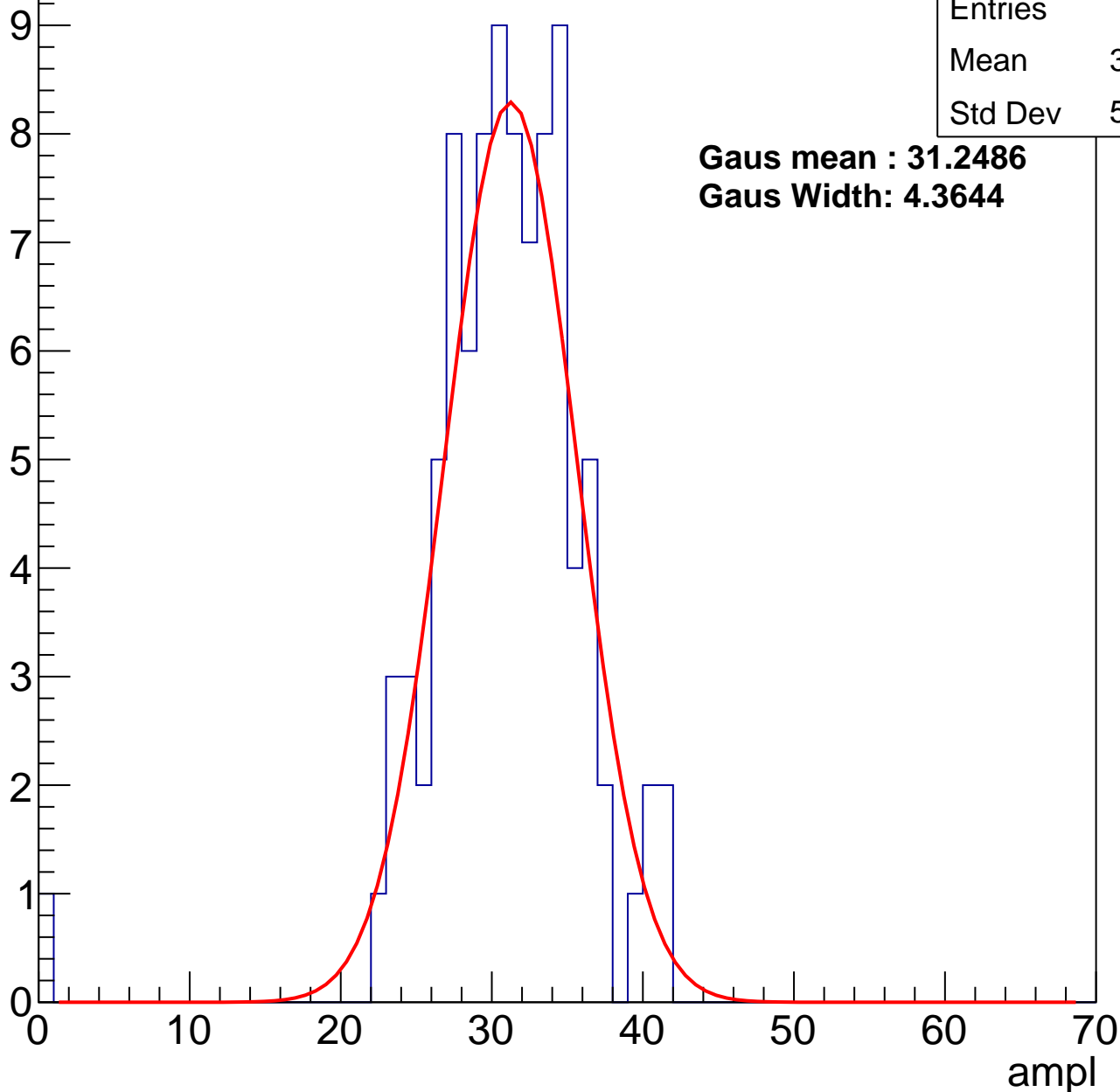
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	94
Mean	30.49
Std Dev	5.243

Gaus mean : 31.2486

Gaus Width: 4.3644



B1L100S, U19-ch119, adc1

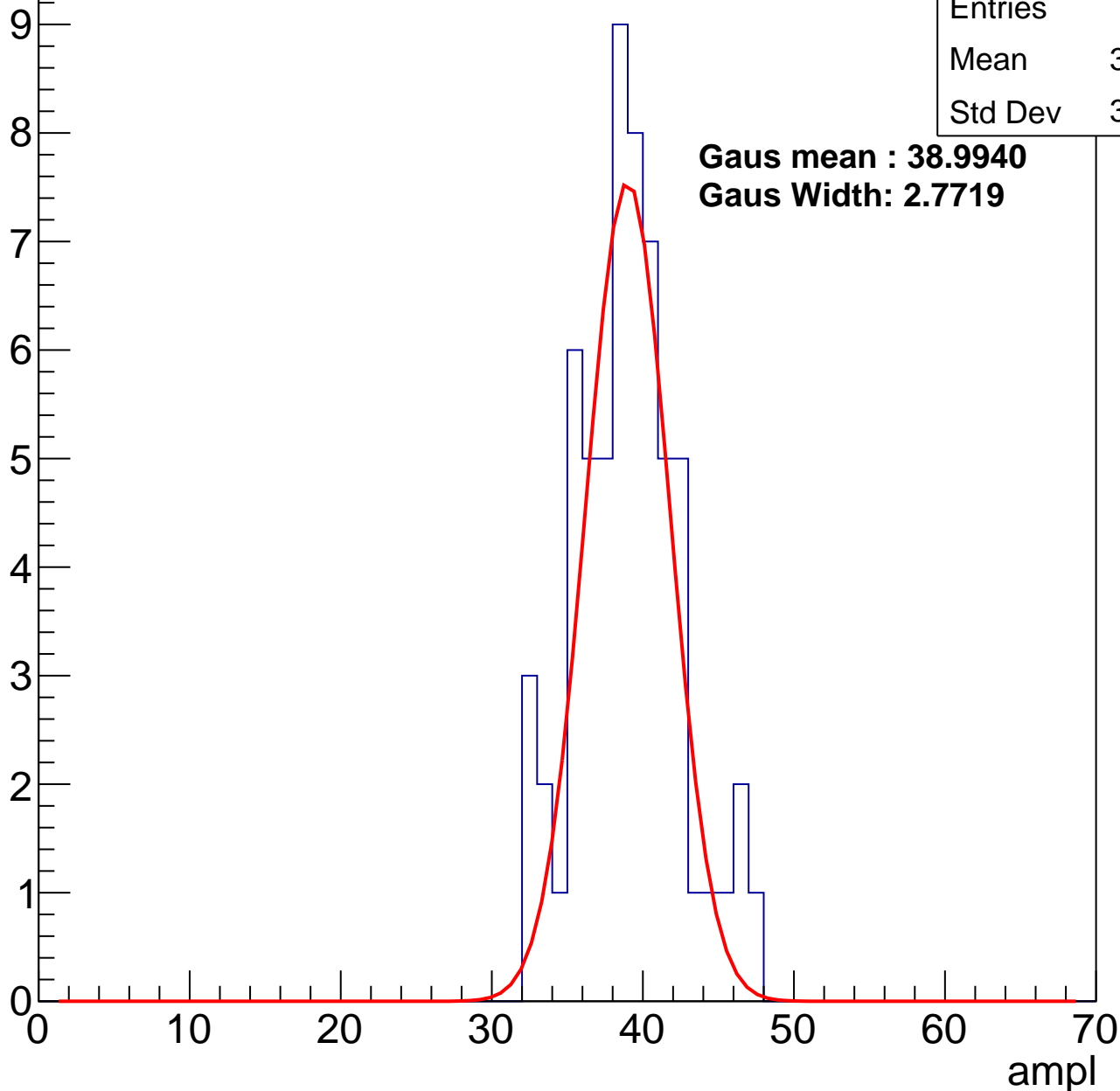
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	38.56
Std Dev	3.392

Gaus mean : 38.9940

Gaus Width: 2.7719



B1L100S, U19-ch119, adc2

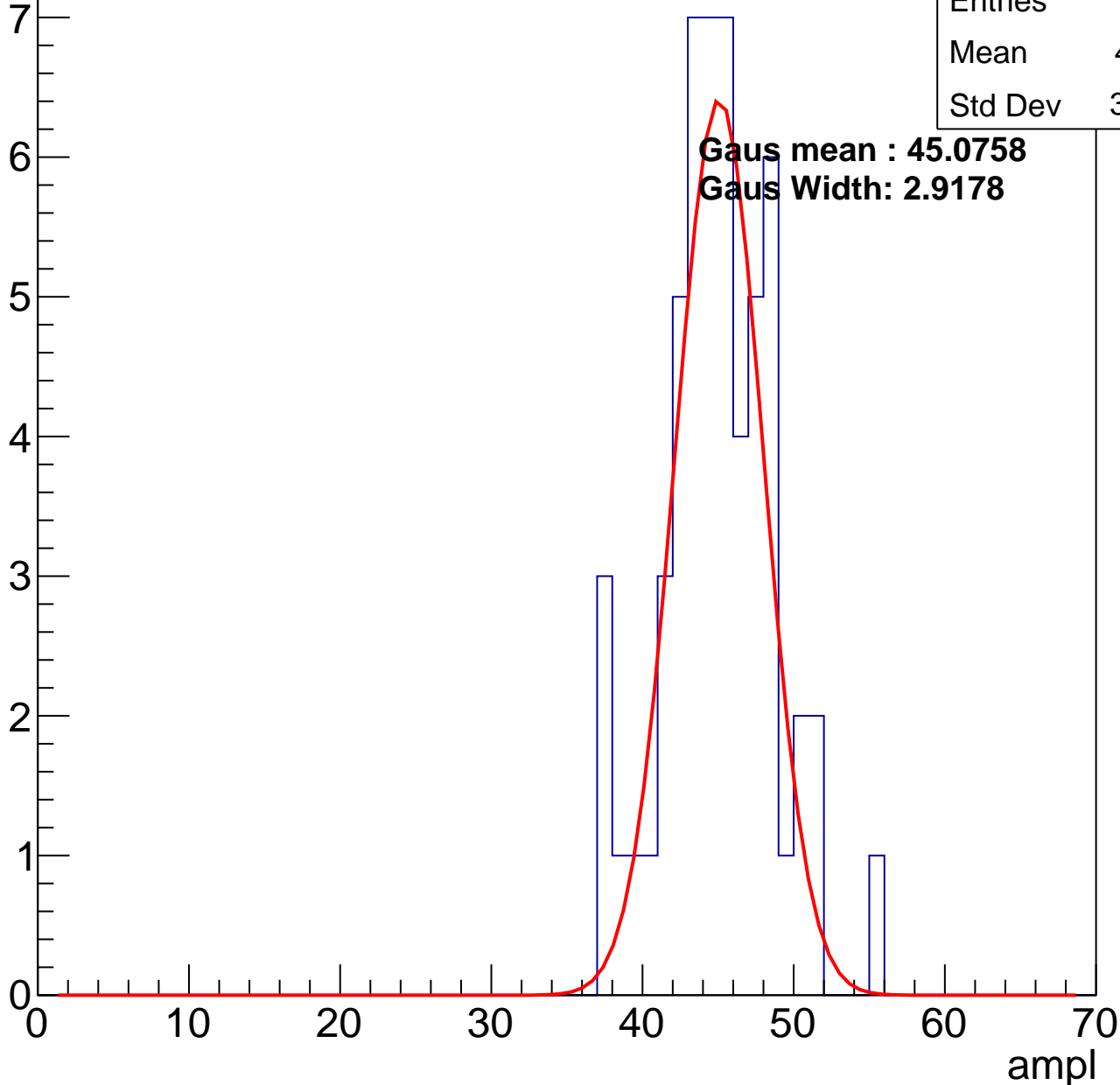
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.61
Std Dev	3.629

Gaus mean : 45.0758

Gaus Width: 2.9178

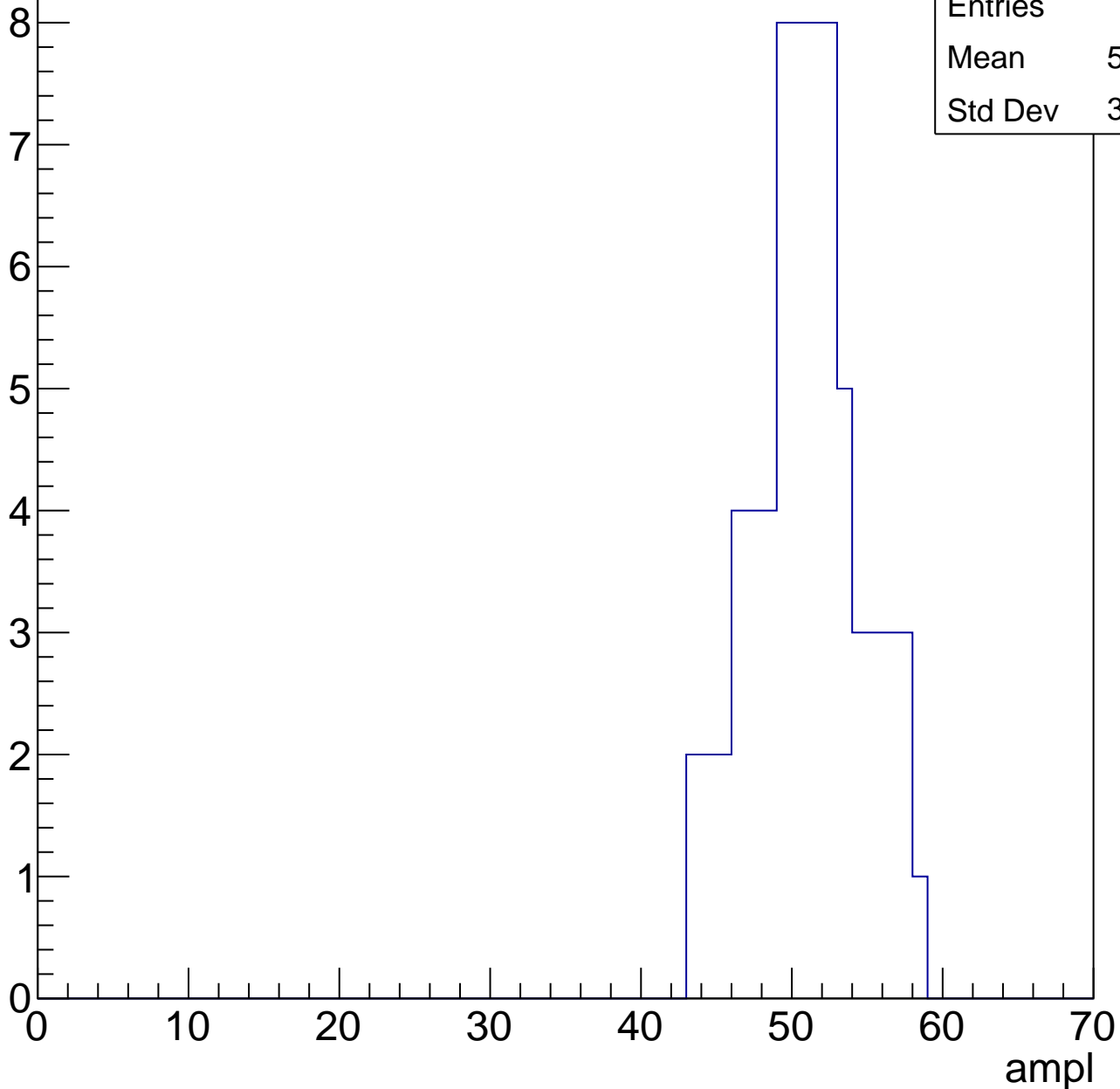


B1L100S, U19-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.49
Std Dev	3.546

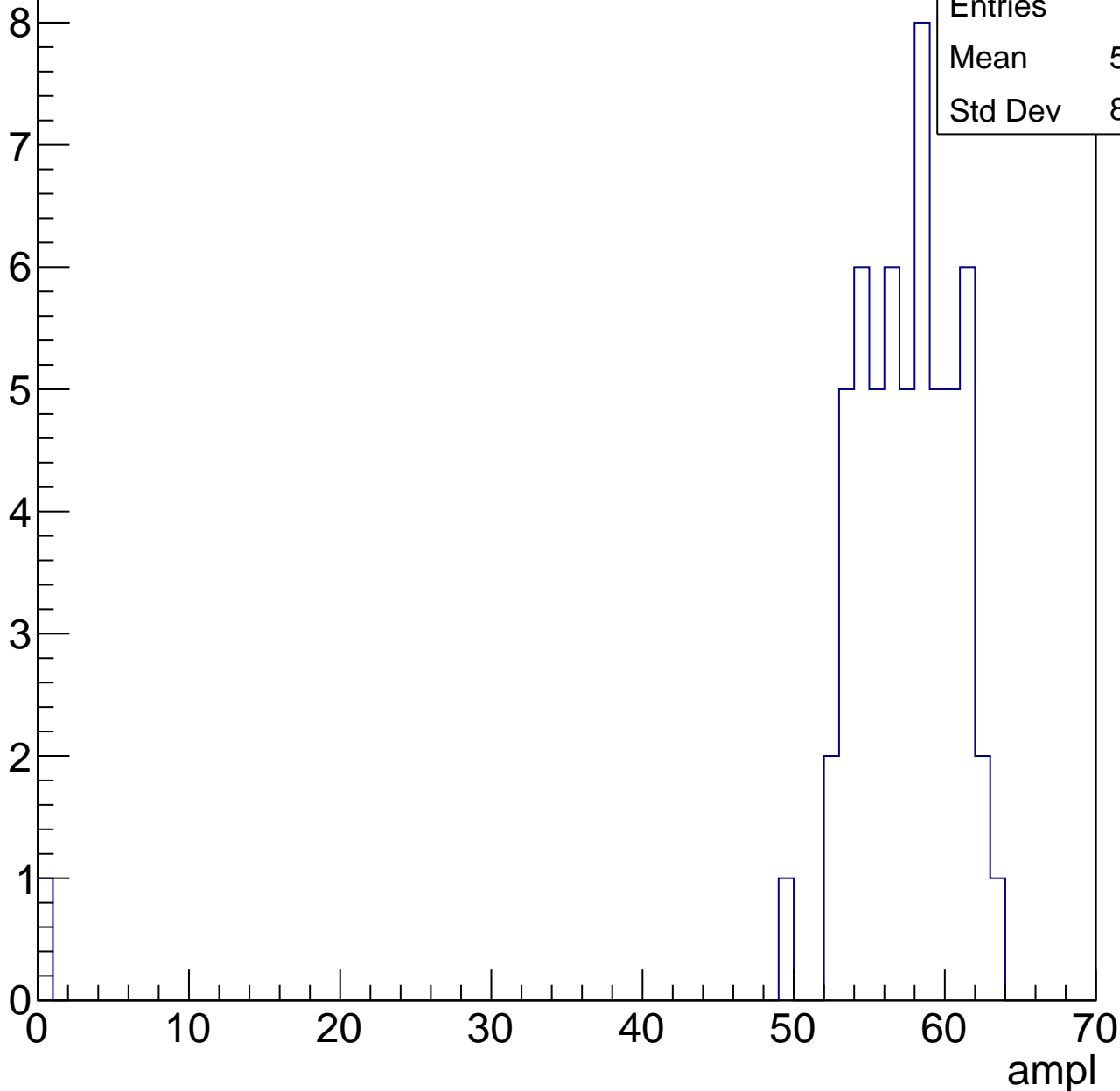


B1L100S, U19-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.03
Std Dev	8.013

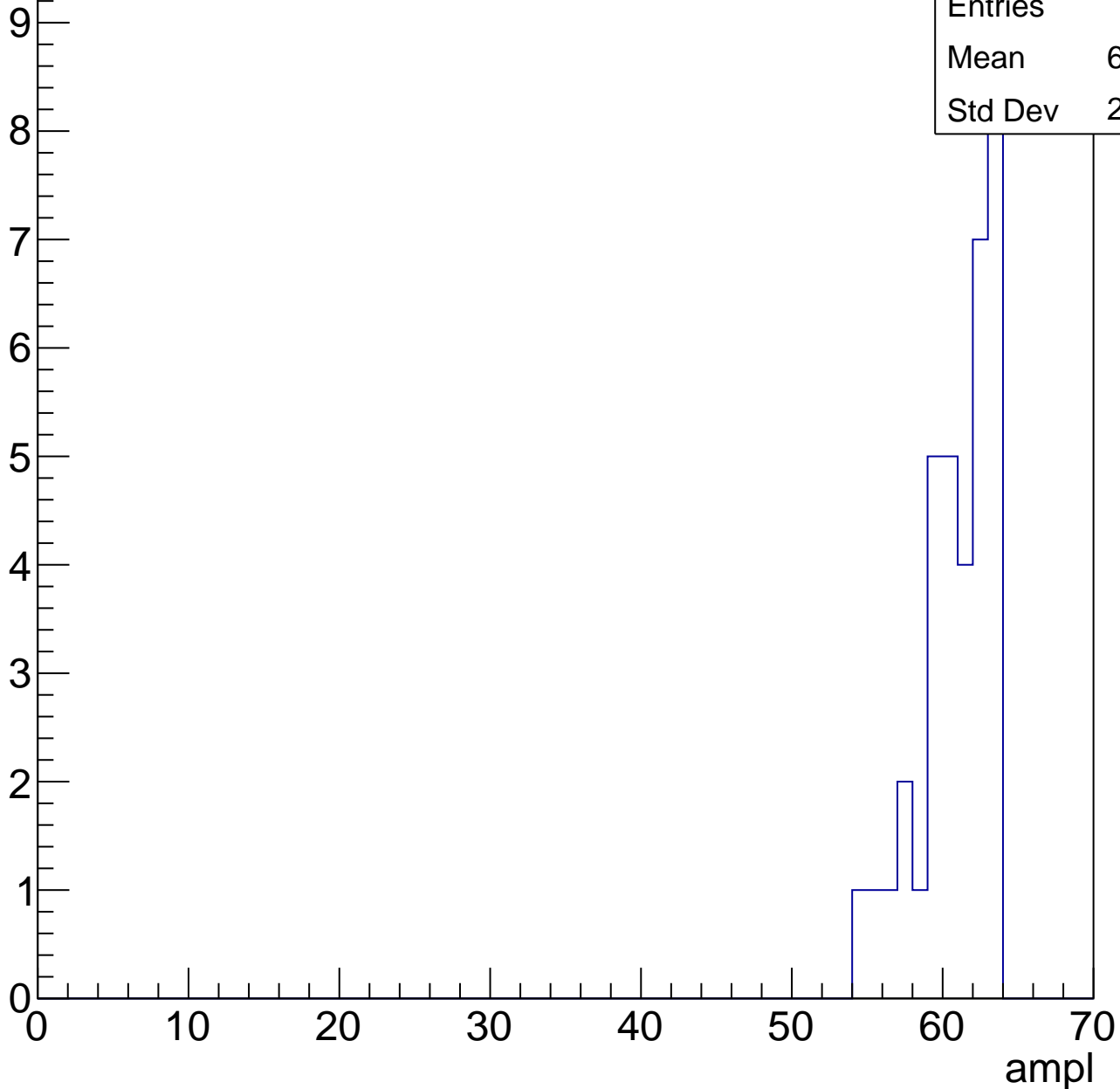


B1L100S, U19-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	60.47
Std Dev	2.409



B1L100S, U19-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U19-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch120, adc0

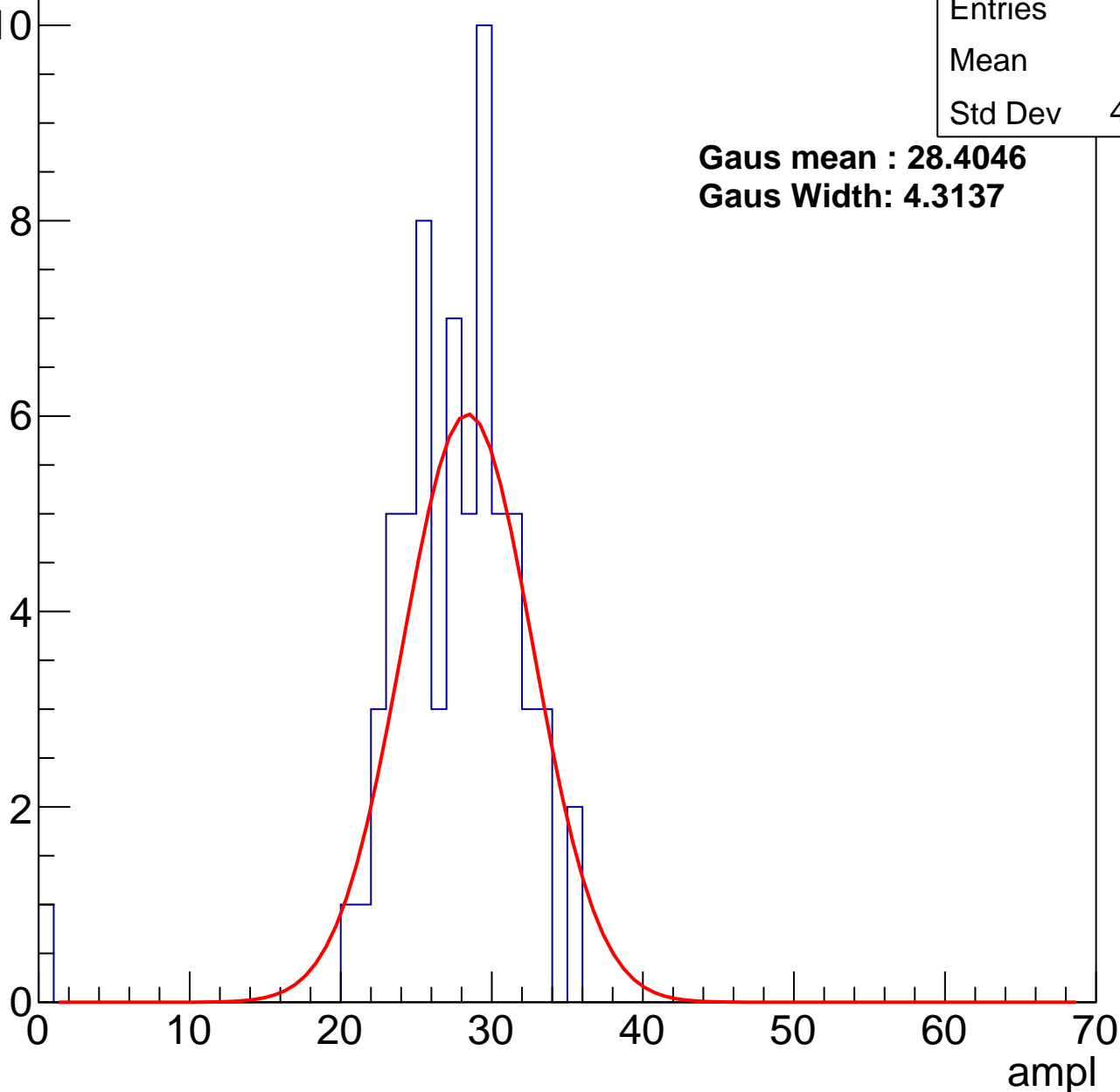
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	27
Std Dev	4.779

Gaus mean : 28.4046

Gaus Width: 4.3137



B1L100S, U19-ch120, adc1

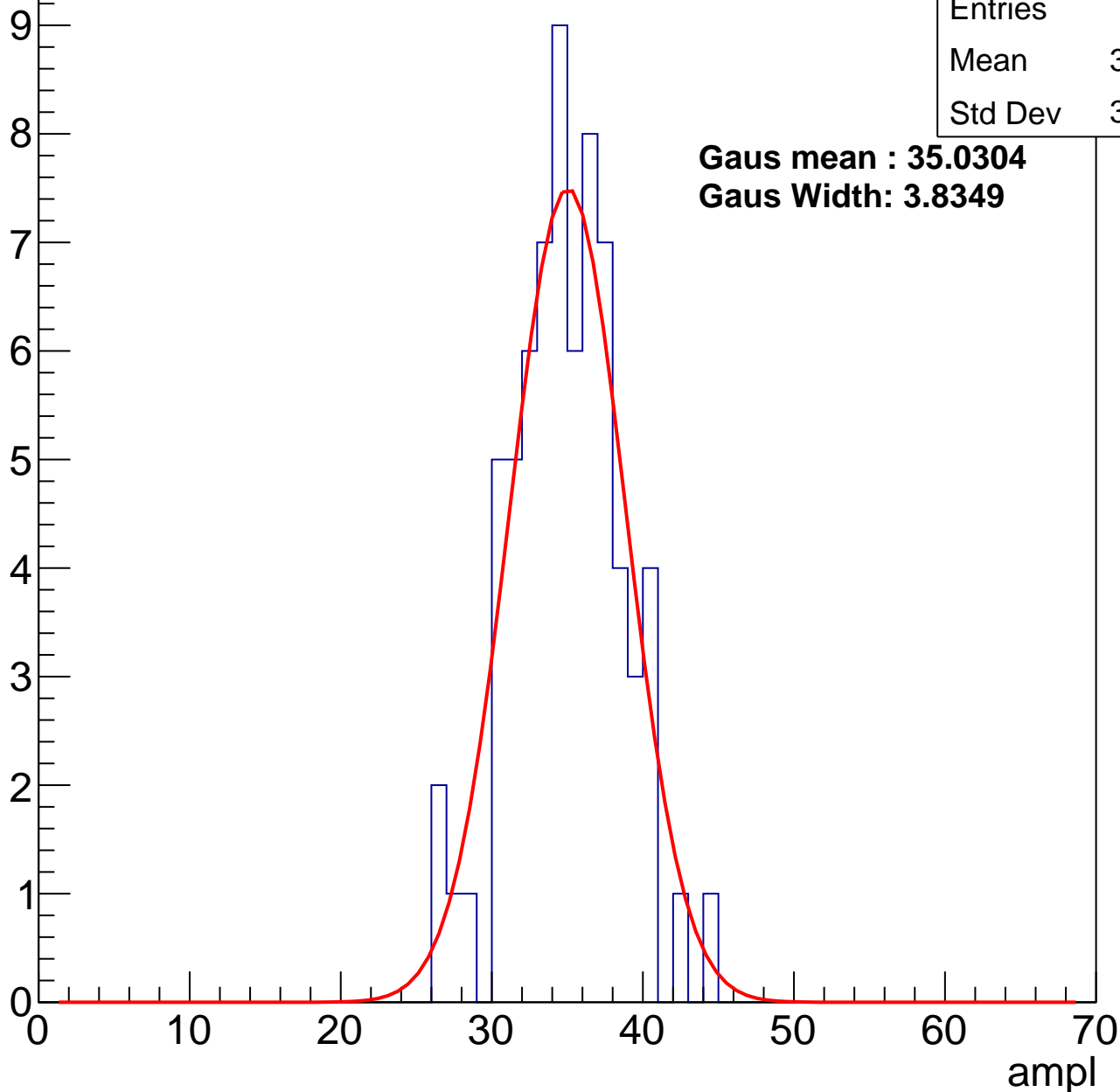
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.47
Std Dev	3.589

Gaus mean : 35.0304

Gaus Width: 3.8349



B1L100S, U19-ch120, adc2

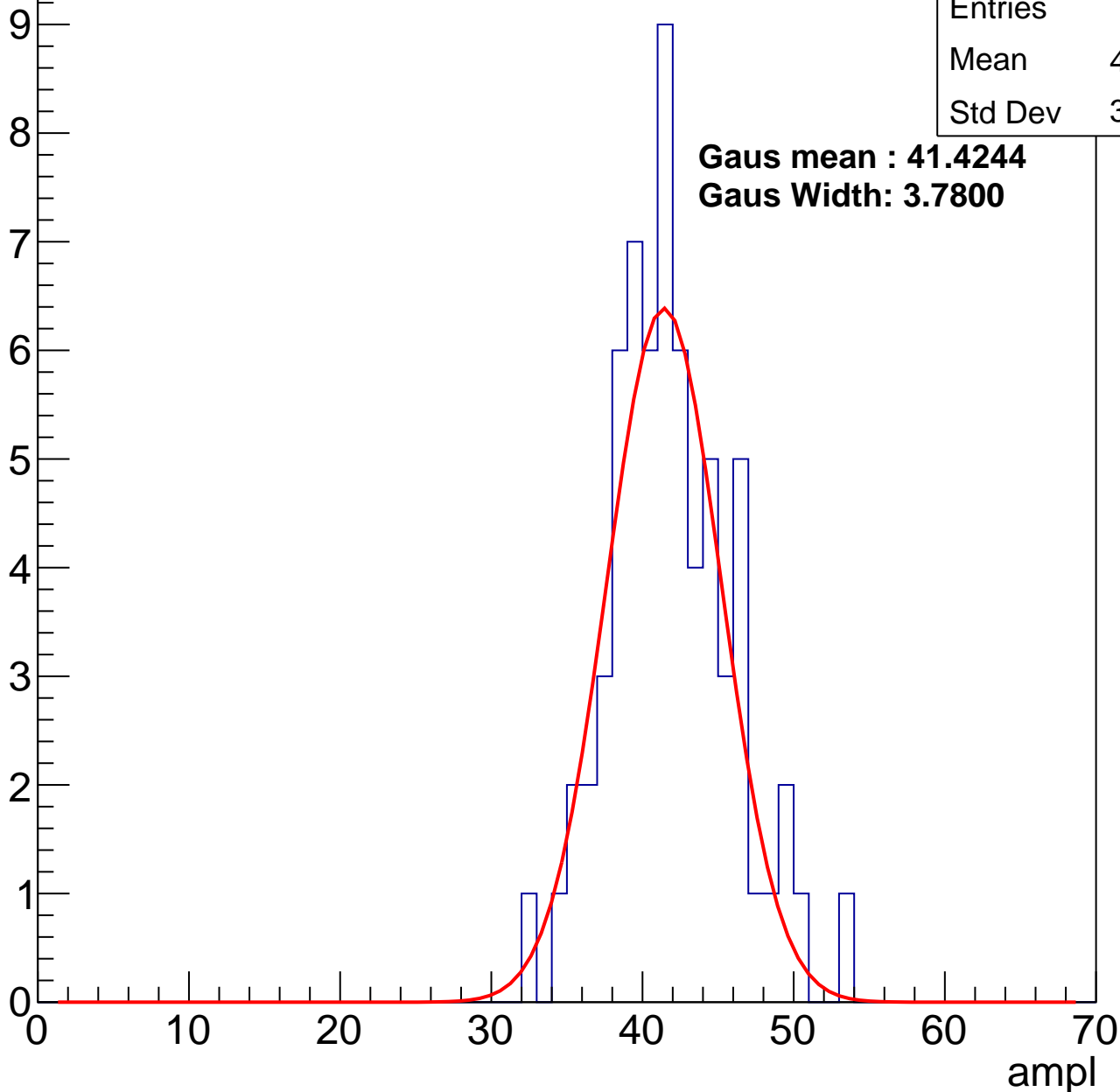
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.42
Std Dev	3.996

Gaus mean : 41.4244

Gaus Width: 3.7800

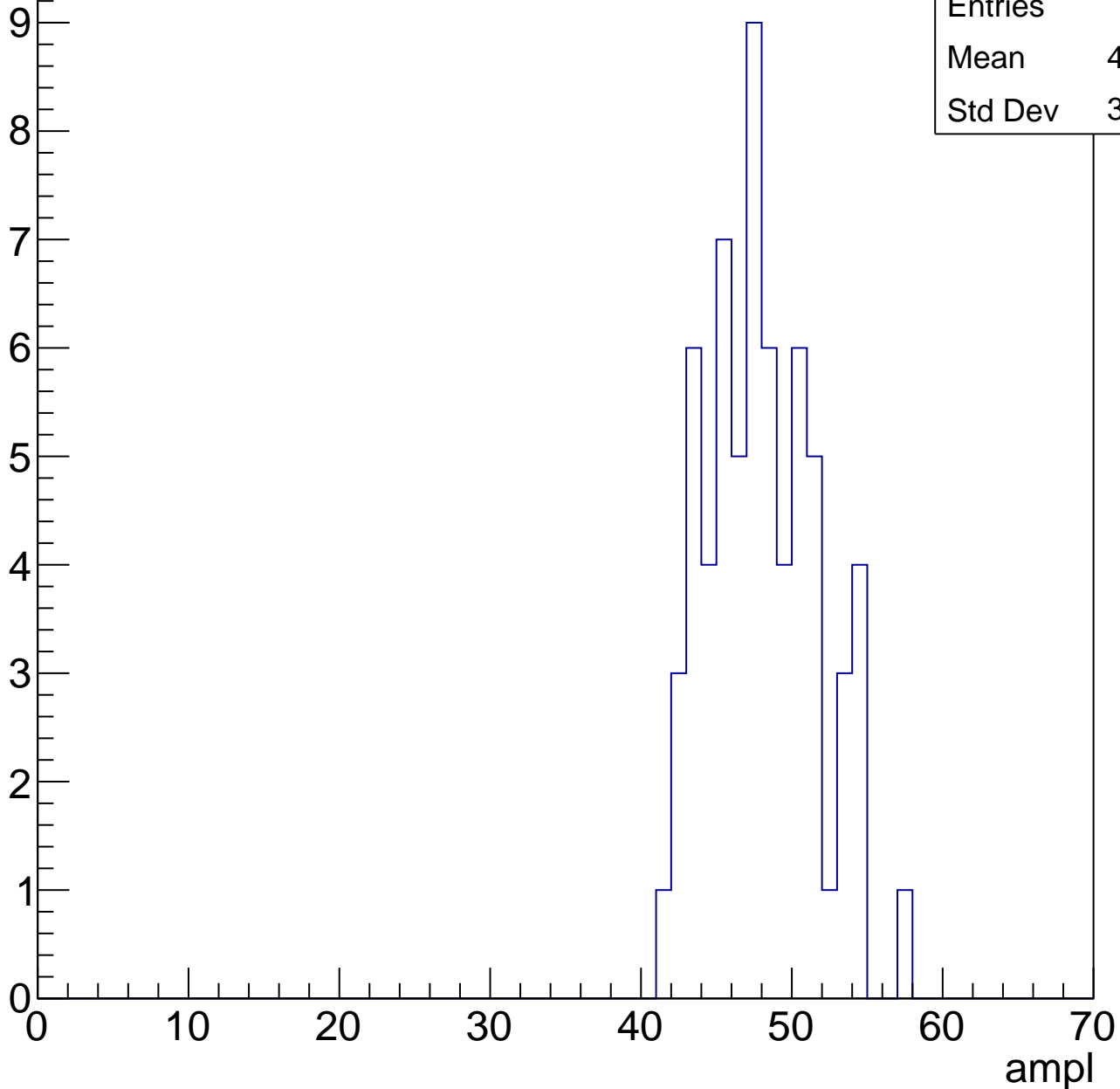


B1L100S, U19-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

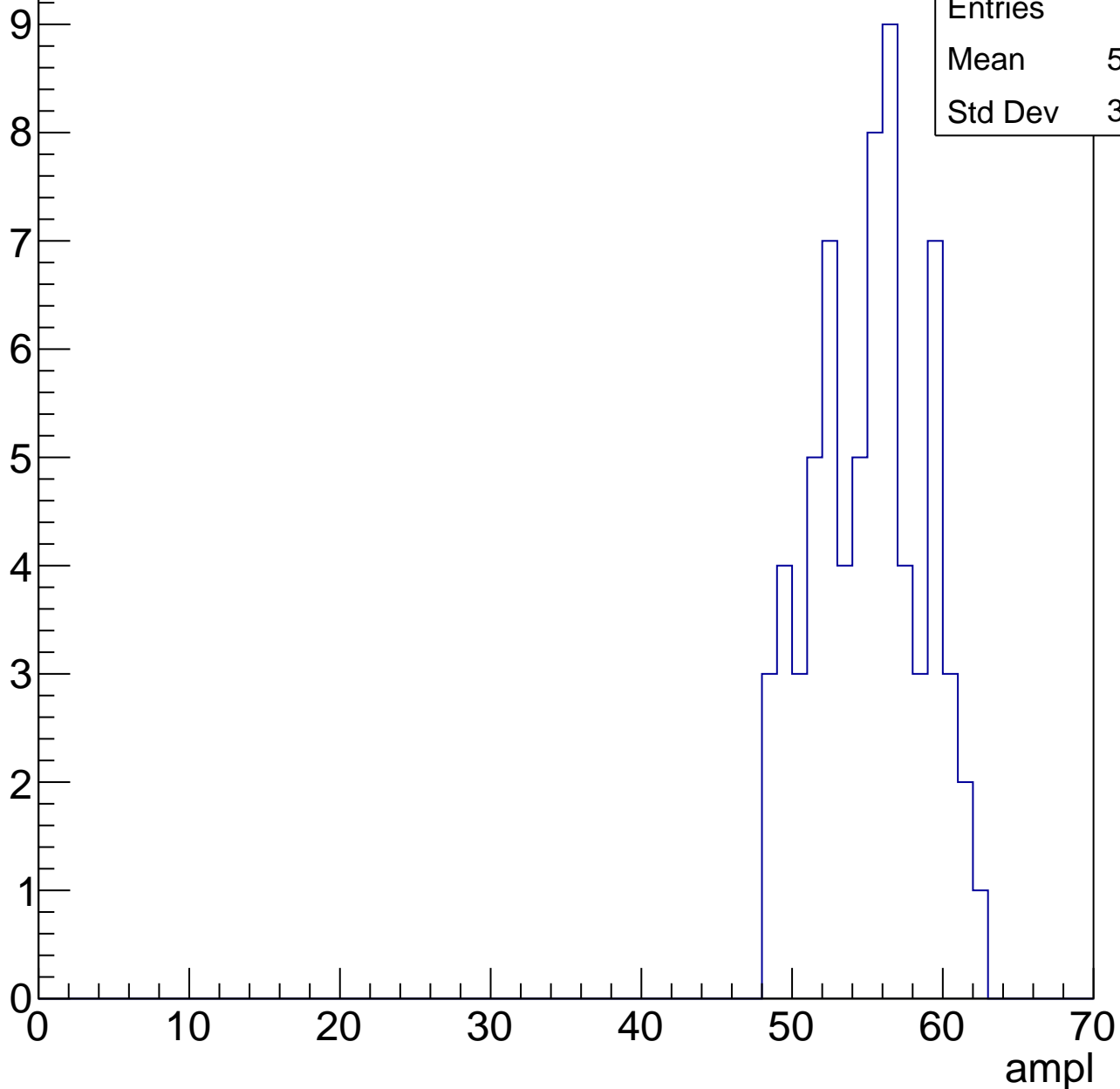
Entries	65
Mean	47.57
Std Dev	3.595



B1L100S, U19-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

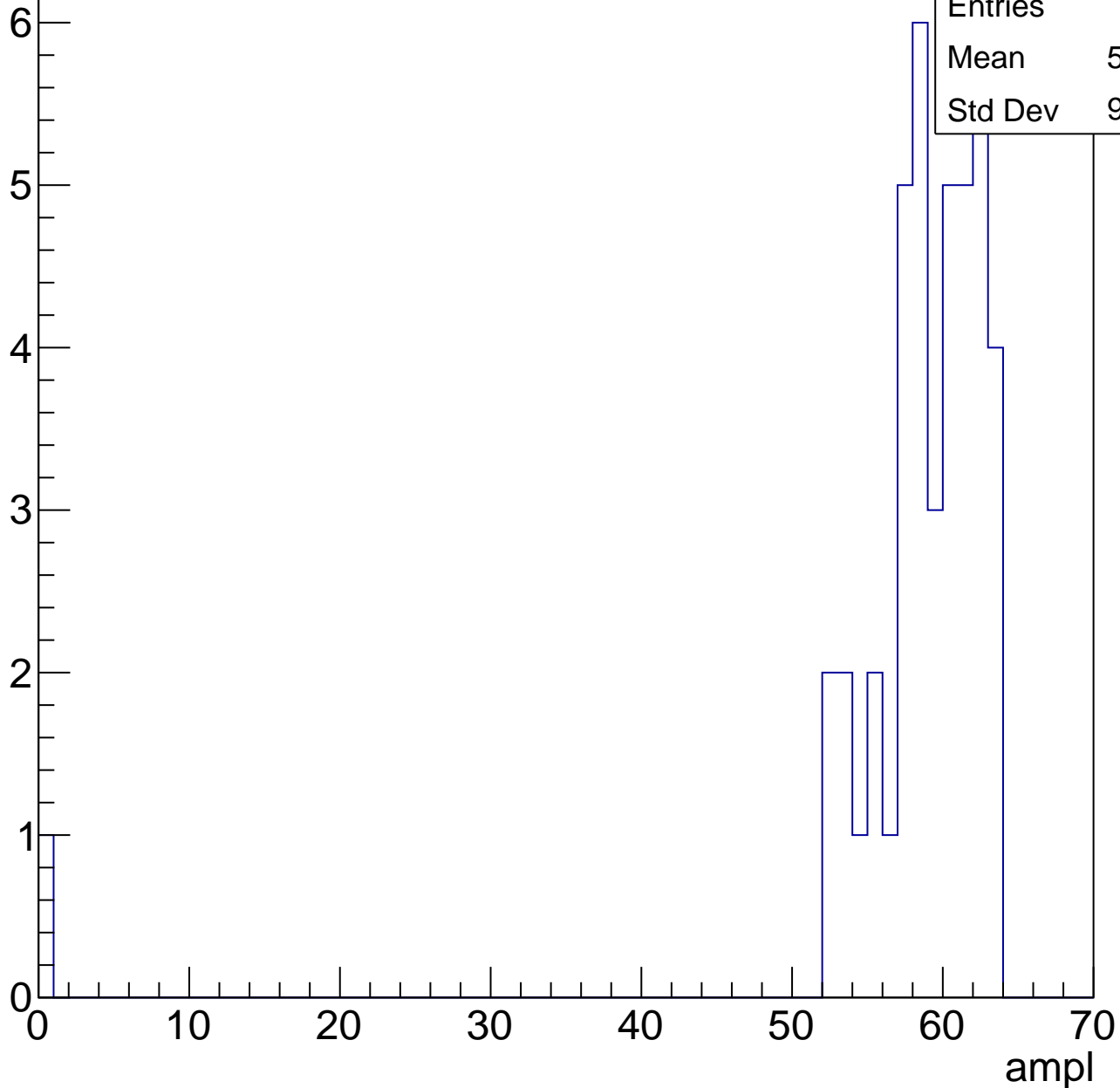
Entry



B1L100S, U19-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

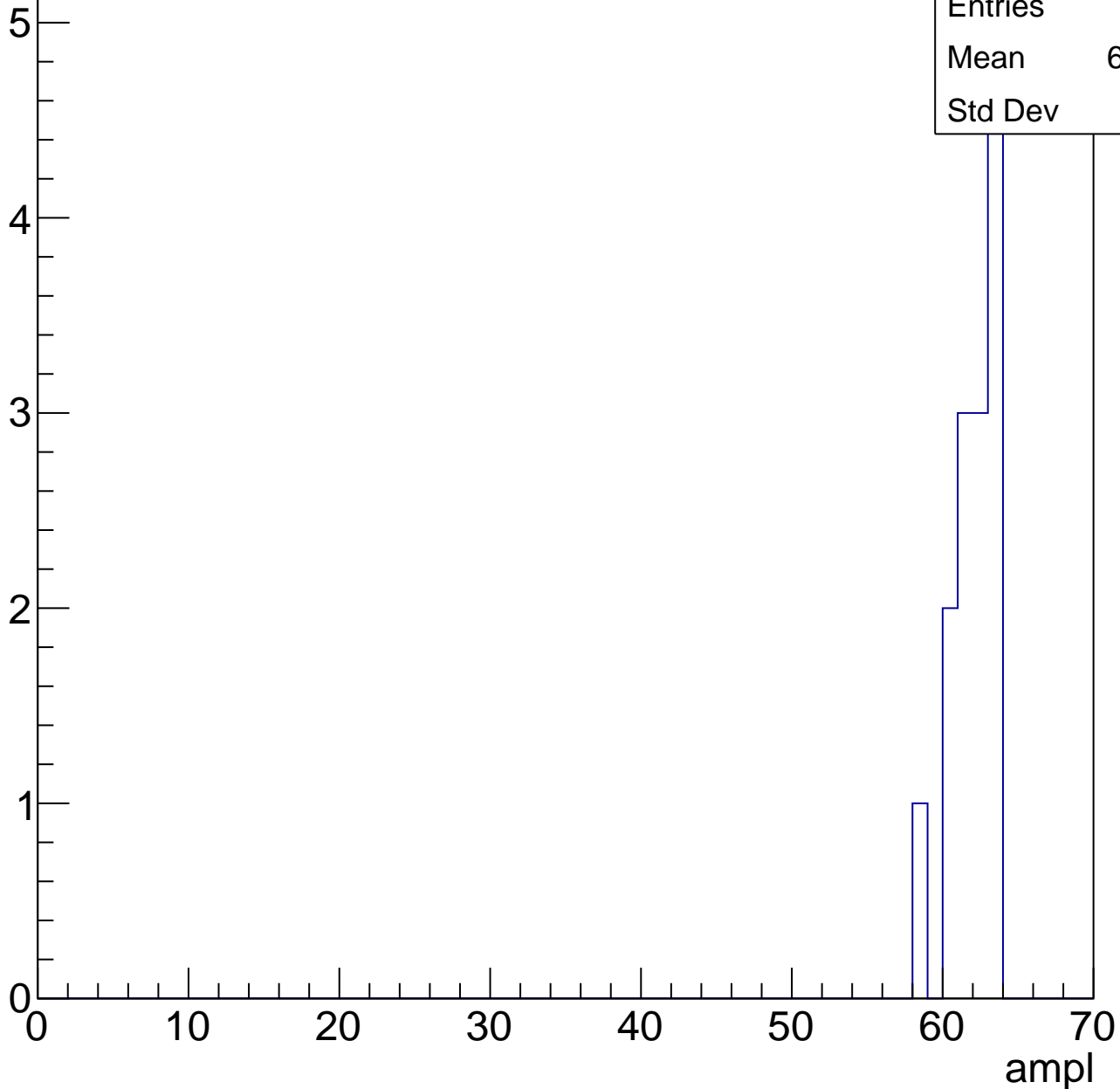


B1L100S, U19-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	61.57
Std Dev	1.45



B1L100S, U19-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch121, adc0

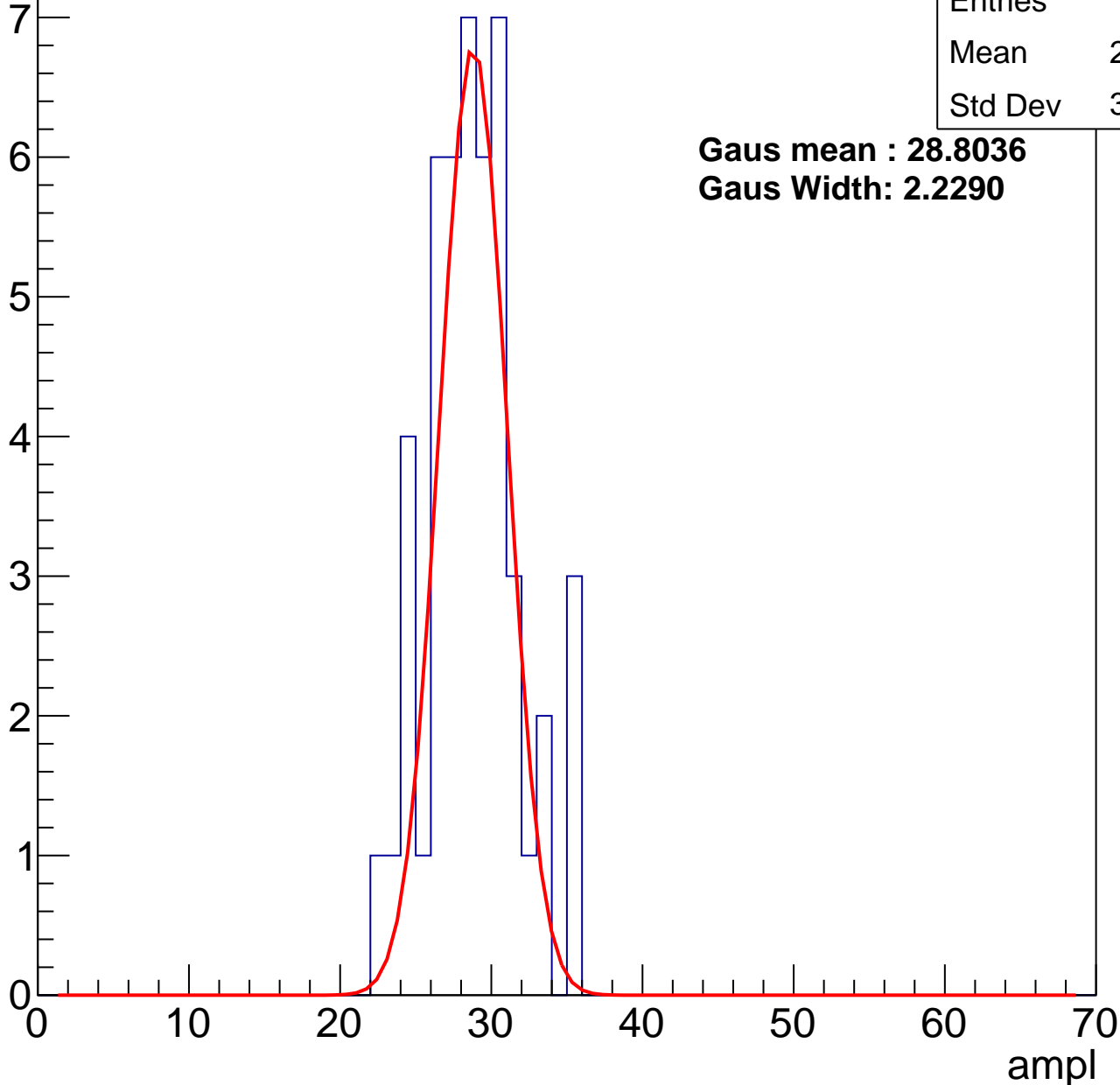
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	28.33
Std Dev	3.002

Gaus mean : 28.8036

Gaus Width: 2.2290



B1L100S, U19-ch121, adc1

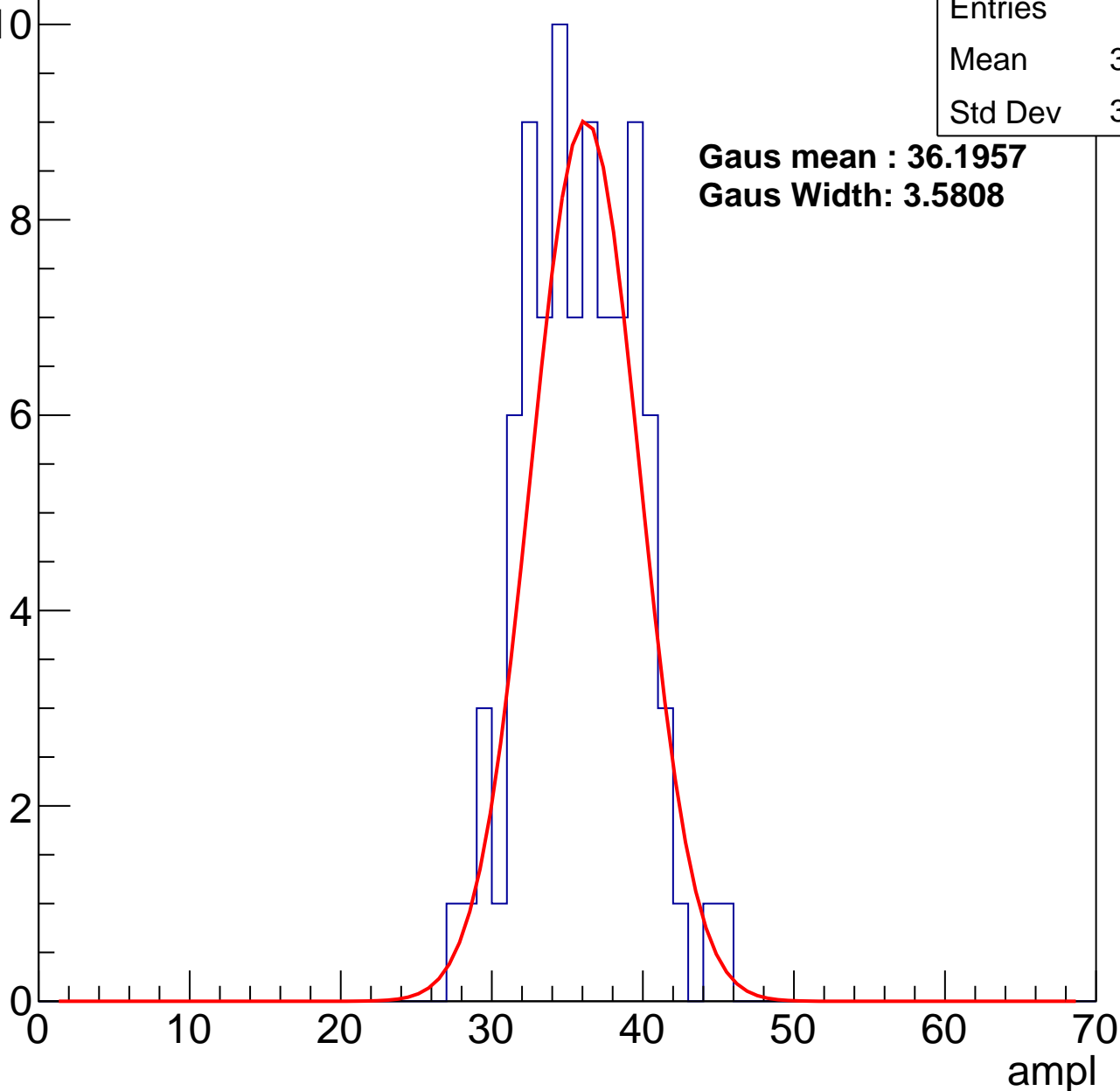
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	35.46
Std Dev	3.632

Gaus mean : 36.1957

Gaus Width: 3.5808



B1L100S, U19-ch121, adc2

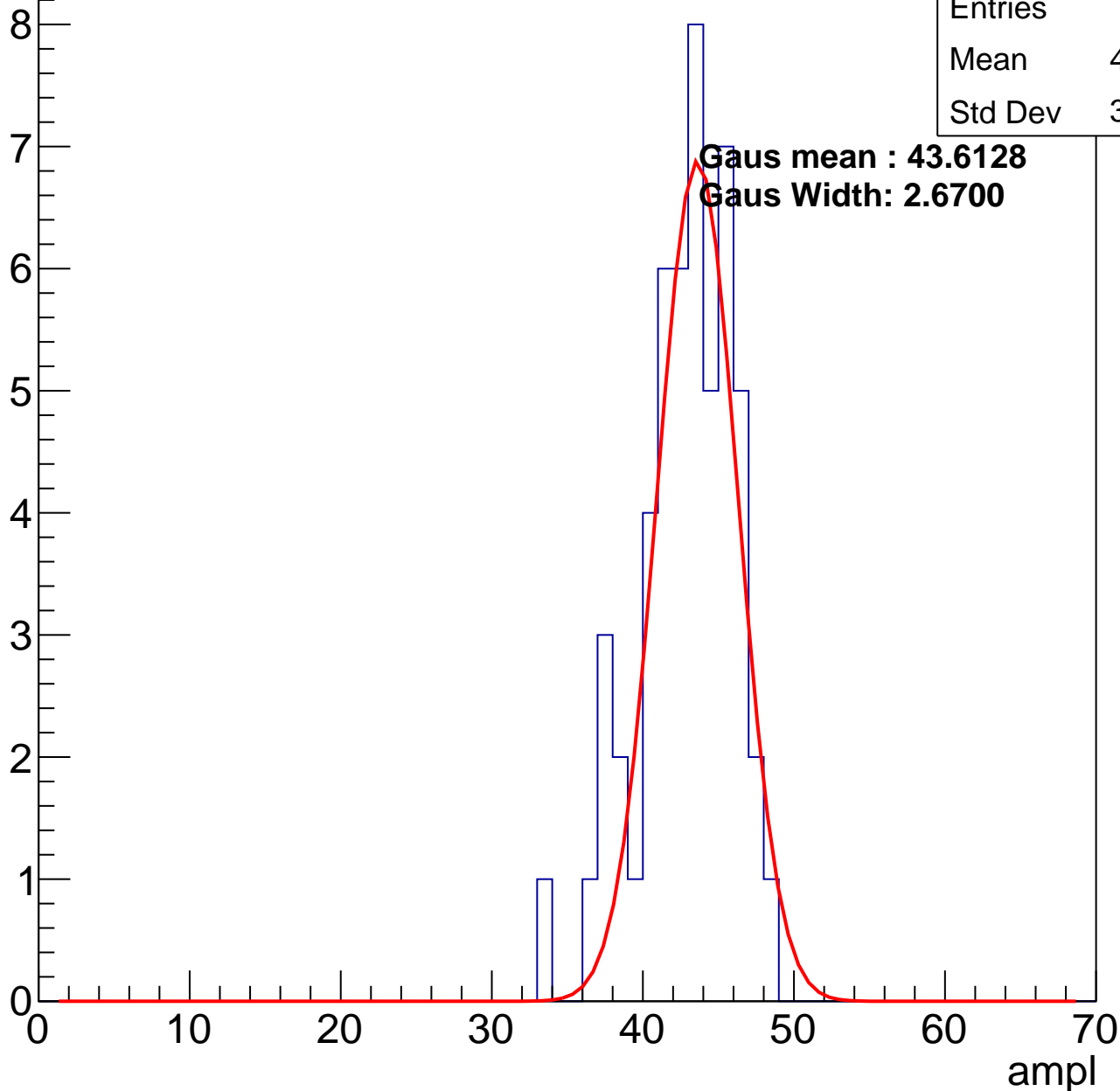
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	42.38
Std Dev	3.114

Gaus mean : 43.6128

Gaus Width: 2.6700

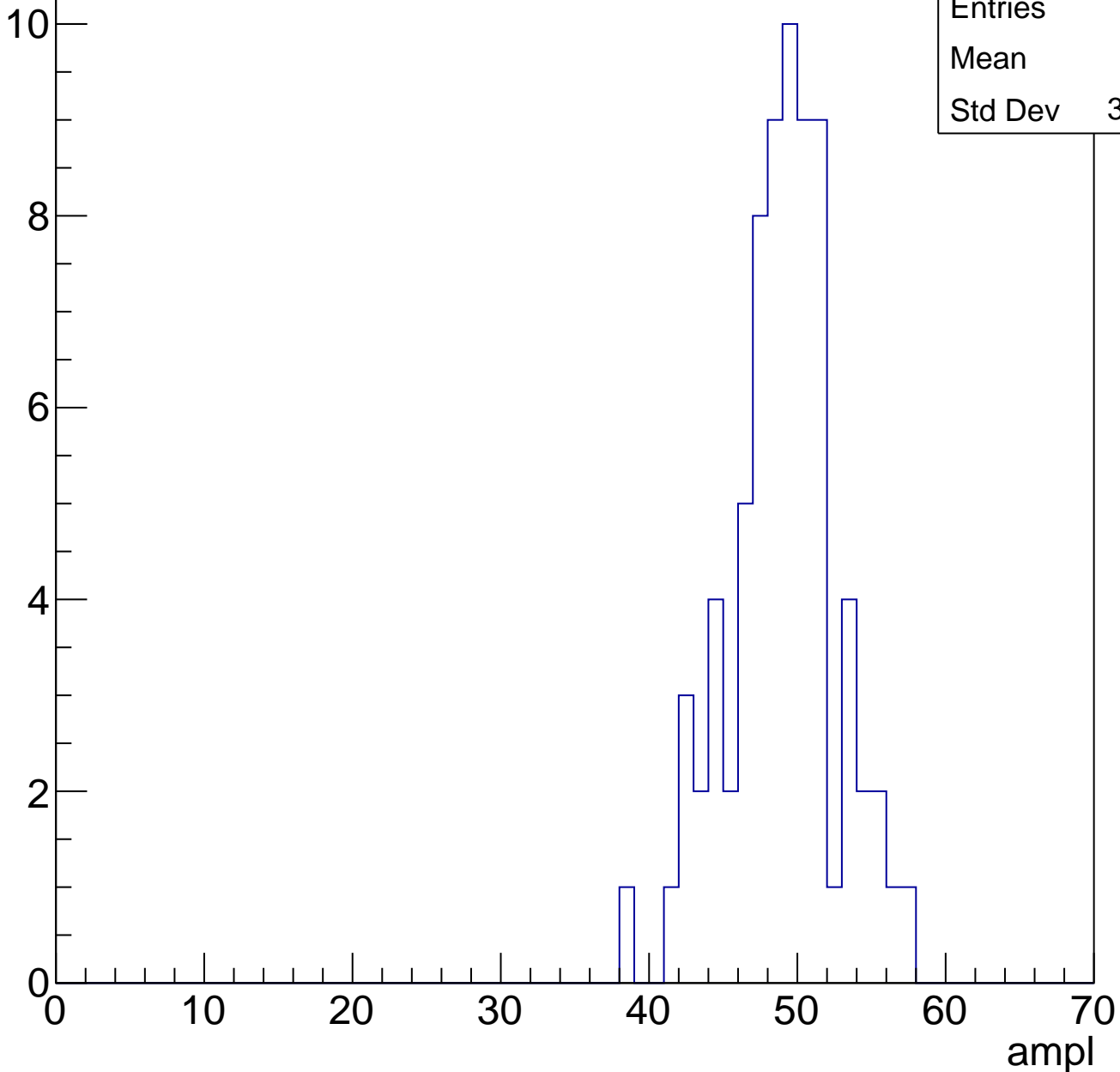


B1L100S, U19-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	48.5
Std Dev	3.595

Entry

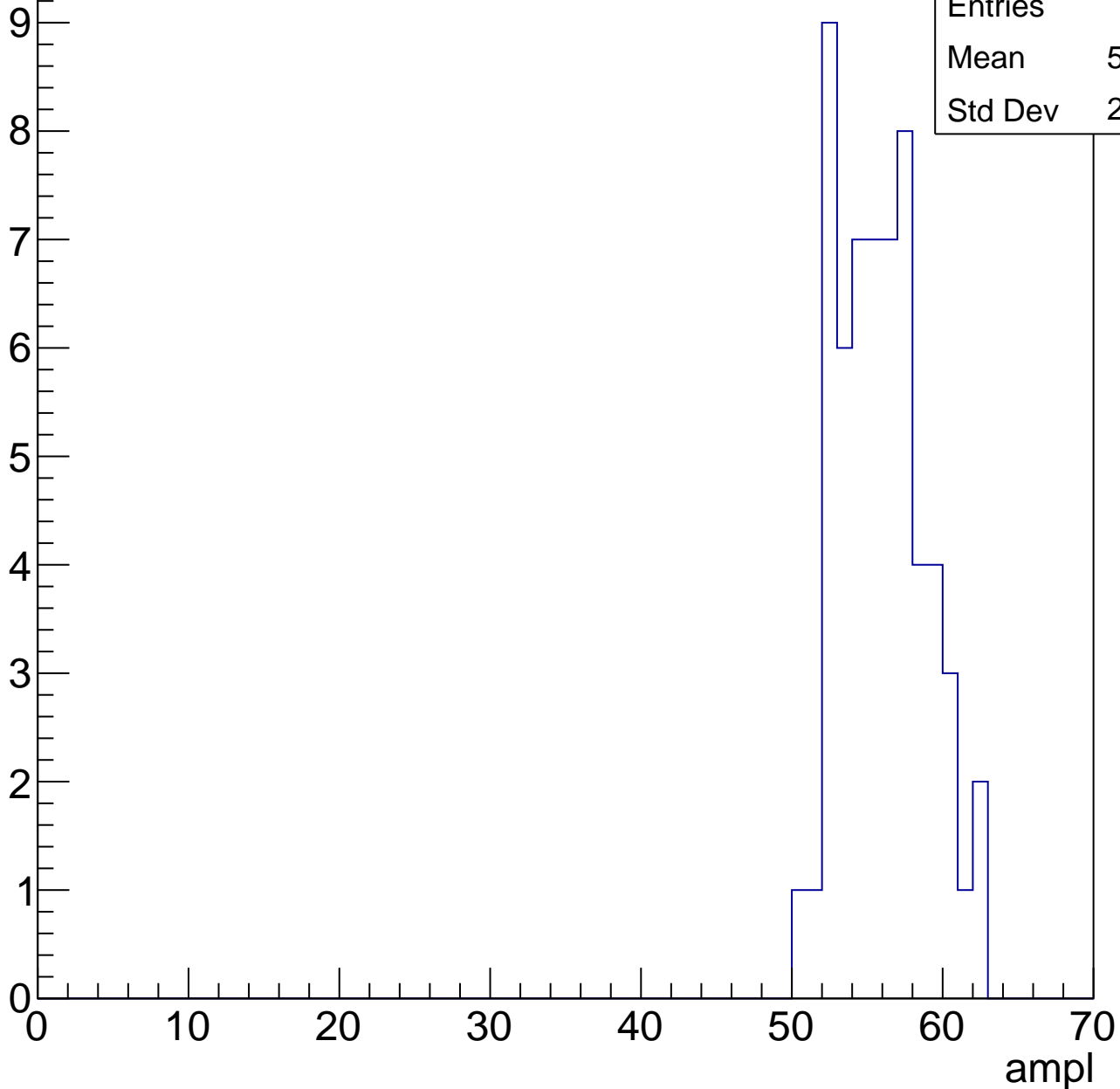


B1L100S, U19-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	55.52
Std Dev	2.843

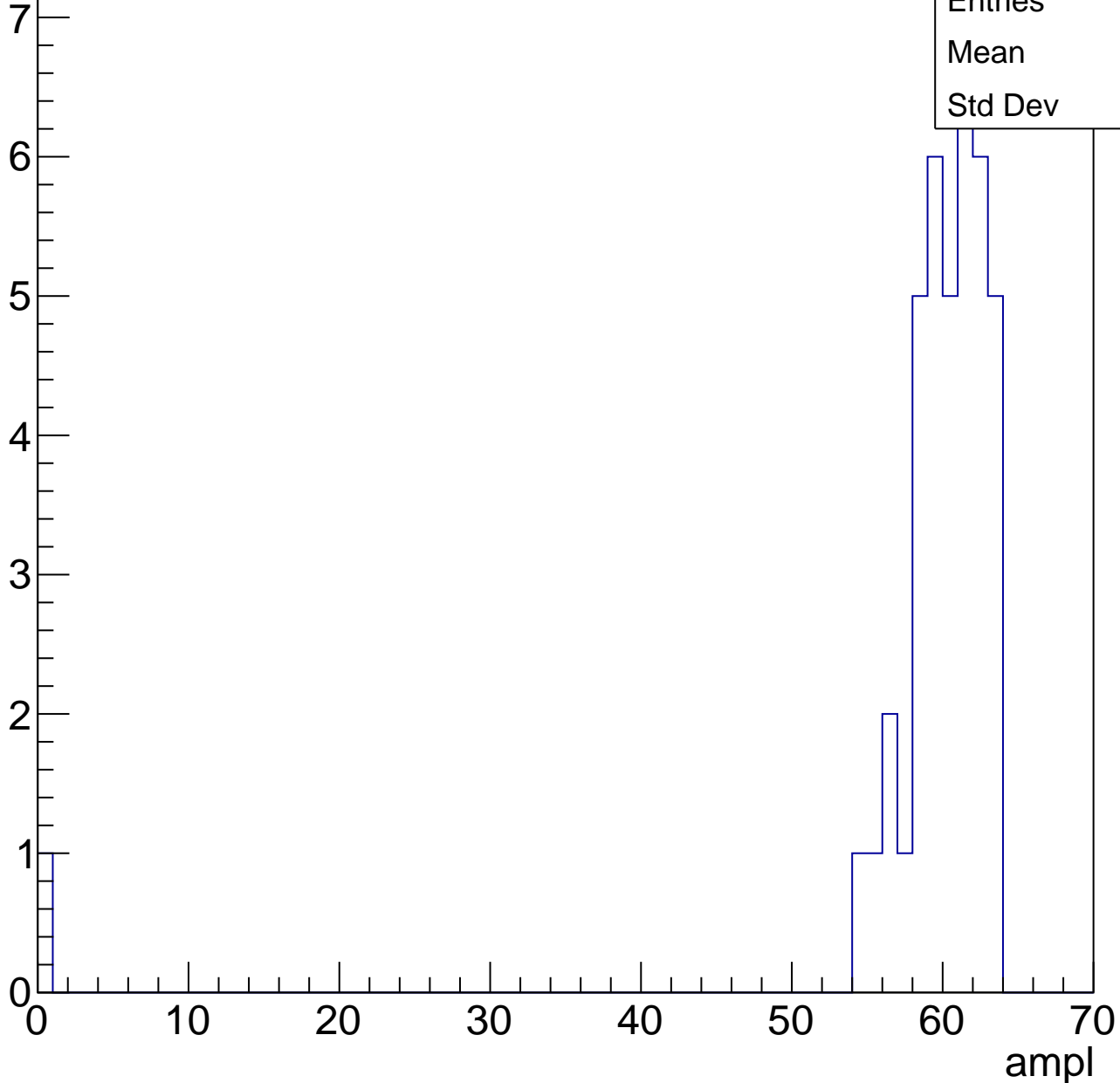


B1L100S, U19-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.4
Std Dev	9.62

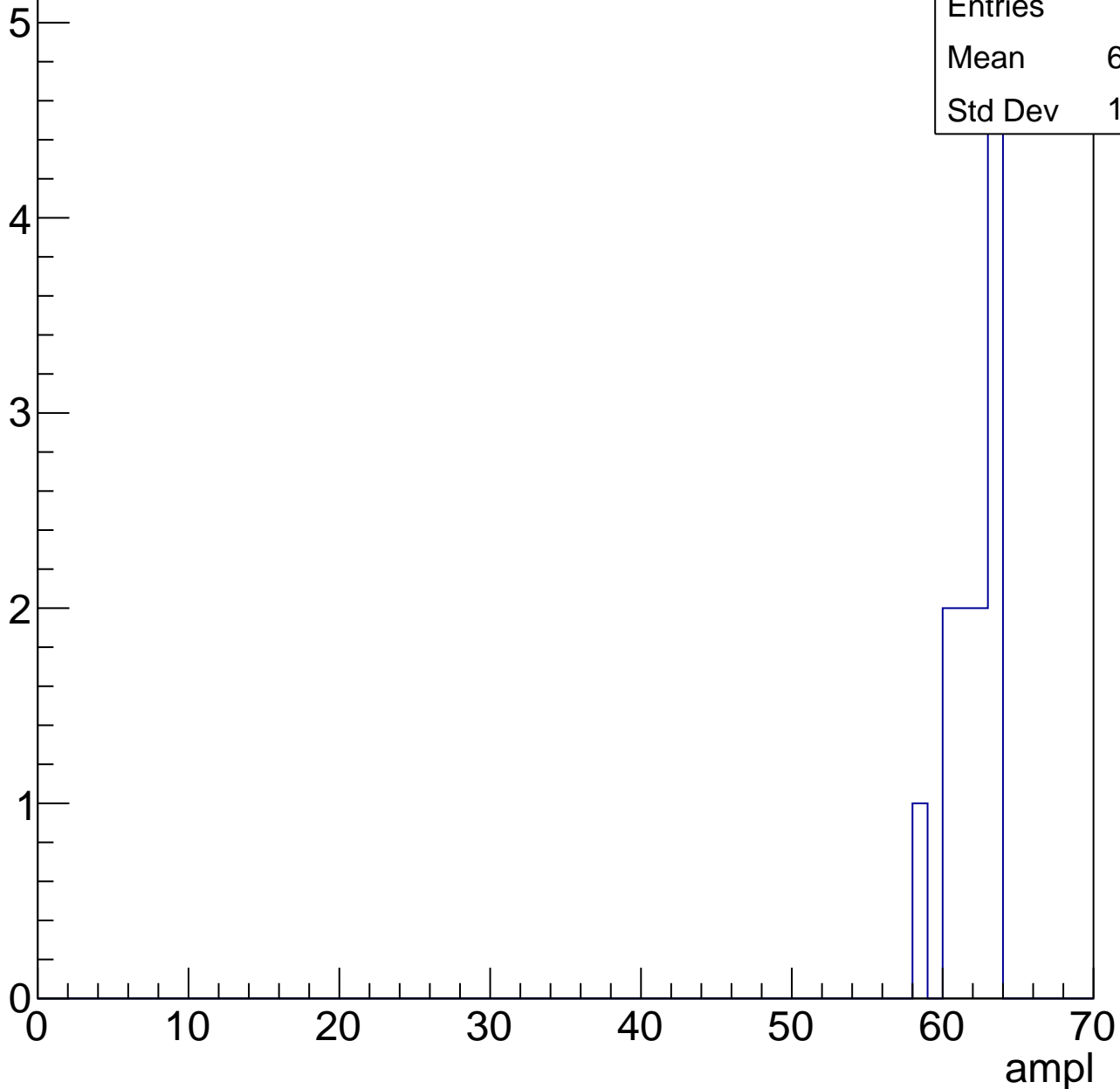


B1L100S, U19-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	61.58
Std Dev	1.552



B1L100S, U19-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch122, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	93
Mean	30.08
Std Dev	5.318

Gaus mean : 30.7419

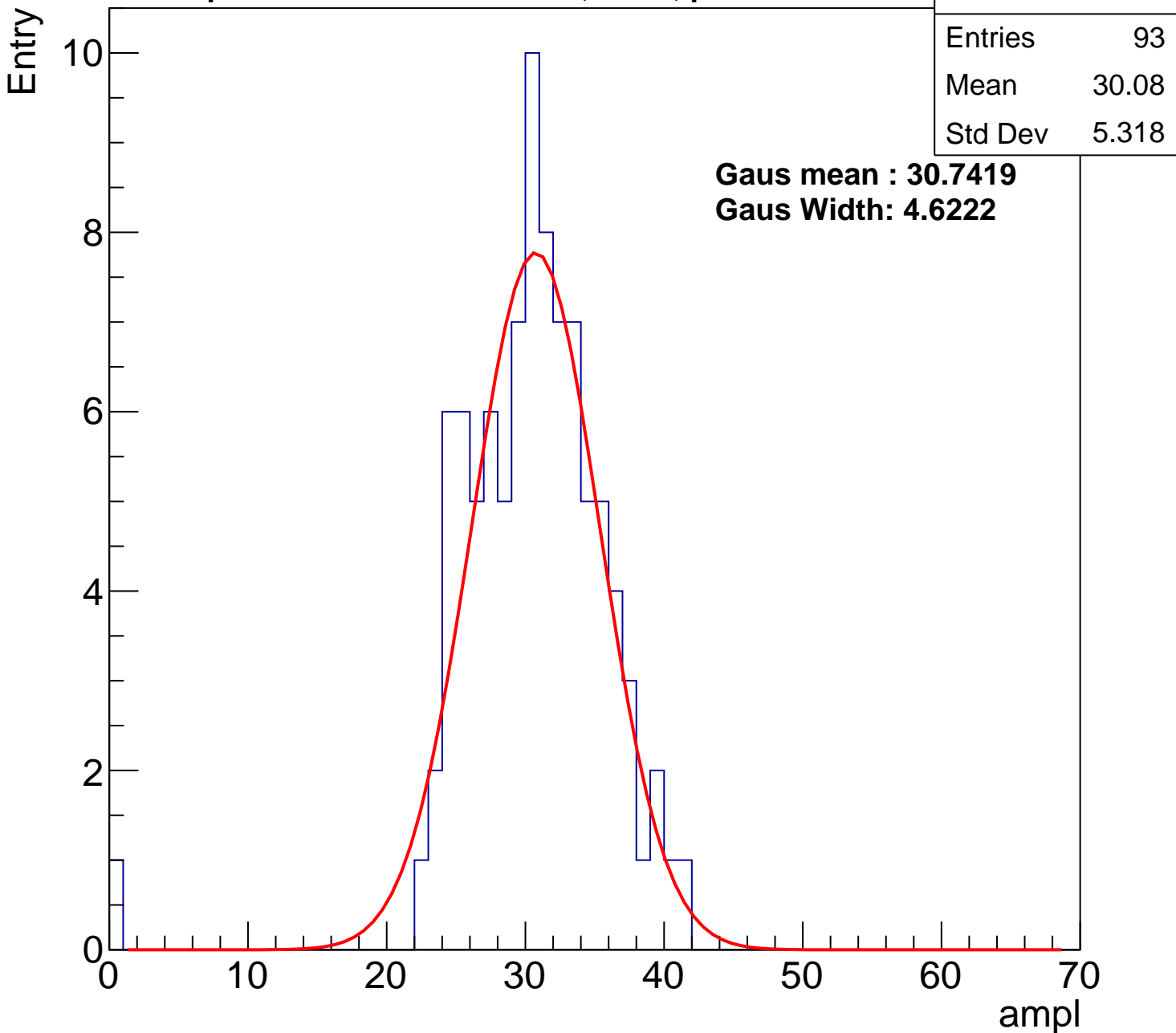
Gaus Width: 4.6222

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch122, adc1

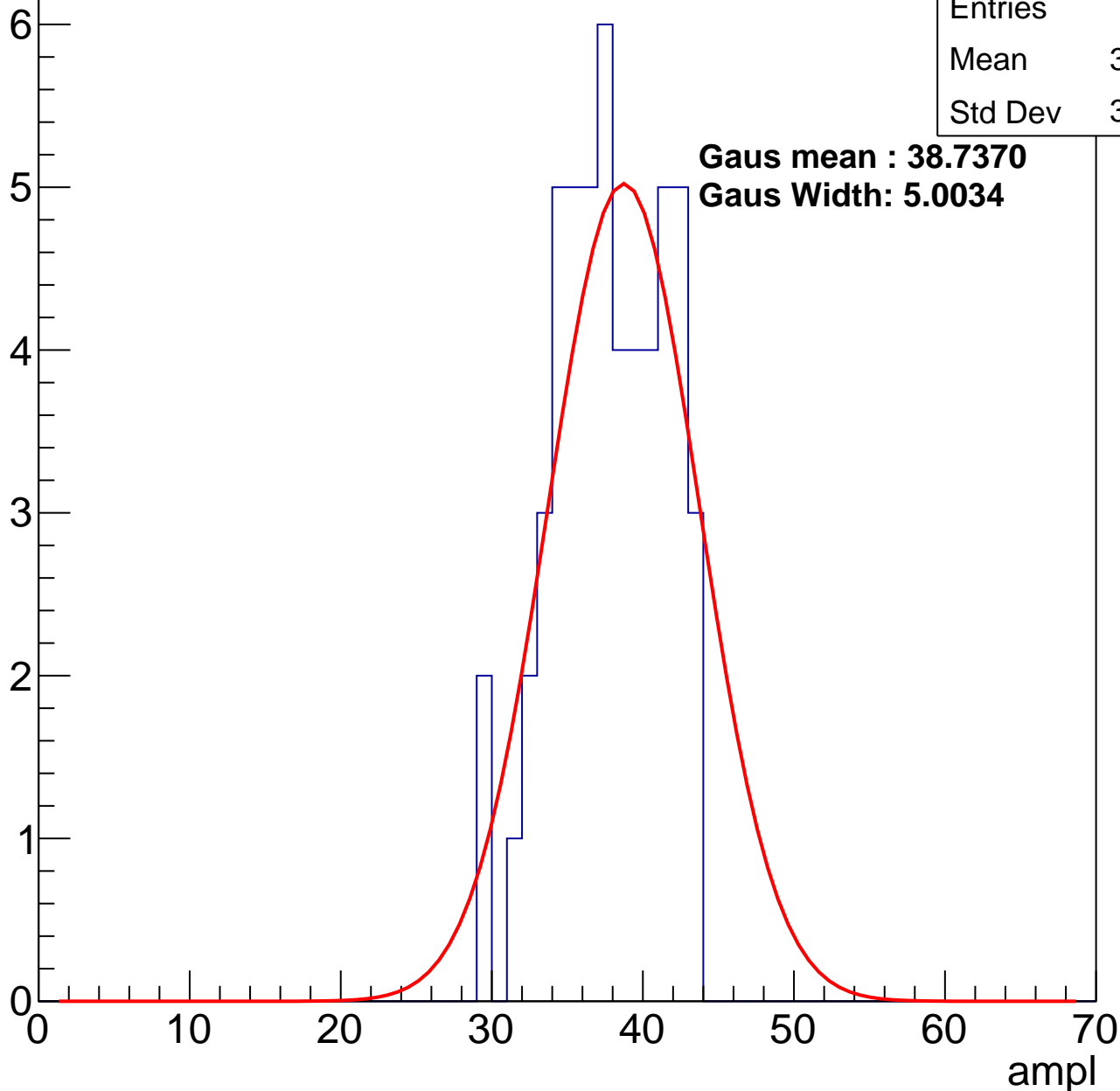
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	37.24
Std Dev	3.595

Gaus mean : 38.7370

Gaus Width: 5.0034



B1L100S, U19-ch122, adc2

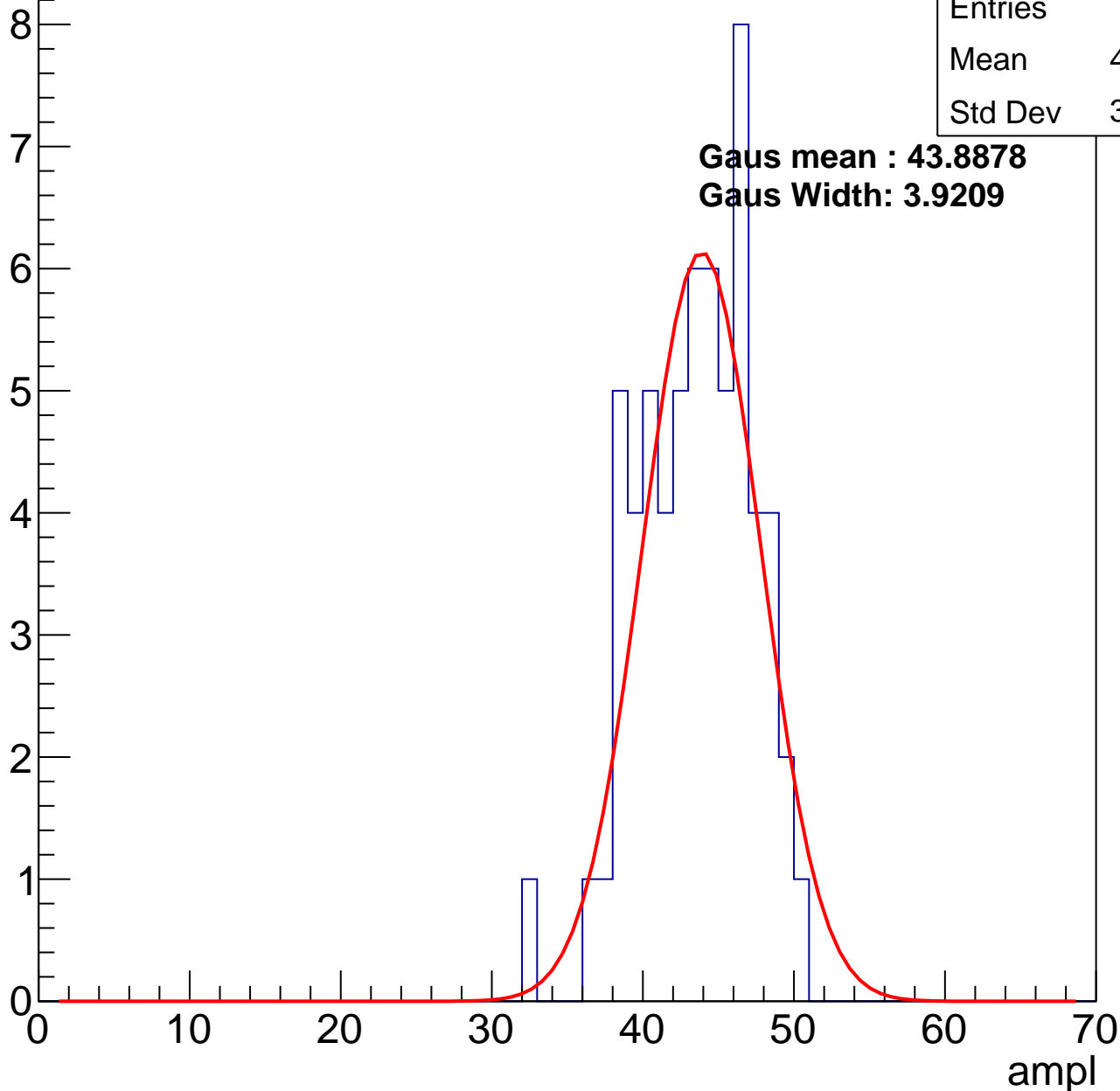
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.03
Std Dev	3.689

Gaus mean : 43.8878

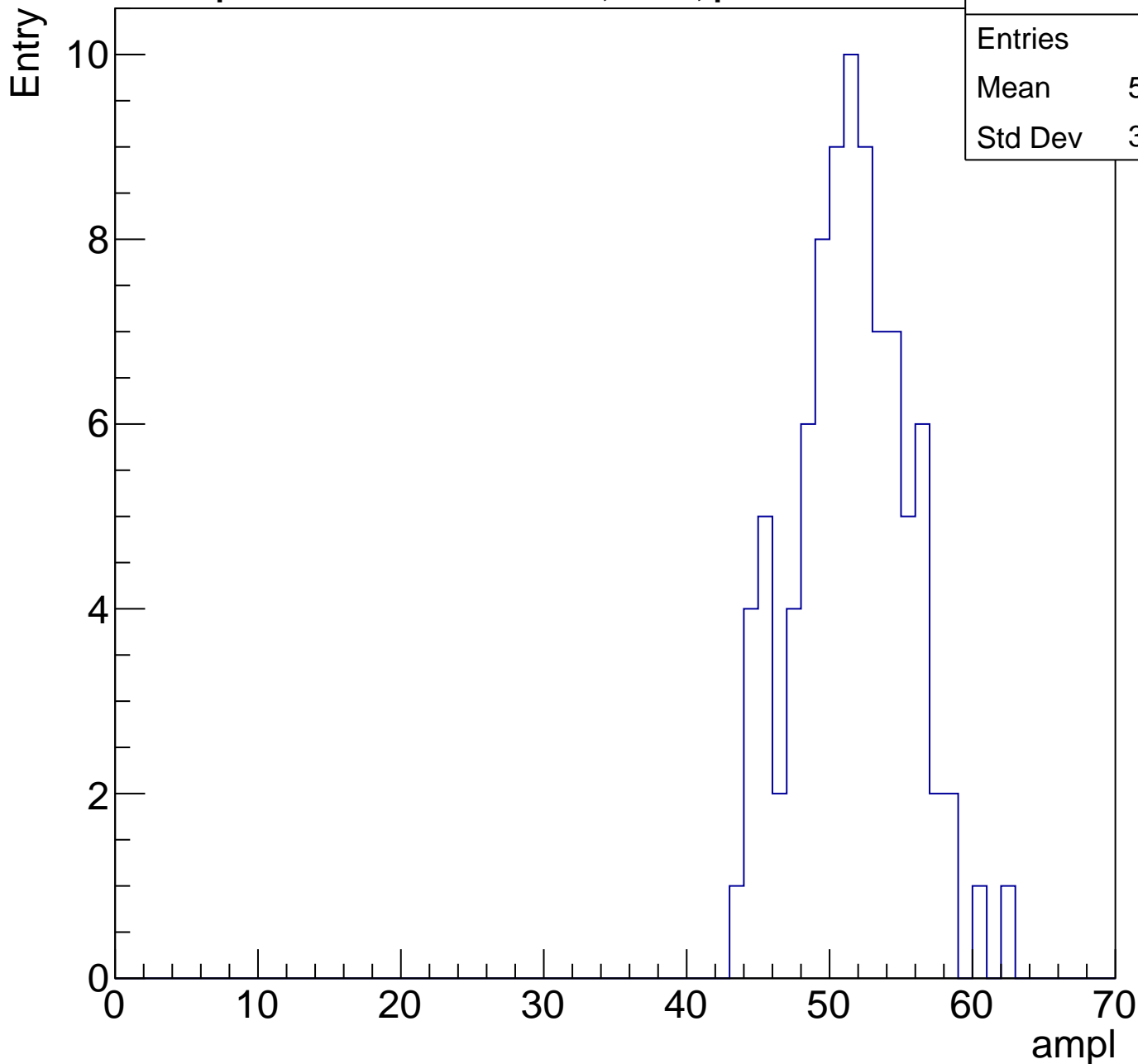
Gaus Width: 3.9209



B1L100S, U19-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	51.06
Std Dev	3.896

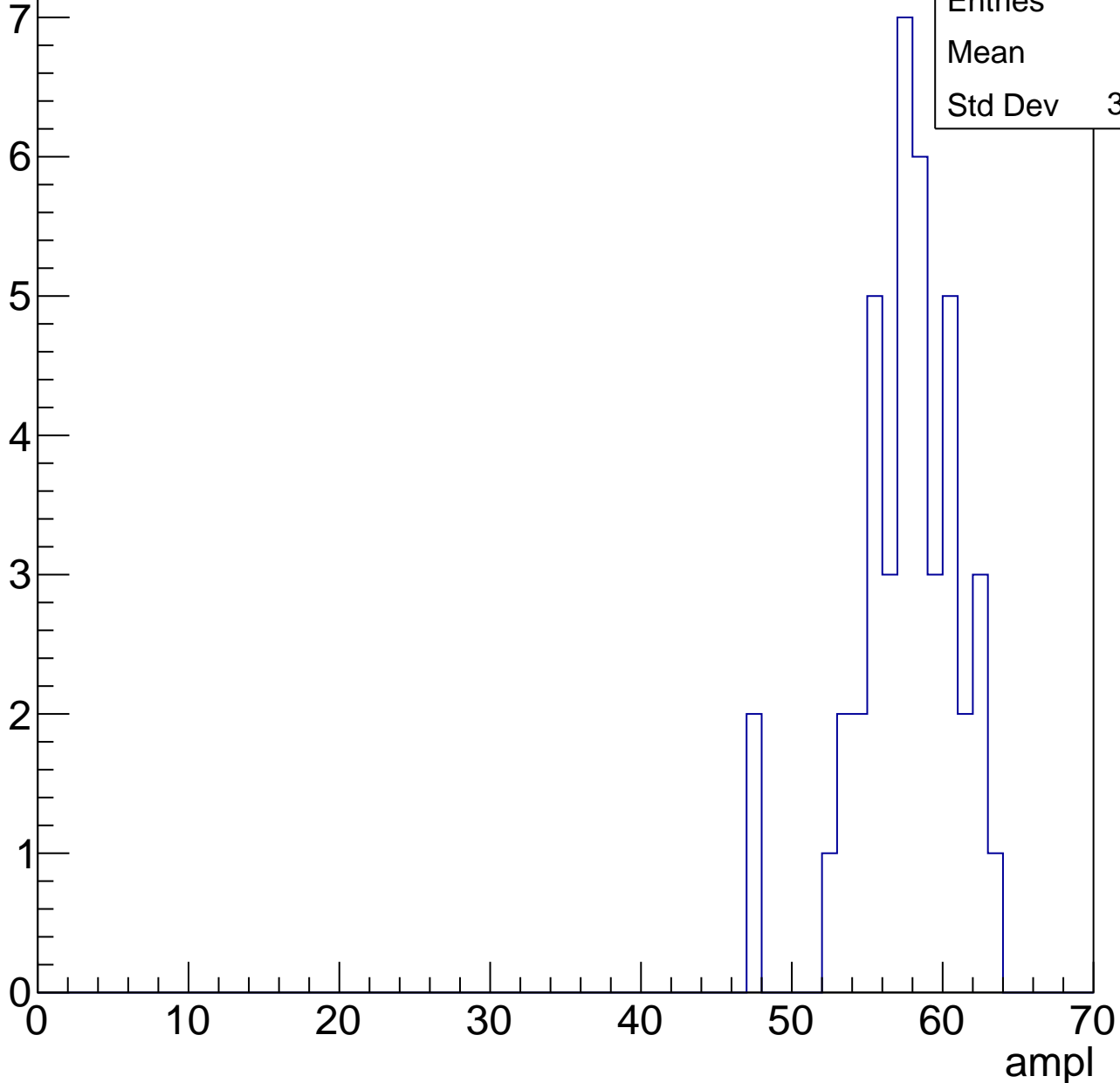


B1L100S, U19-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	57.1
Std Dev	3.463

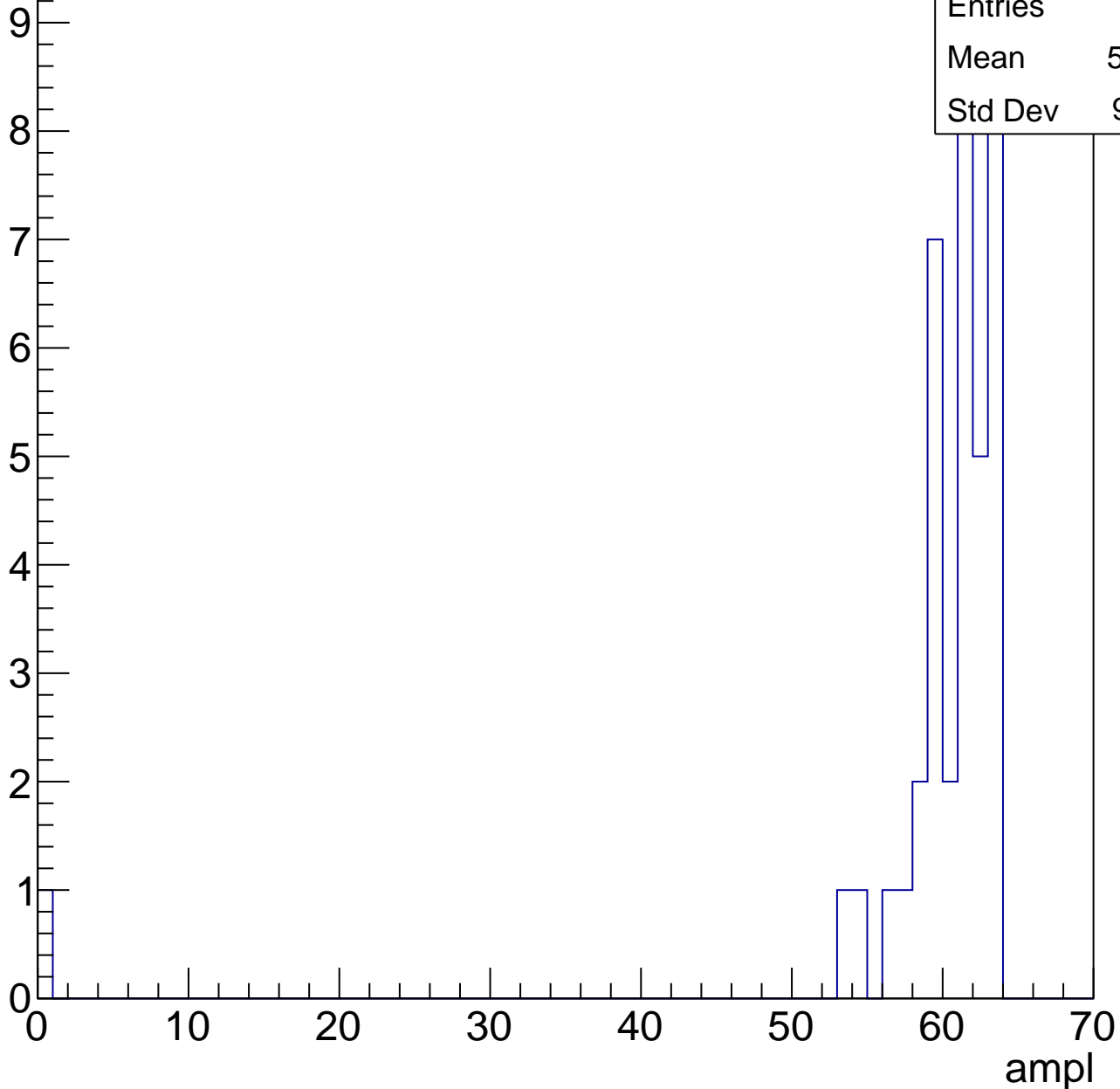


B1L100S, U19-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.79
Std Dev	9.971



B1L100S, U19-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

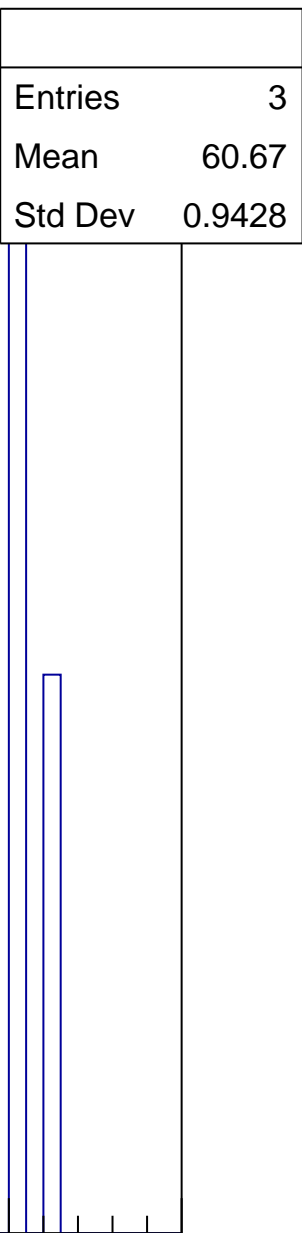
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.67
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U19-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch123, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	28.96
Std Dev	4.814

Gaus mean : 30.0140

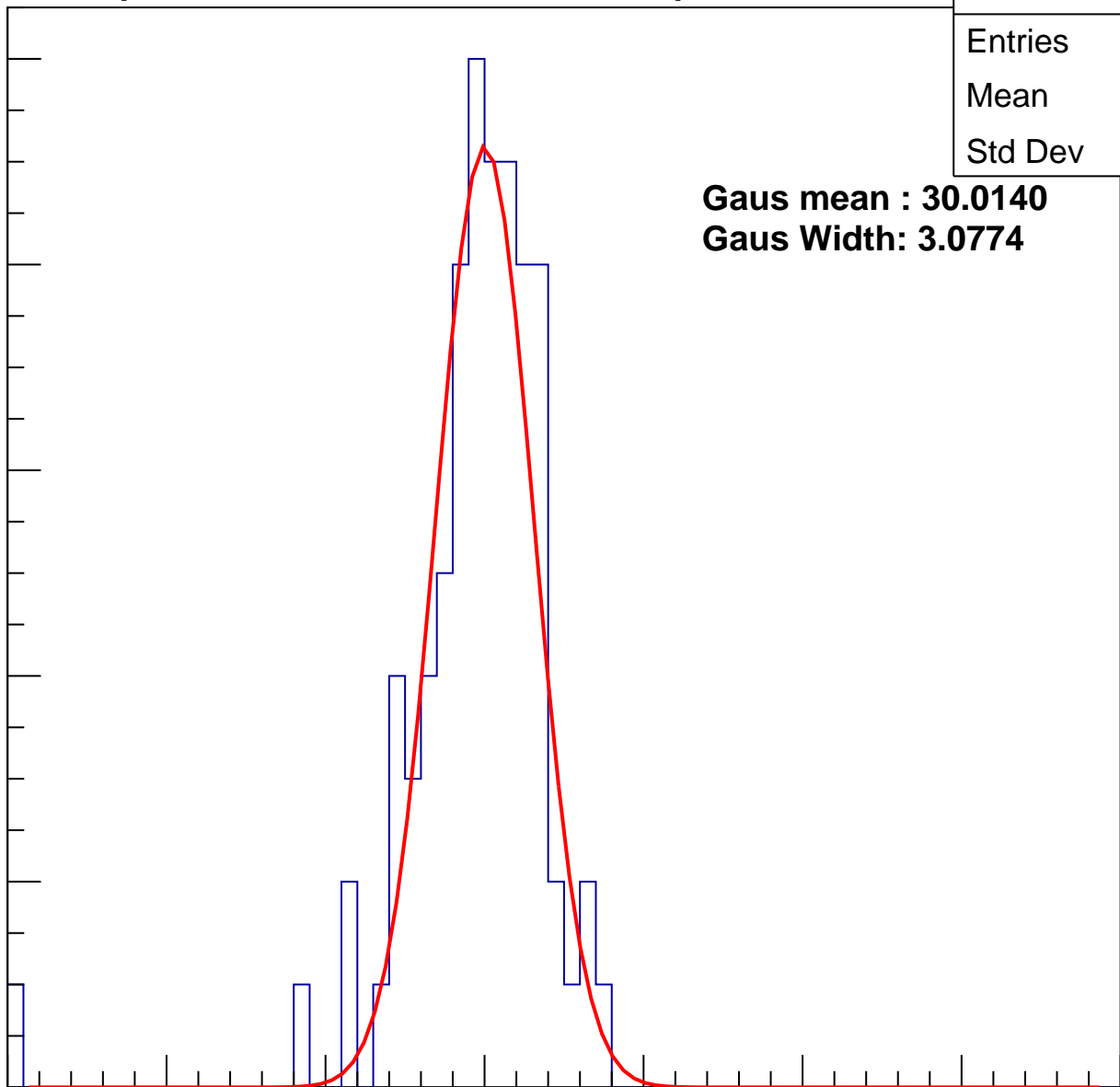
Gaus Width: 3.0774

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U19-ch123, adc1

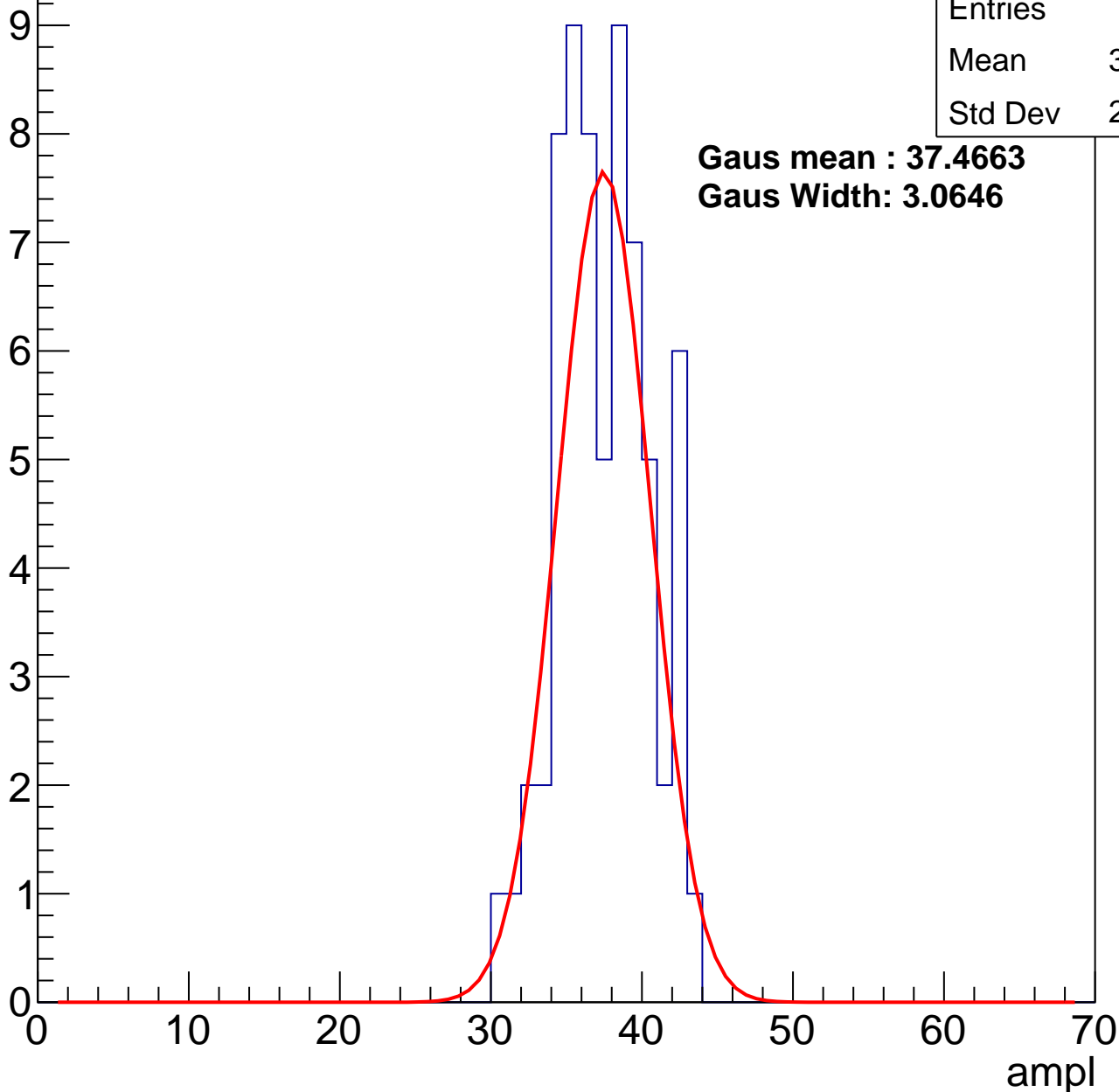
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.02
Std Dev	2.972

Gaus mean : 37.4663

Gaus Width: 3.0646



B1L100S, U19-ch123, adc2

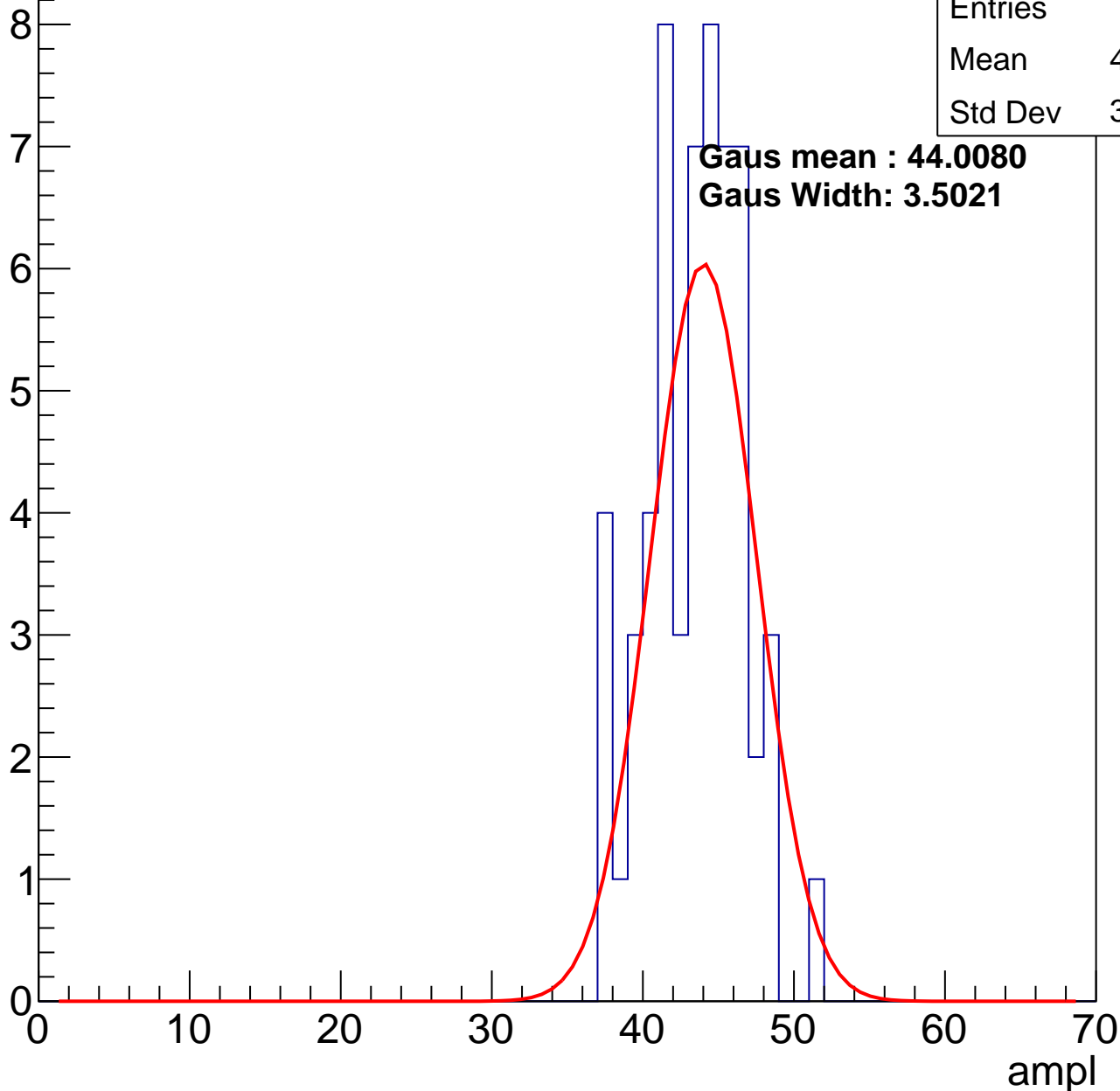
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.03
Std Dev	3.102

Gaus mean : 44.0080

Gaus Width: 3.5021

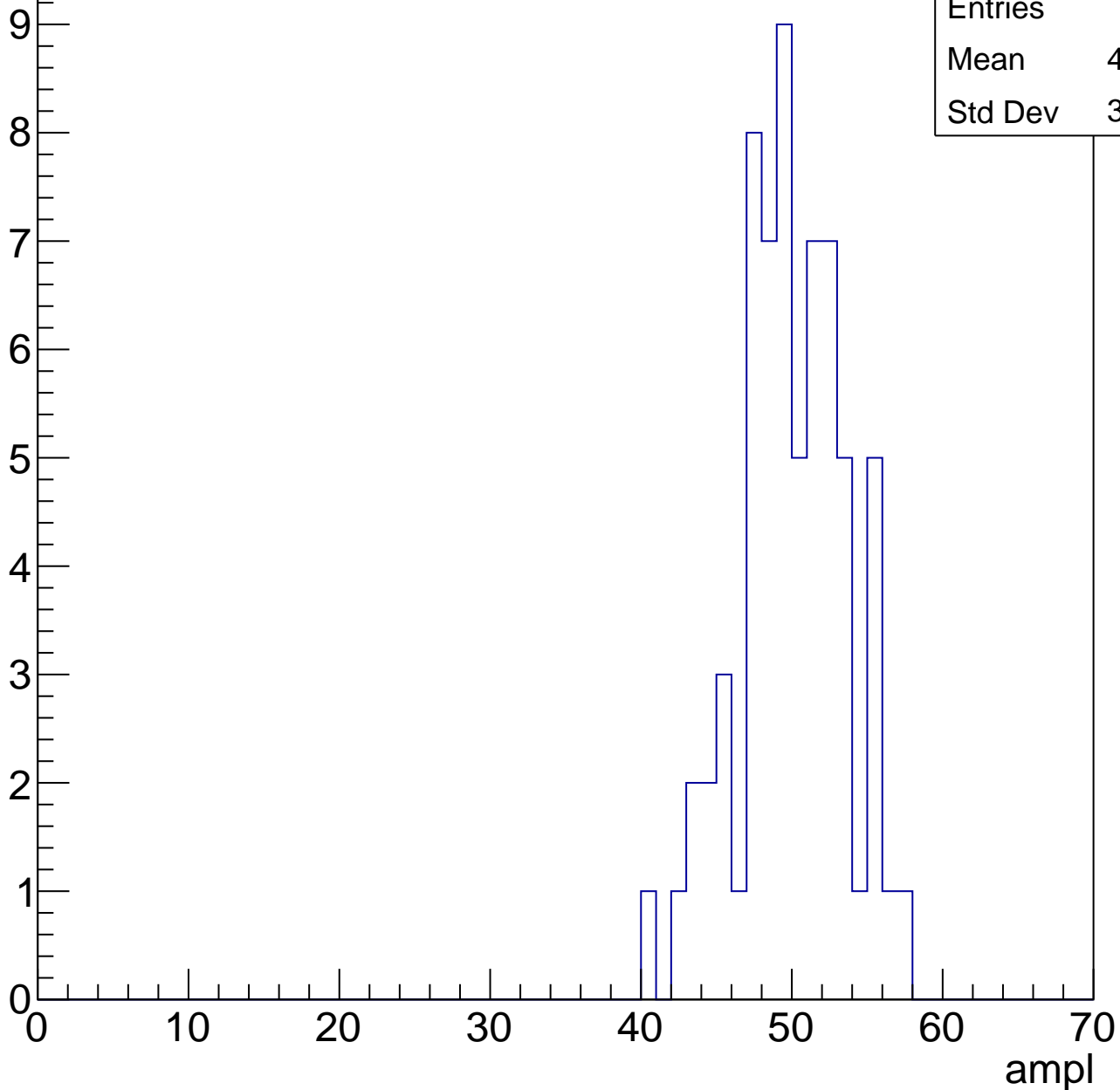


B1L100S, U19-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.52
Std Dev	3.534

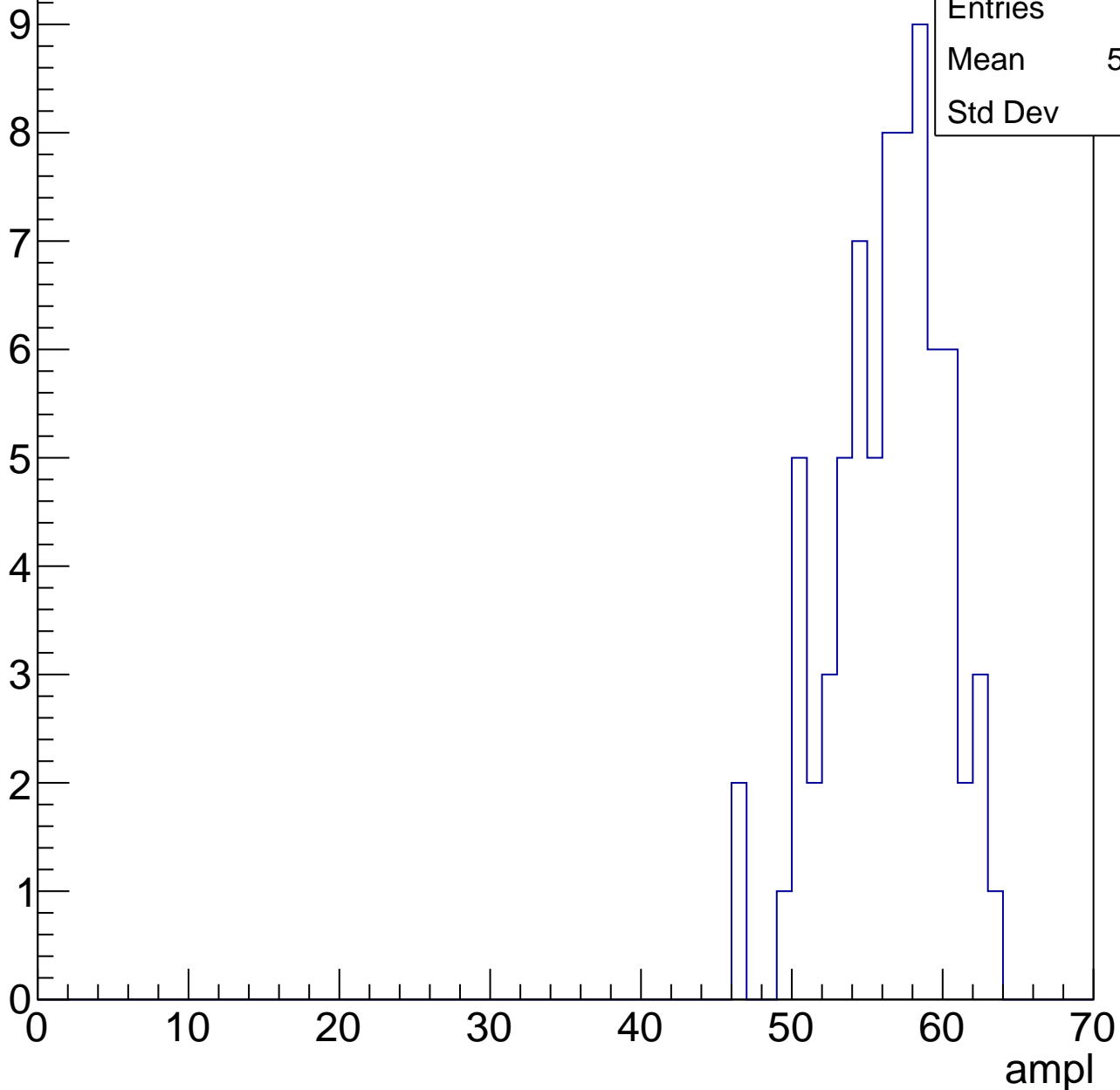


B1L100S, U19-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	55.86
Std Dev	3.71

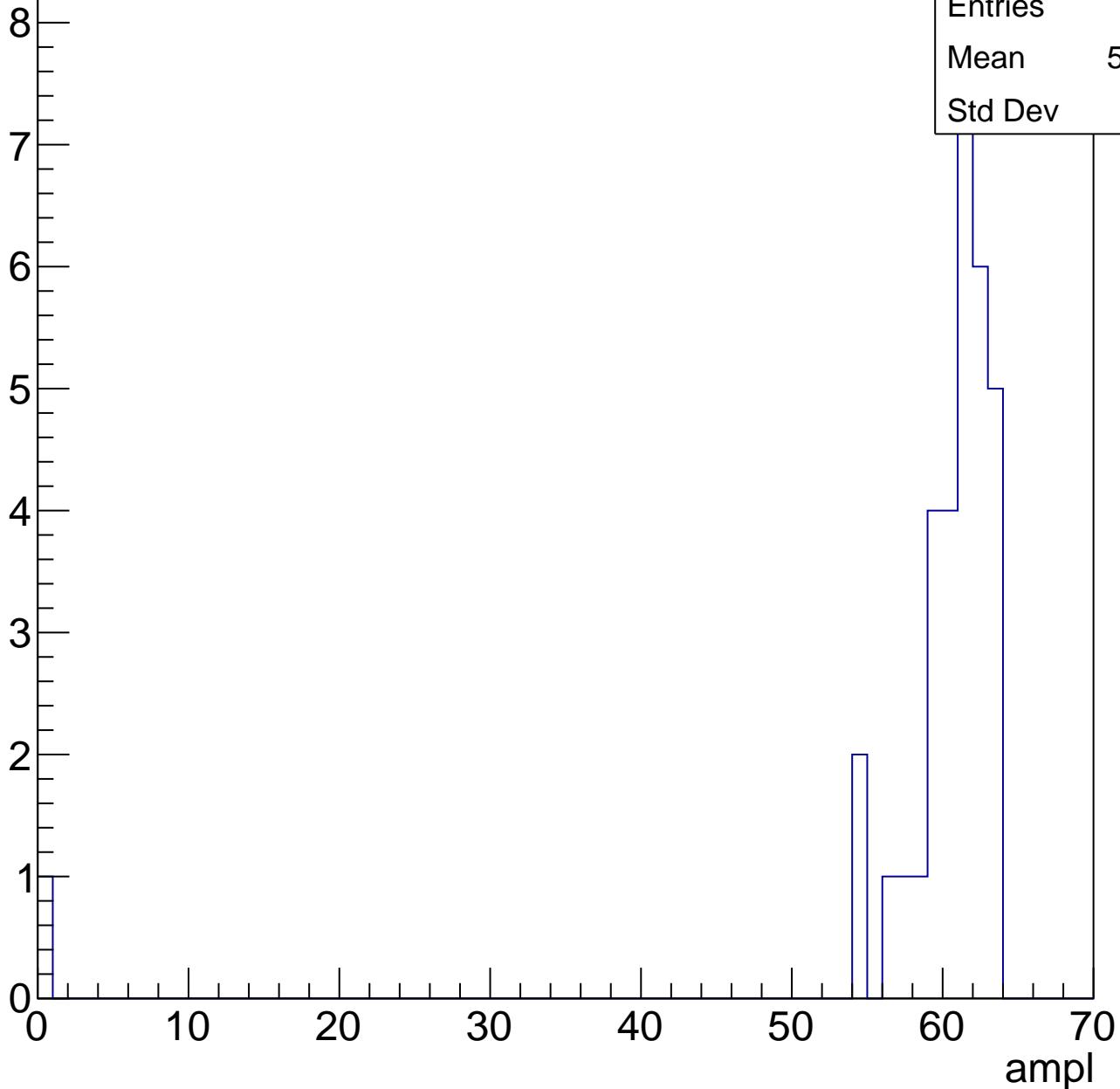


B1L100S, U19-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

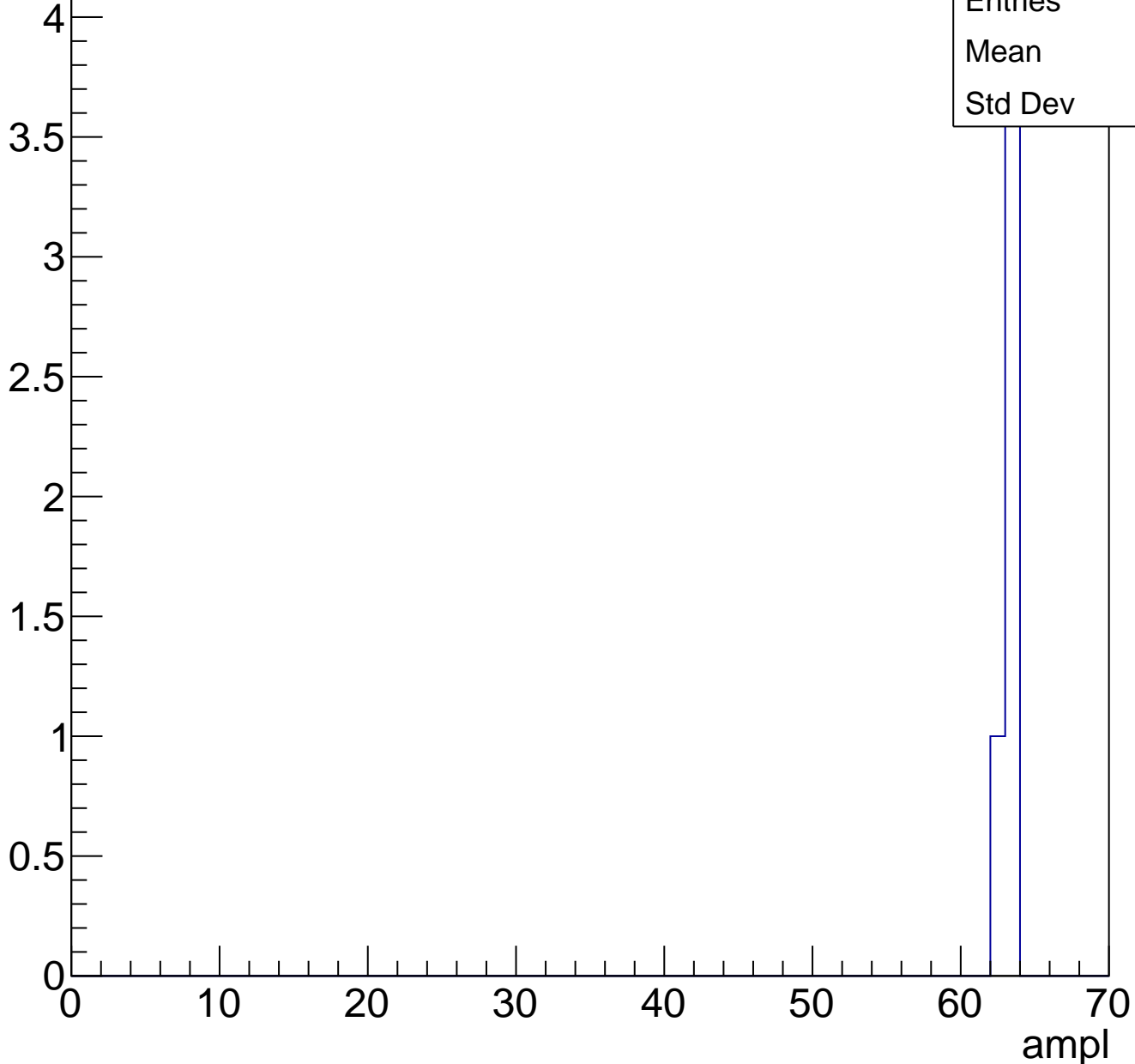
Entries	33
Mean	58.48
Std Dev	10.6



B1L100S, U19-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B1L100S, U19-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U19-ch124, adc0

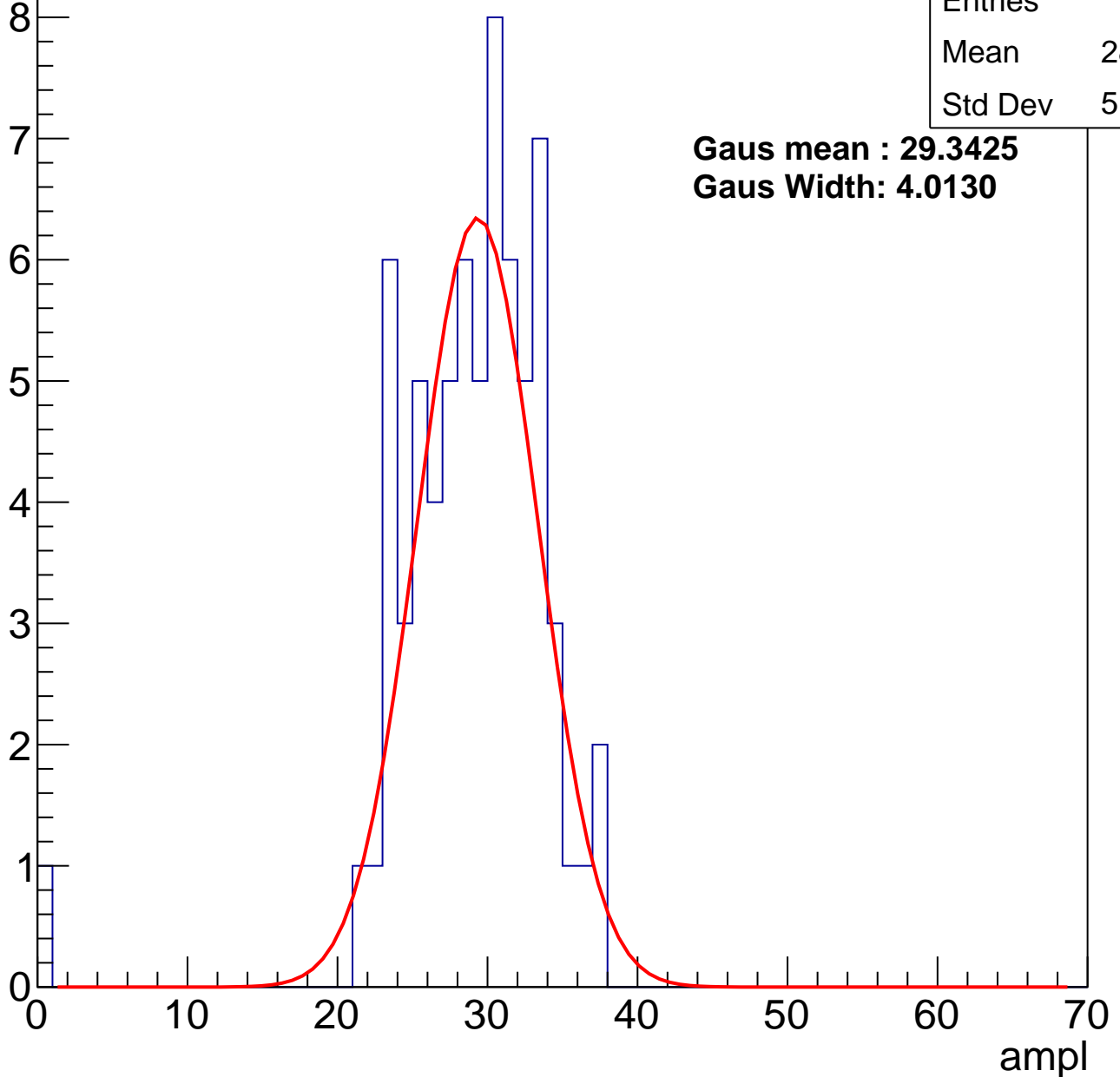
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.49
Std Dev	5.135

Gaus mean : 29.3425

Gaus Width: 4.0130



B1L100S, U19-ch124, adc1

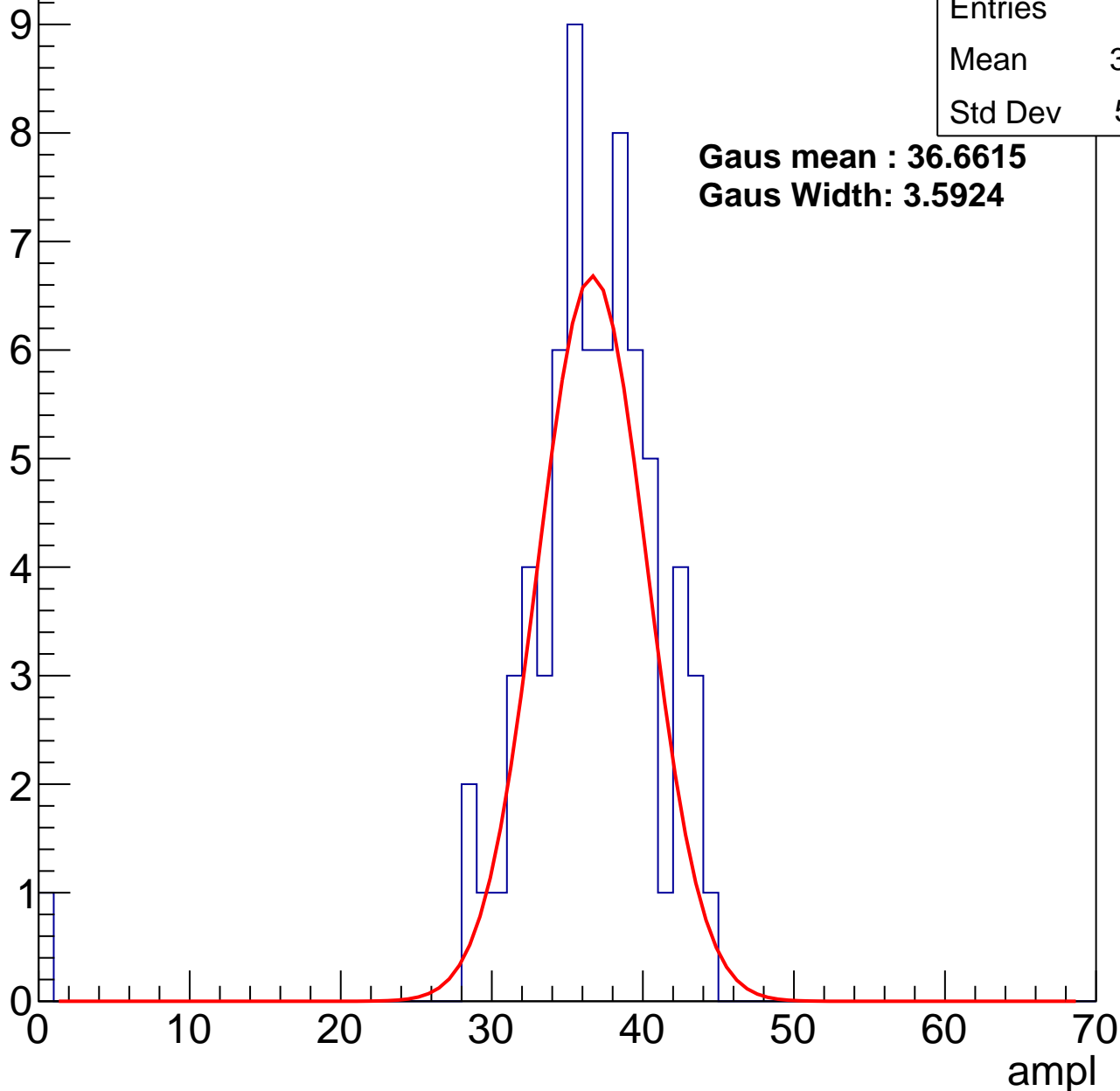
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.89
Std Dev	5.681

Gaus mean : 36.6615

Gaus Width: 3.5924



B1L100S, U19-ch124, adc2

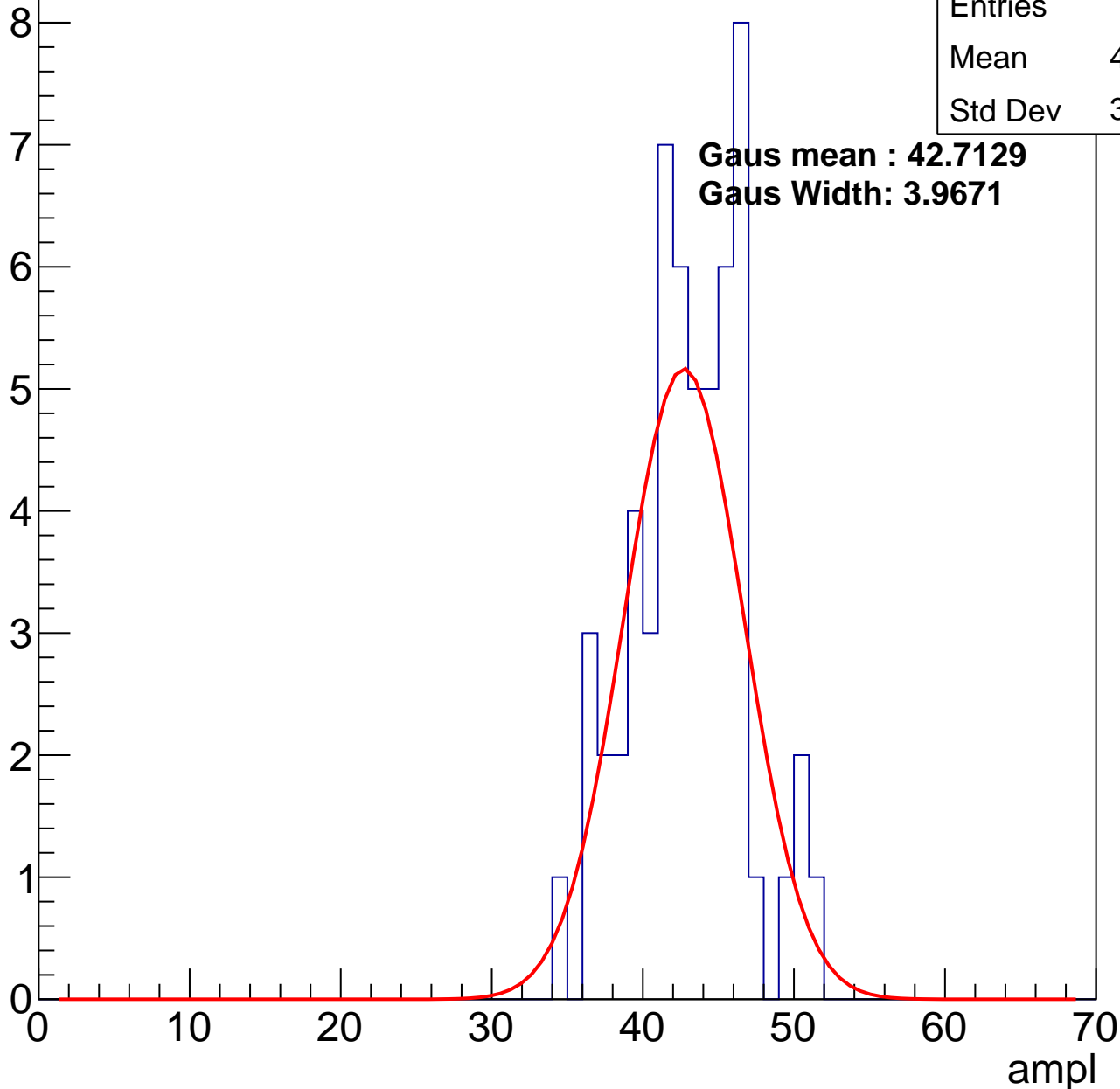
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.58
Std Dev	3.699

Gaus mean : 42.7129

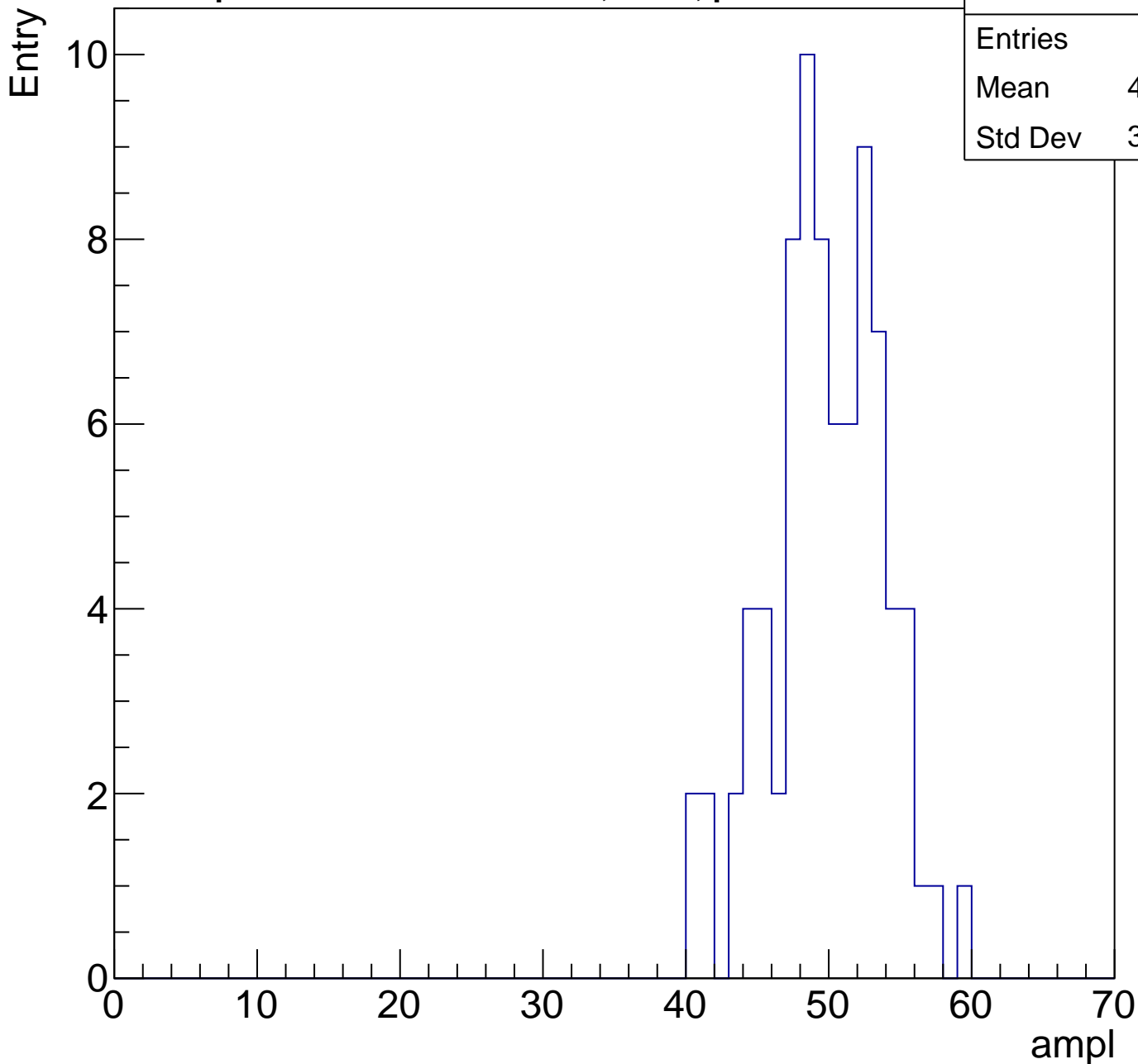
Gaus Width: 3.9671



B1L100S, U19-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	49.35
Std Dev	3.938

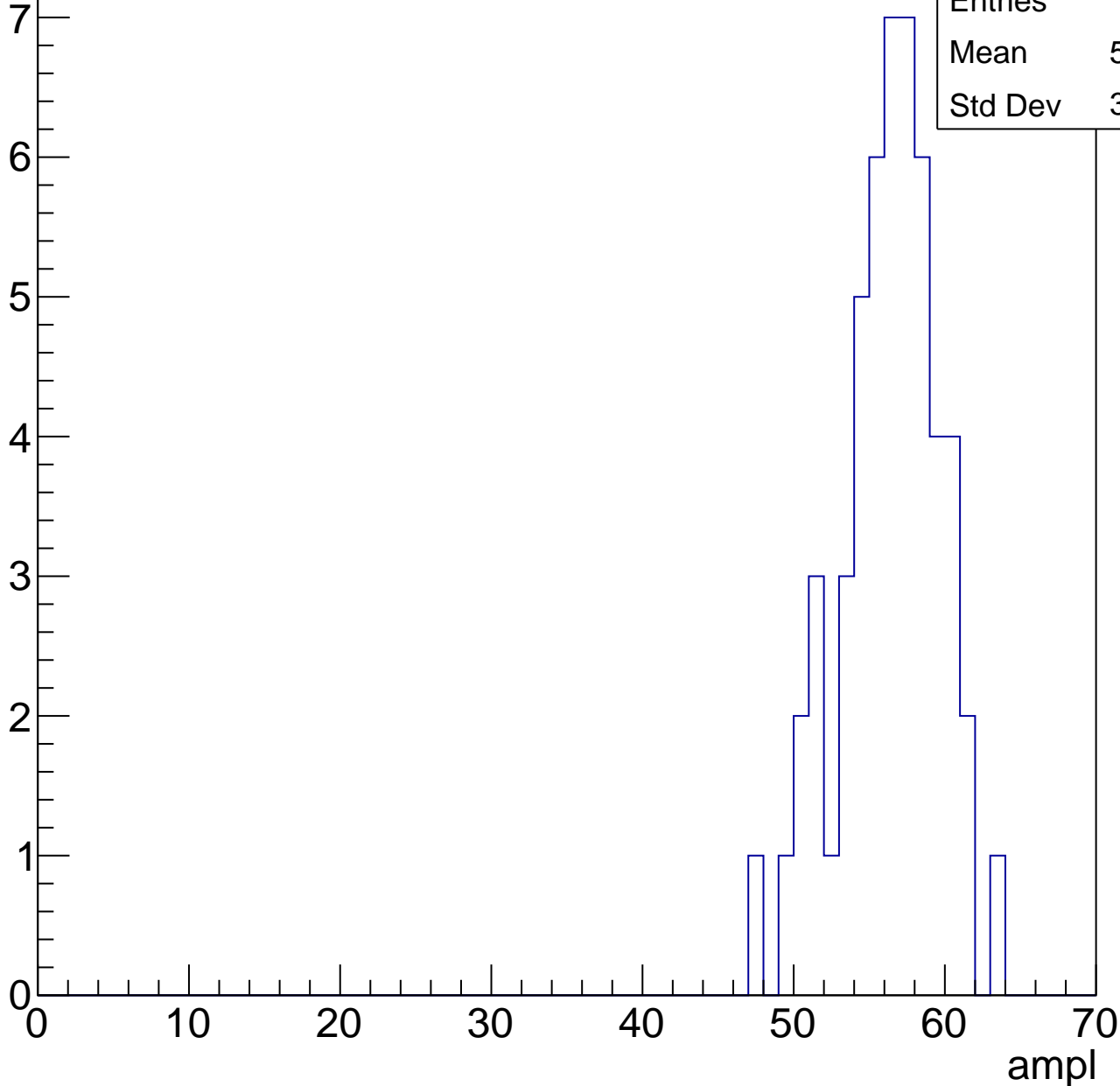


B1L100S, U19-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.85
Std Dev	3.293

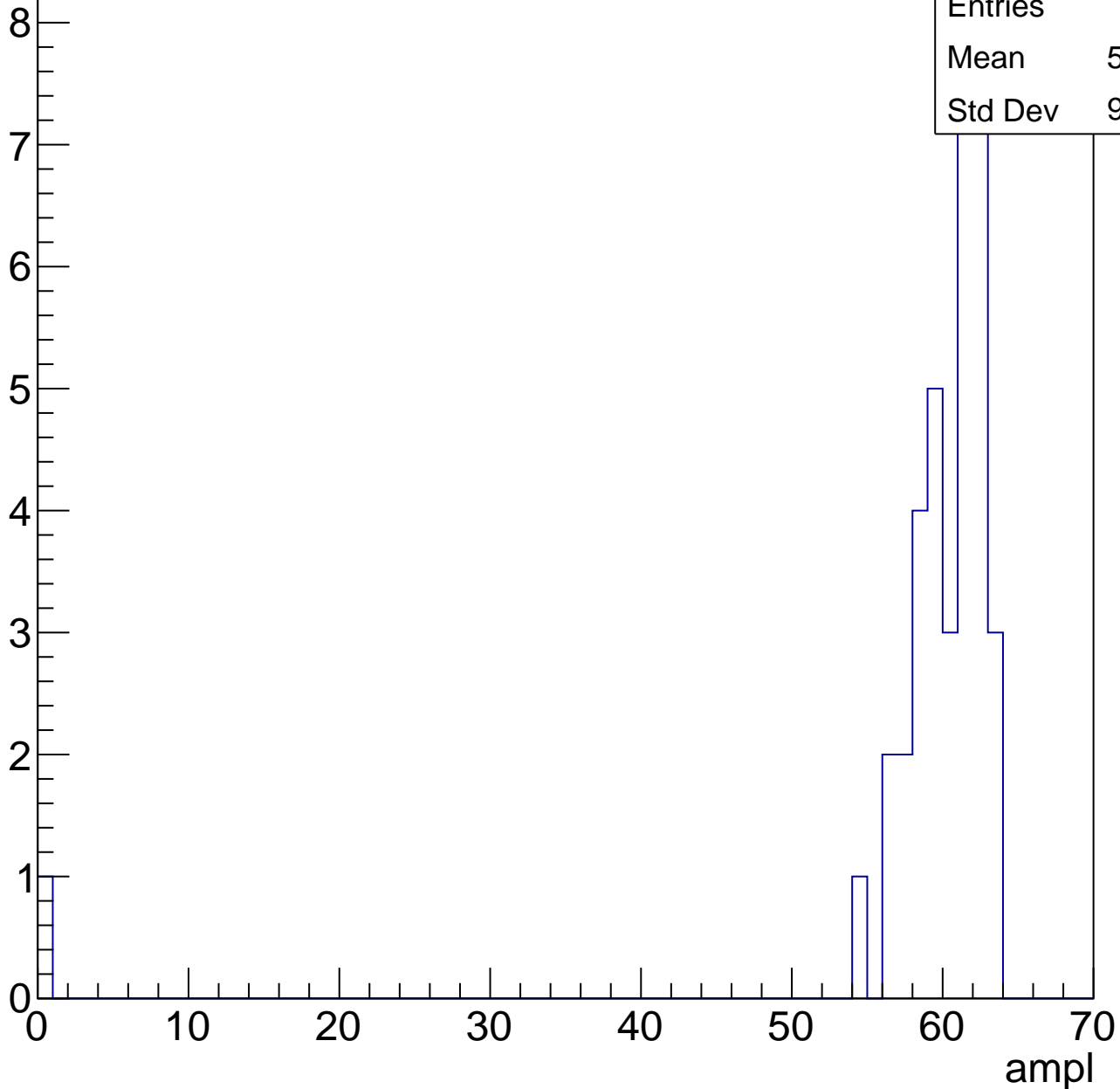


B1L100S, U19-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

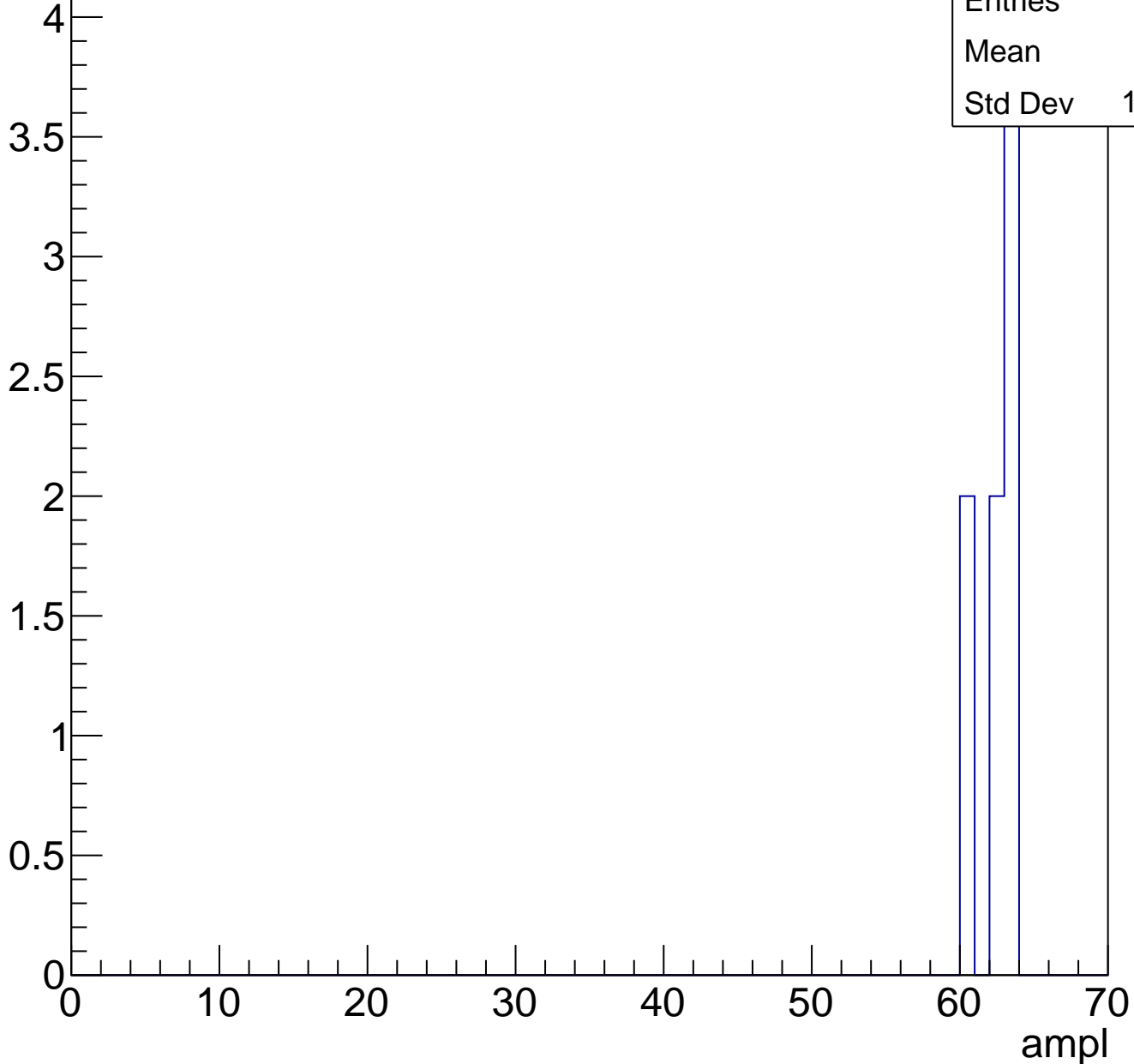
Entries	37
Mean	58.38
Std Dev	9.968



B1L100S, U19-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

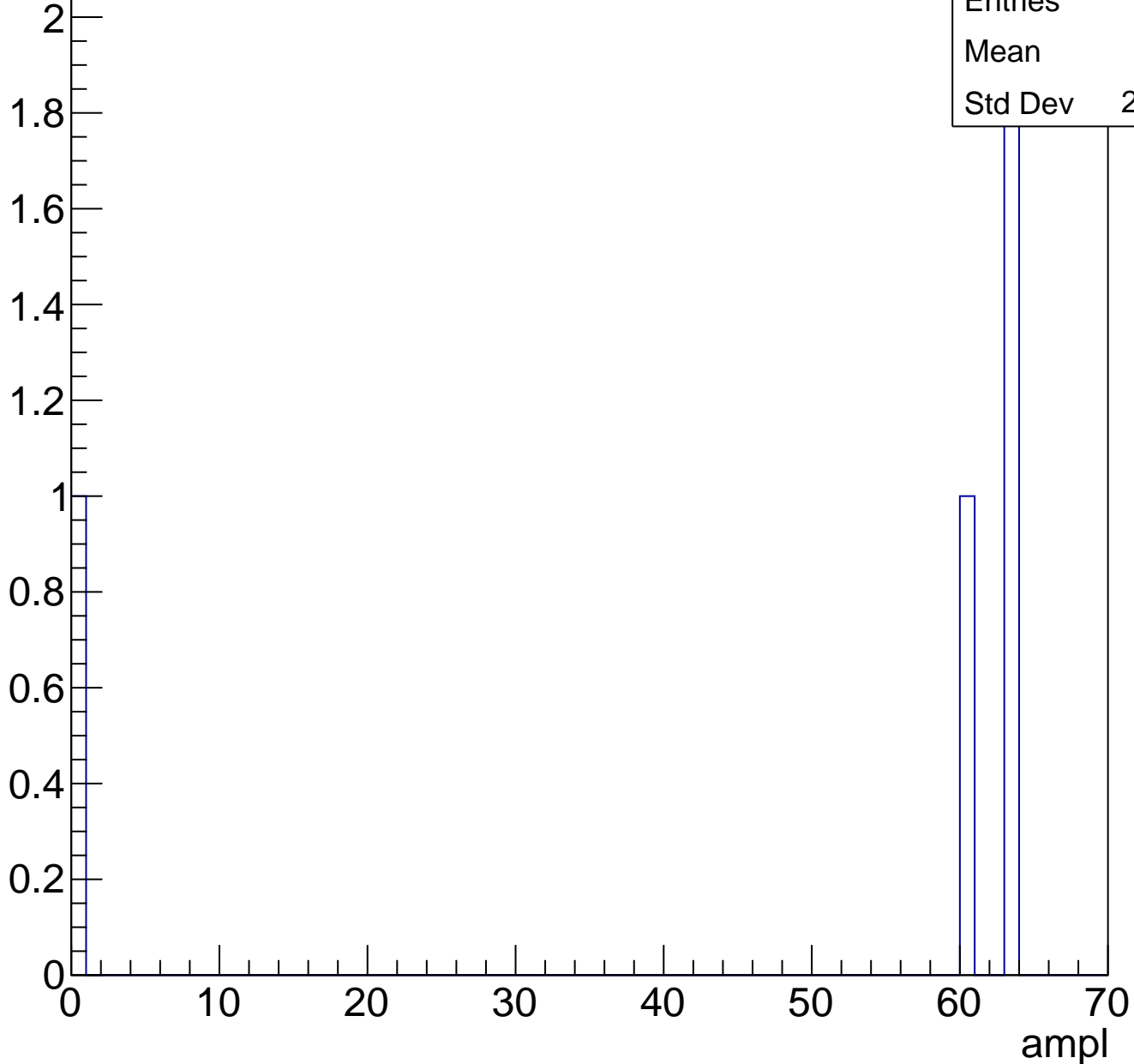
Entry



B1L100S, U19-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch125, adc0

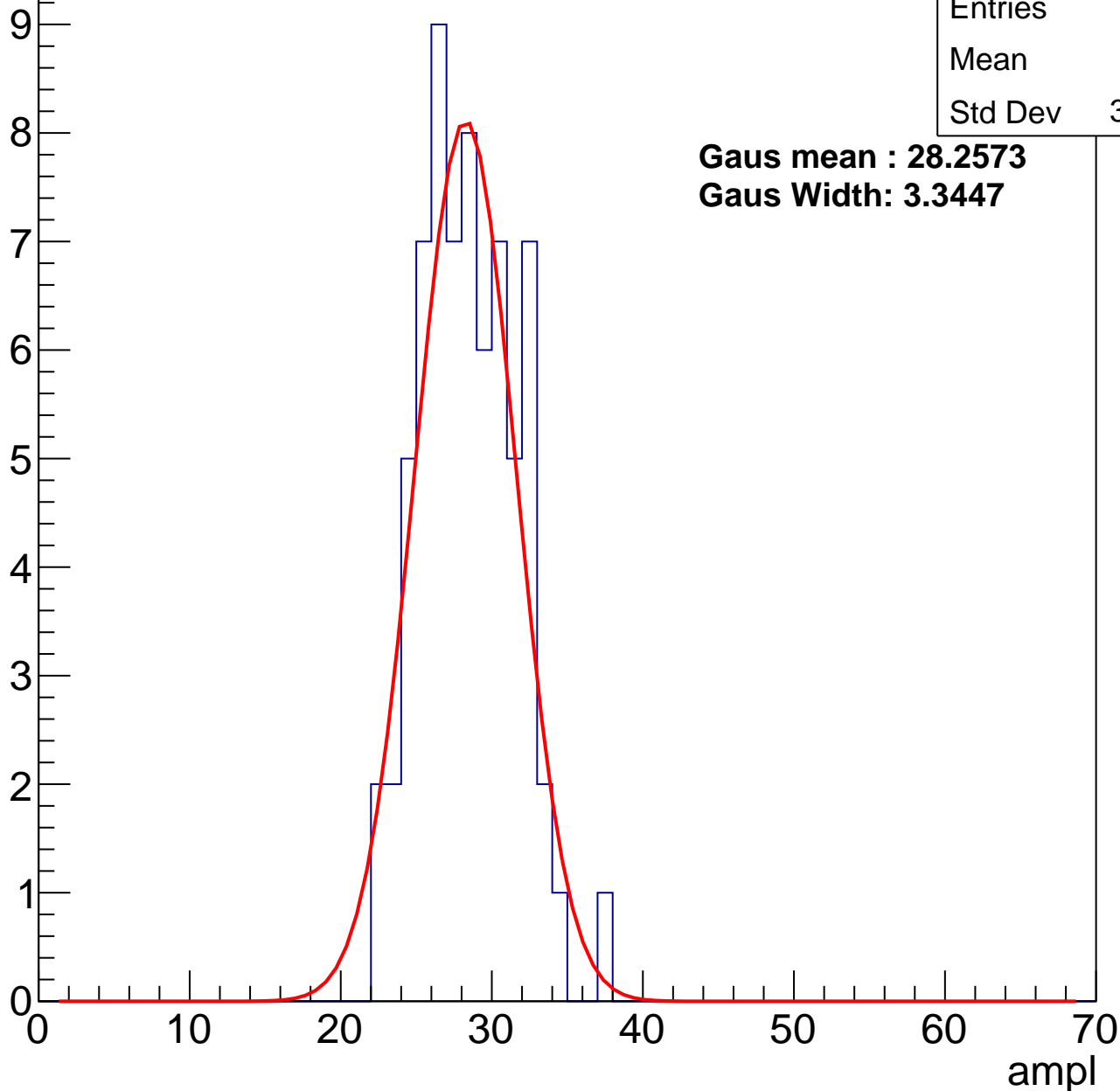
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28
Std Dev	3.107

Gaus mean : 28.2573

Gaus Width: 3.3447



B1L100S, U19-ch125, adc1

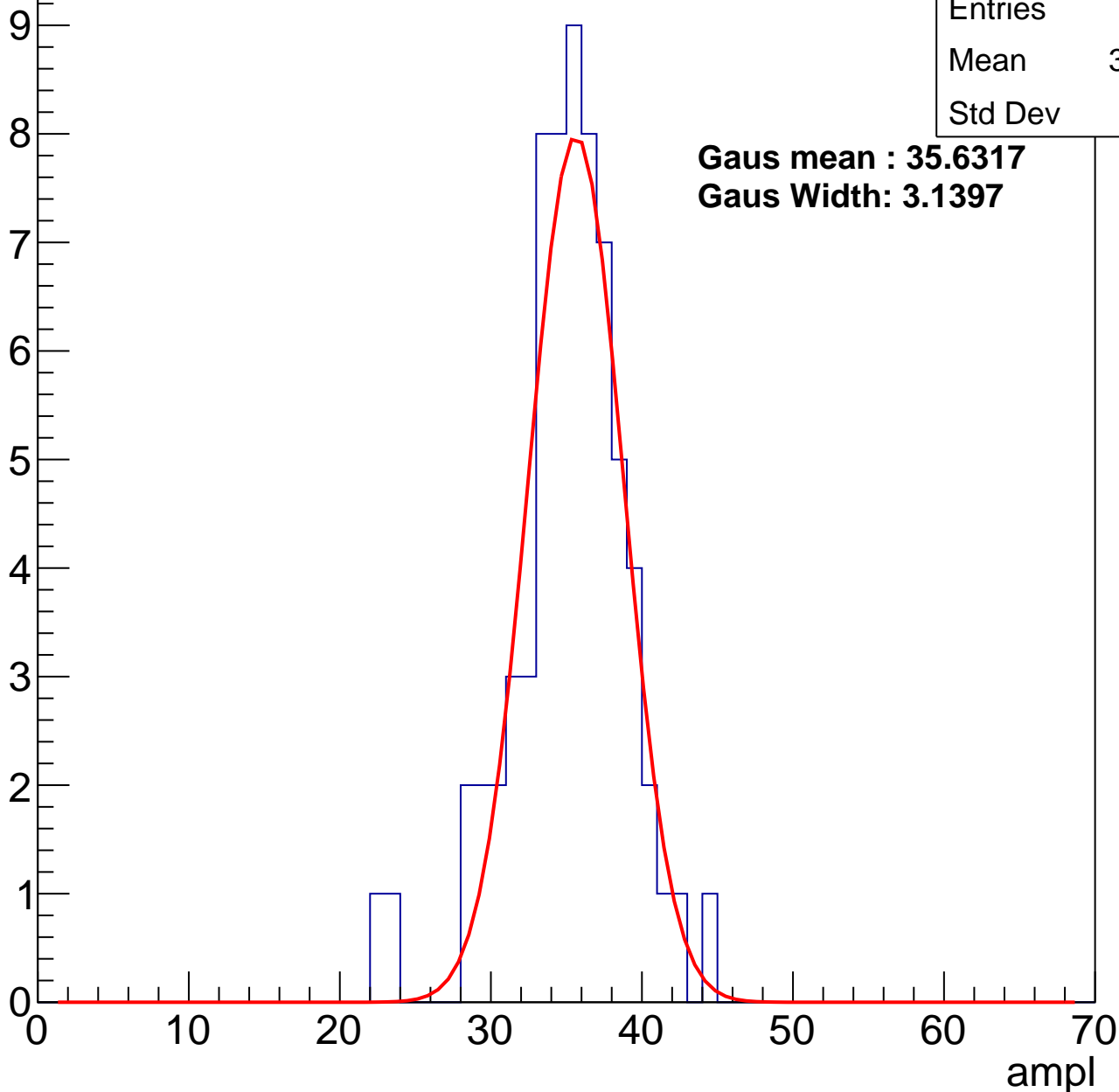
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	34.69
Std Dev	3.84

Gaus mean : 35.6317

Gaus Width: 3.1397



B1L100S, U19-ch125, adc2

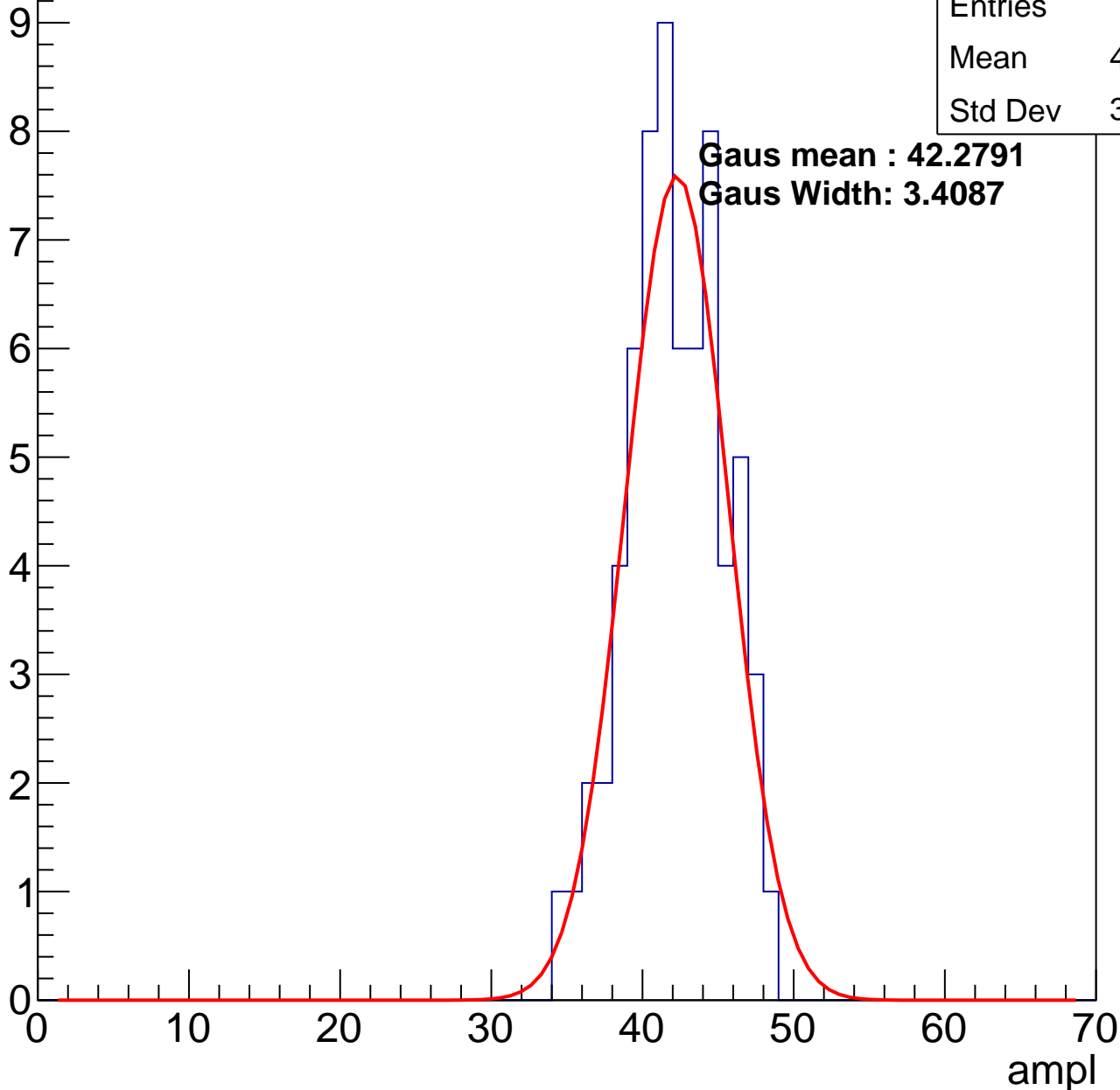
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.68
Std Dev	3.144

Gaus mean : 42.2791

Gaus Width: 3.4087

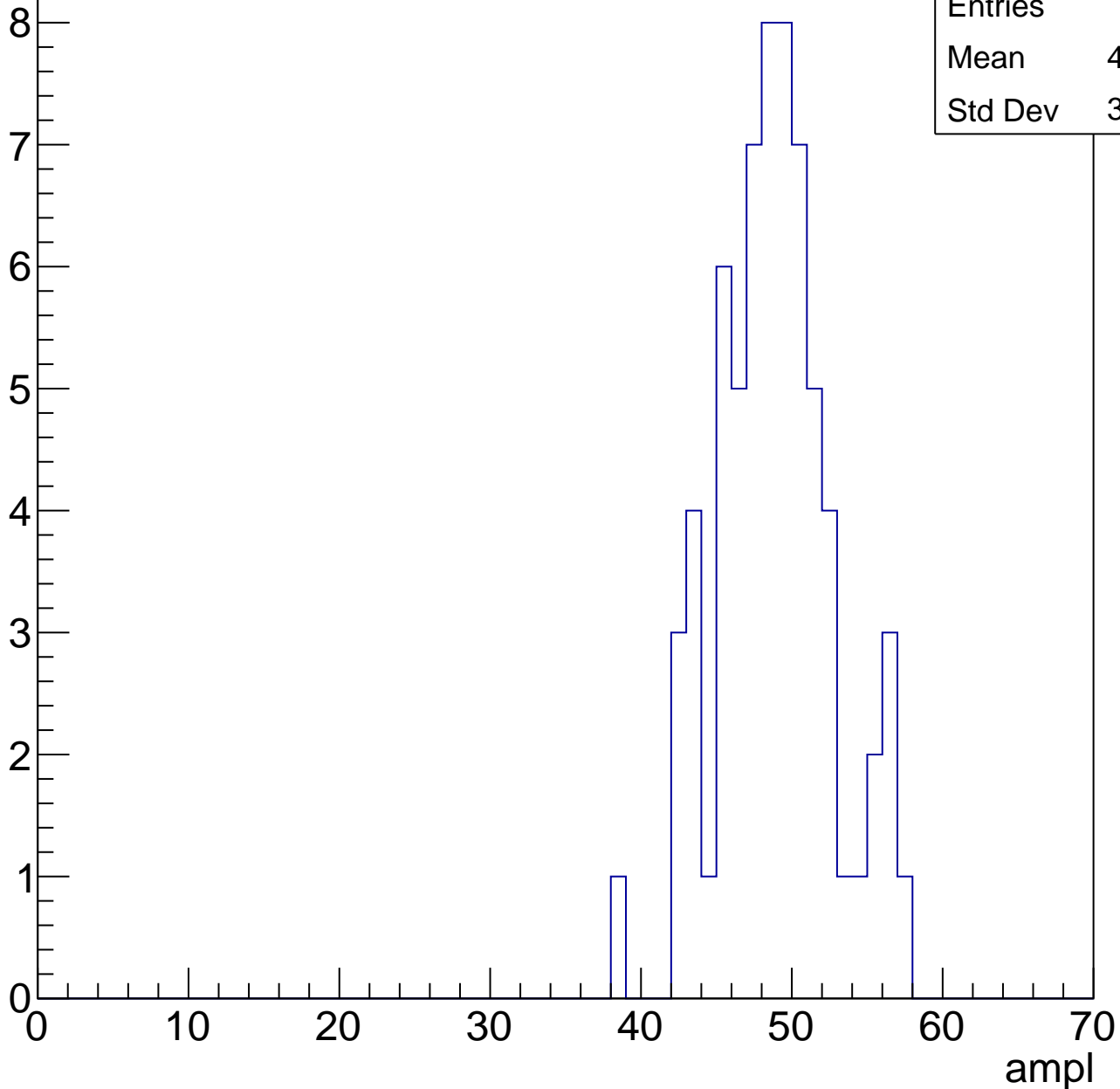


B1L100S, U19-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	48.36
Std Dev	3.812

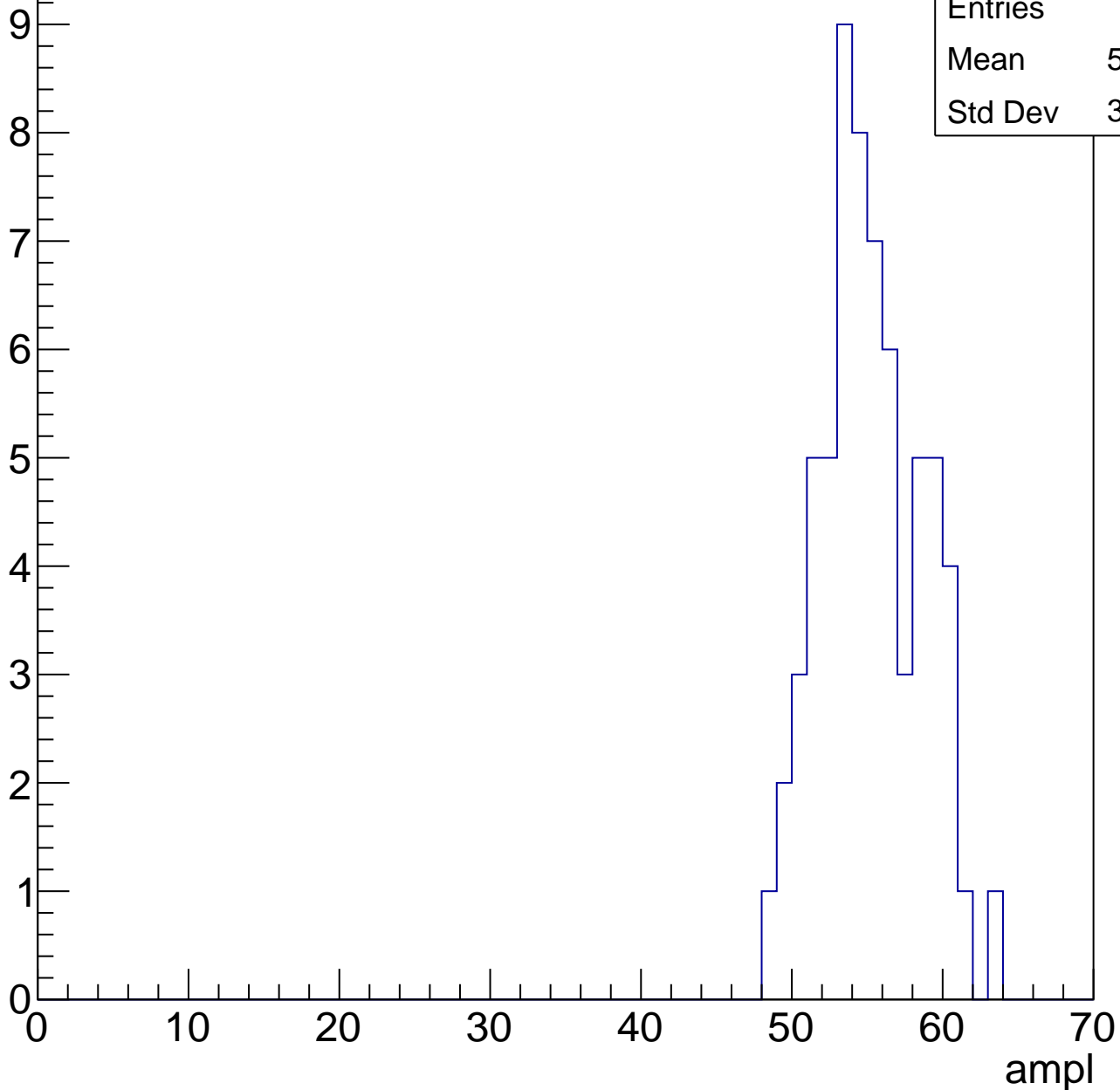


B1L100S, U19-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	54.78
Std Dev	3.293

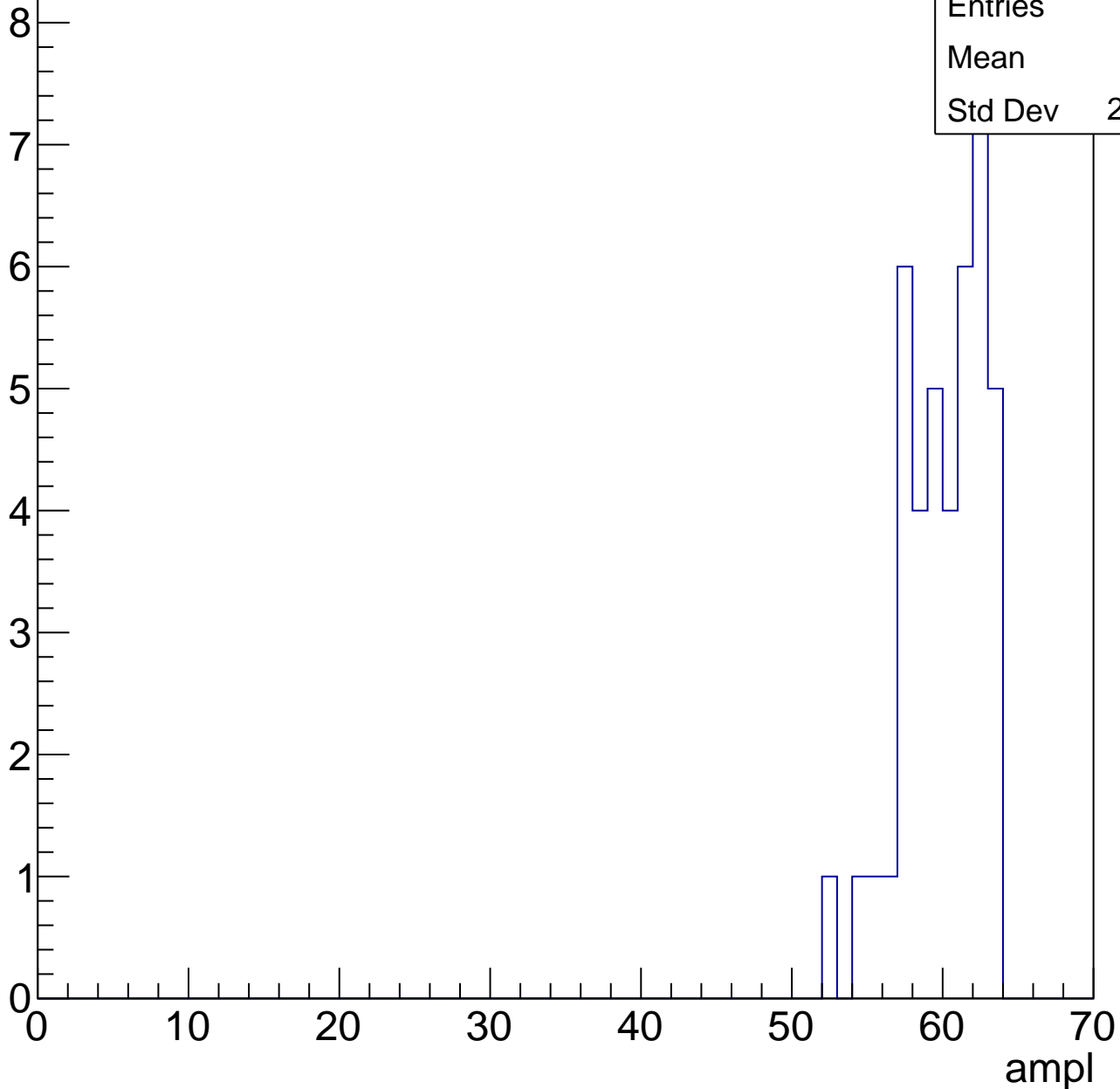


B1L100S, U19-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

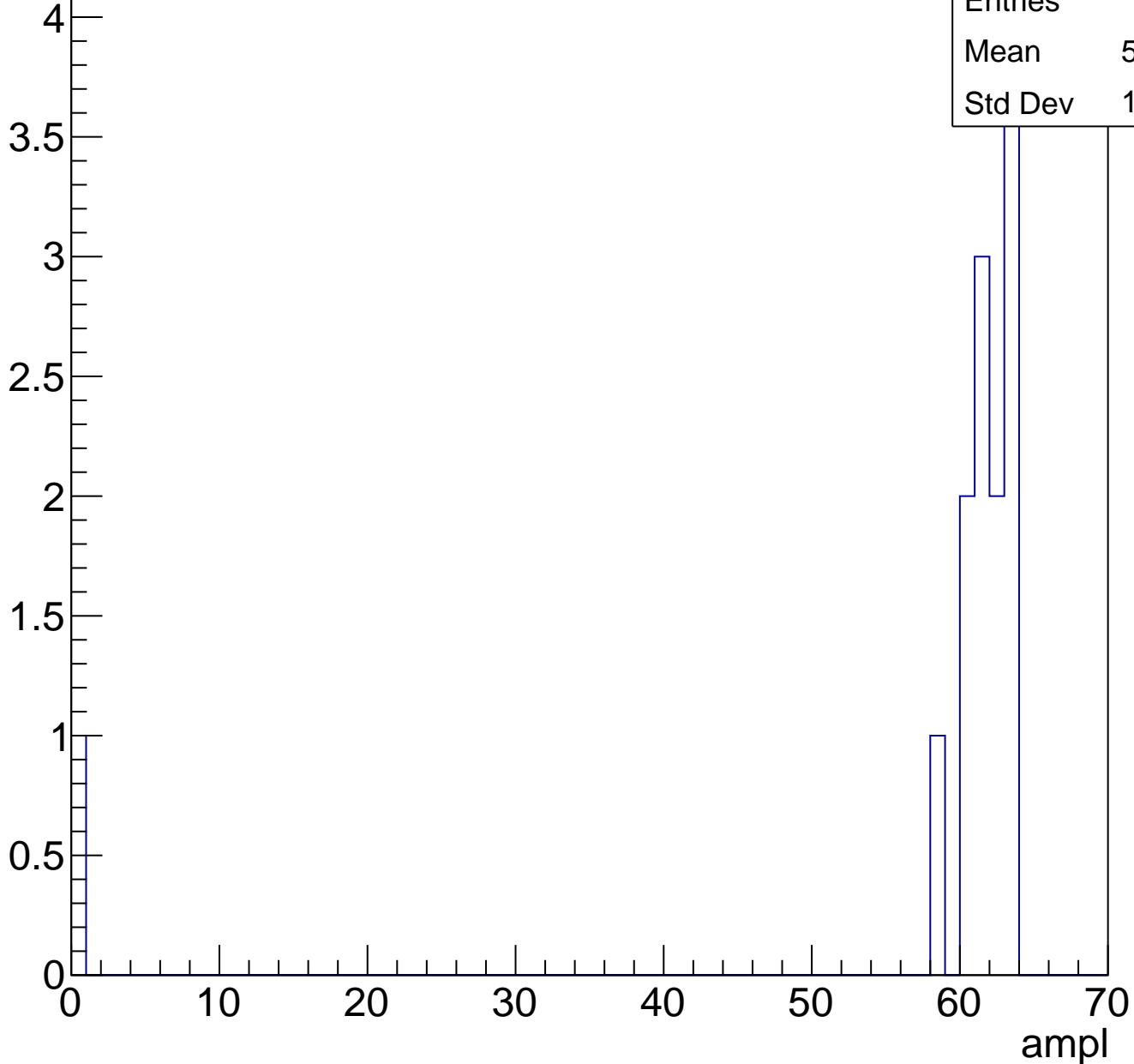
Entries	42
Mean	59.6
Std Dev	2.637



B1L100S, U19-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch126, adc0

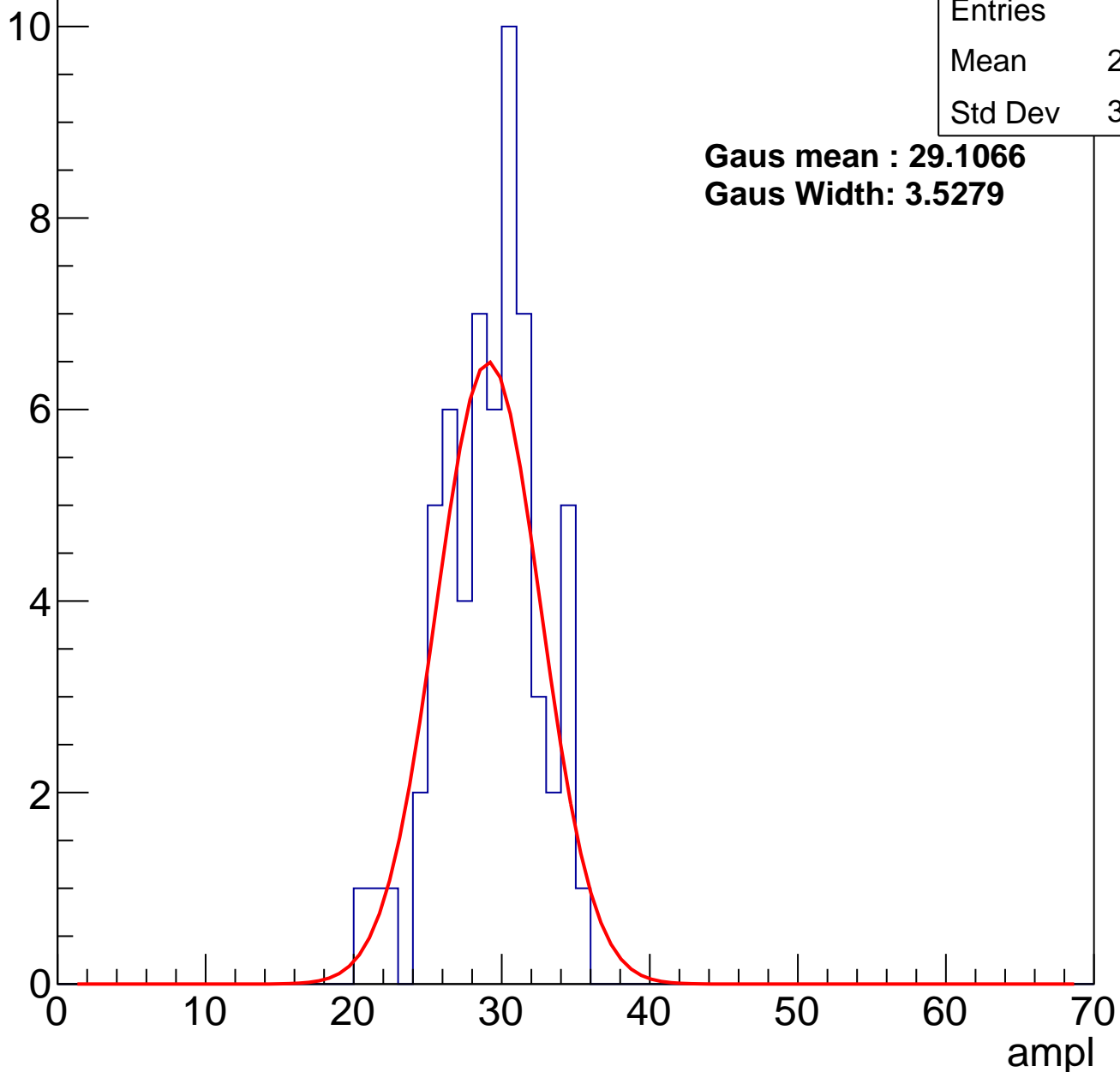
calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	28.75
Std Dev	3.278

Gaus mean : 29.1066

Gaus Width: 3.5279

Entry



B1L100S, U19-ch126, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	35.84
Std Dev	5.481

Gaus mean : 36.9588

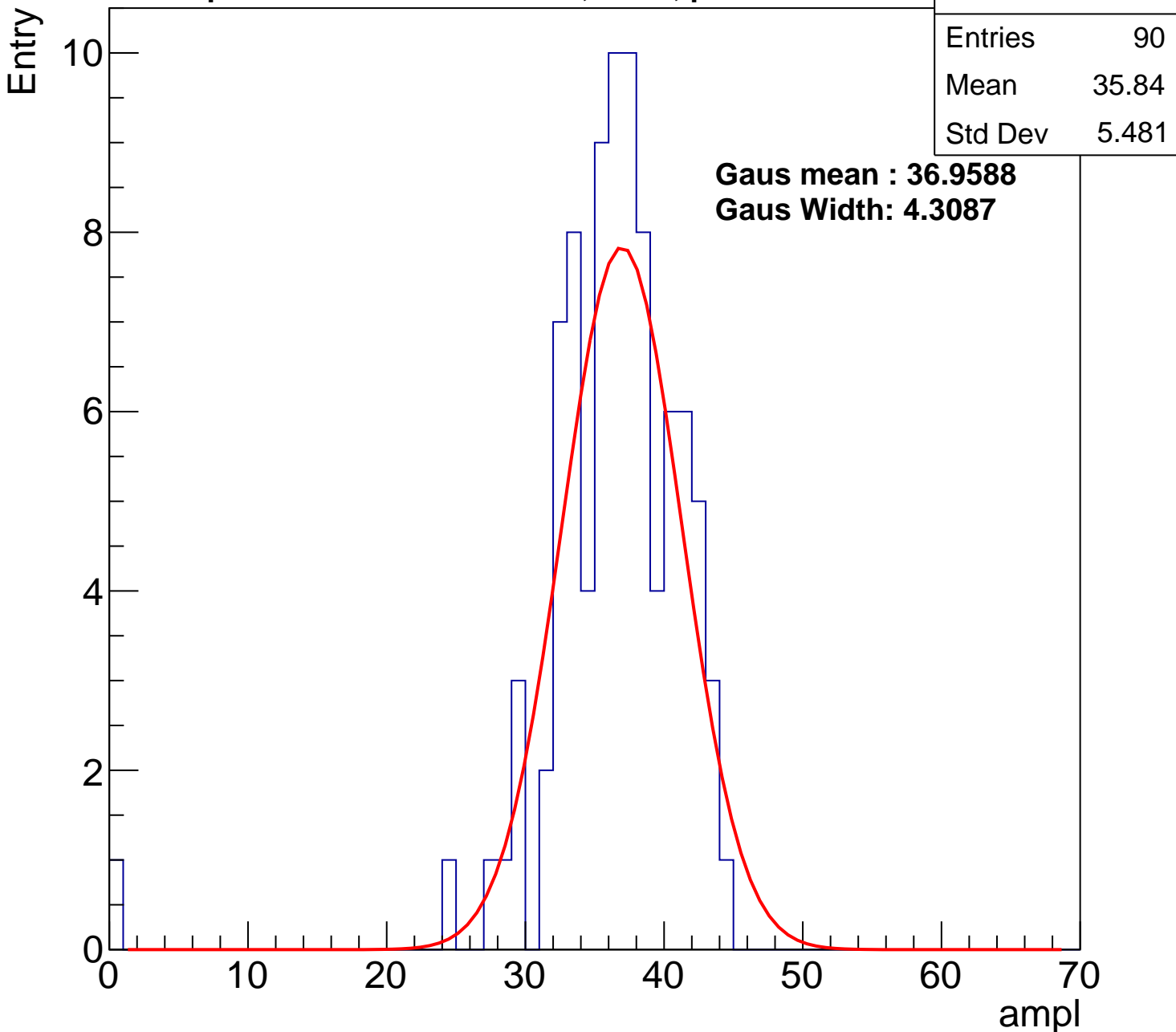
Gaus Width: 4.3087

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

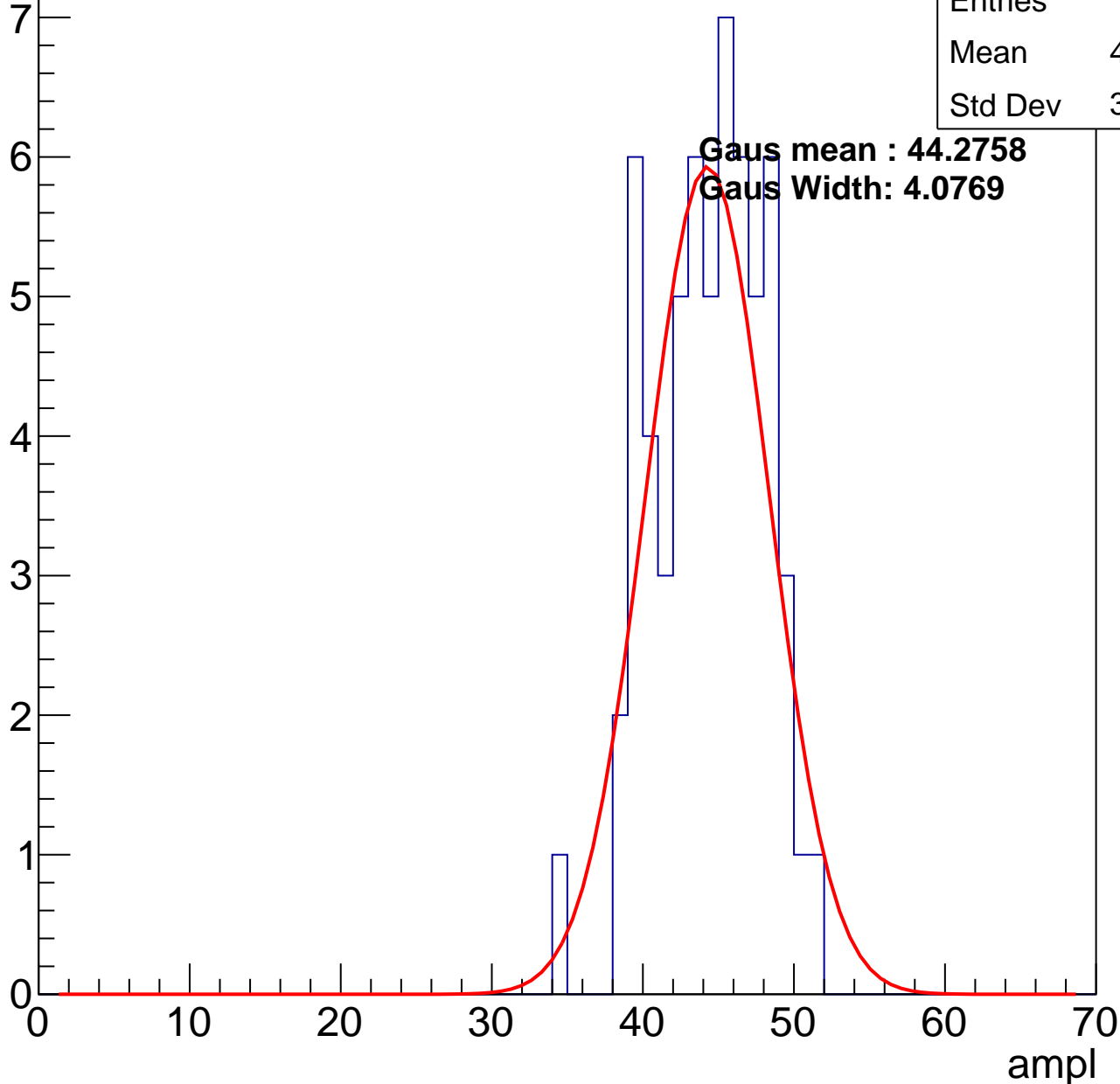


B1L100S, U19-ch126, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.89
Std Dev	3.553

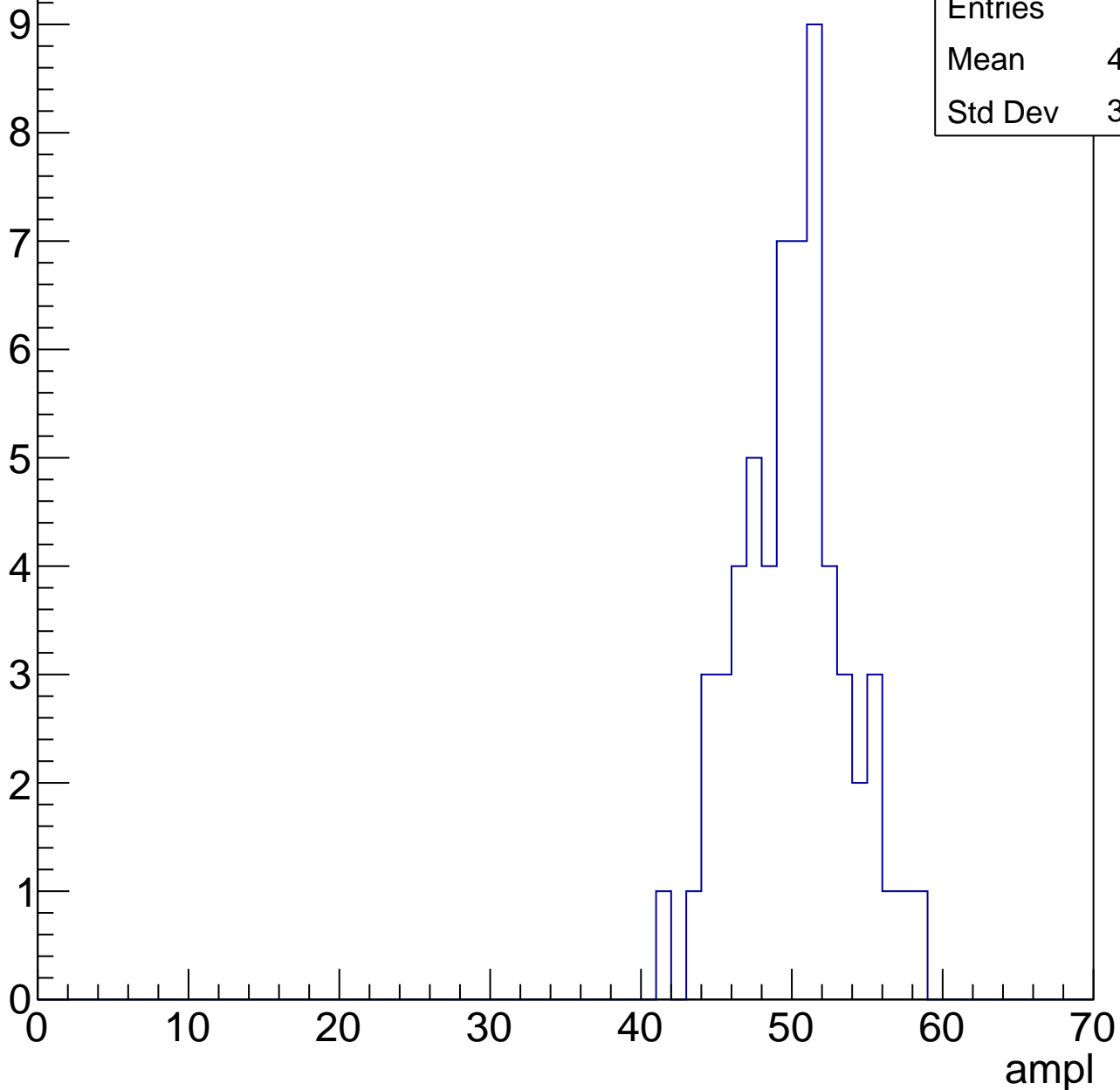


B1L100S, U19-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.58
Std Dev	3.547

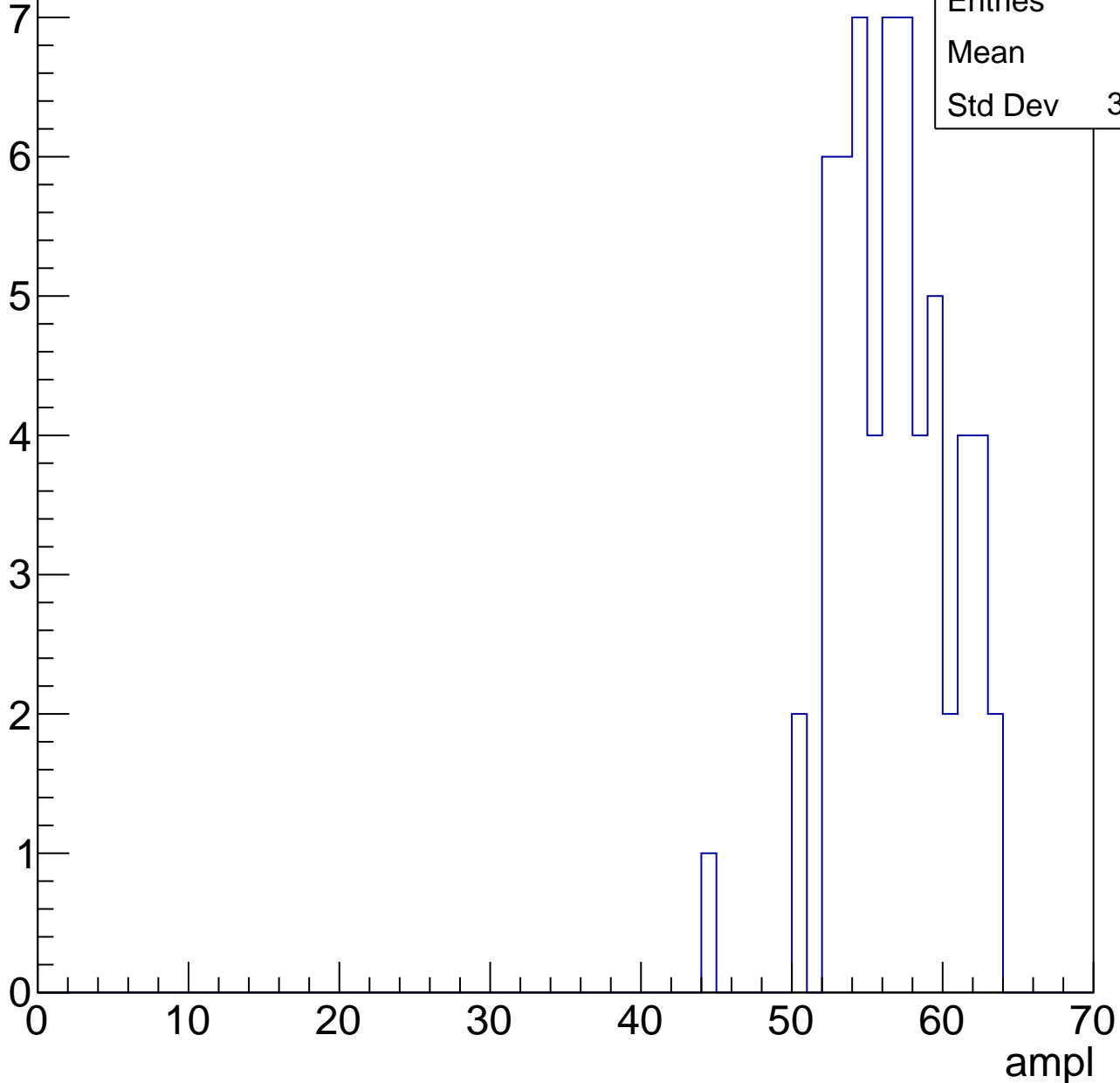


B1L100S, U19-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

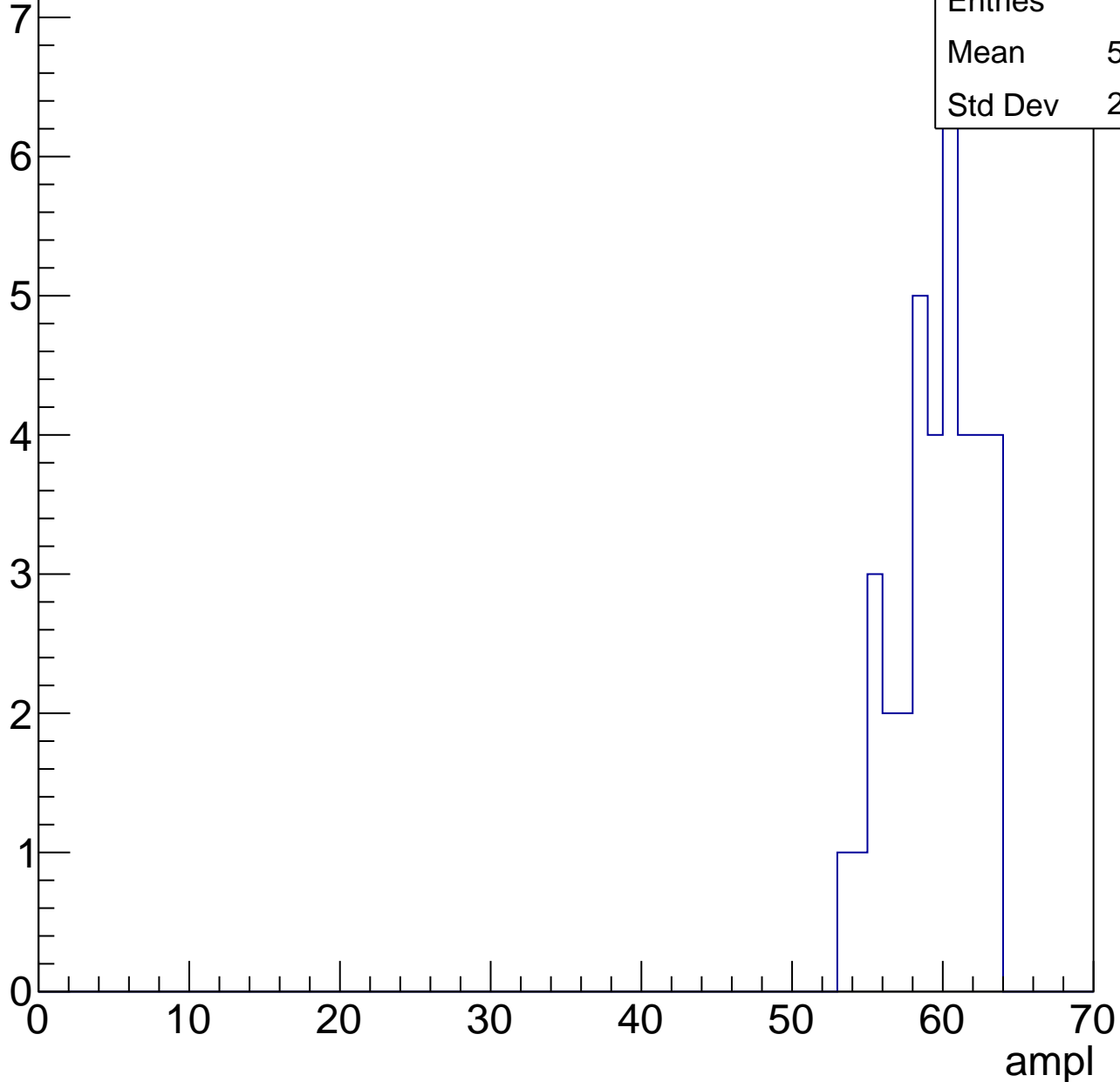
Entries	61
Mean	56.2
Std Dev	3.706



B1L100S, U19-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

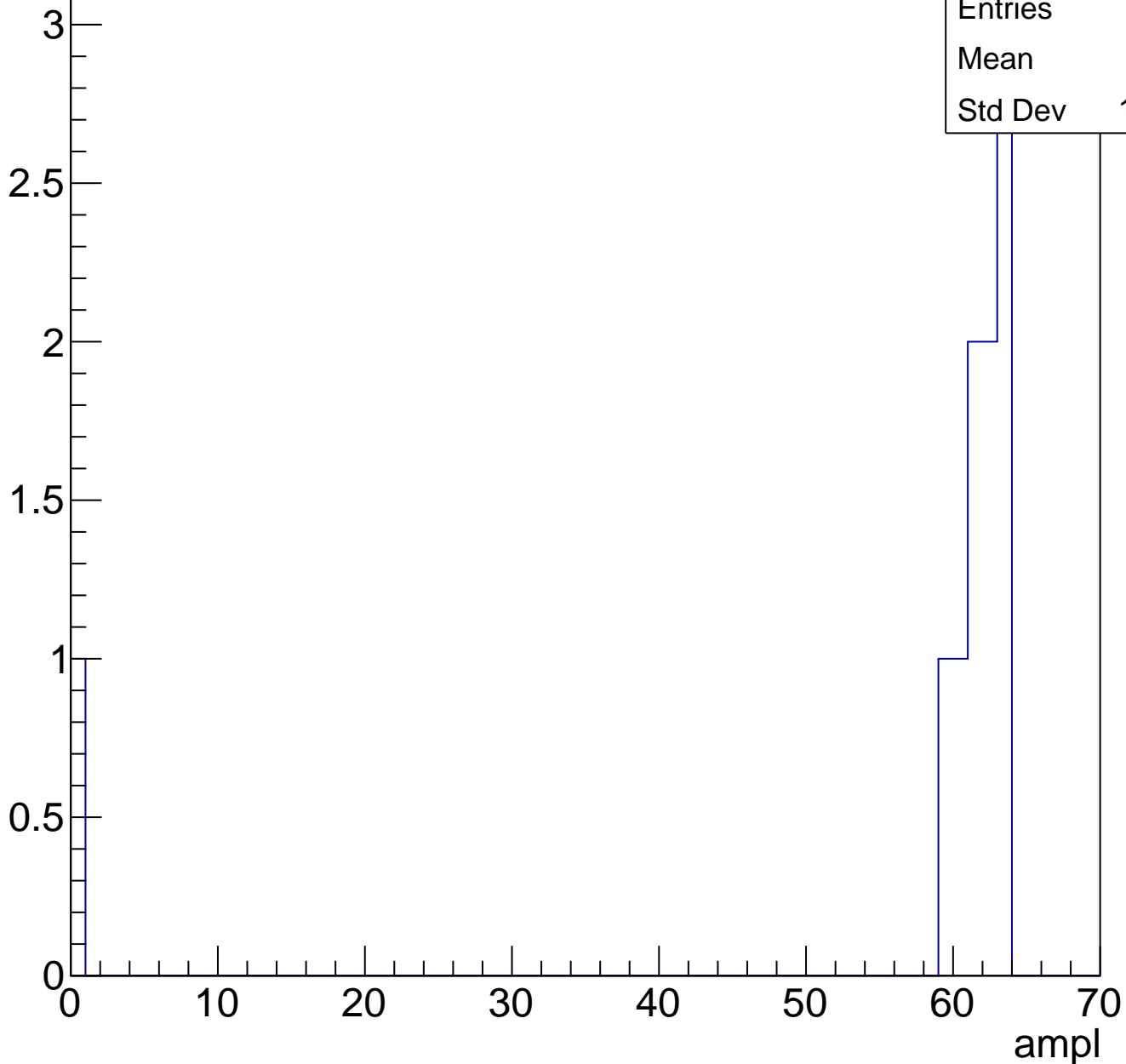
Entry



B1L100S, U19-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U19-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U19-ch127, adc0

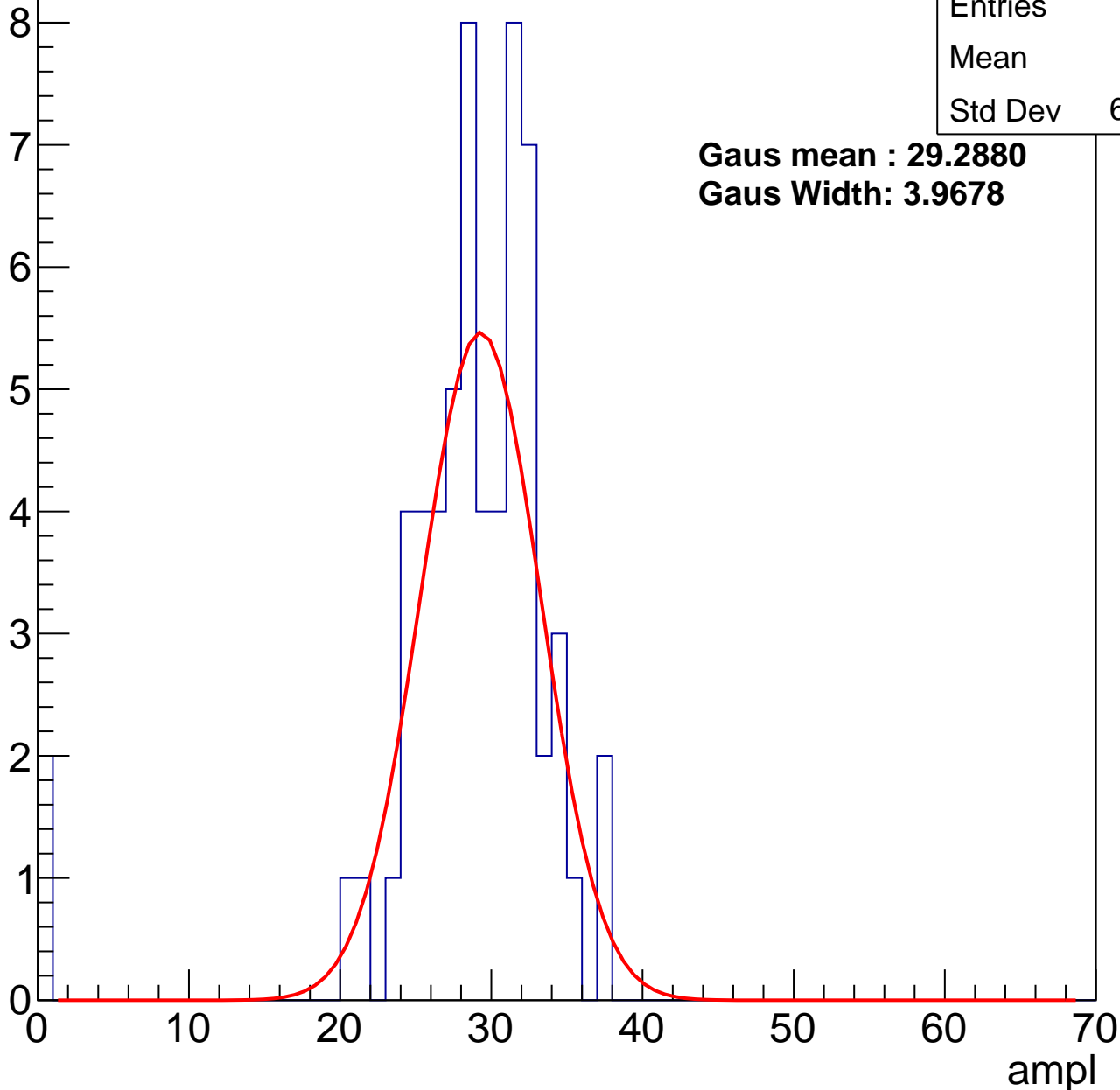
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	28
Std Dev	6.273

Gaus mean : 29.2880

Gaus Width: 3.9678



B1L100S, U19-ch127, adc1

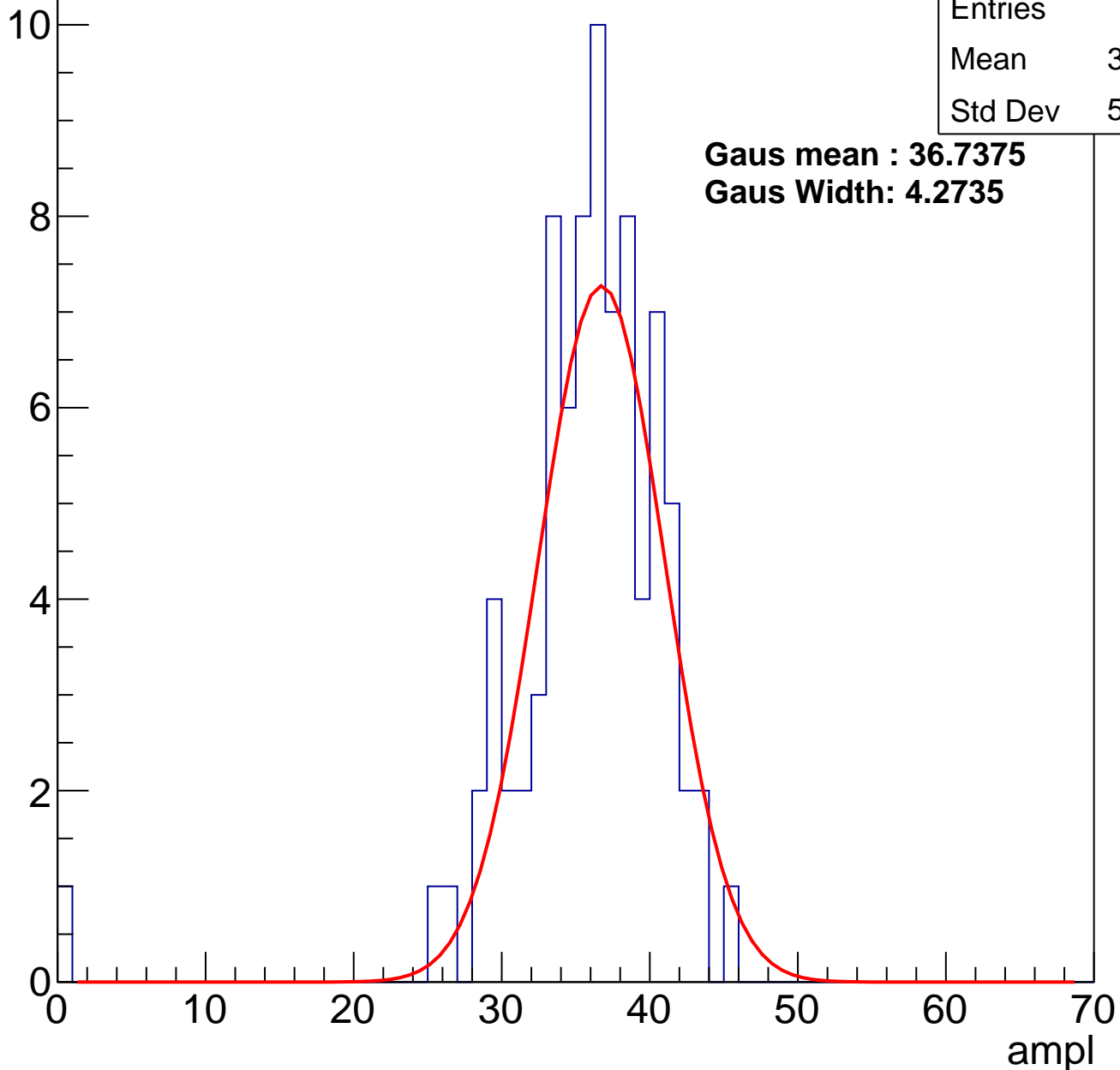
calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	35.33
Std Dev	5.607

Gaus mean : 36.7375

Gaus Width: 4.2735

Entry



B1L100S, U19-ch127, adc2

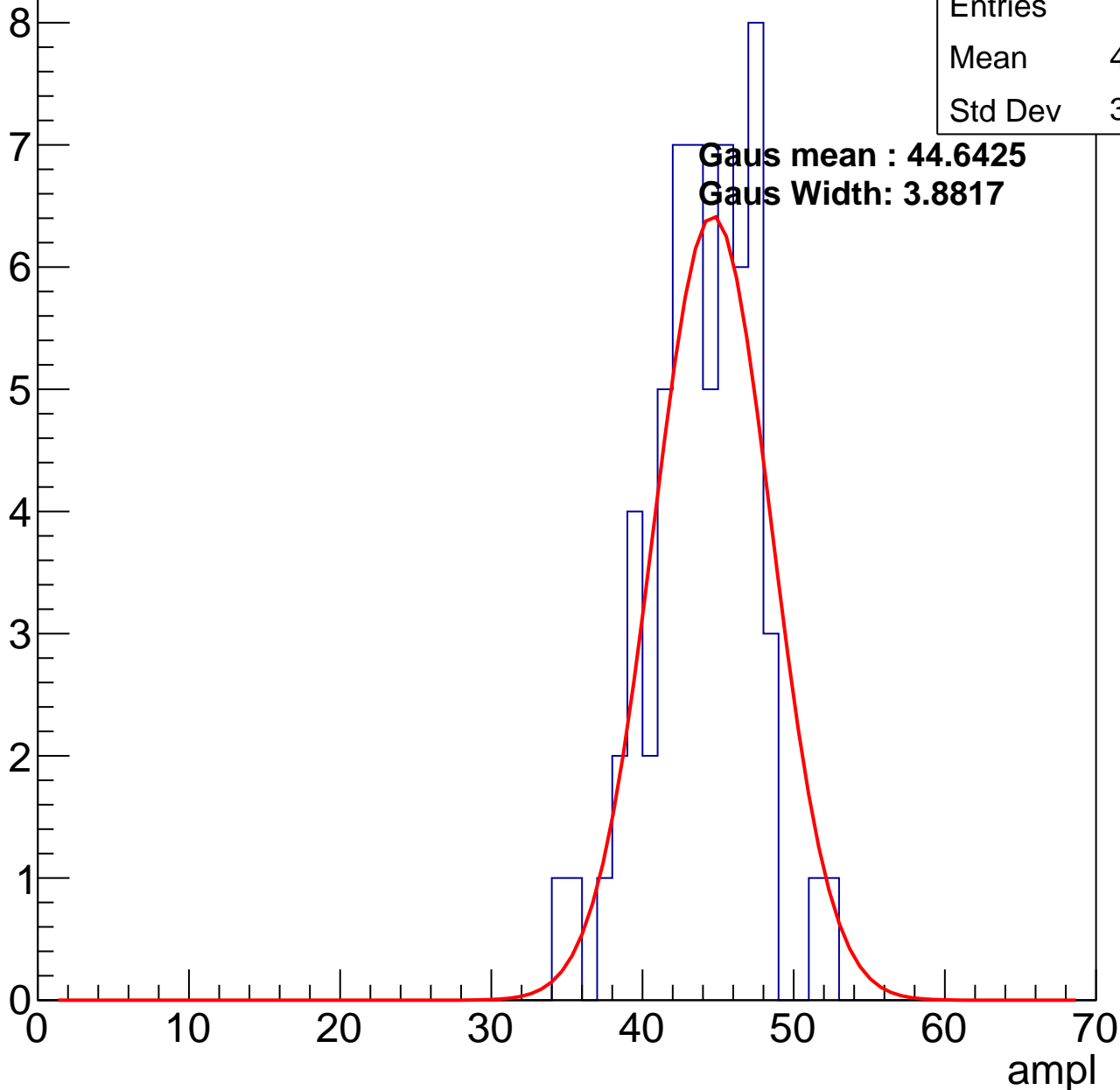
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.48
Std Dev	3.533

Gaus mean : 44.6425

Gaus Width: 3.8817

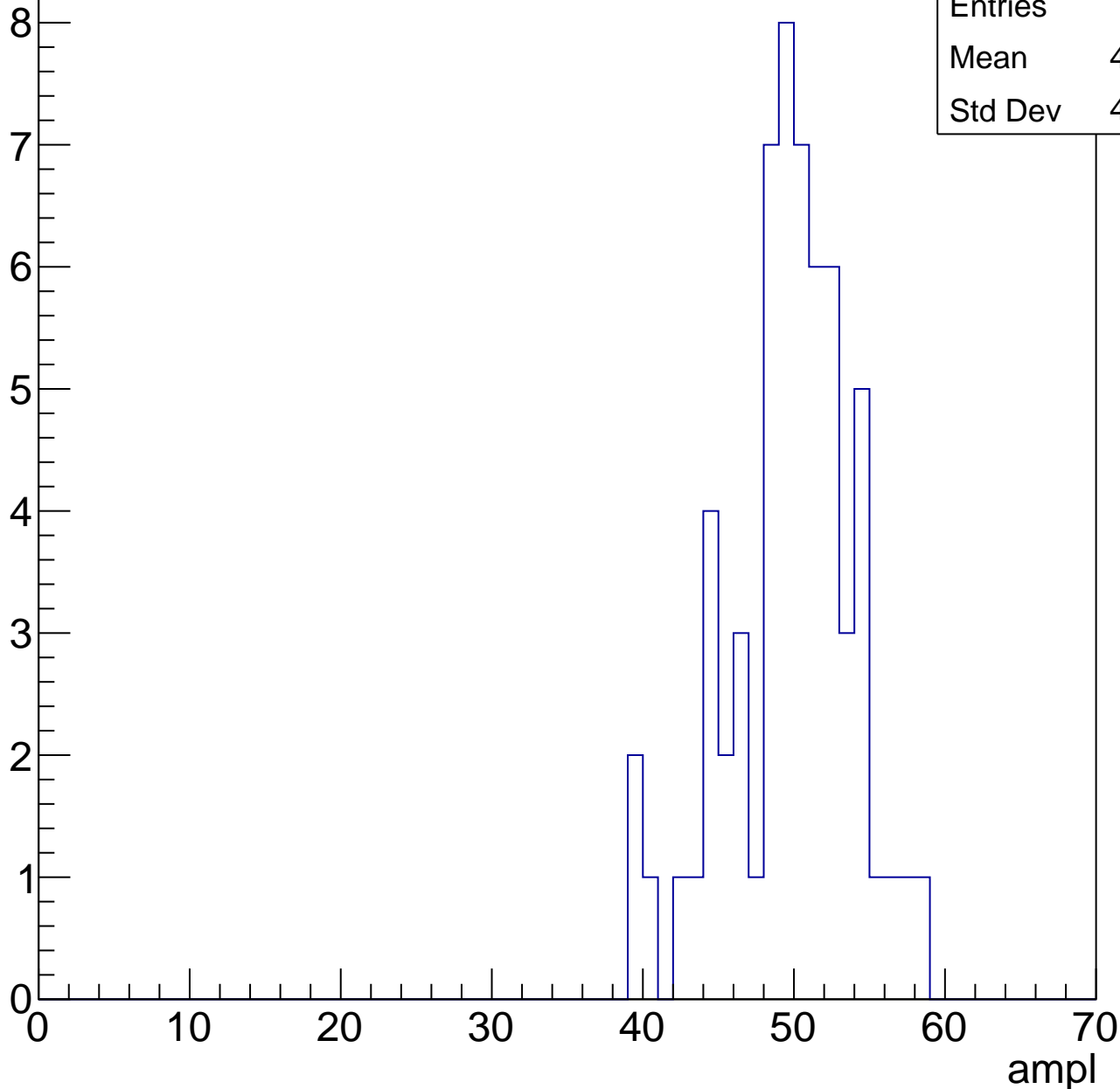


B1L100S, U19-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.26
Std Dev	4.089

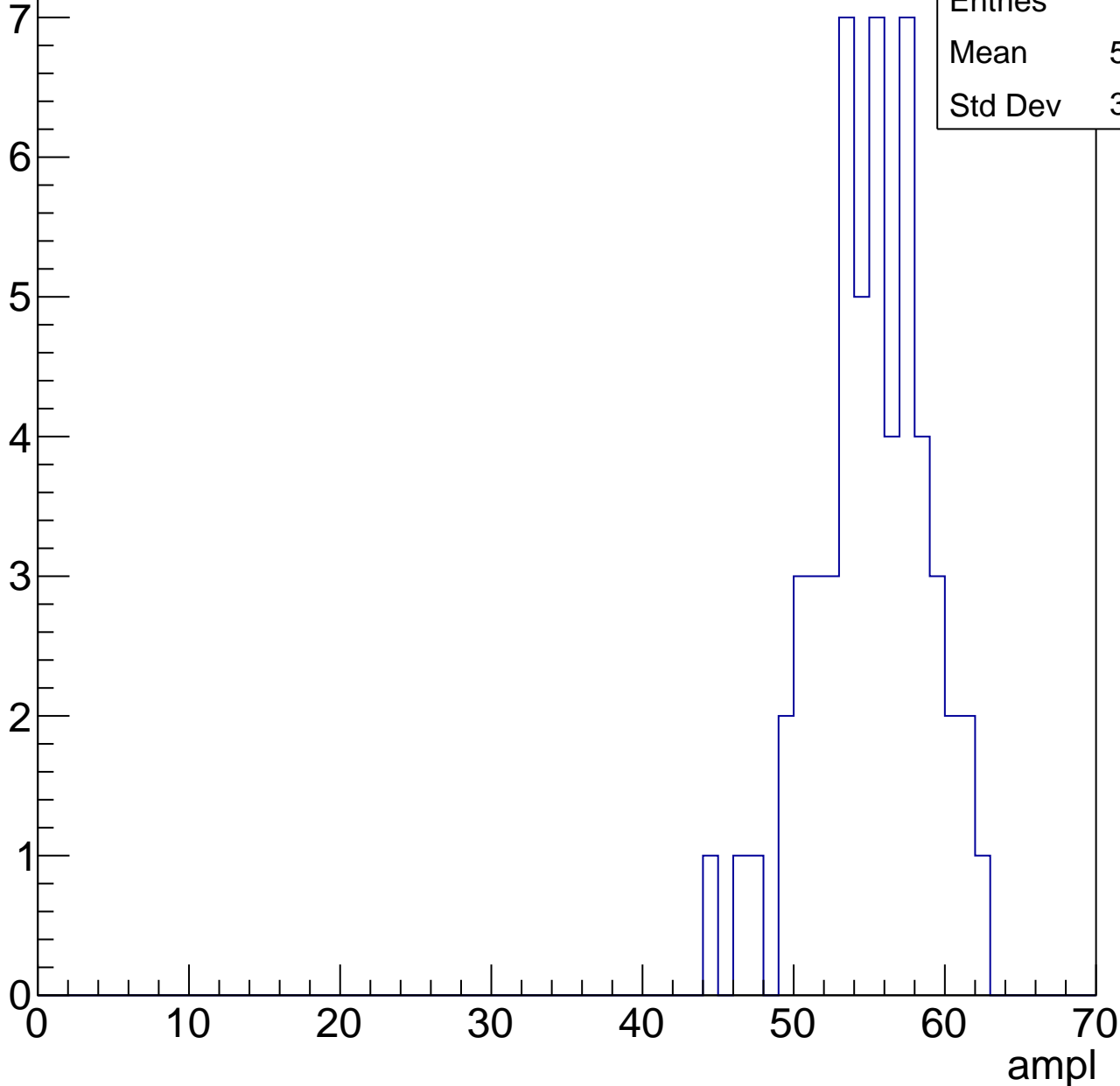


B1L100S, U19-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	54.57
Std Dev	3.774

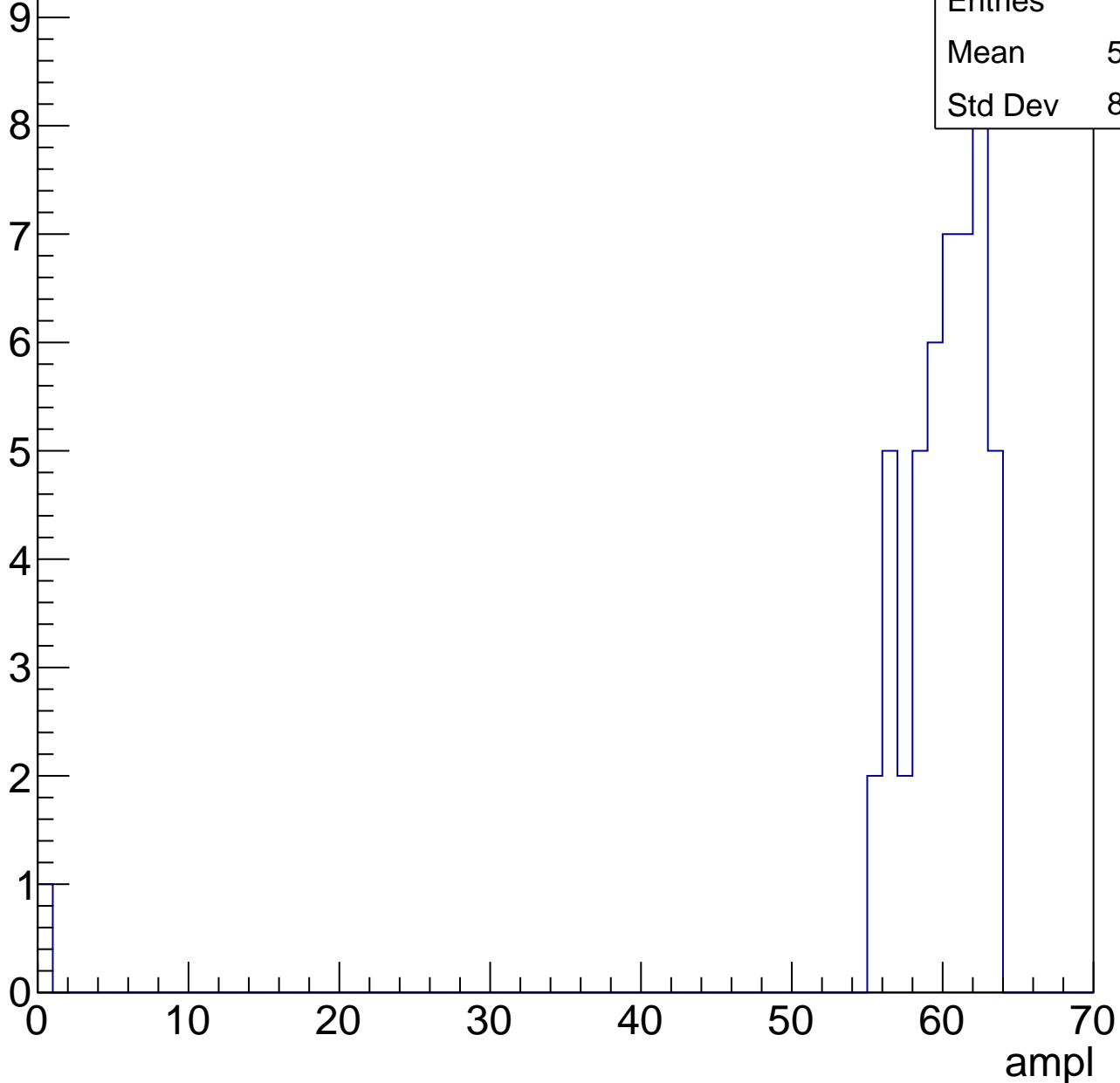


B1L100S, U19-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.53
Std Dev	8.755

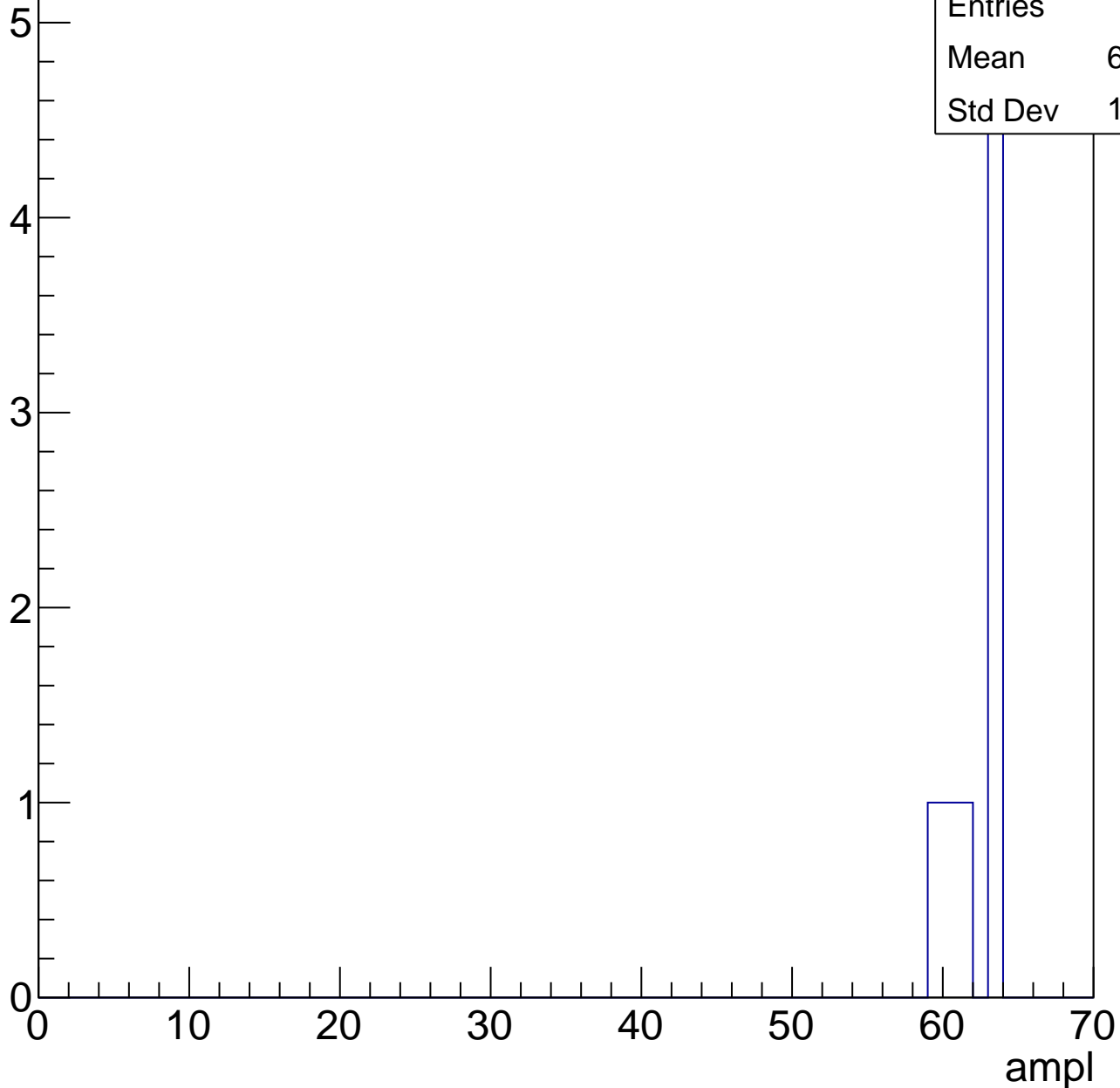


B1L100S, U19-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	8
Mean	61.88
Std Dev	1.536



B1L100S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

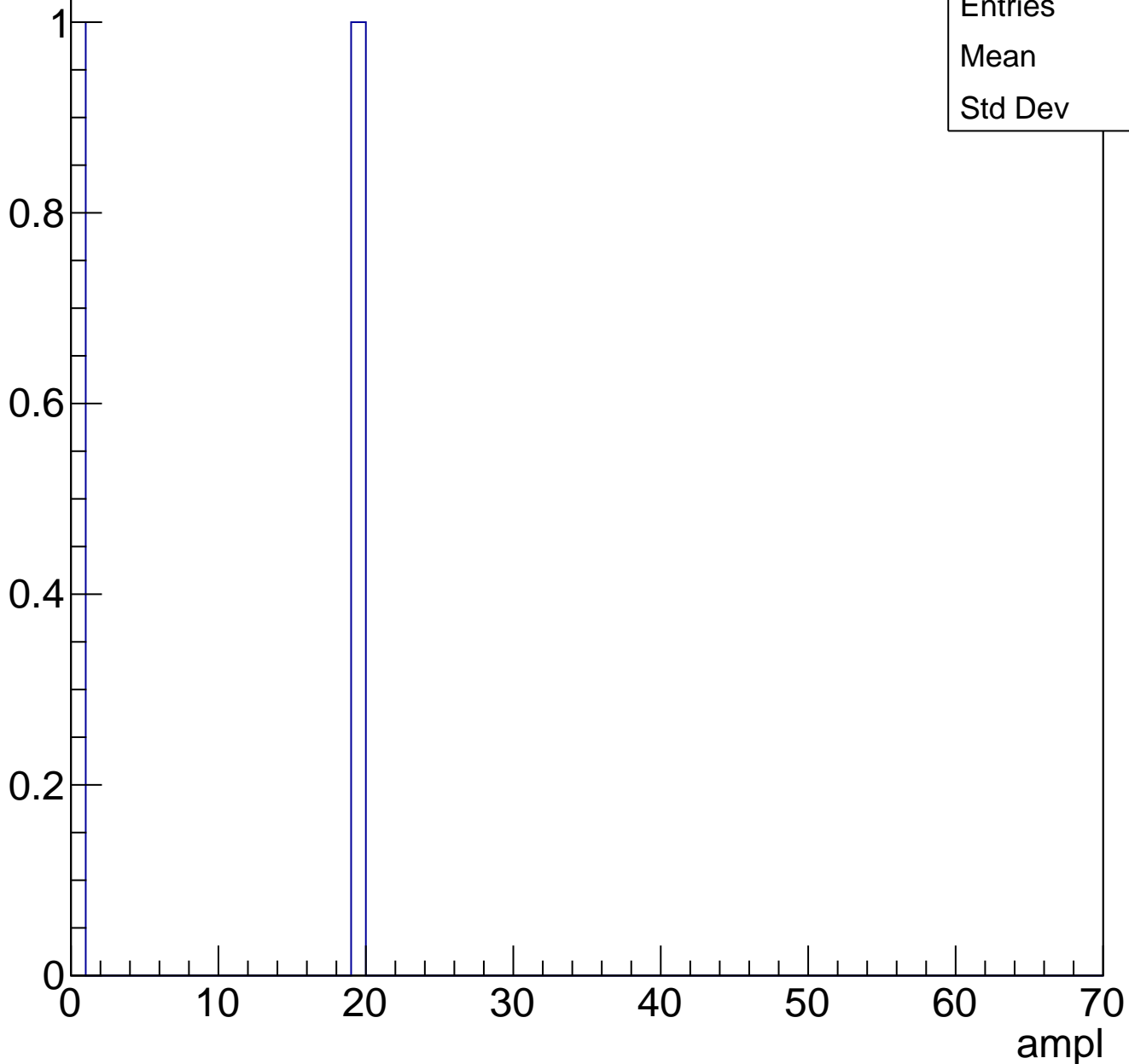


Entries	2
Mean	9.5
Std Dev	9.5

B1L100S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5