

B1L003S, U20-ch0, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	30.55
Std Dev	4.614

Gaus mean : 31.5531

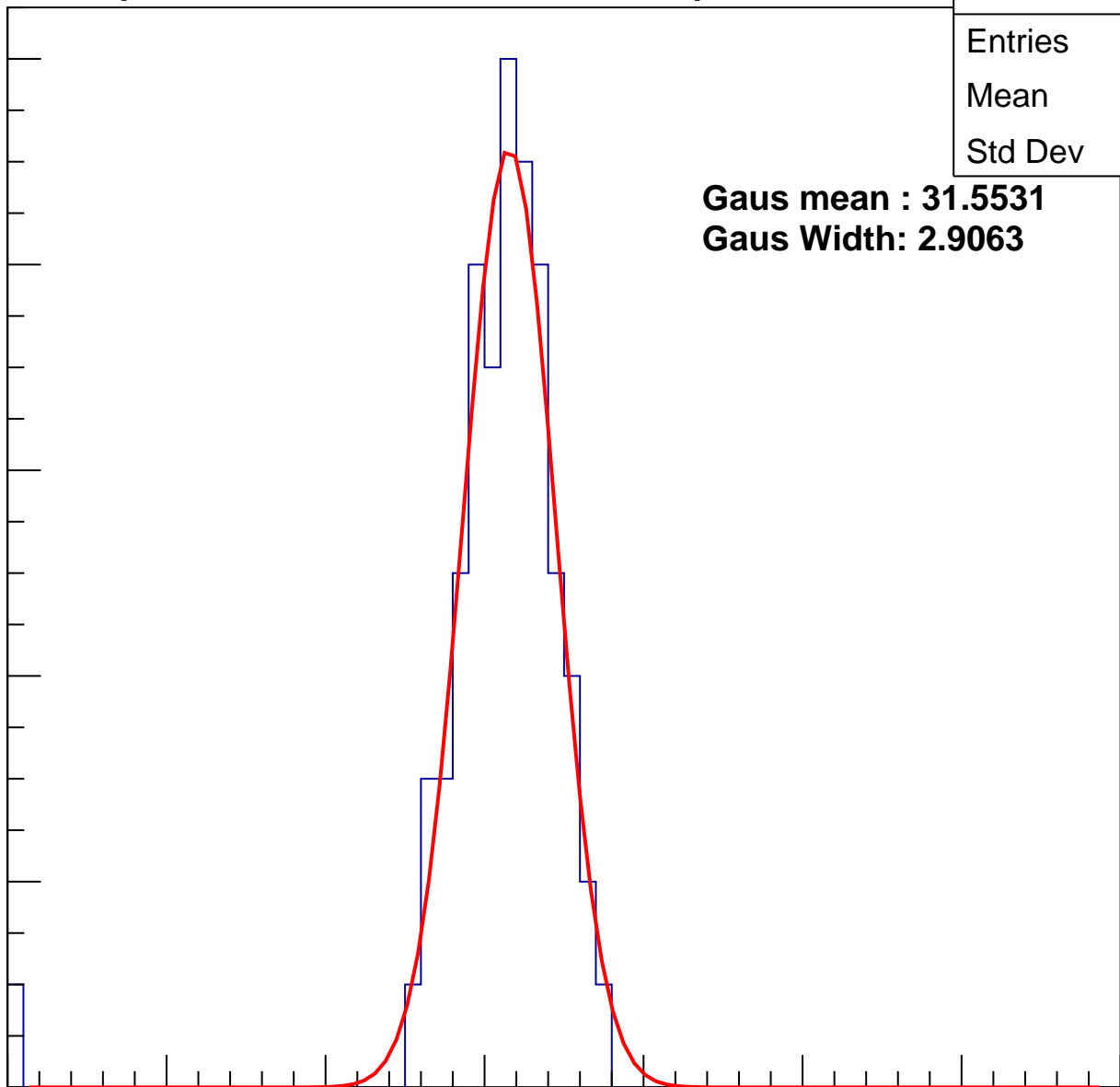
Gaus Width: 2.9063

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch0, adc1

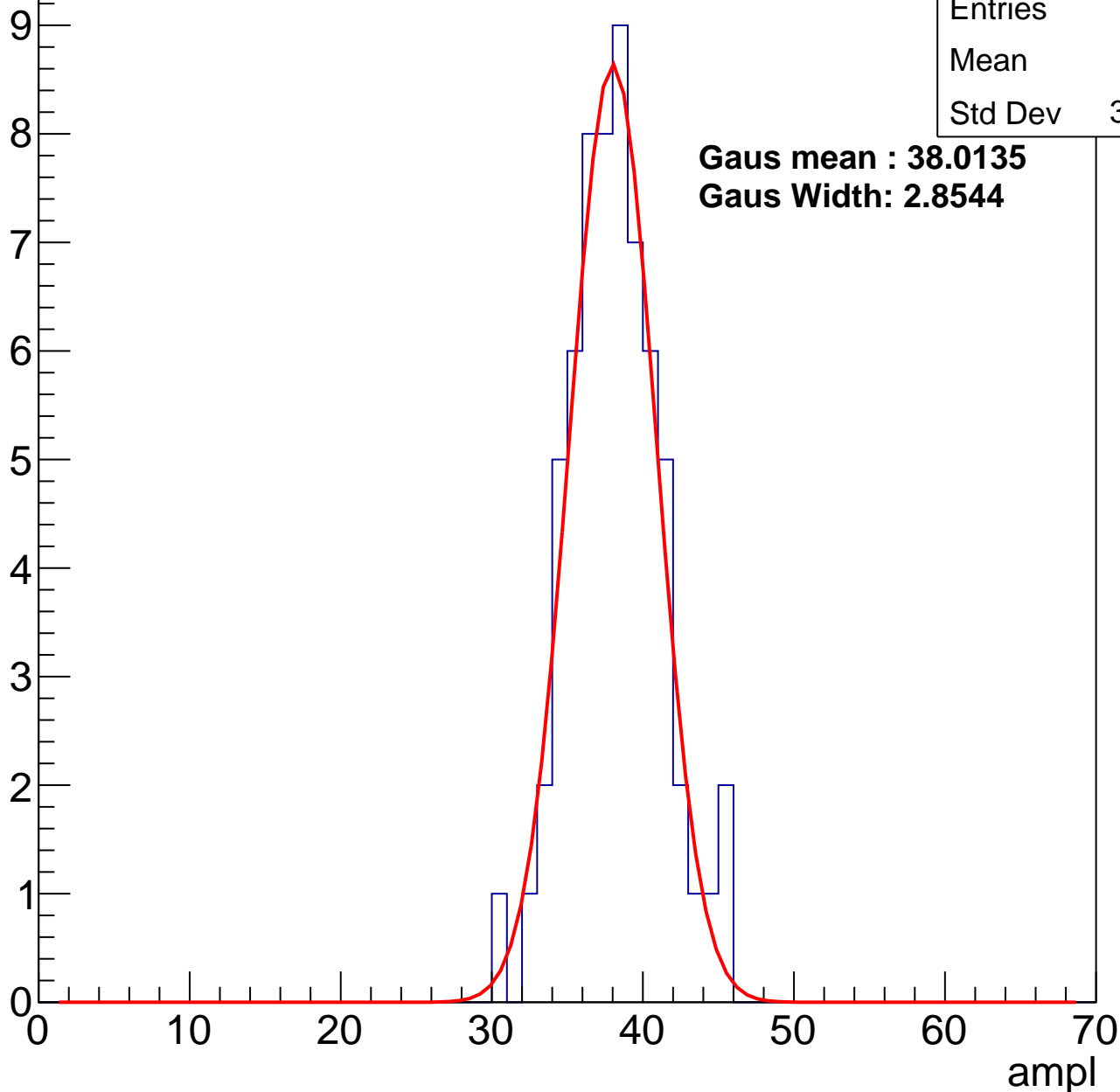
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.7
Std Dev	3.024

Gaus mean : 38.0135

Gaus Width: 2.8544



B1L003S, U20-ch0, adc2

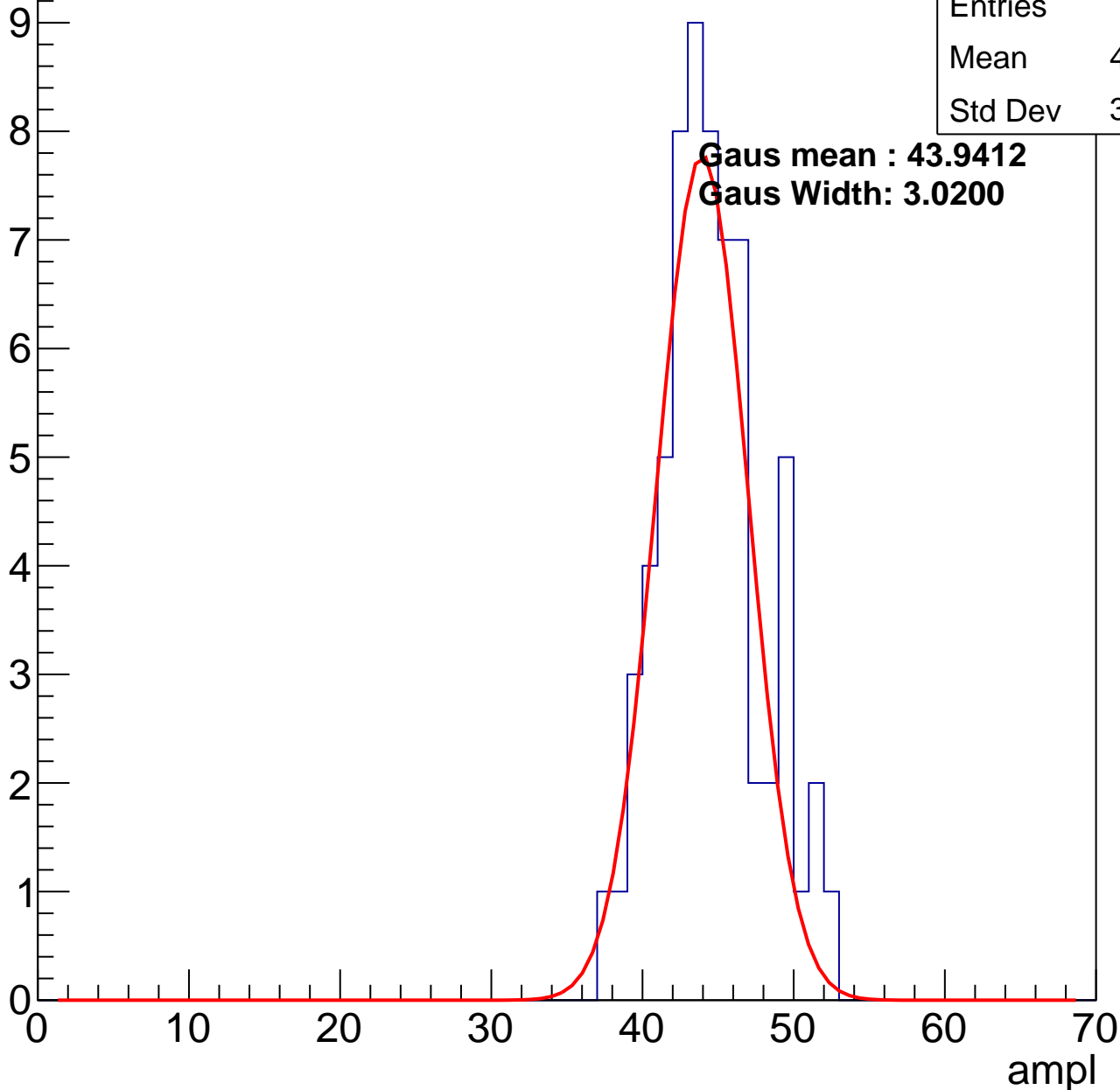
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.06
Std Dev	3.298

Gaus mean : 43.9412

Gaus Width: 3.0200

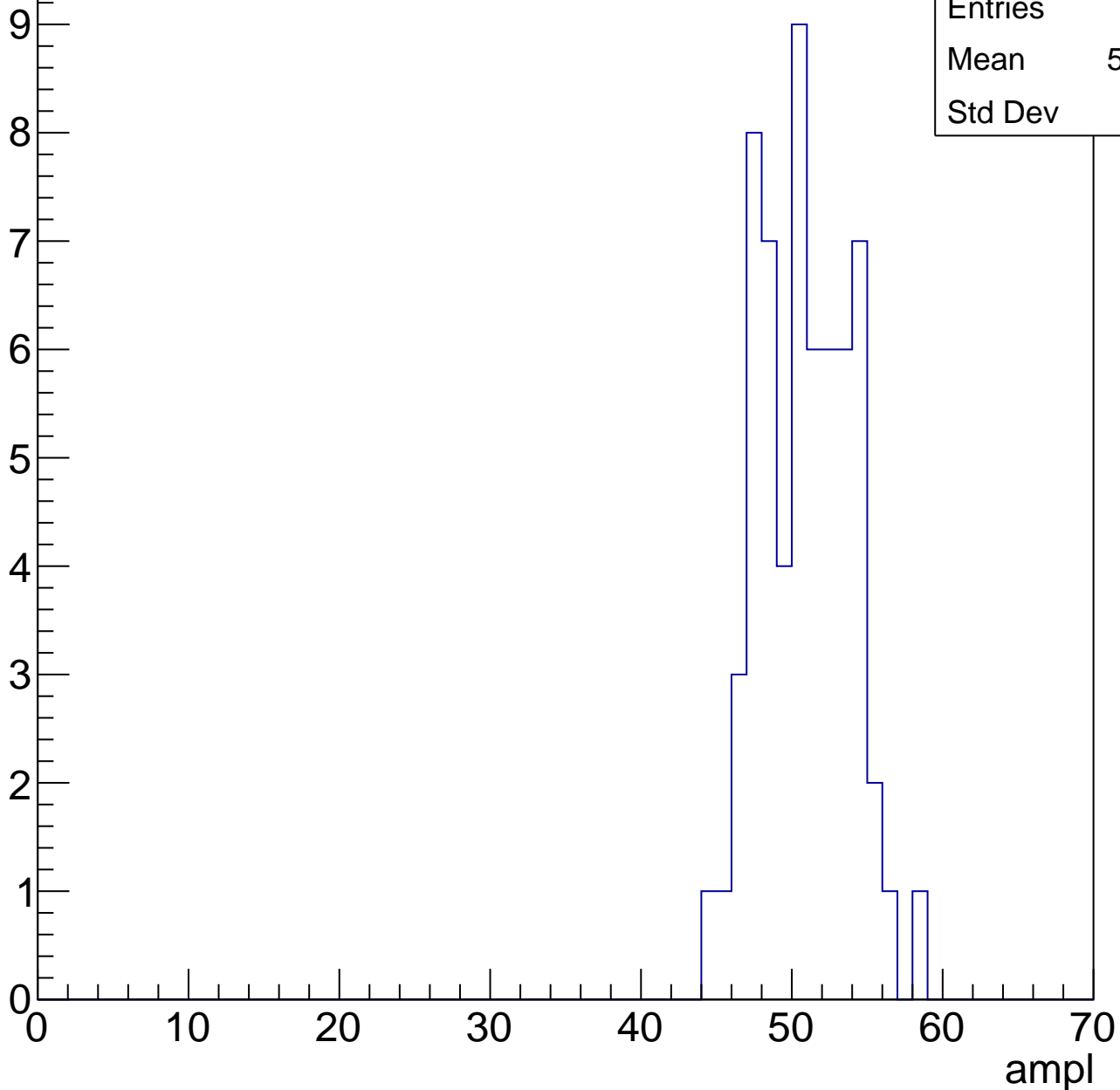


B1L003S, U20-ch0, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	50.37
Std Dev	2.98

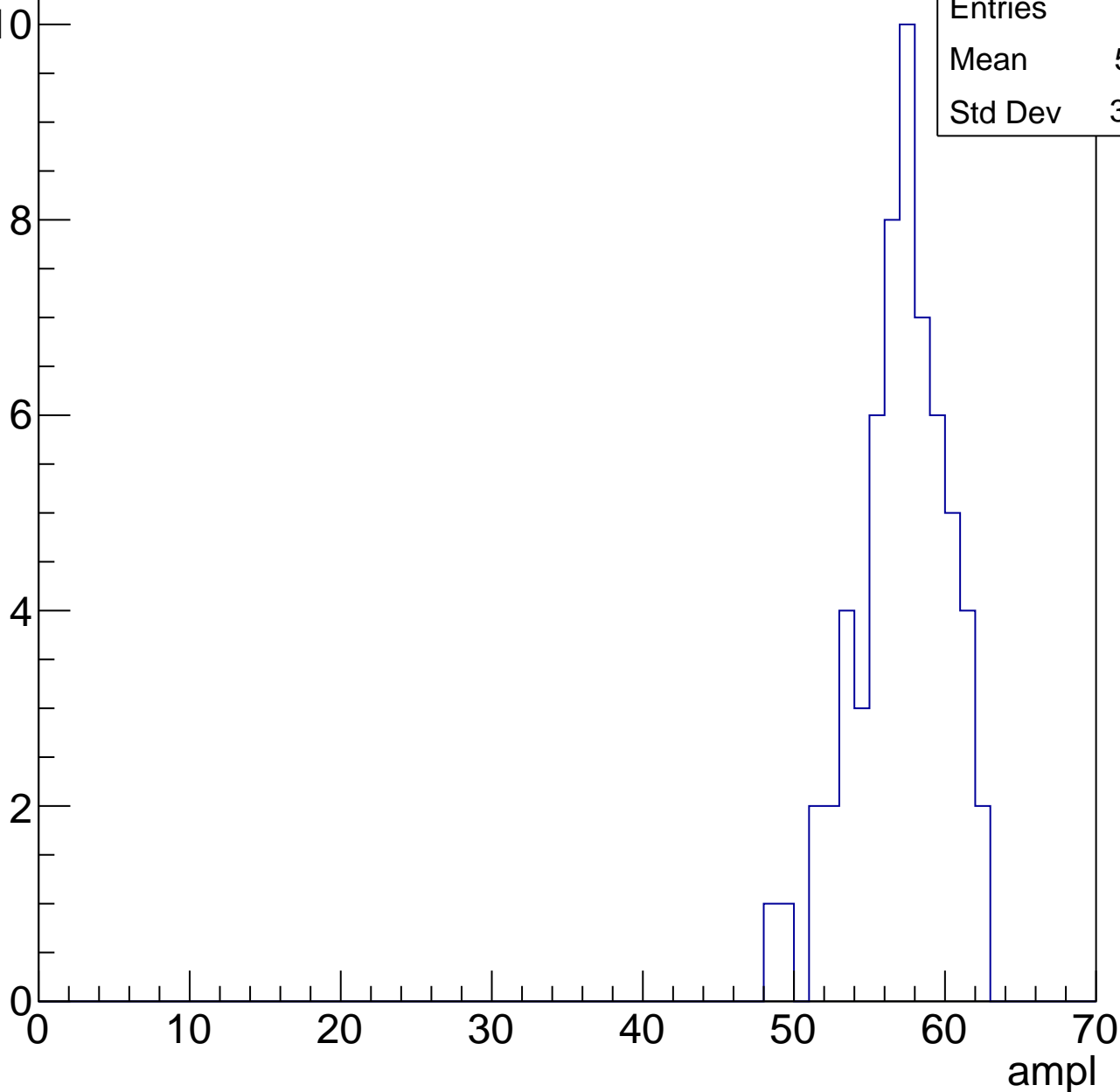


B1L003S, U20-ch0, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	56.61
Std Dev	3.058

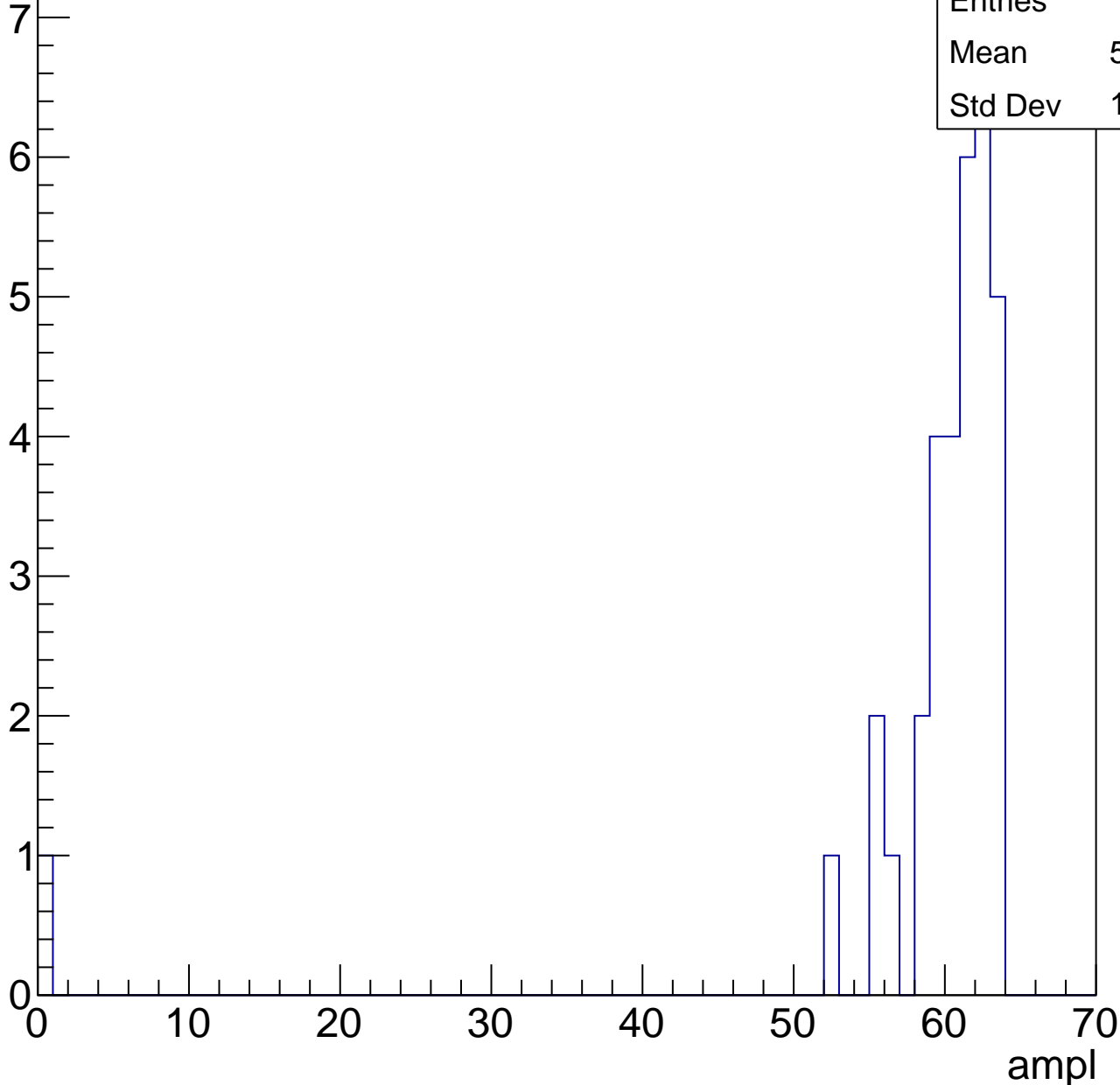


B1L003S, U20-ch0, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.33
Std Dev	10.63

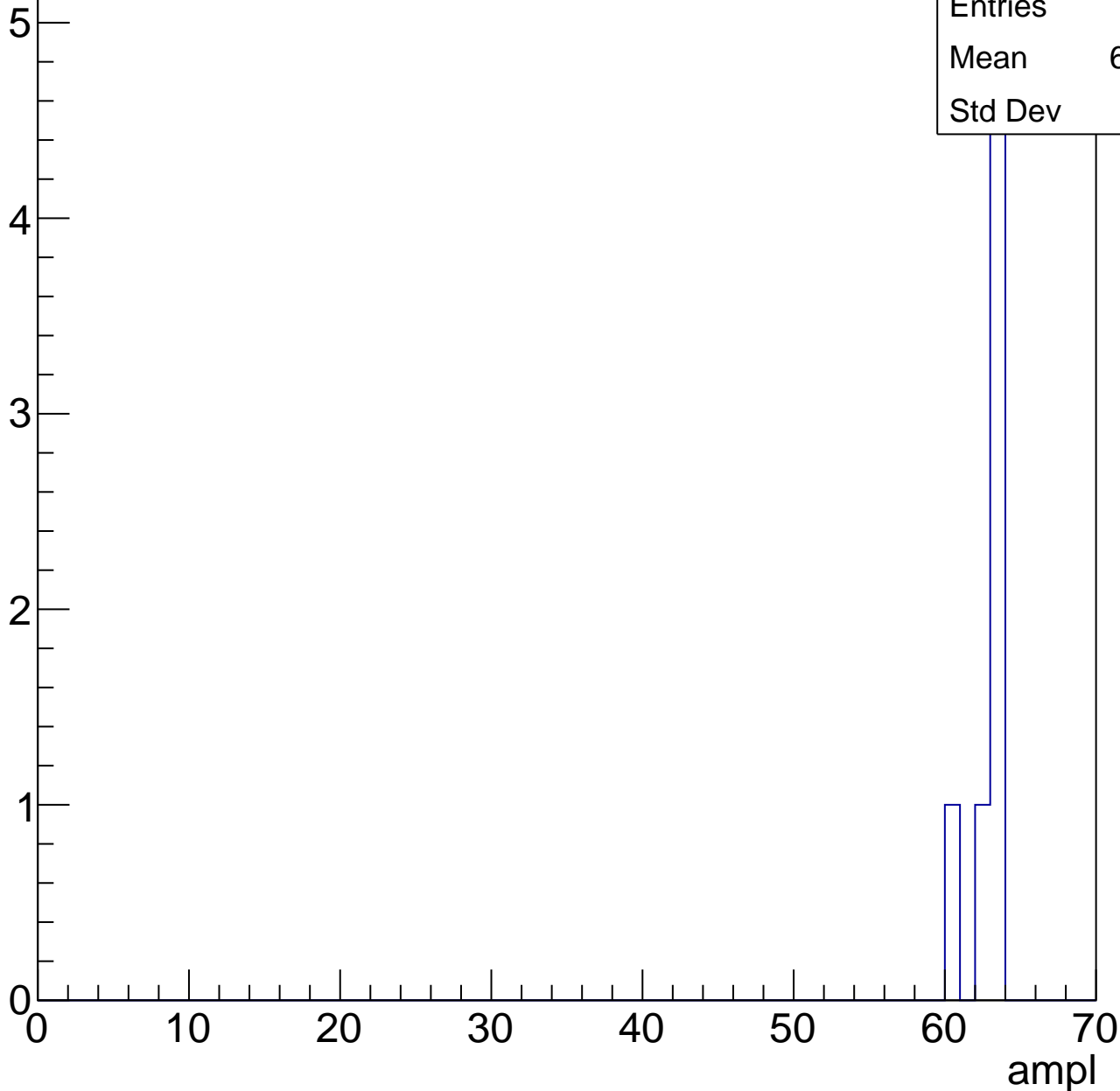


B1L003S, U20-ch0, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	7
Mean	62.43
Std Dev	1.05



B1L003S, U20-ch0, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch1, adc0

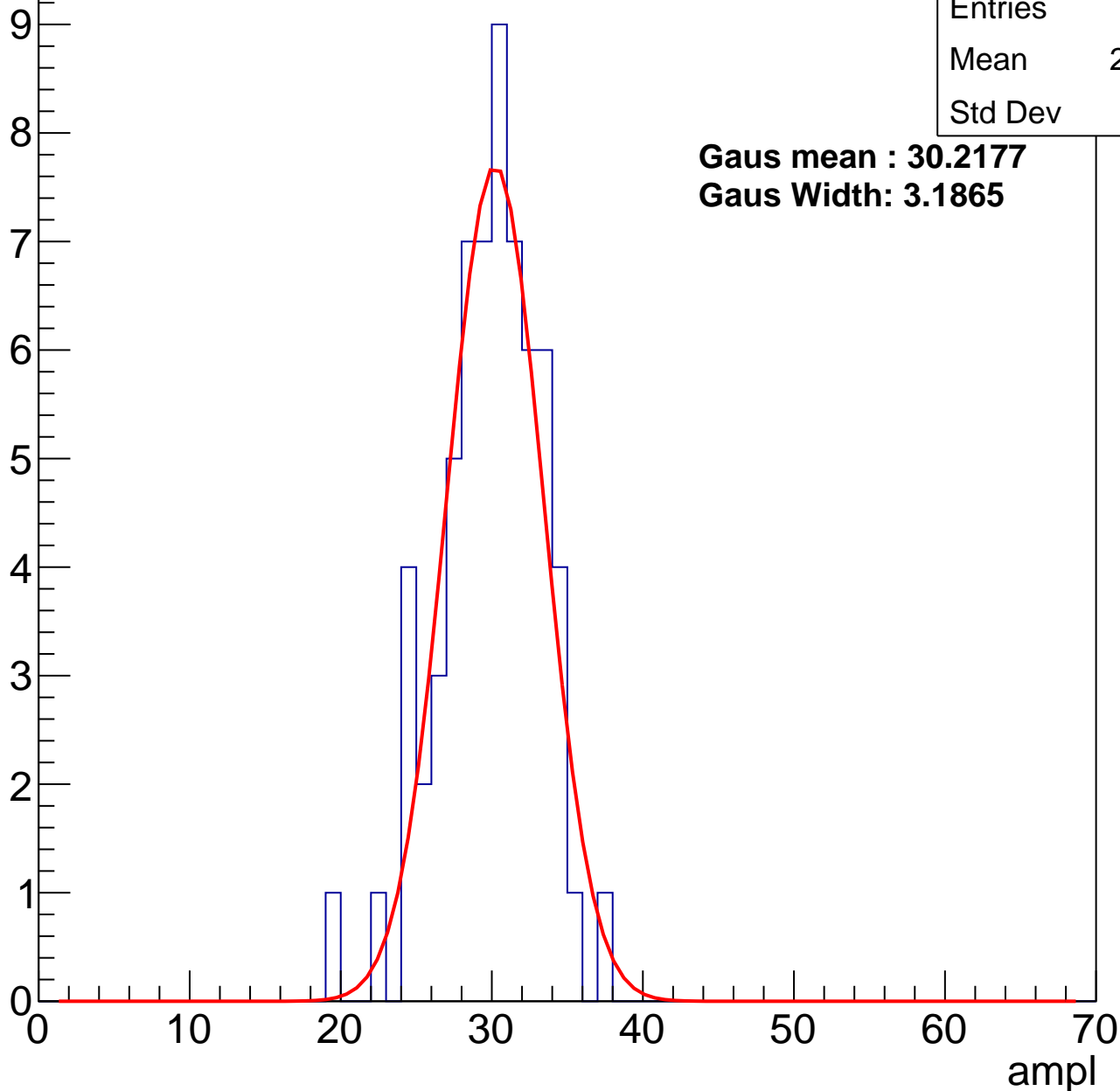
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.44
Std Dev	3.33

Gaus mean : 30.2177

Gaus Width: 3.1865



B1L003S, U20-ch1, adc1

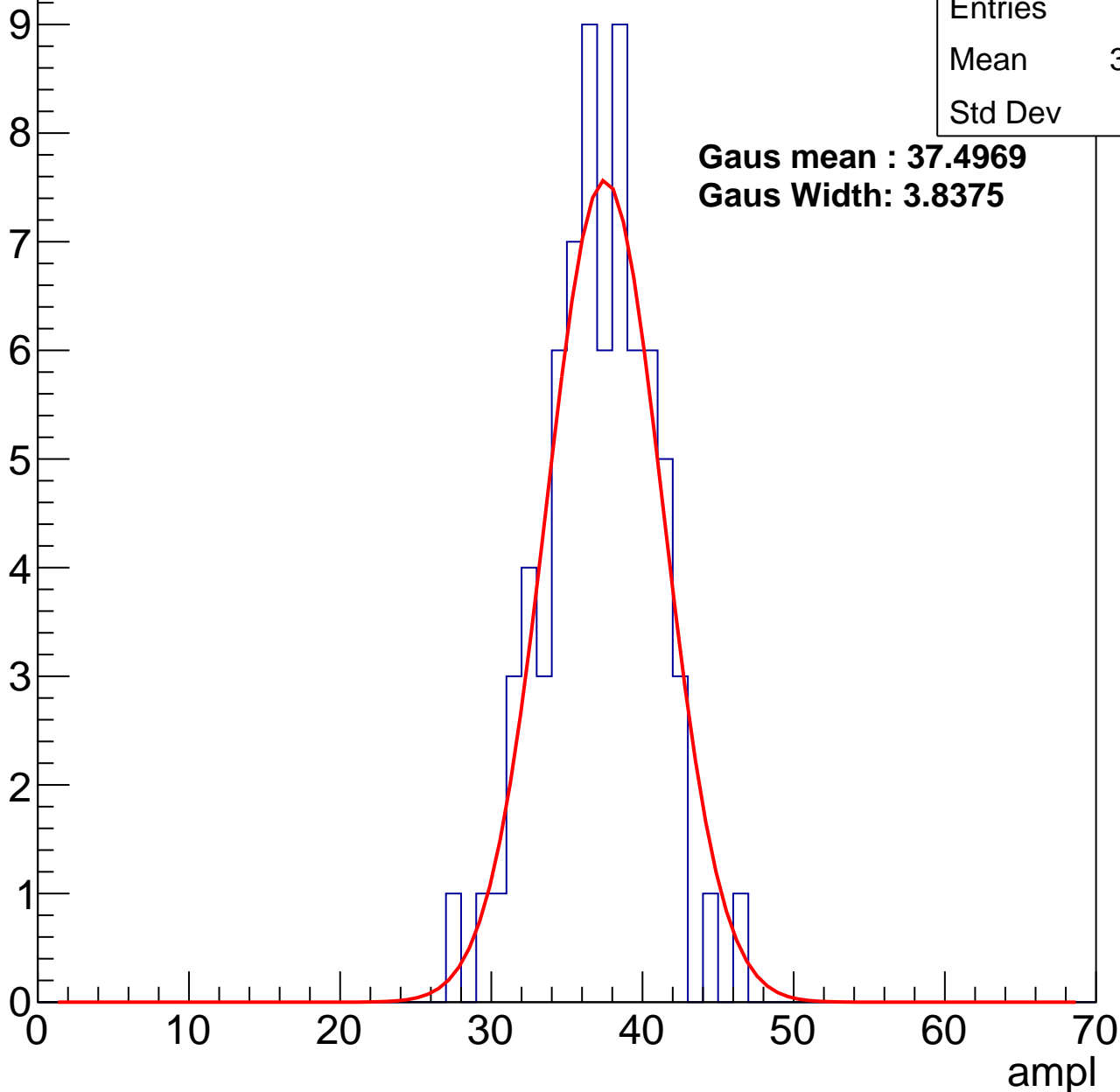
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.64
Std Dev	3.58

Gaus mean : 37.4969

Gaus Width: 3.8375



B1L003S, U20-ch1, adc2

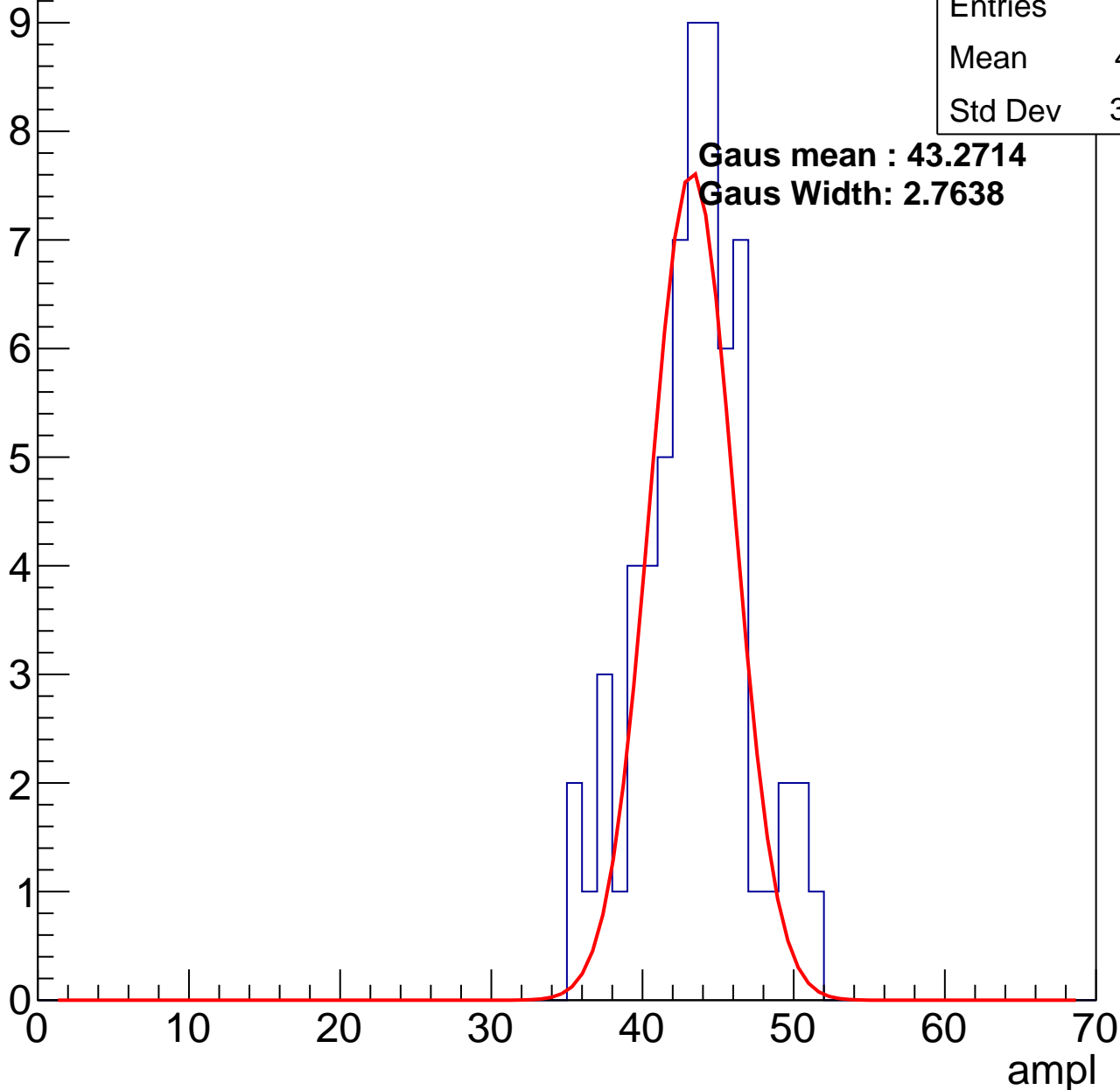
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.91
Std Dev	3.525

Gaus mean : 43.2714

Gaus Width: 2.7638

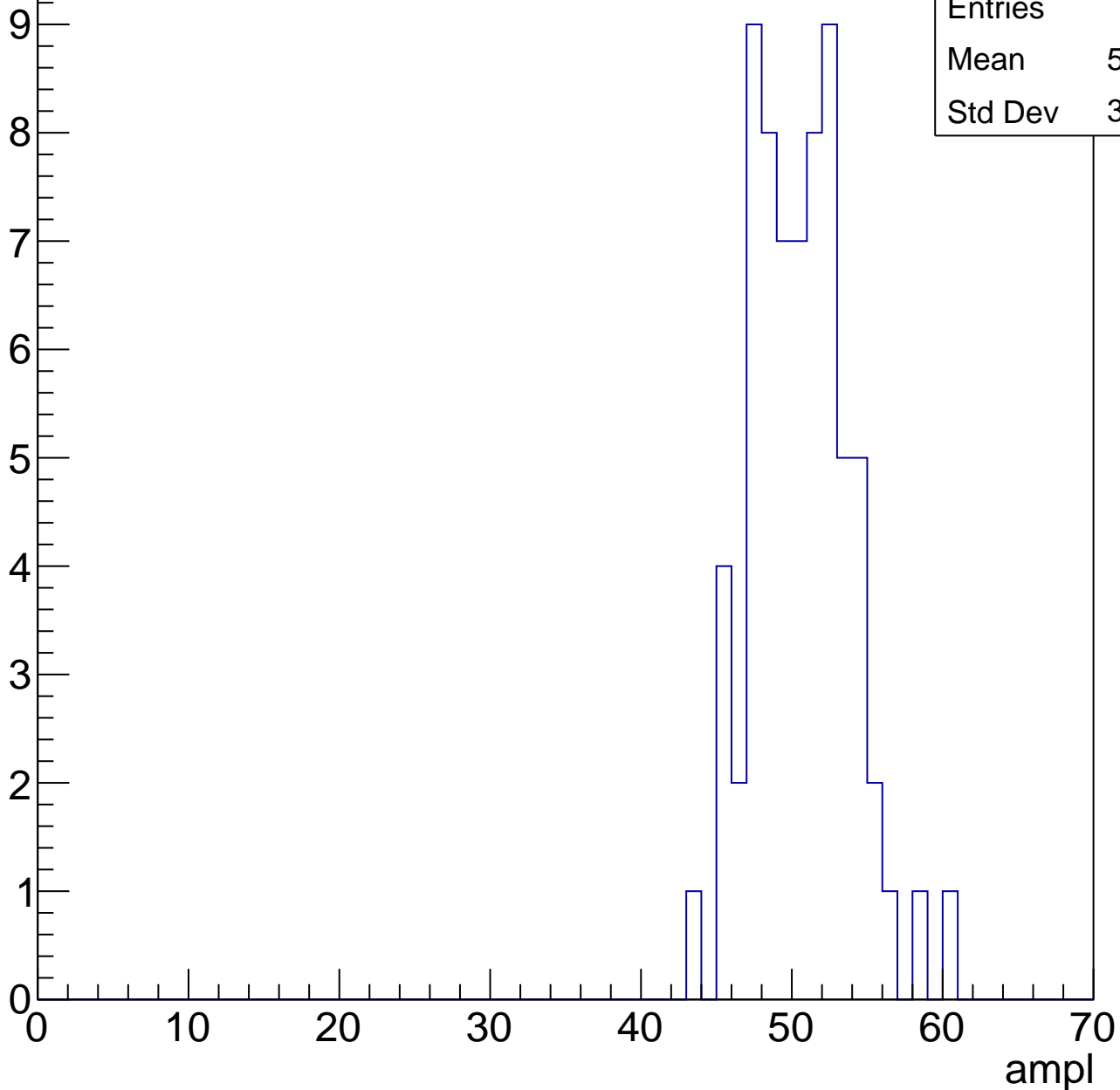


B1L003S, U20-ch1, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	50.14
Std Dev	3.204

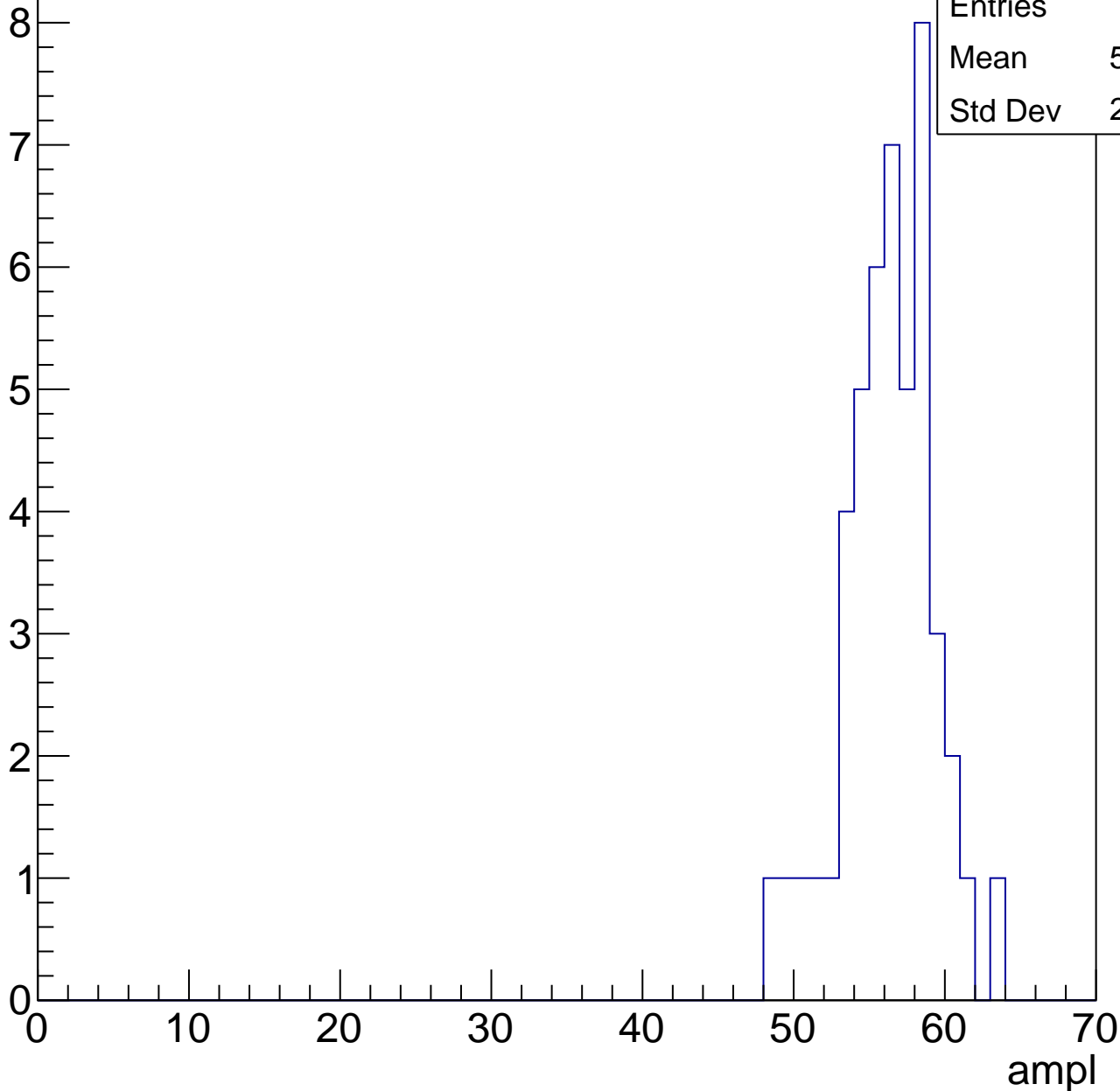


B1L003S, U20-ch1, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	55.83
Std Dev	2.992

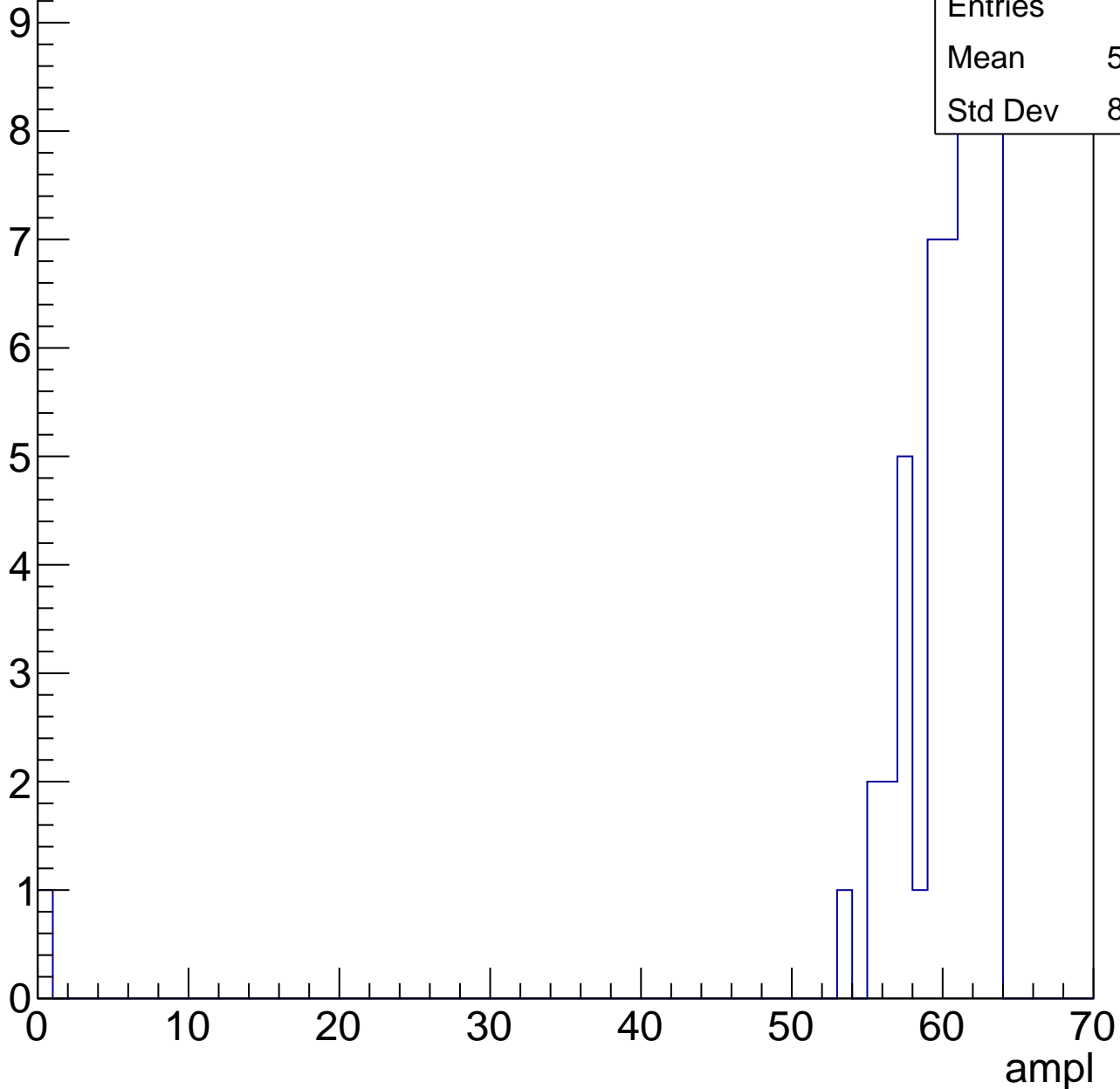


B1L003S, U20-ch1, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	58.88
Std Dev	8.593



B1L003S, U20-ch1, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch1, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch2, adc0

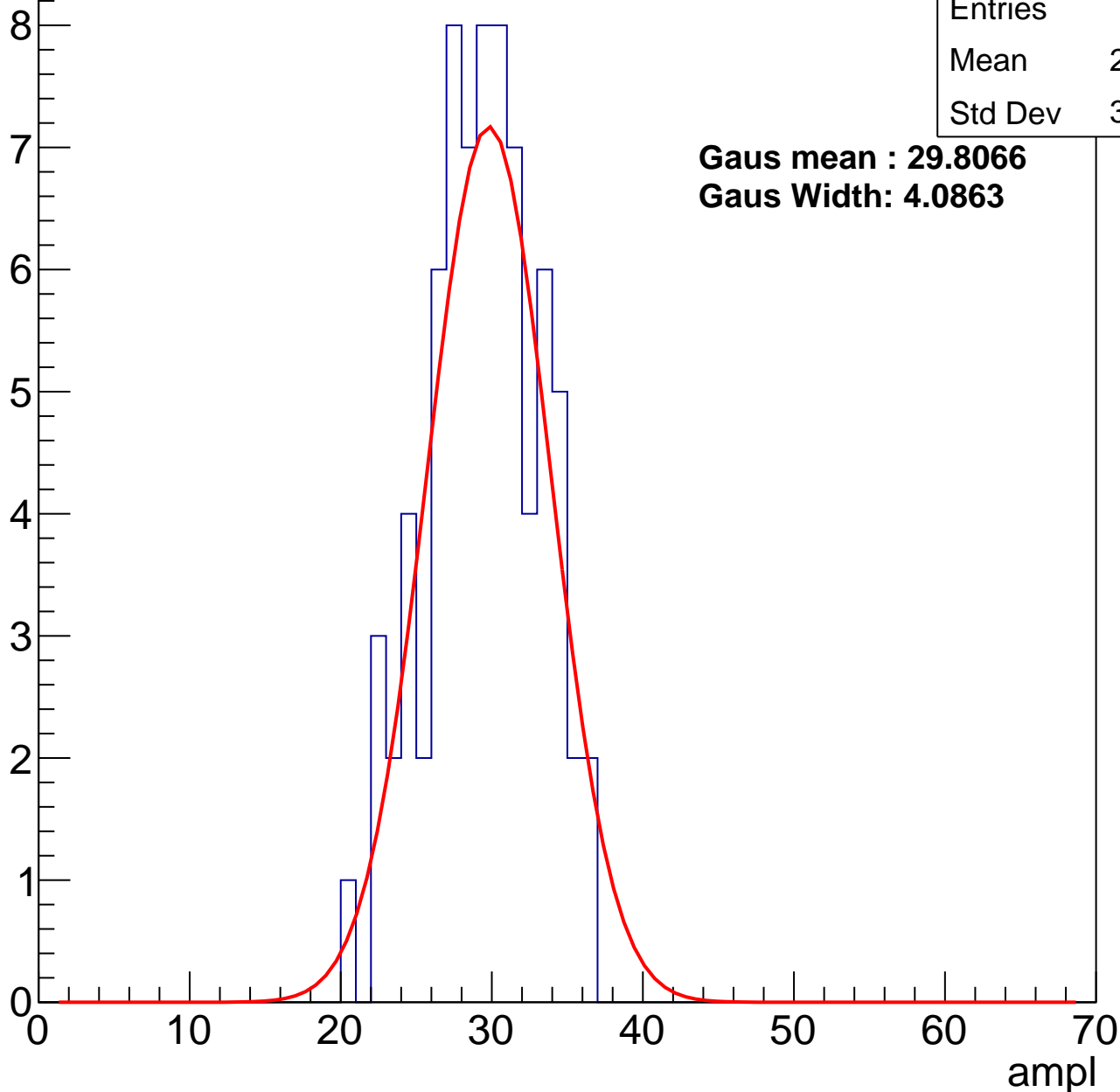
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.97
Std Dev	3.629

Gaus mean : 29.8066

Gaus Width: 4.0863



B1L003S, U20-ch2, adc1

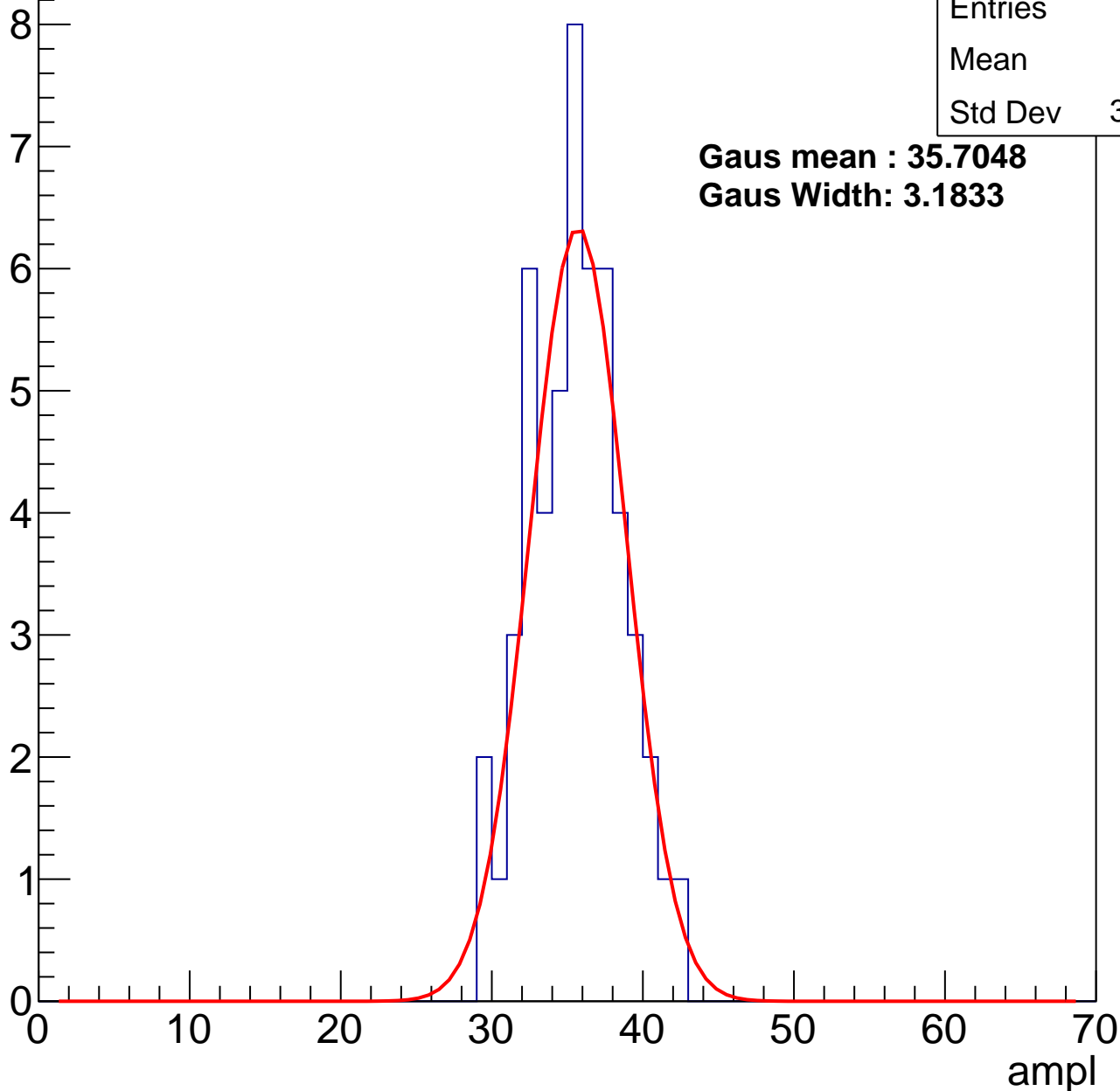
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	35.1
Std Dev	3.002

Gaus mean : 35.7048

Gaus Width: 3.1833



B1L003S, U20-ch2, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	41.82
Std Dev	2.965

Gaus mean : 42.1493

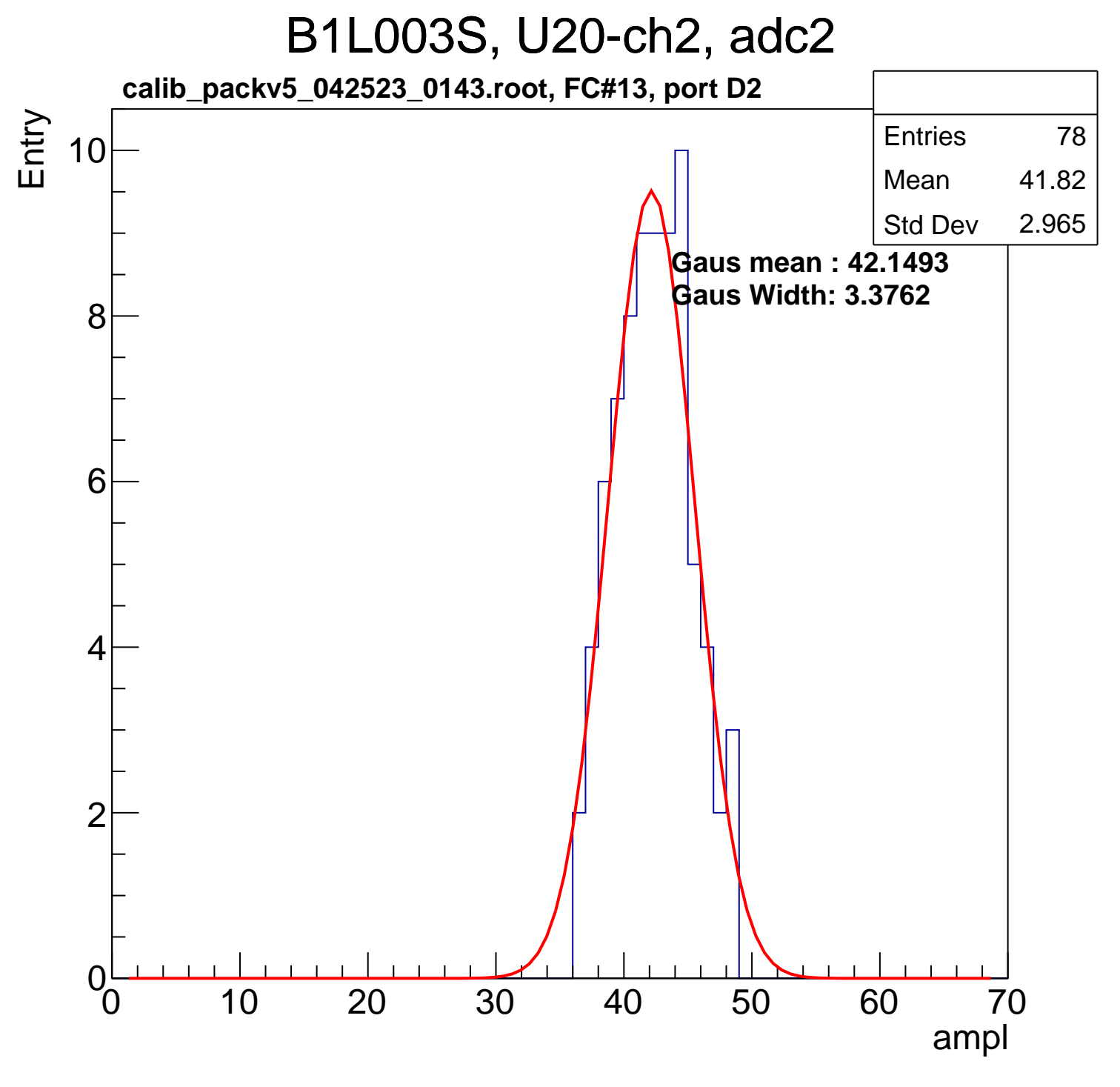
Gaus Width: 3.3762

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

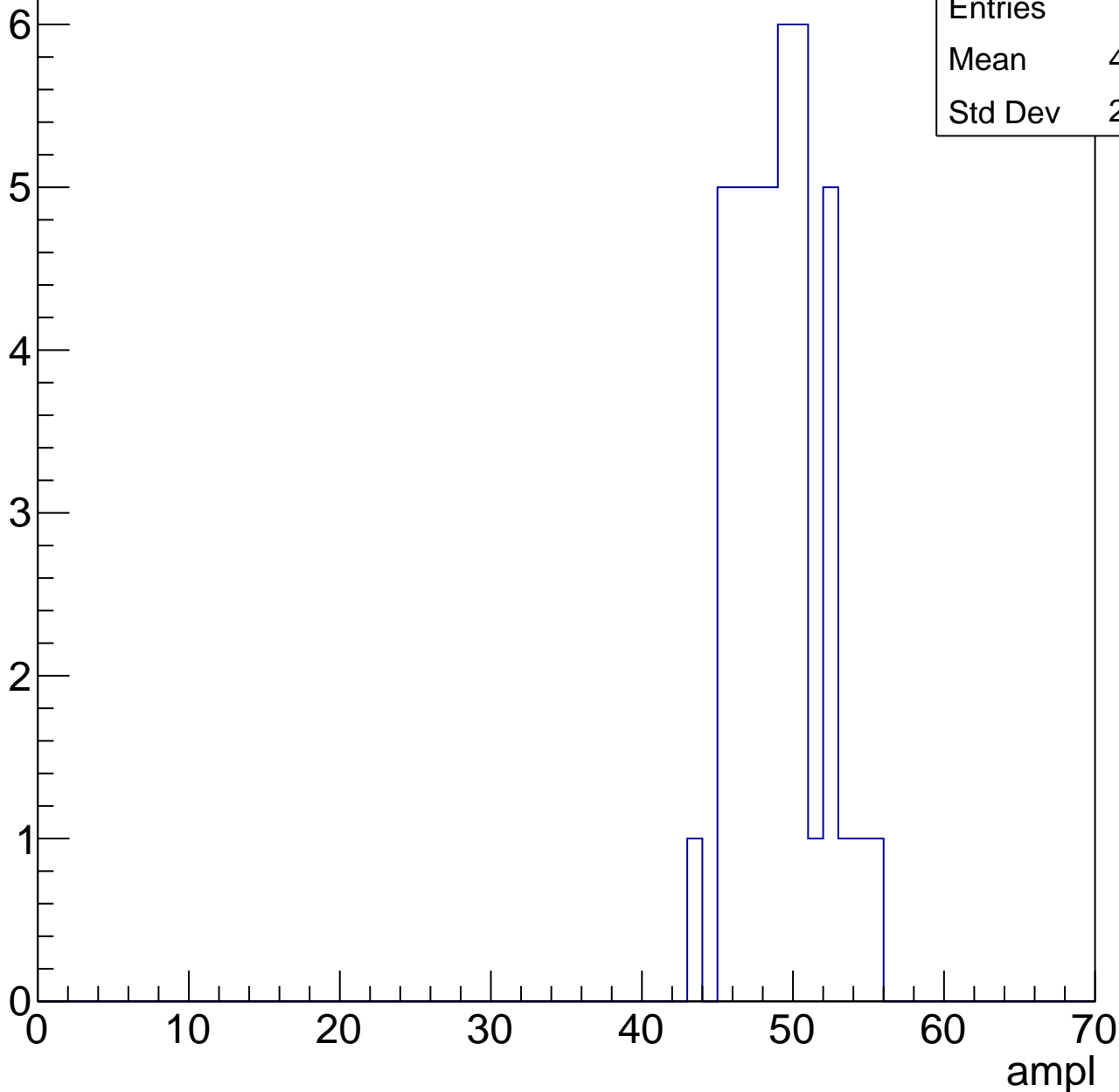


B1L003S, U20-ch2, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	48.57
Std Dev	2.718

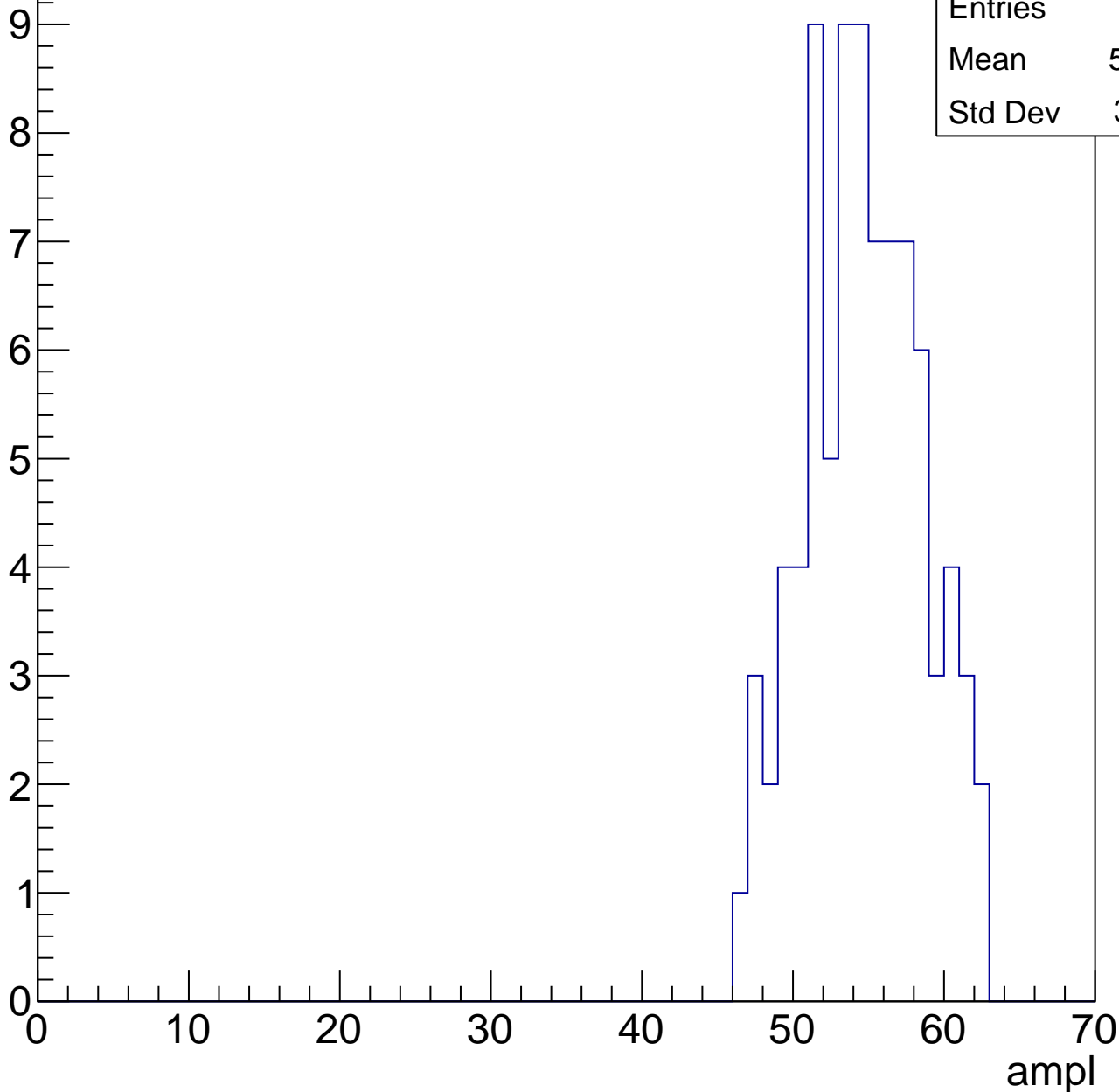


B1L003S, U20-ch2, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	54.22
Std Dev	3.821

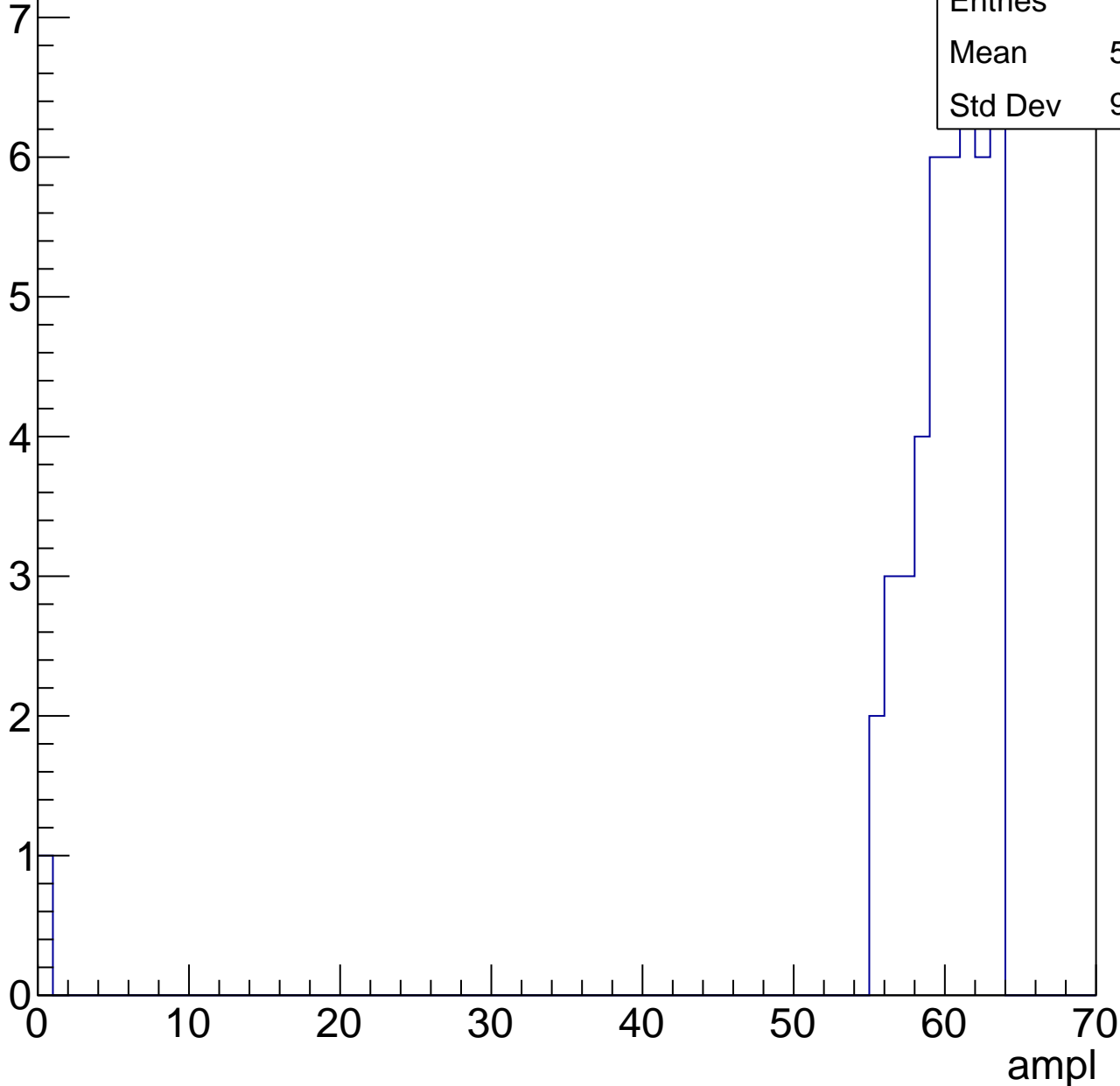


B1L003S, U20-ch2, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.56
Std Dev	9.125



B1L003S, U20-ch2, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch2, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch3, adc0

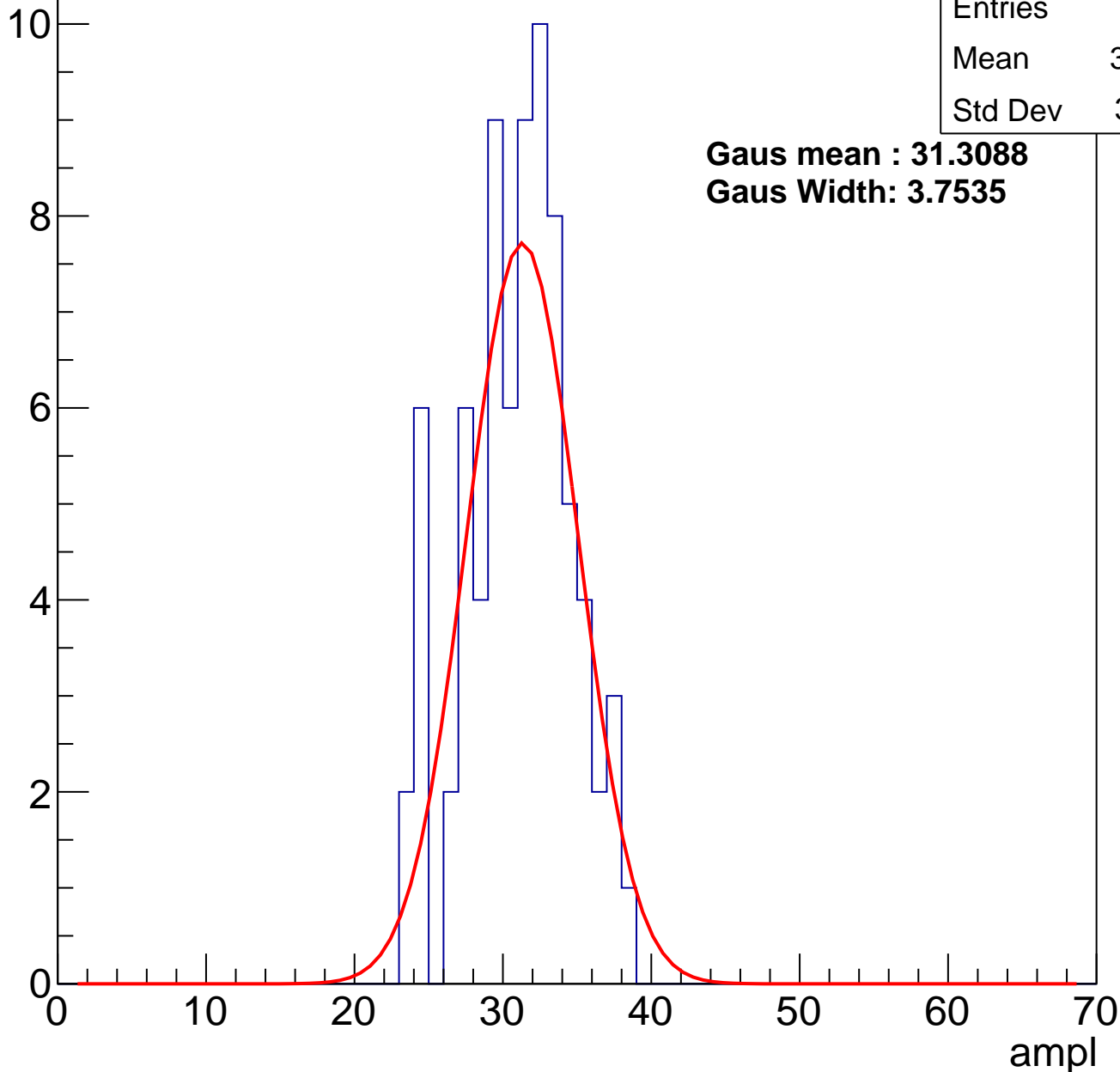
calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	30.53
Std Dev	3.581

Gaus mean : 31.3088

Gaus Width: 3.7535

Entry



B1L003S, U20-ch3, adc1

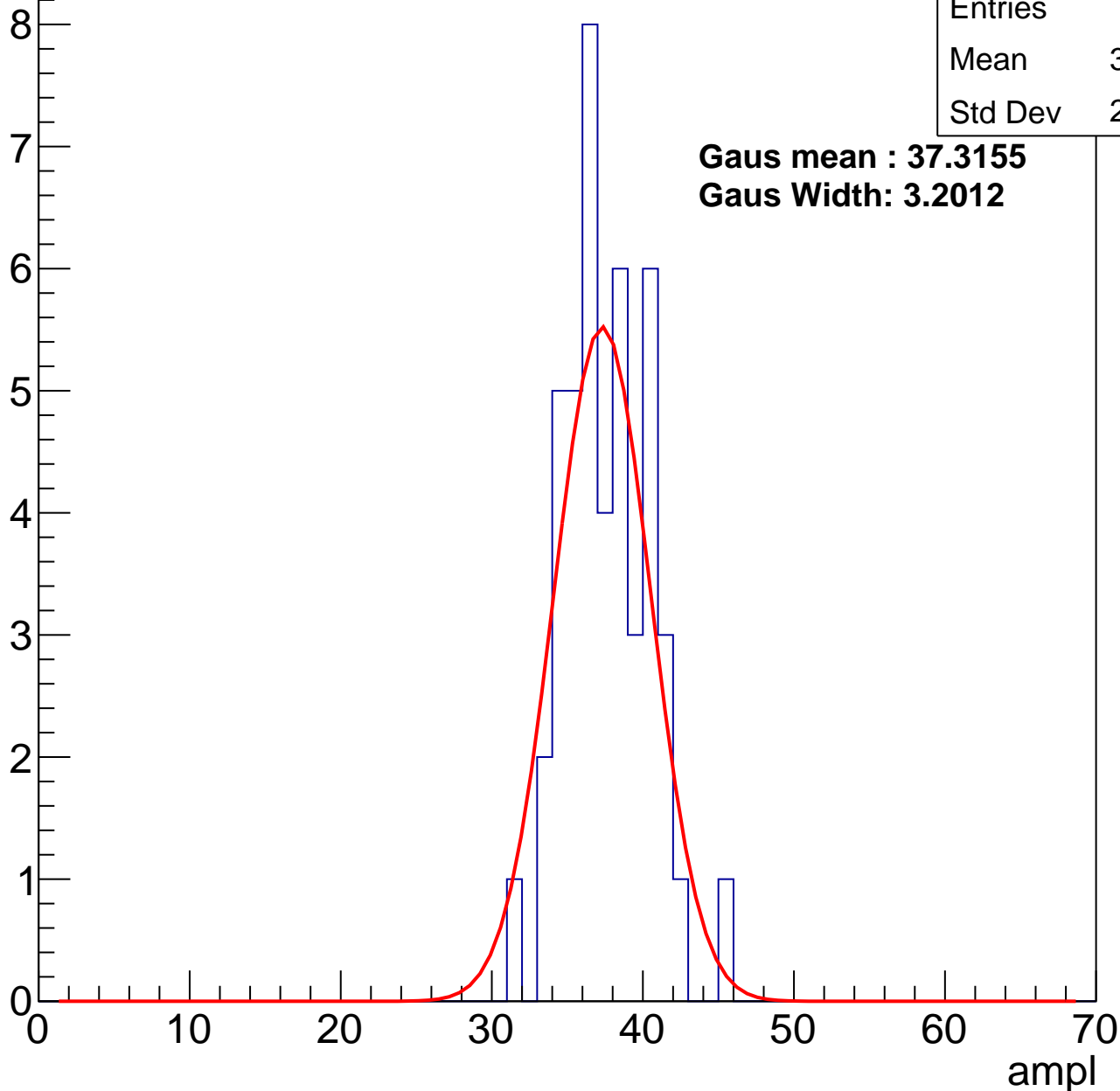
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	37.18
Std Dev	2.783

Gaus mean : 37.3155

Gaus Width: 3.2012



B1L003S, U20-ch3, adc2

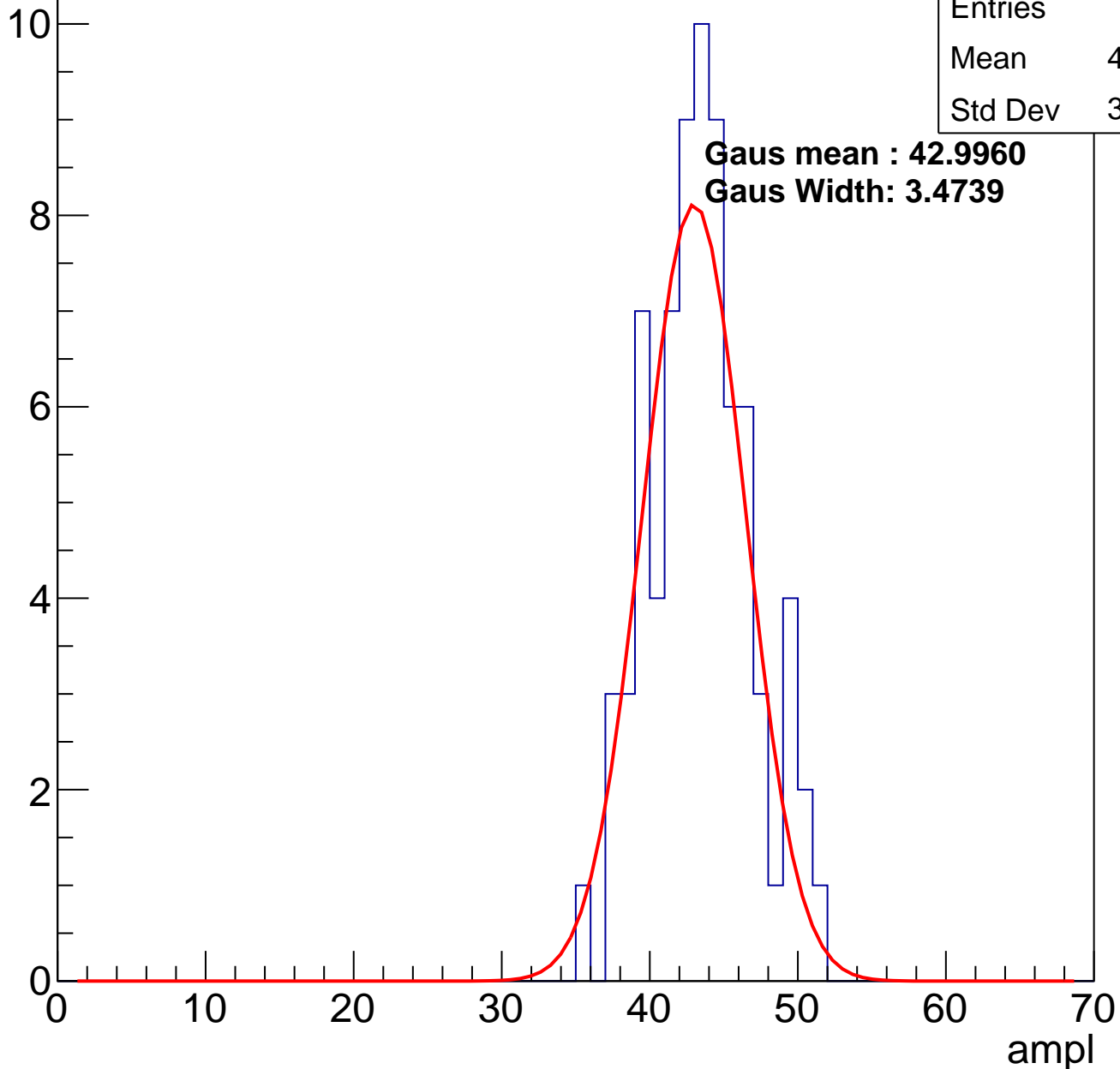
calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	42.97
Std Dev	3.437

Gaus mean : 42.9960

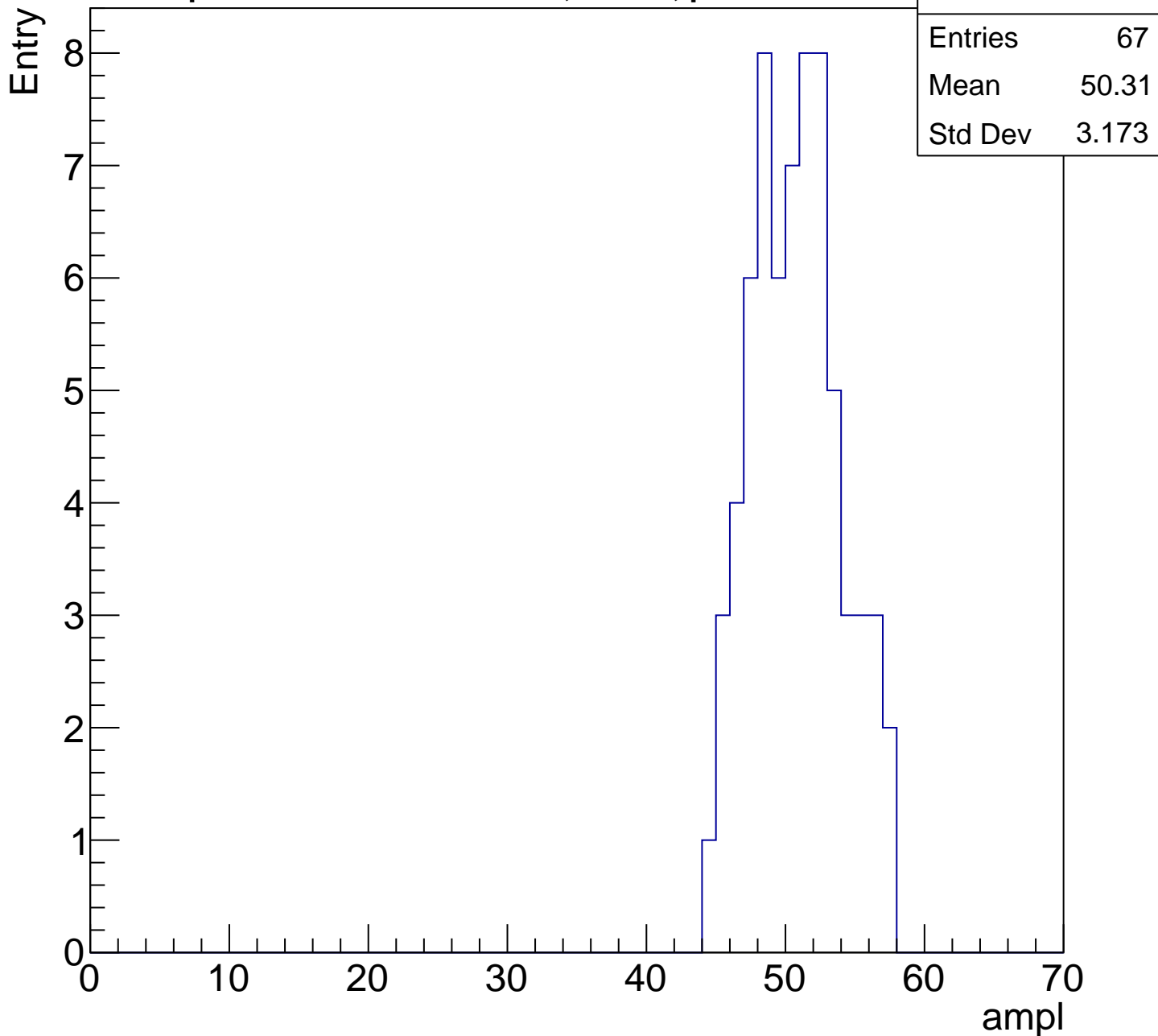
Gaus Width: 3.4739

Entry



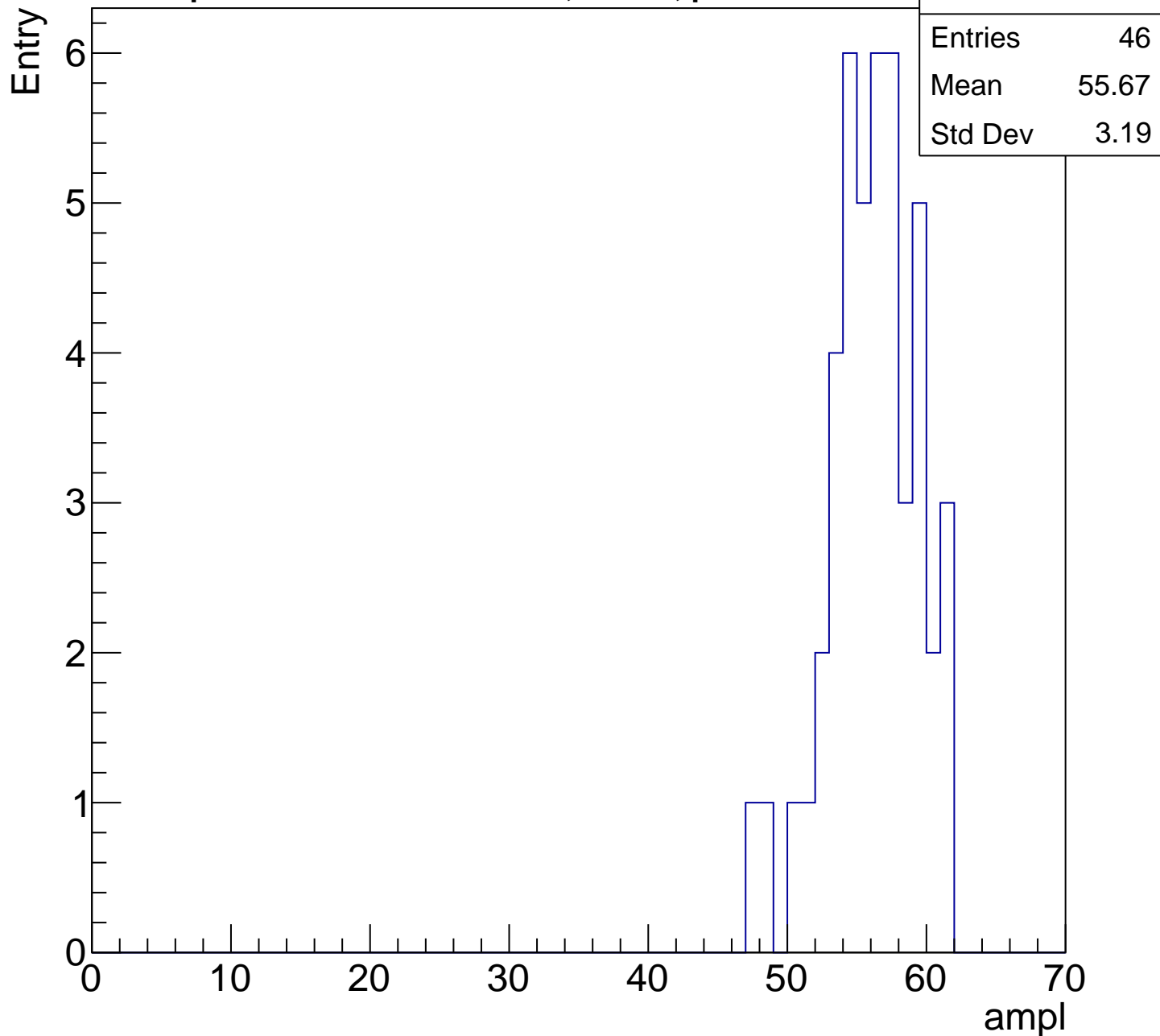
B1L003S, U20-ch3, adc3

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch3, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch3, adc5

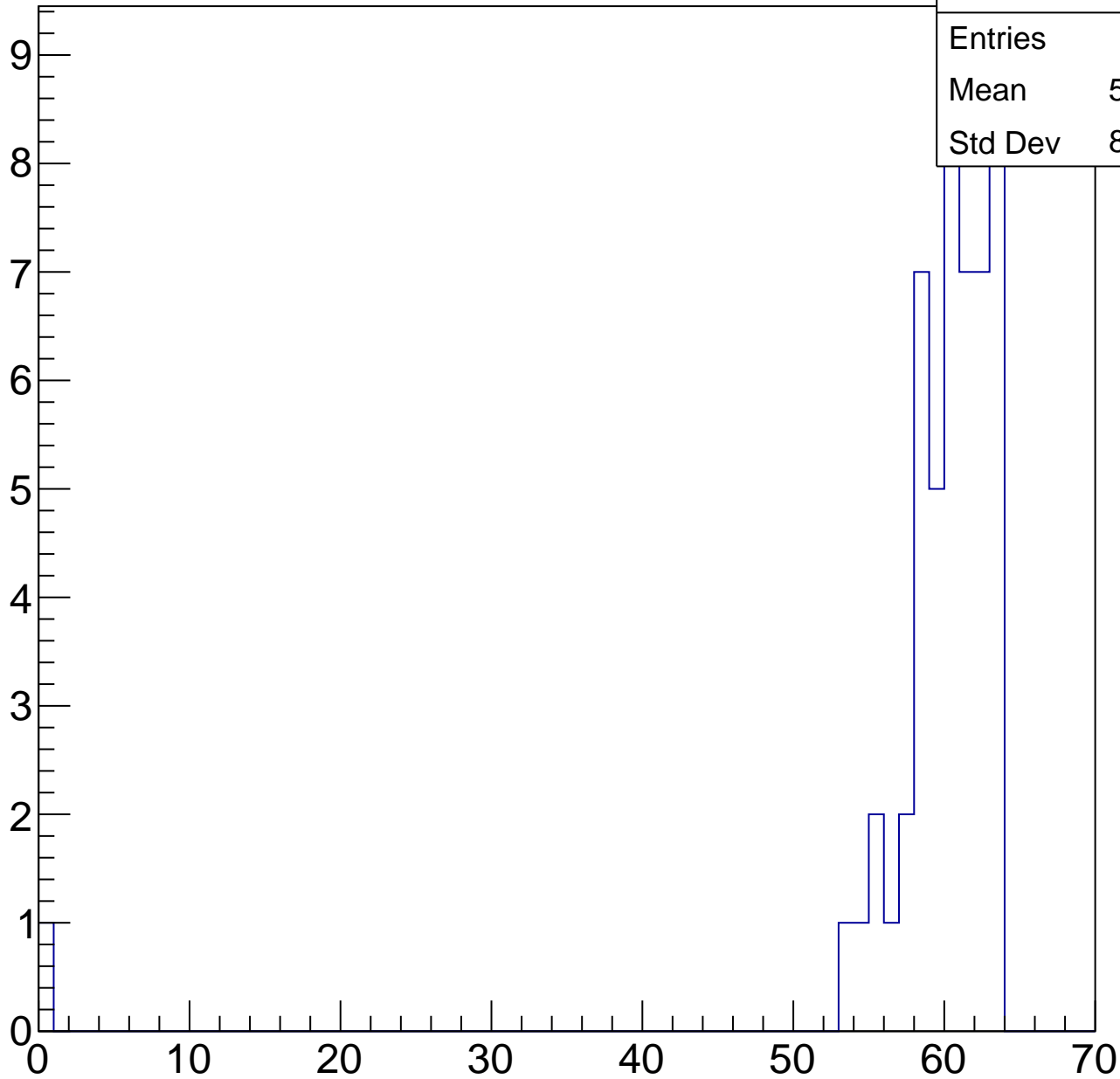
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.75
Std Dev	8.675

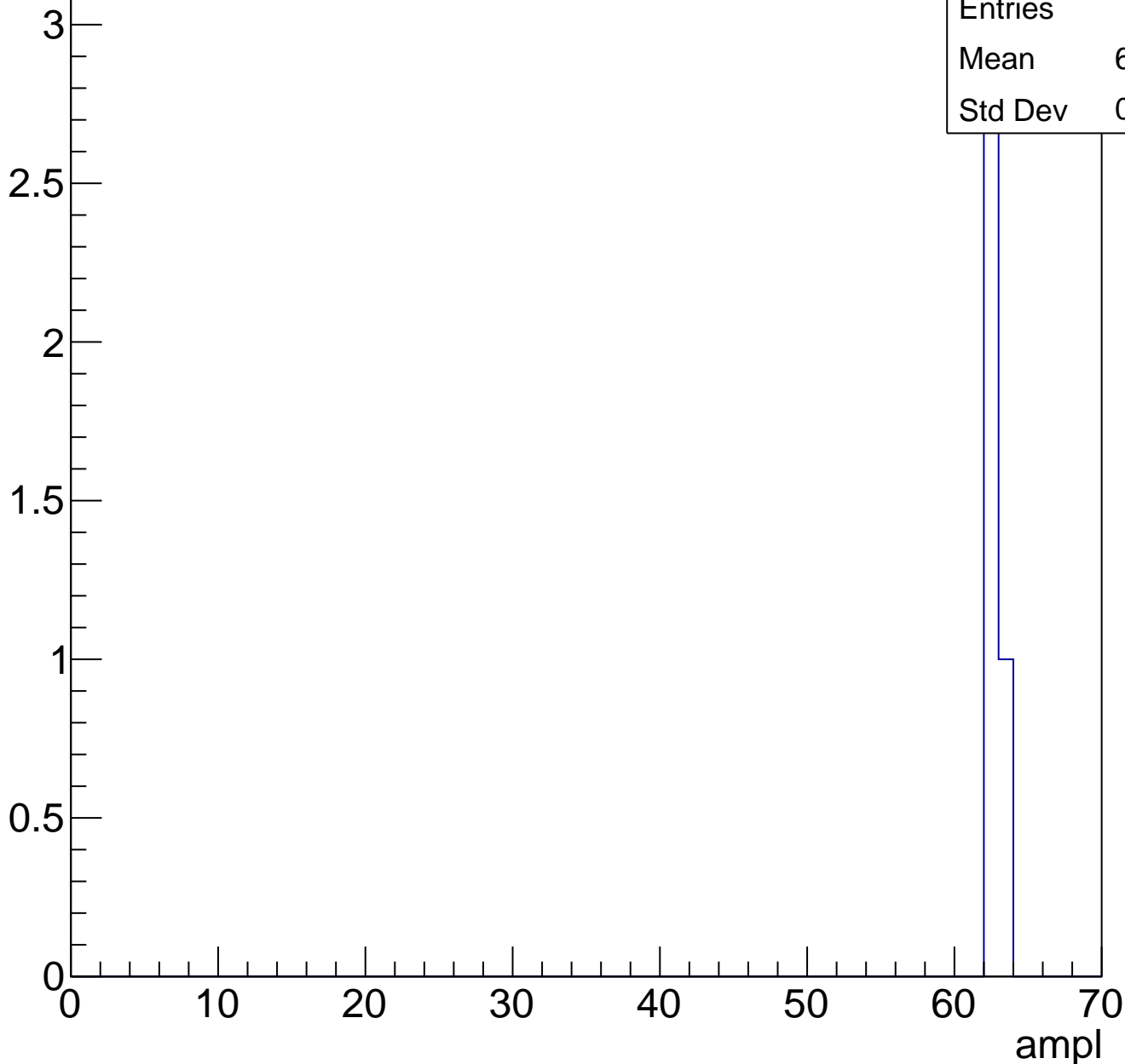
ampl



B1L003S, U20-ch3, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	62.25
Std Dev	0.433

B1L003S, U20-ch3, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch4, adc0

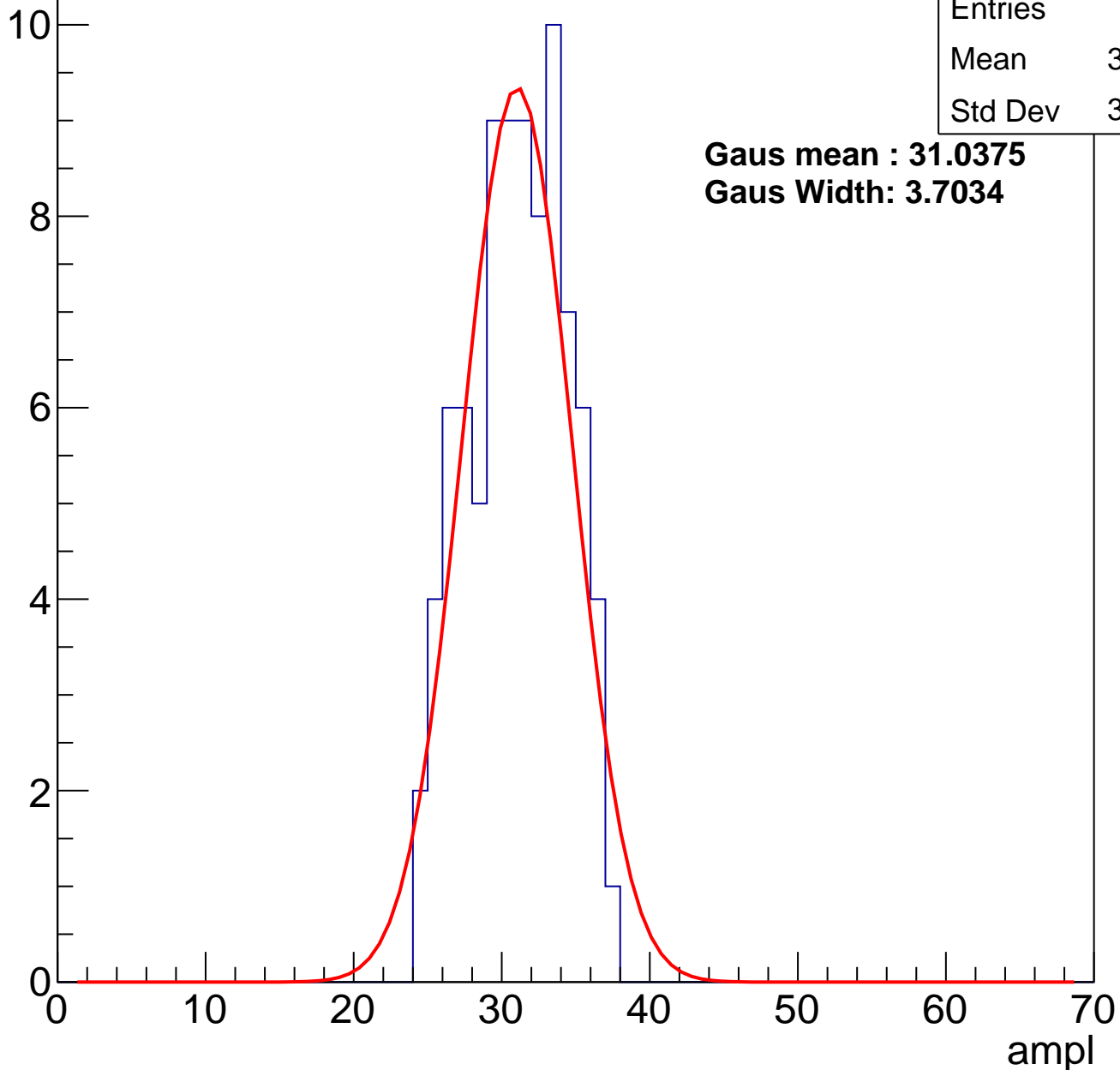
calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	30.59
Std Dev	3.247

Gaus mean : 31.0375

Gaus Width: 3.7034

Entry



B1L003S, U20-ch4, adc1

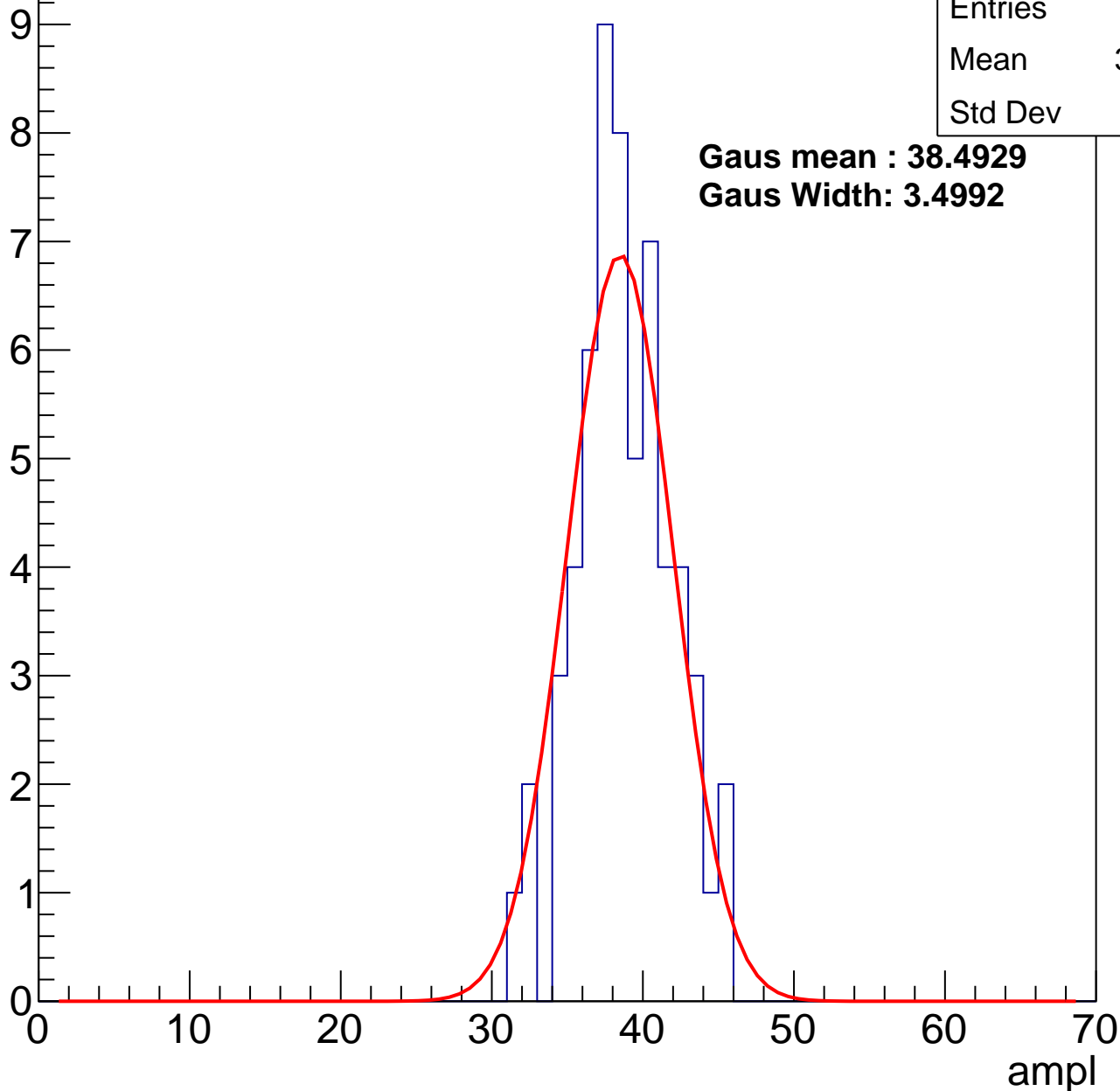
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	38.31
Std Dev	3.12

Gaus mean : 38.4929

Gaus Width: 3.4992



B1L003S, U20-ch4, adc2

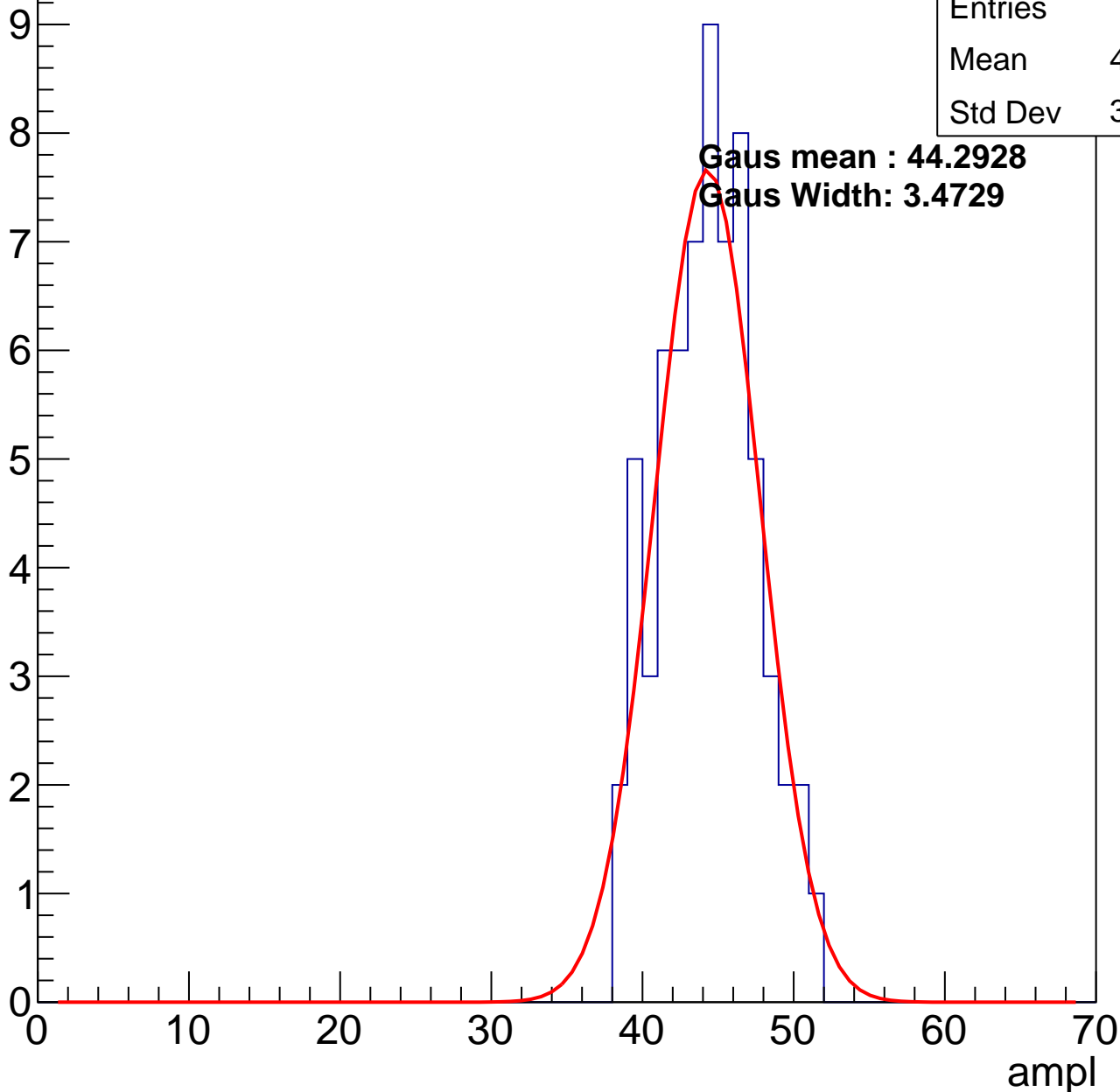
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	43.89
Std Dev	3.095

Gaus mean : 44.2928

Gaus Width: 3.4729

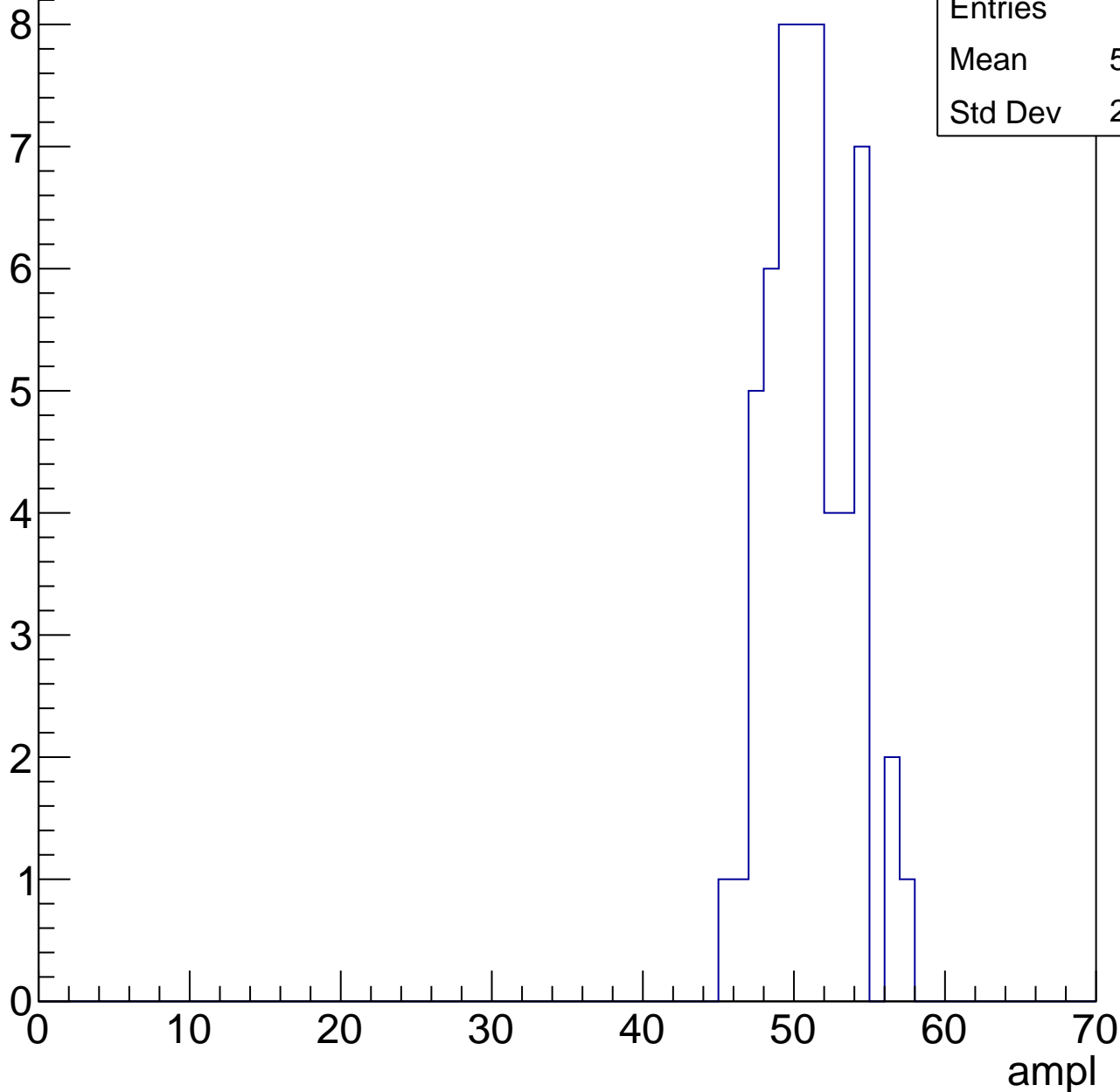


B1L003S, U20-ch4, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	50.56
Std Dev	2.675

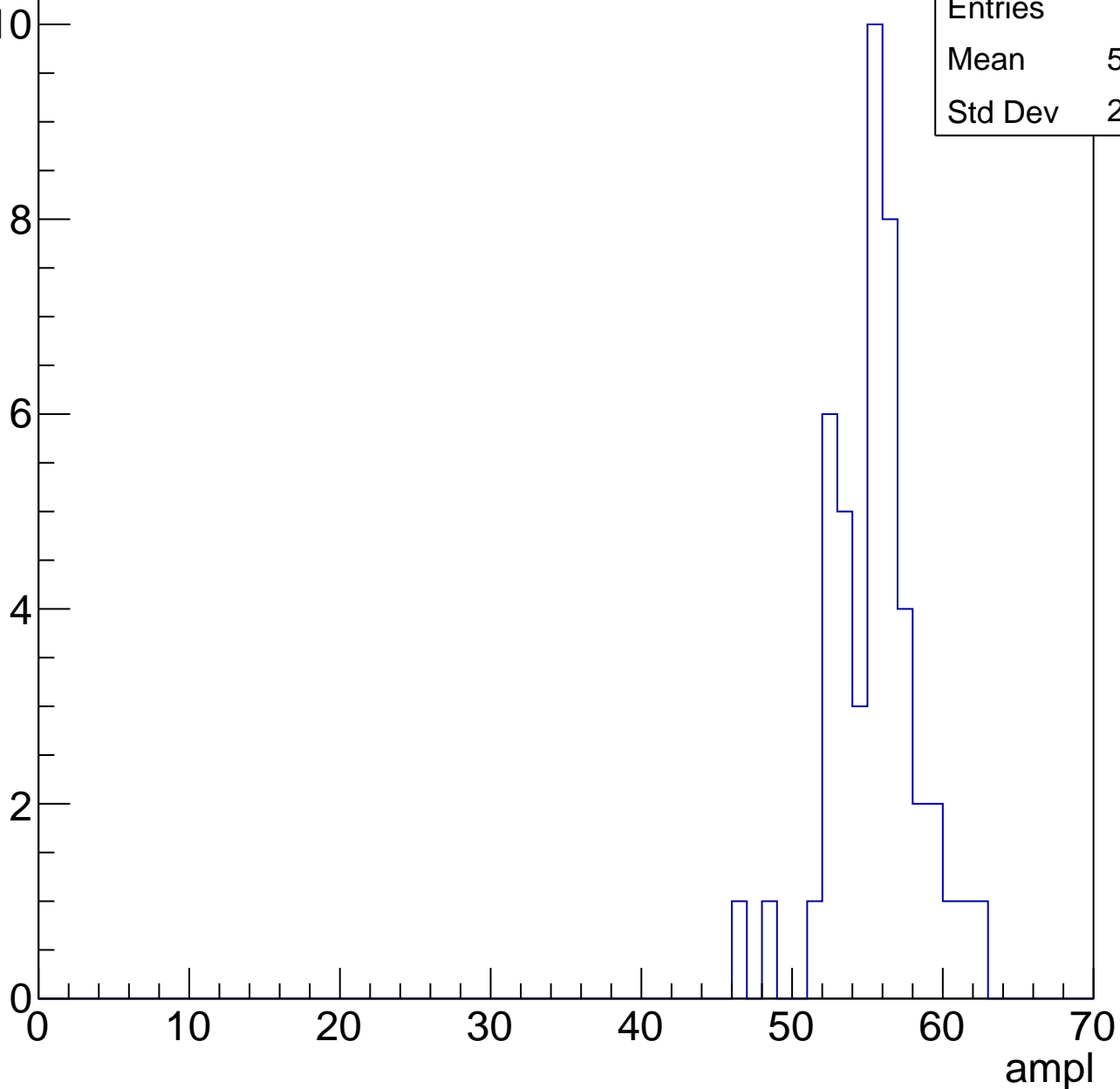


B1L003S, U20-ch4, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	54.93
Std Dev	2.974



B1L003S, U20-ch4, adc5

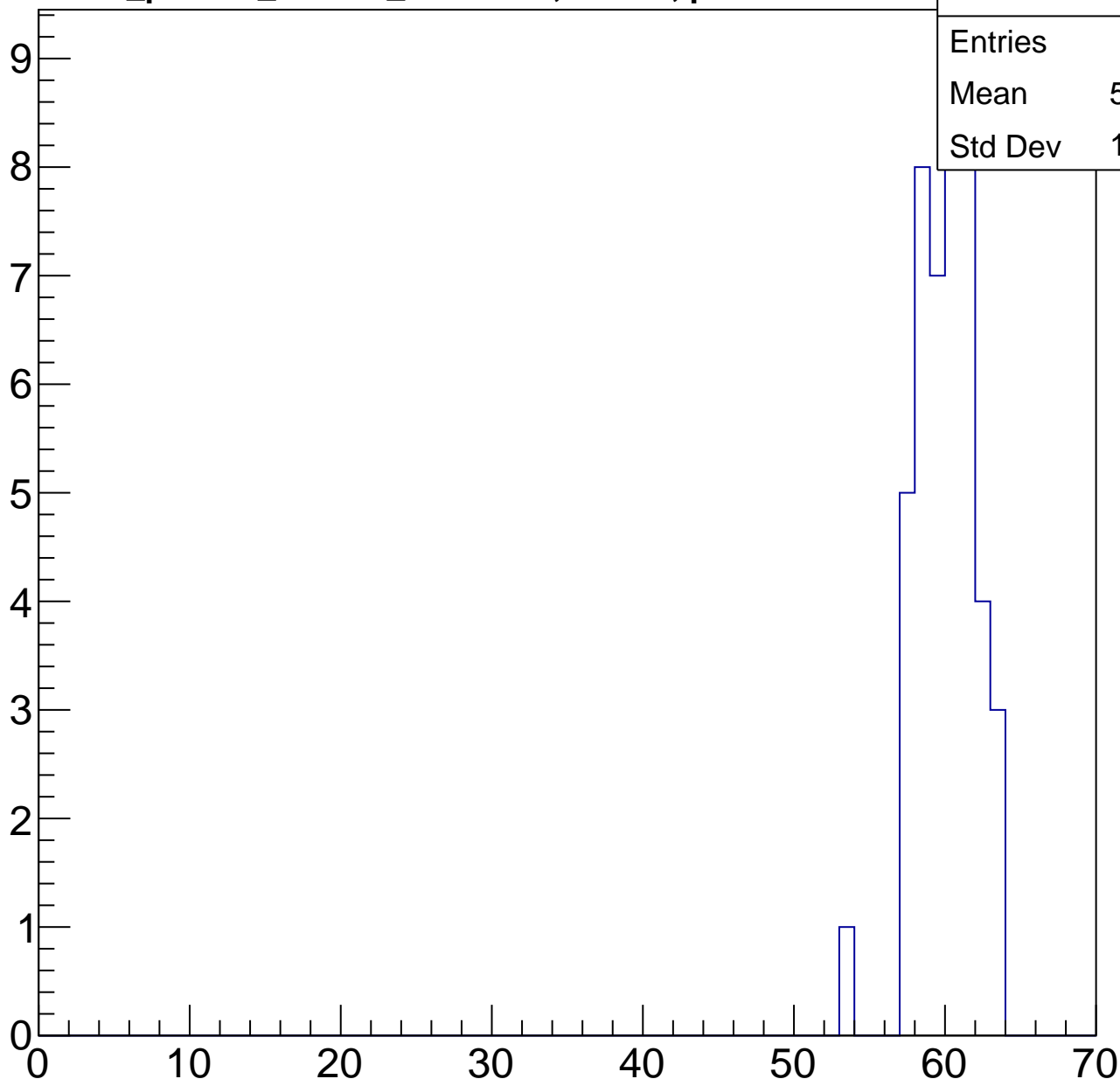
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	45
Mean	59.56
Std Dev	1.973

ampl

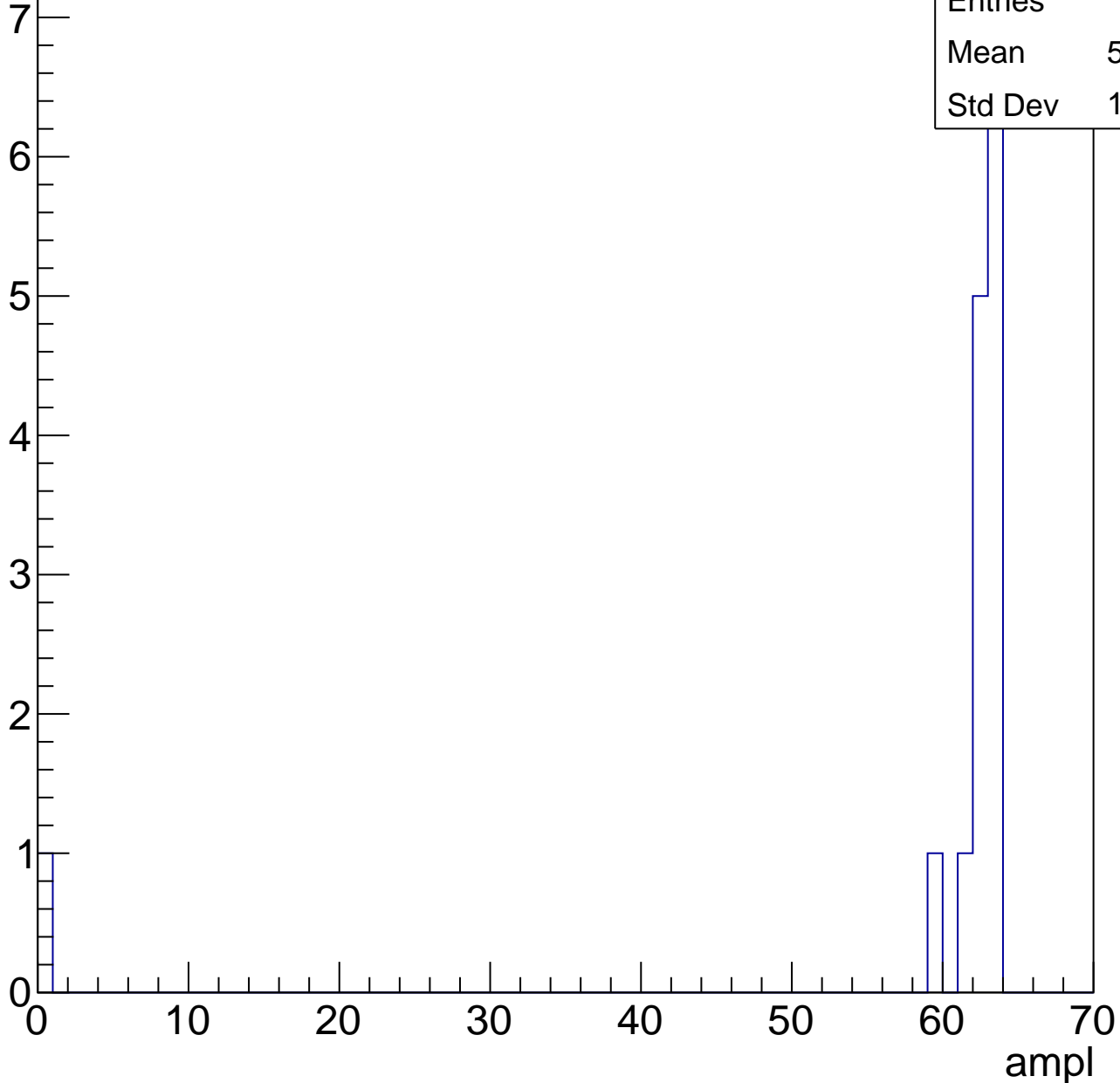


B1L003S, U20-ch4, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	58.07
Std Dev	15.55



B1L003S, U20-ch4, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch5, adc0

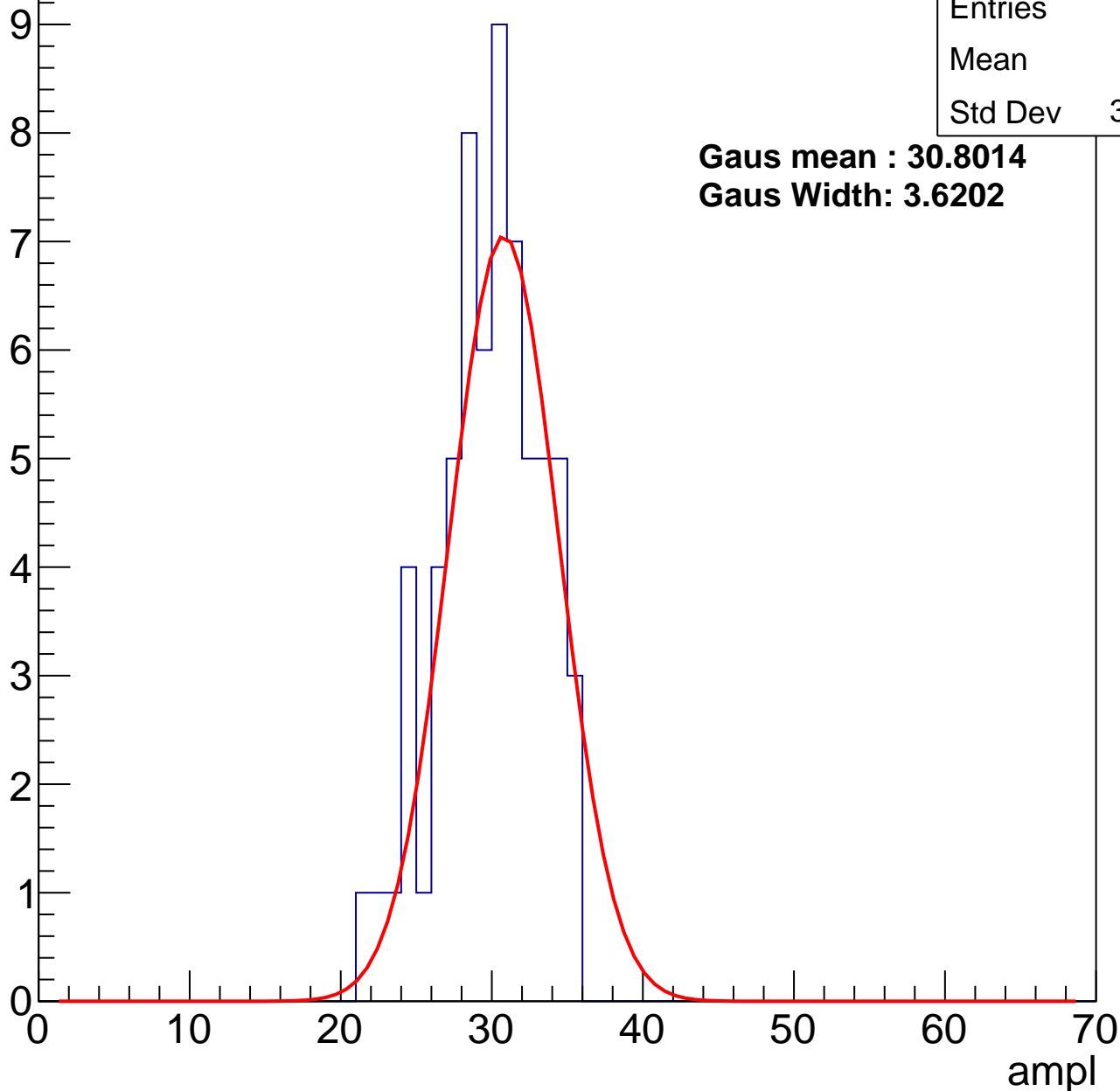
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.4
Std Dev	3.313

Gaus mean : 30.8014

Gaus Width: 3.6202



B1L003S, U20-ch5, adc1

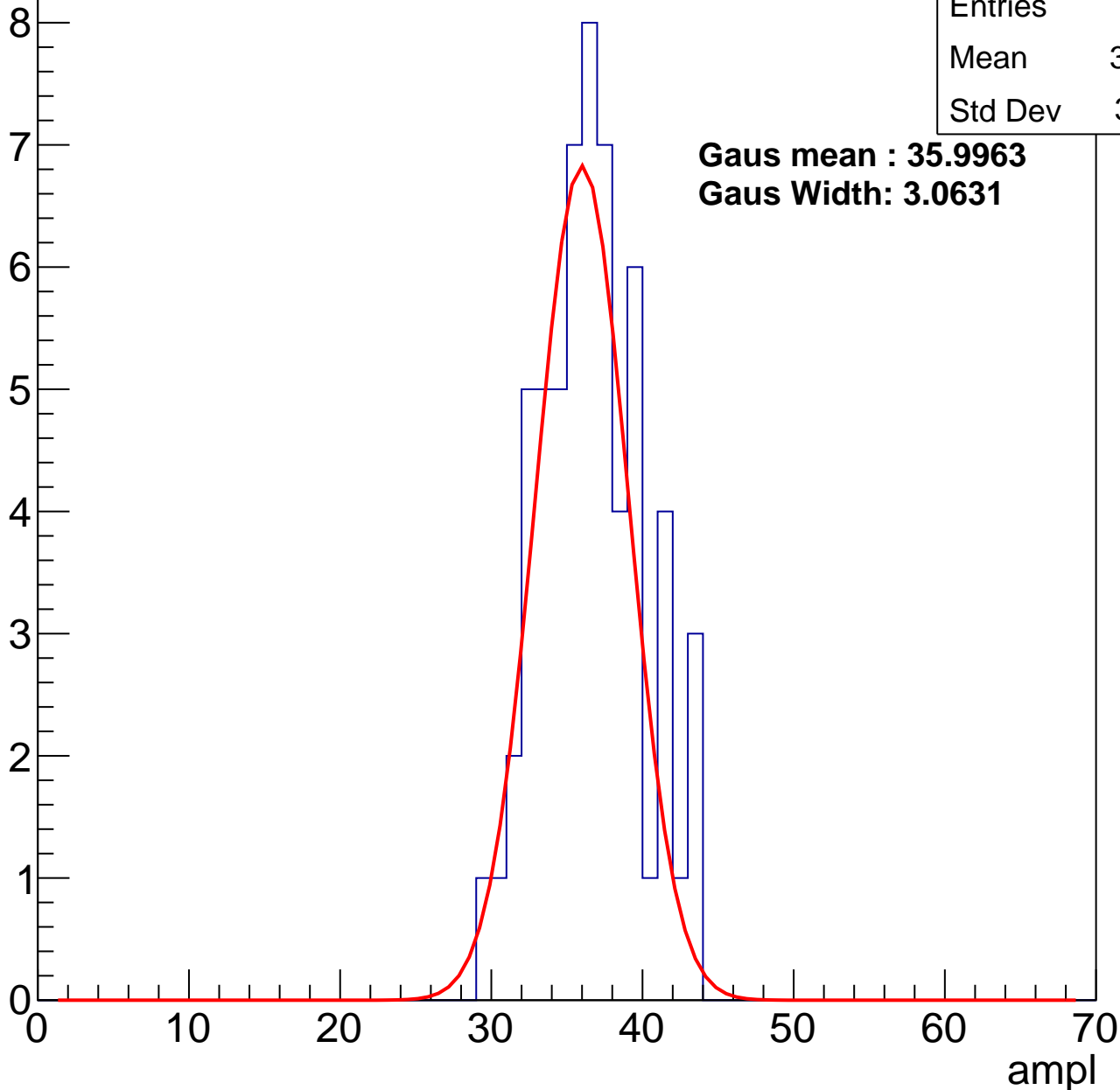
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.15
Std Dev	3.321

Gaus mean : 35.9963

Gaus Width: 3.0631



B1L003S, U20-ch5, adc2

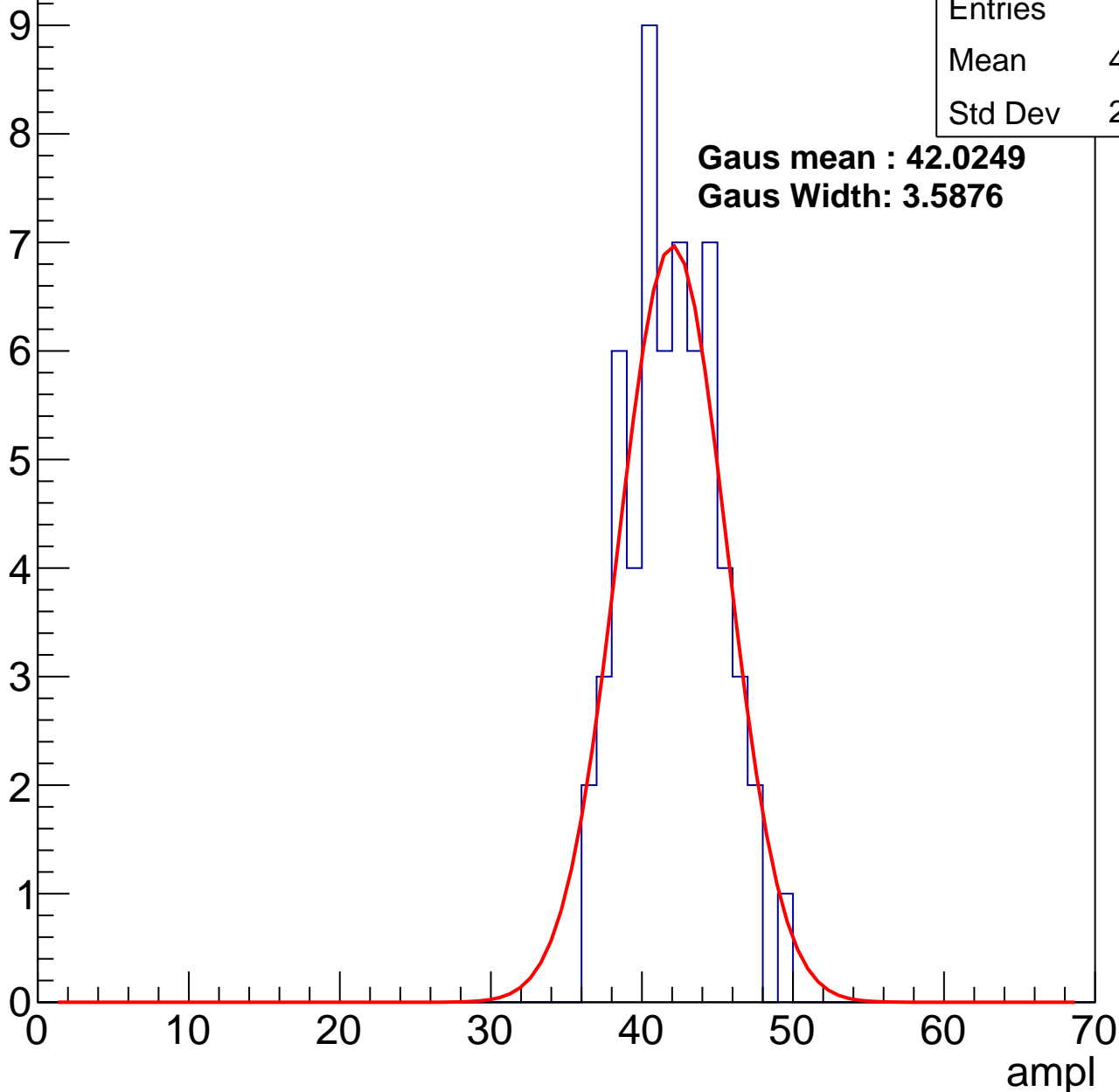
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	41.57
Std Dev	2.963

Gaus mean : 42.0249

Gaus Width: 3.5876

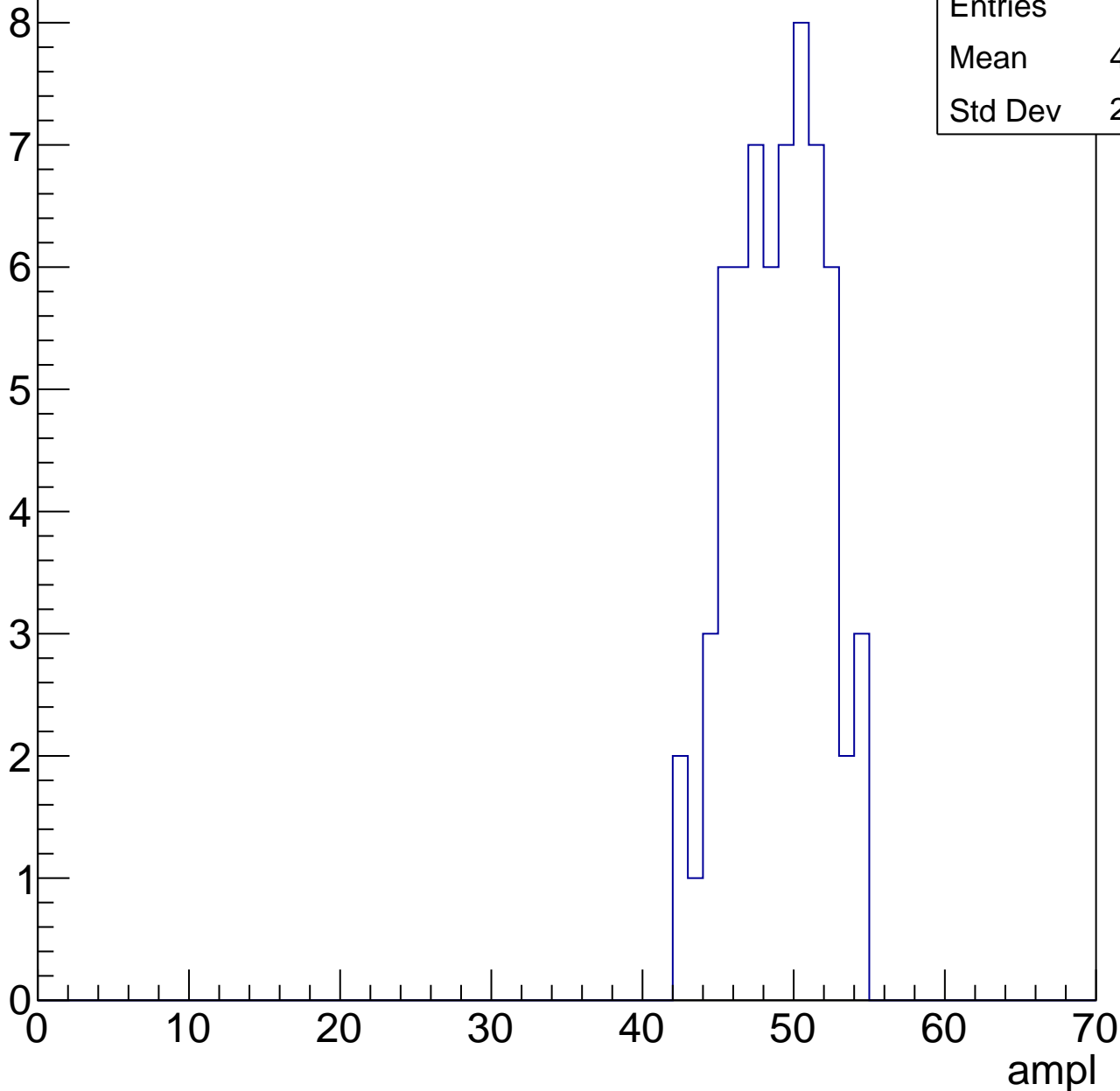


B1L003S, U20-ch5, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.47
Std Dev	2.989

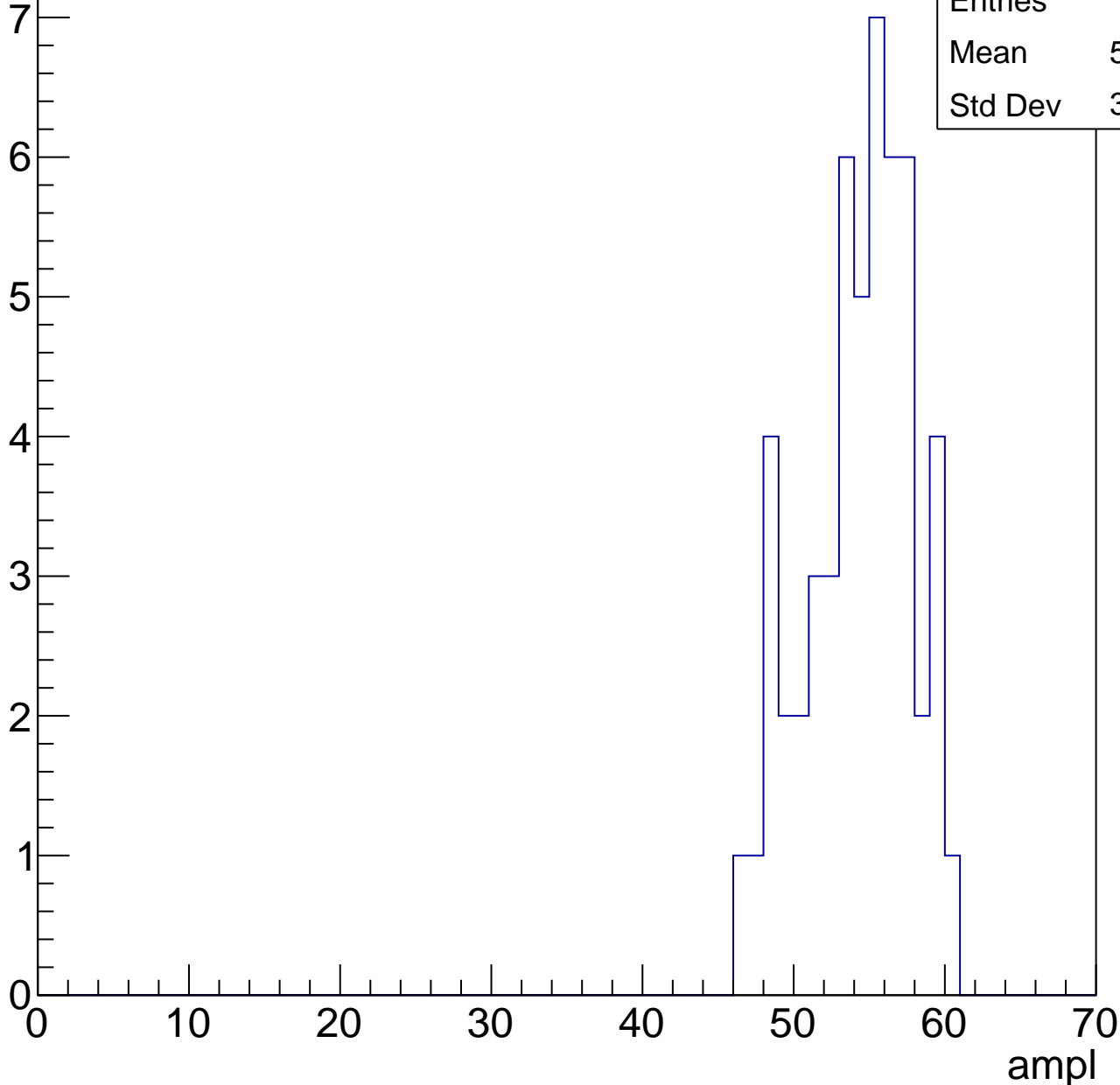


B1L003S, U20-ch5, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	53.87
Std Dev	3.464



B1L003S, U20-ch5, adc5

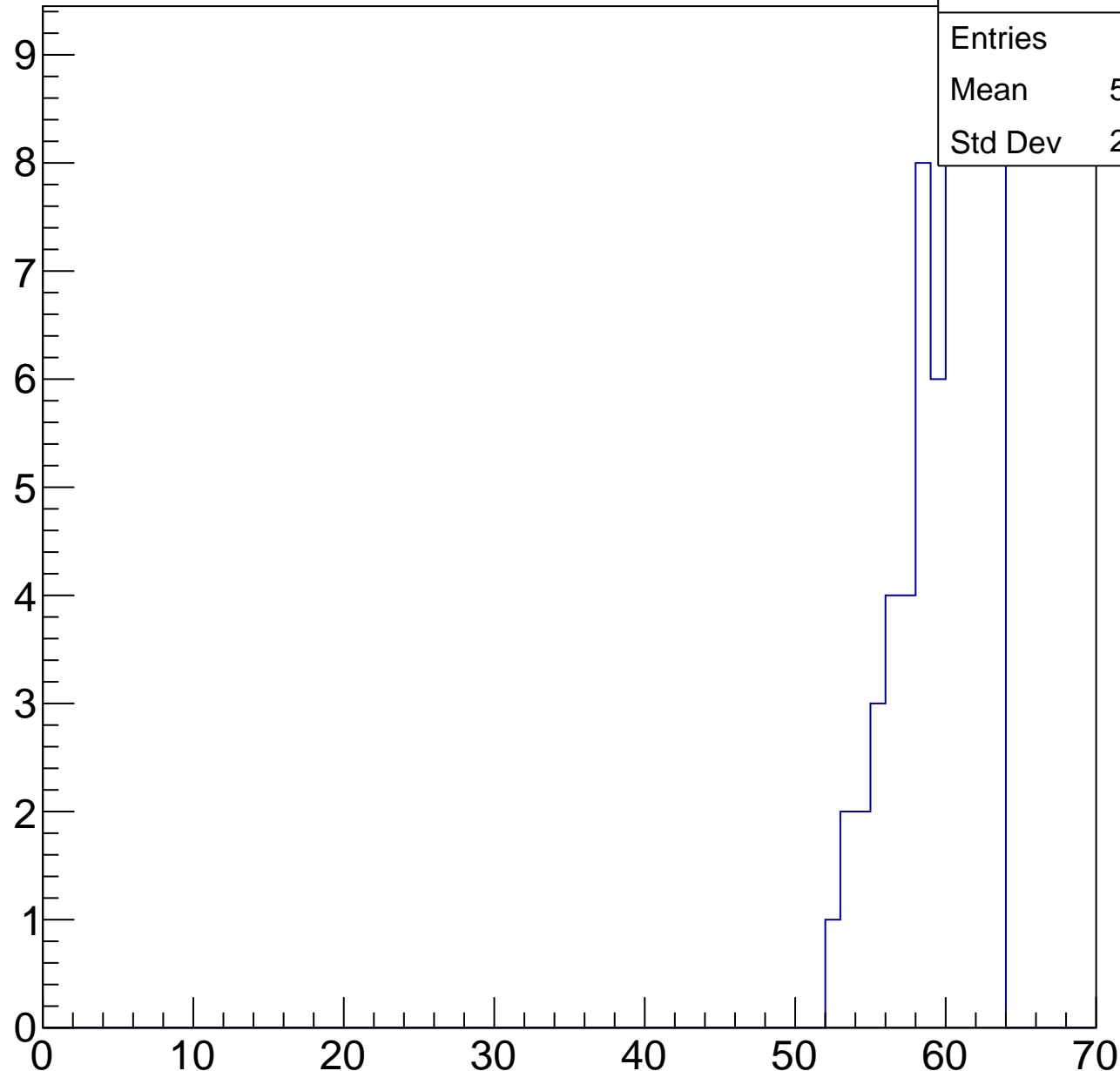
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	59.22
Std Dev	2.853

ampl



B1L003S, U20-ch5, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.83
Std Dev	23.19

ampl

0 10 20 30 40 50 60 70

B1L003S, U20-ch5, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L003S, U20-ch6, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	29.41
Std Dev	5.849

Gaus mean : 30.4748

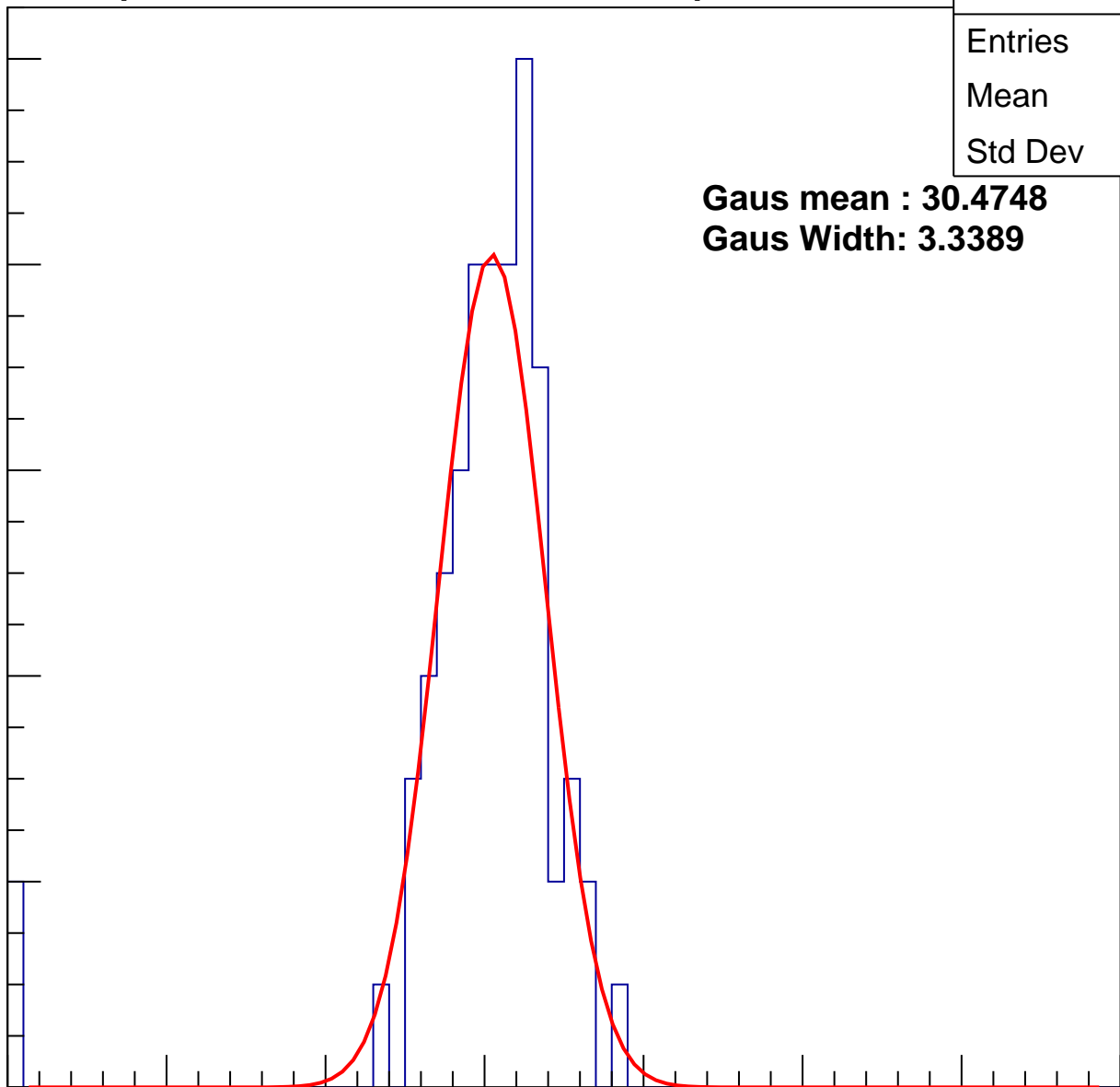
Gaus Width: 3.3389

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch6, adc1

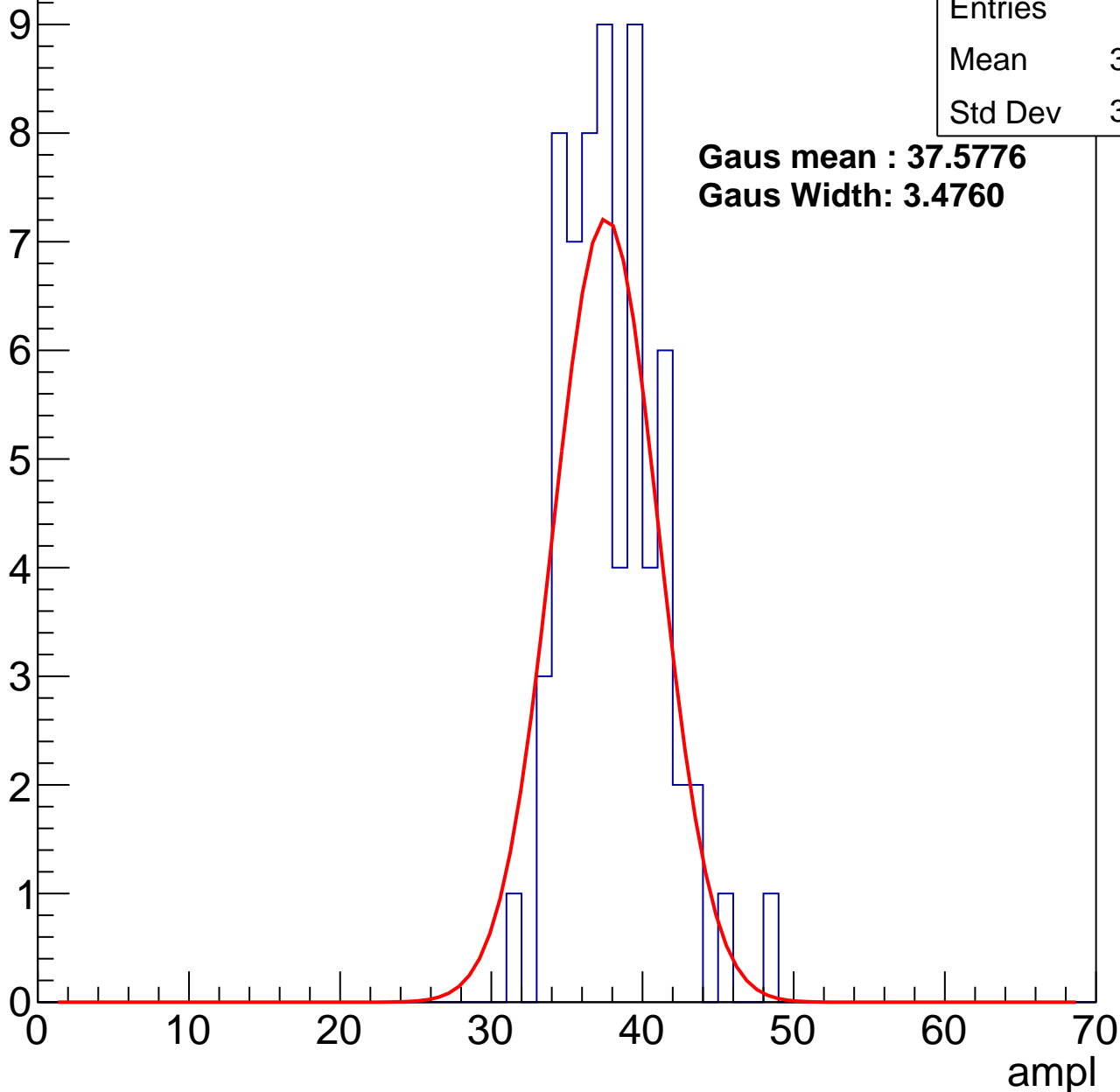
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	37.54
Std Dev	3.168

Gaus mean : 37.5776

Gaus Width: 3.4760



B1L003S, U20-ch6, adc2

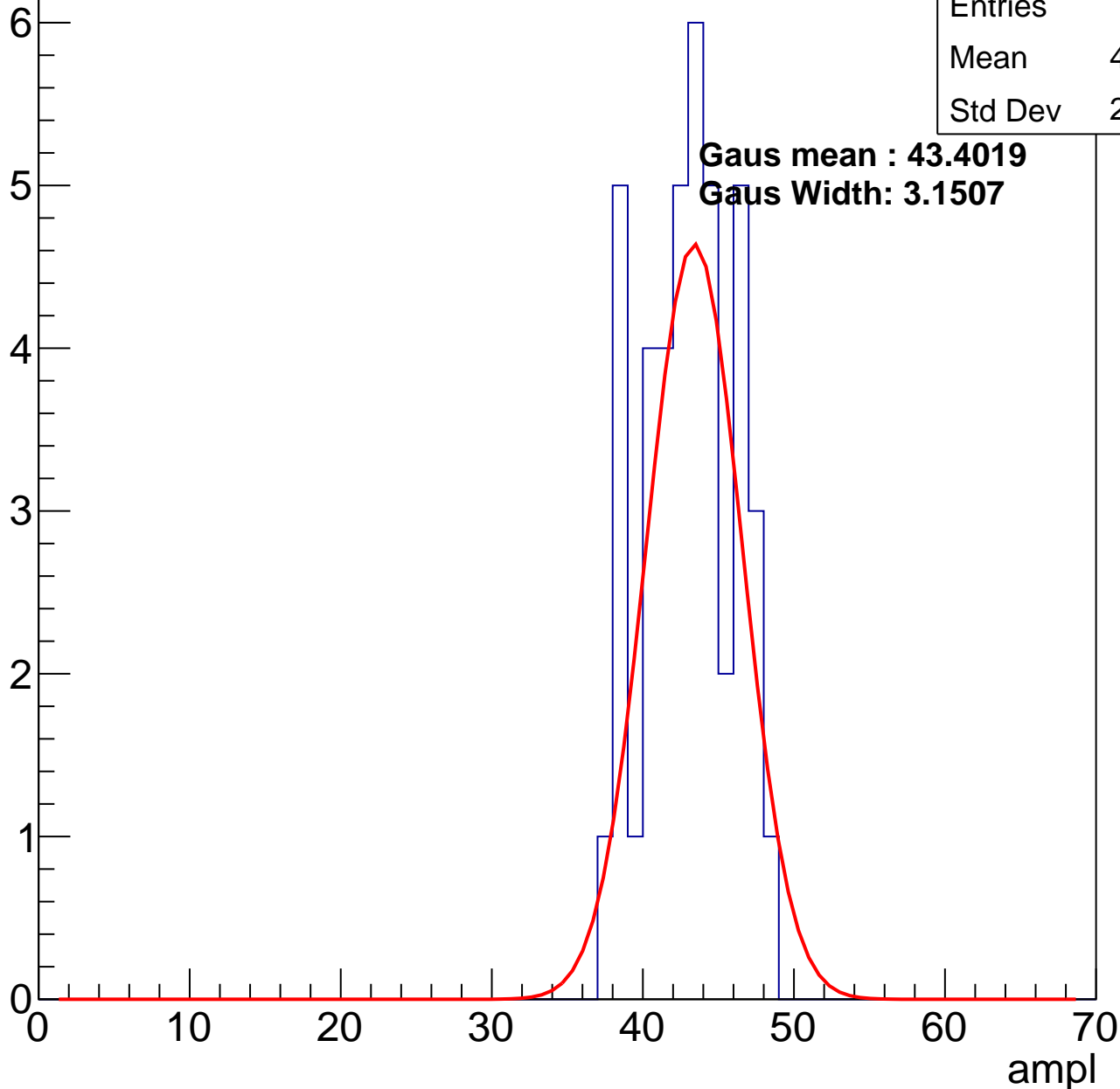
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	42.55
Std Dev	2.913

Gaus mean : 43.4019

Gaus Width: 3.1507

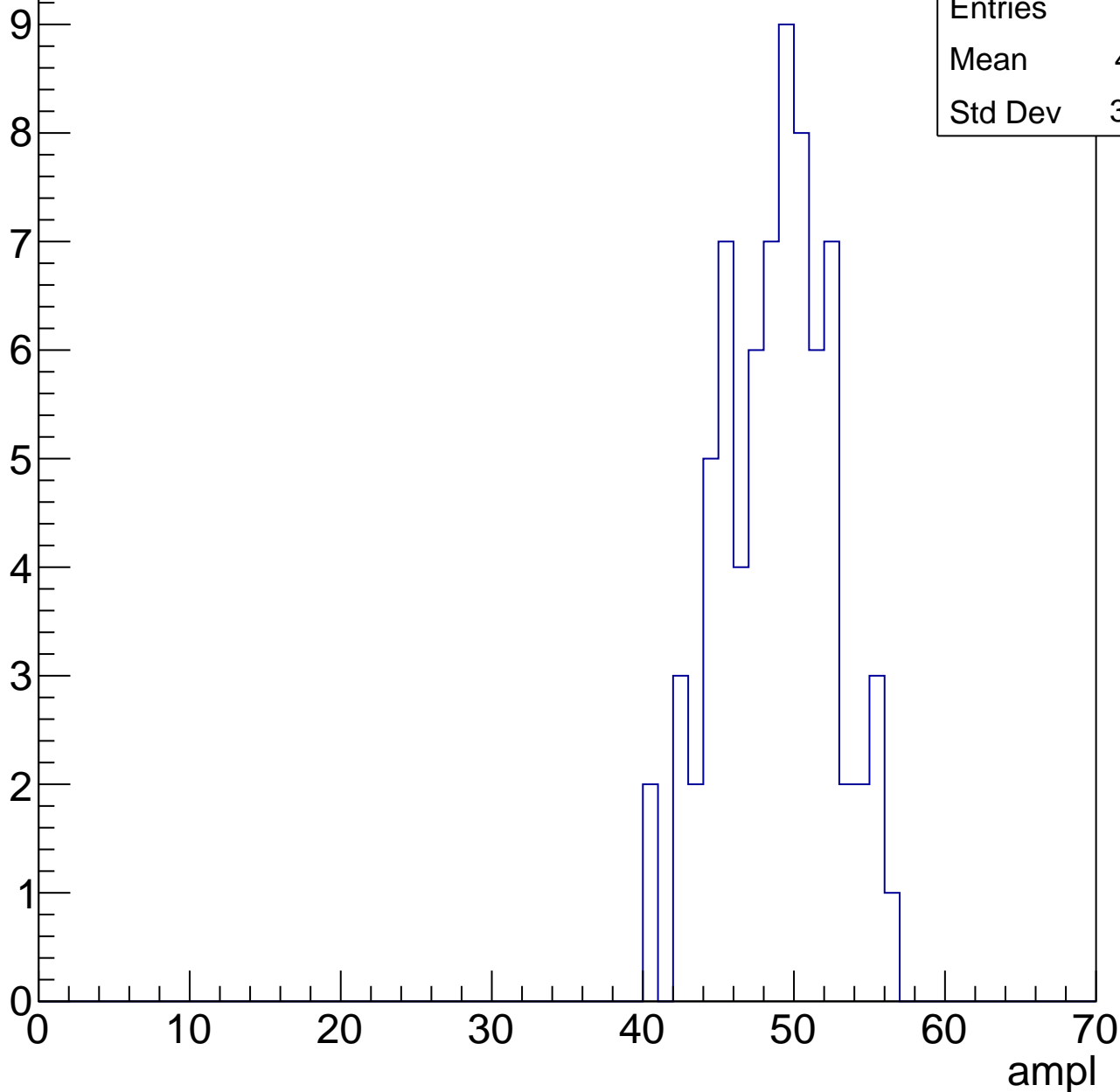


B1L003S, U20-ch6, adc3

calib_packv5_042523_0143.root, FC#13, port D2

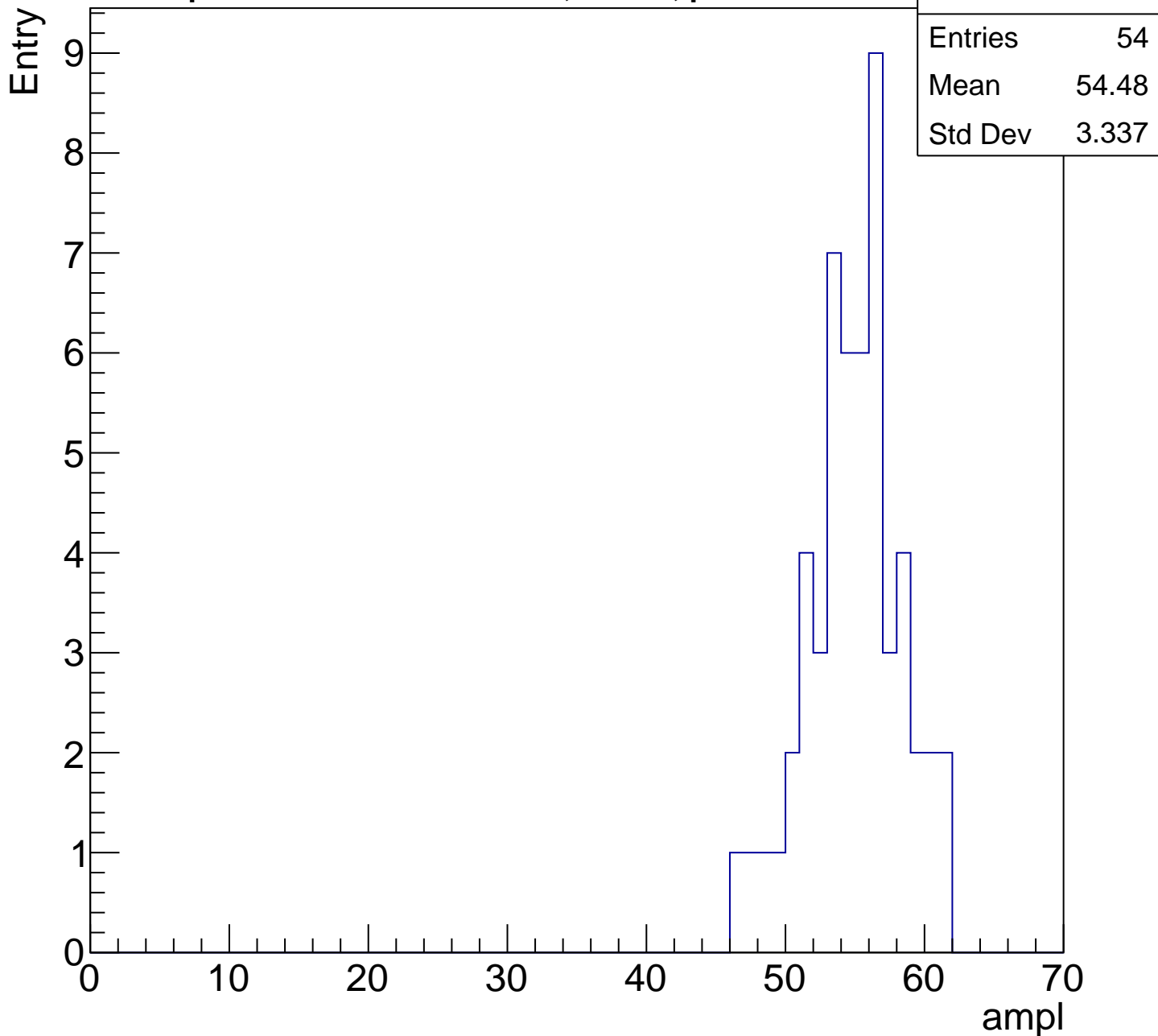
Entry

Entries	74
Mean	48.31
Std Dev	3.646



B1L003S, U20-ch6, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch6, adc5

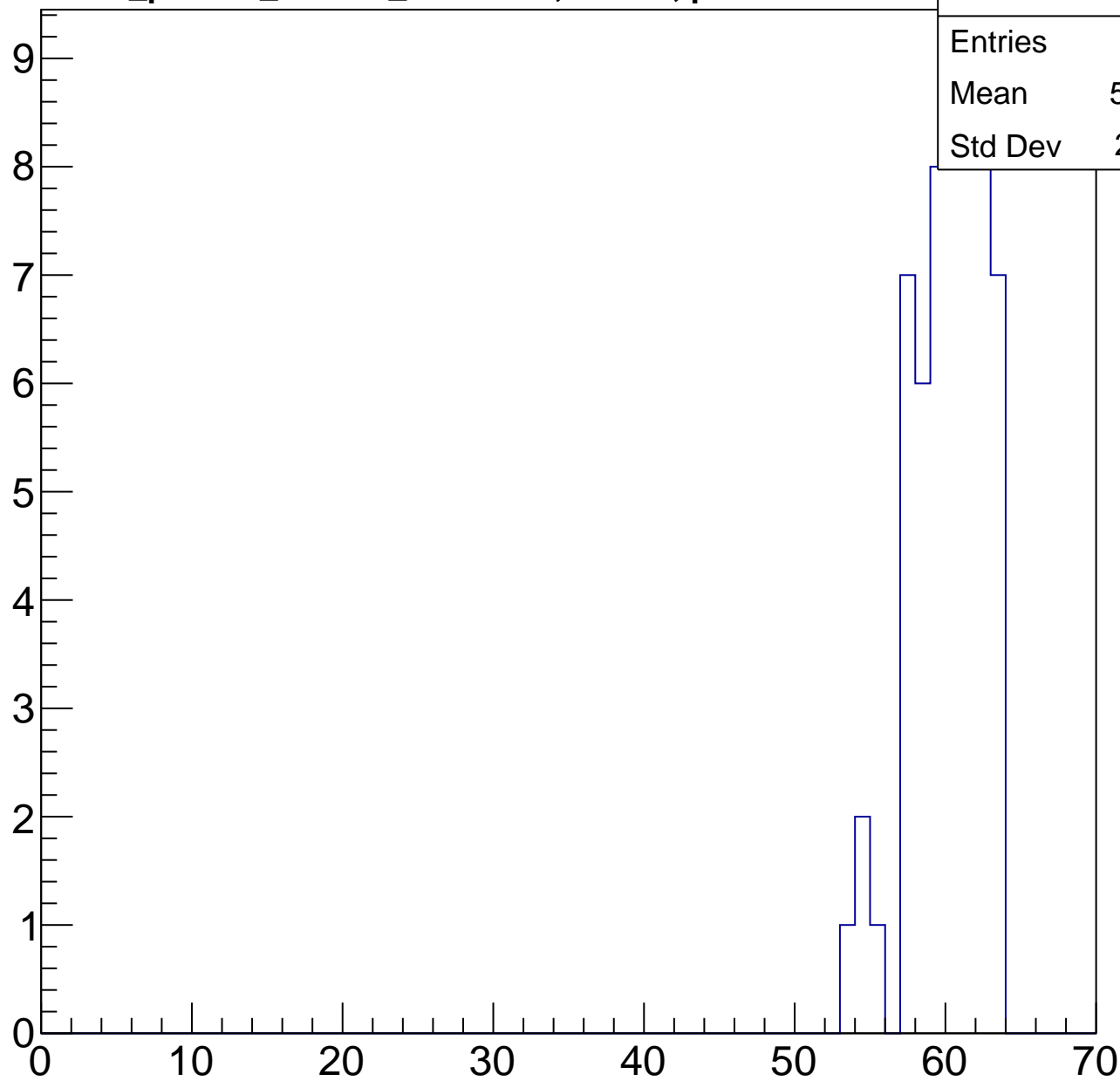
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	59.68
Std Dev	2.451

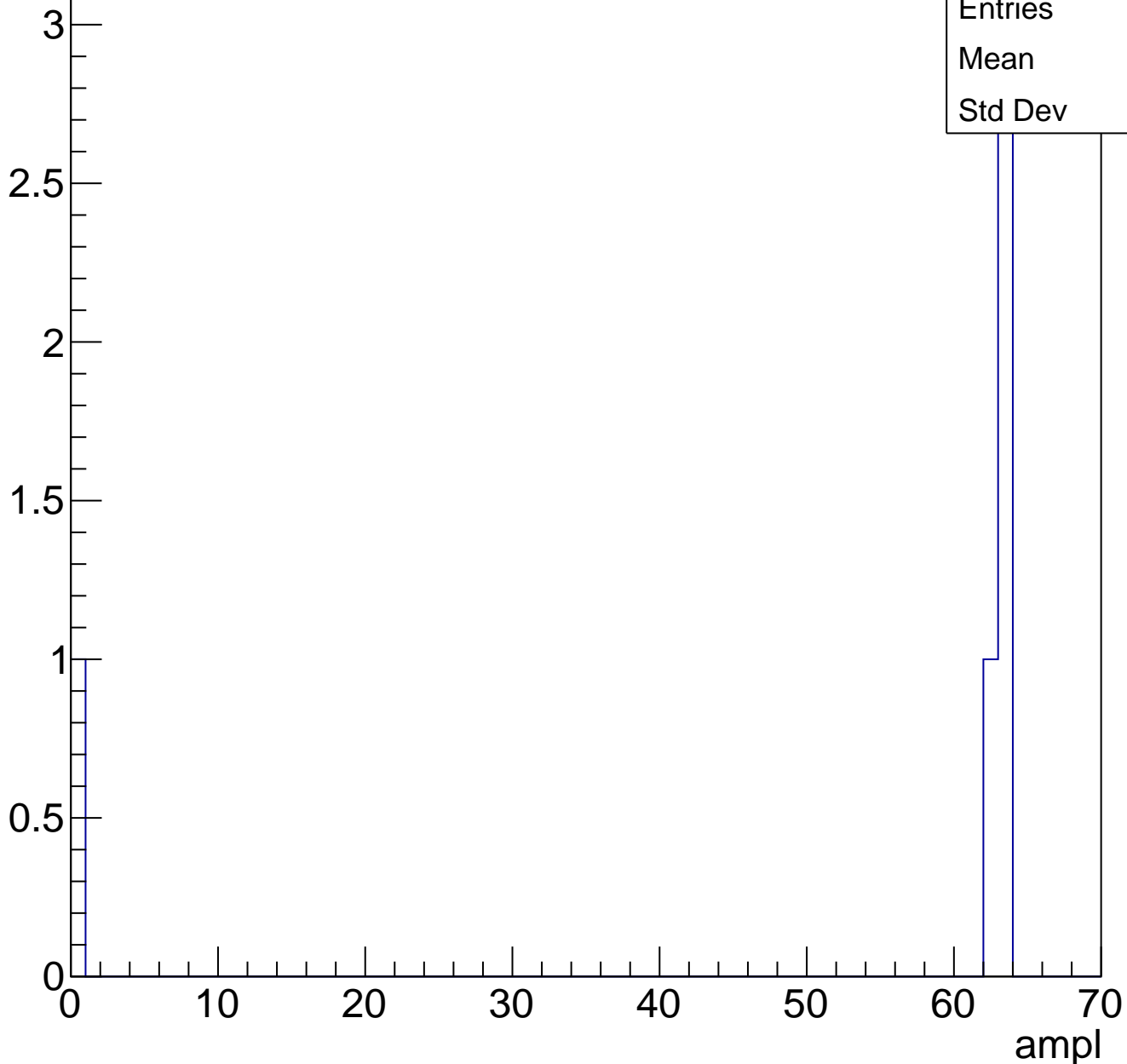
ampl



B1L003S, U20-ch6, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch6, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch7, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	30
Std Dev	3.187

Gaus mean : 30.6970

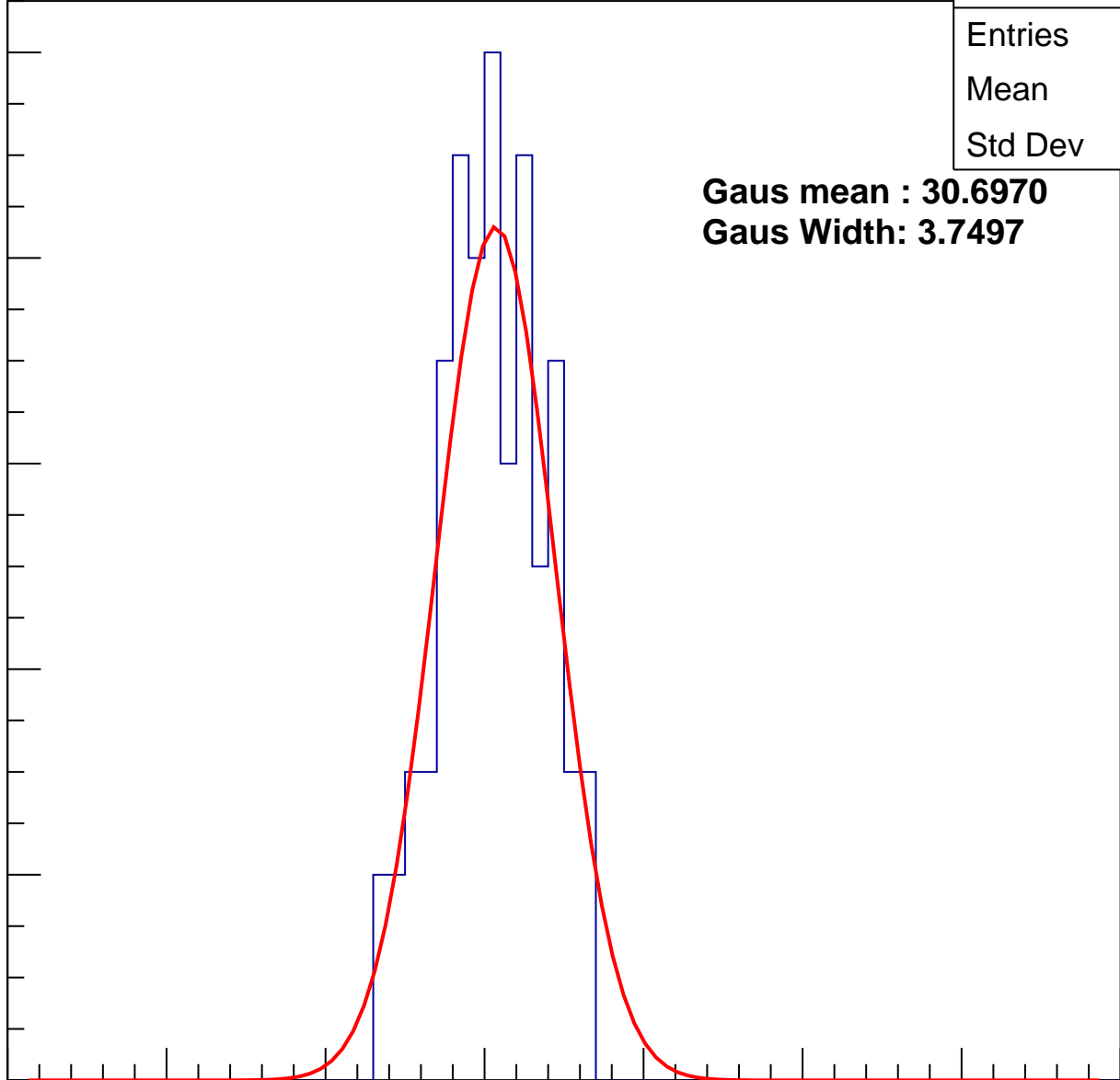
Gaus Width: 3.7497

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch7, adc1

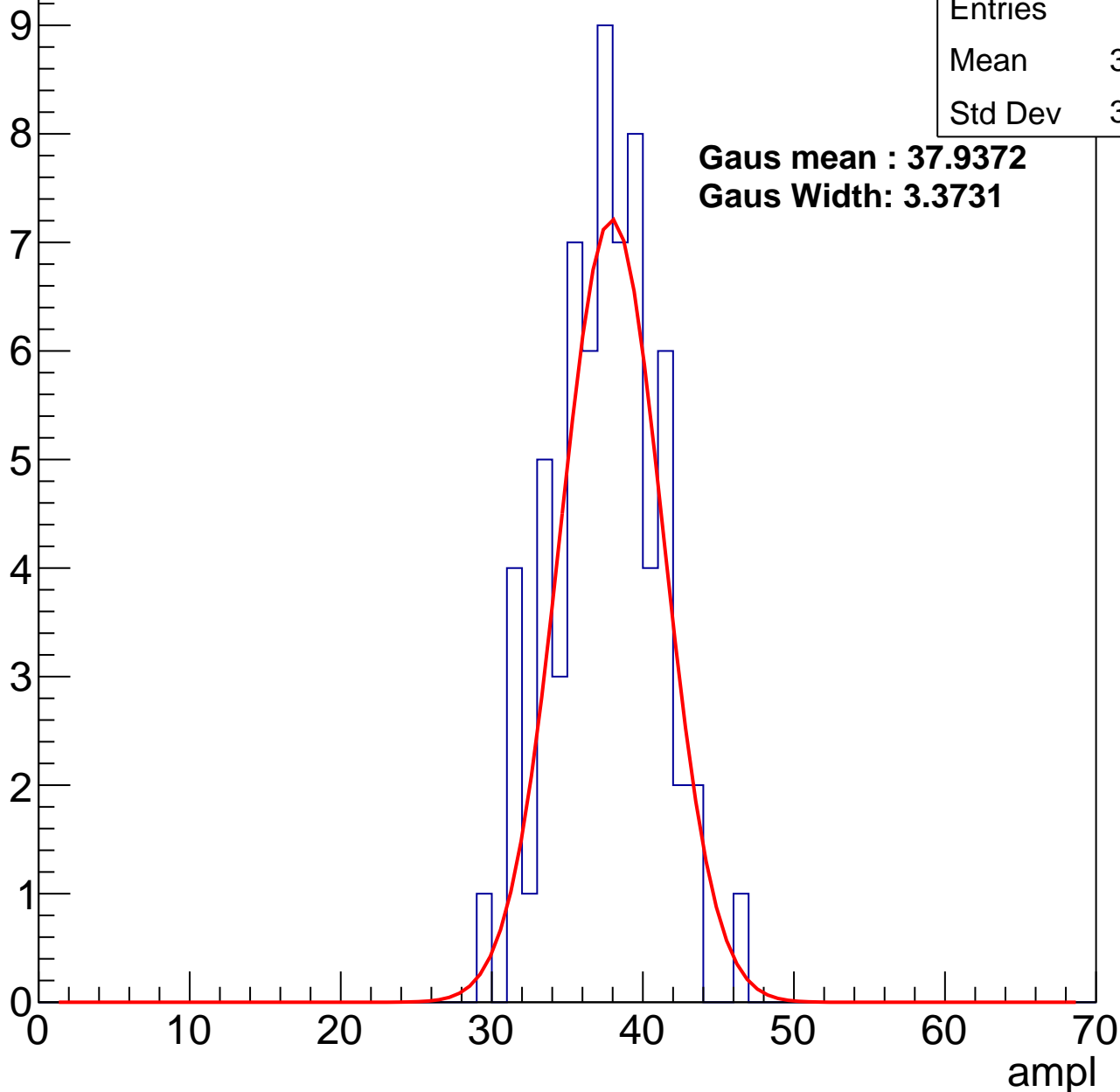
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	37.06
Std Dev	3.366

Gaus mean : 37.9372

Gaus Width: 3.3731

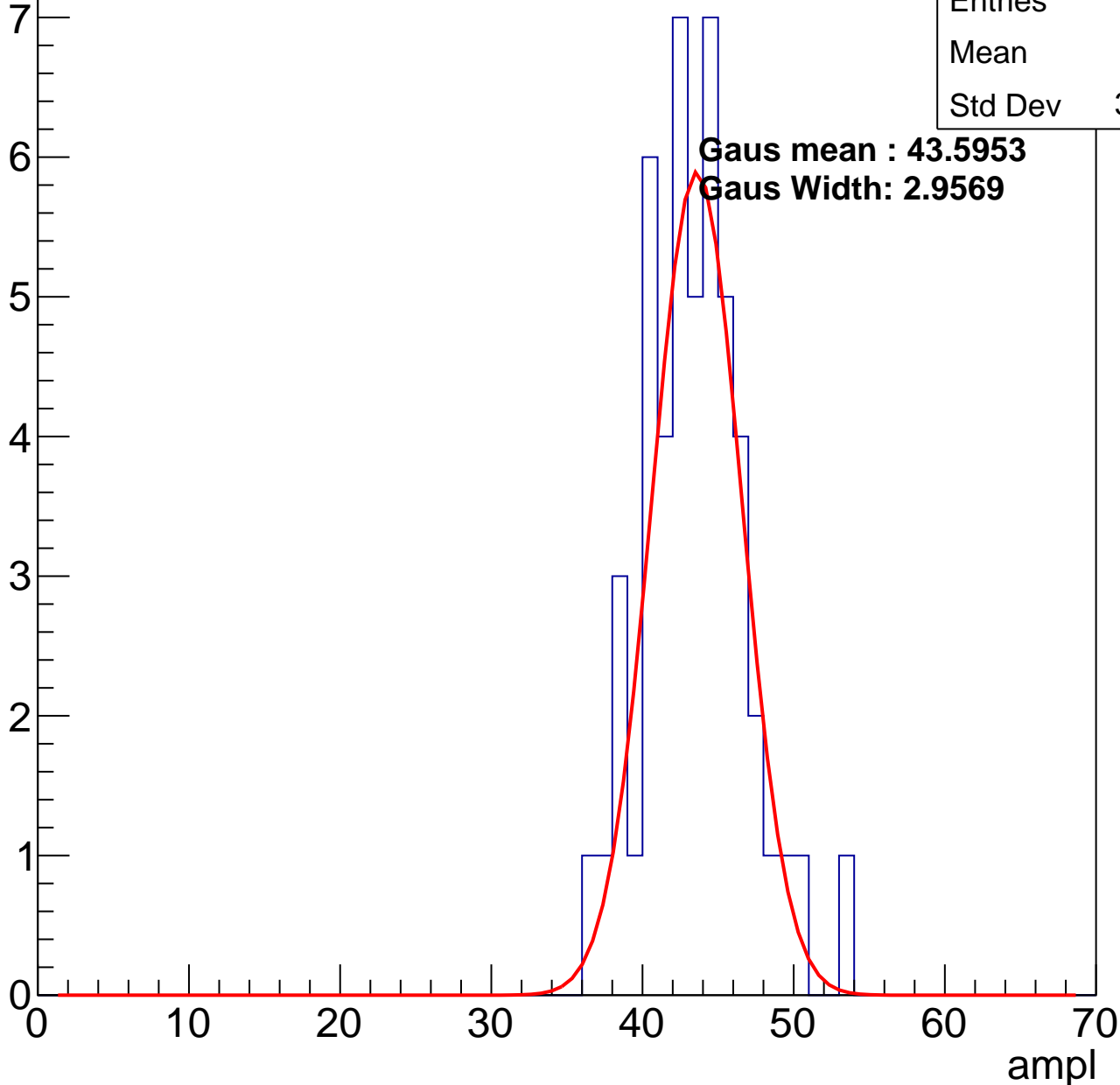


B1L003S, U20-ch7, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43
Std Dev	3.341

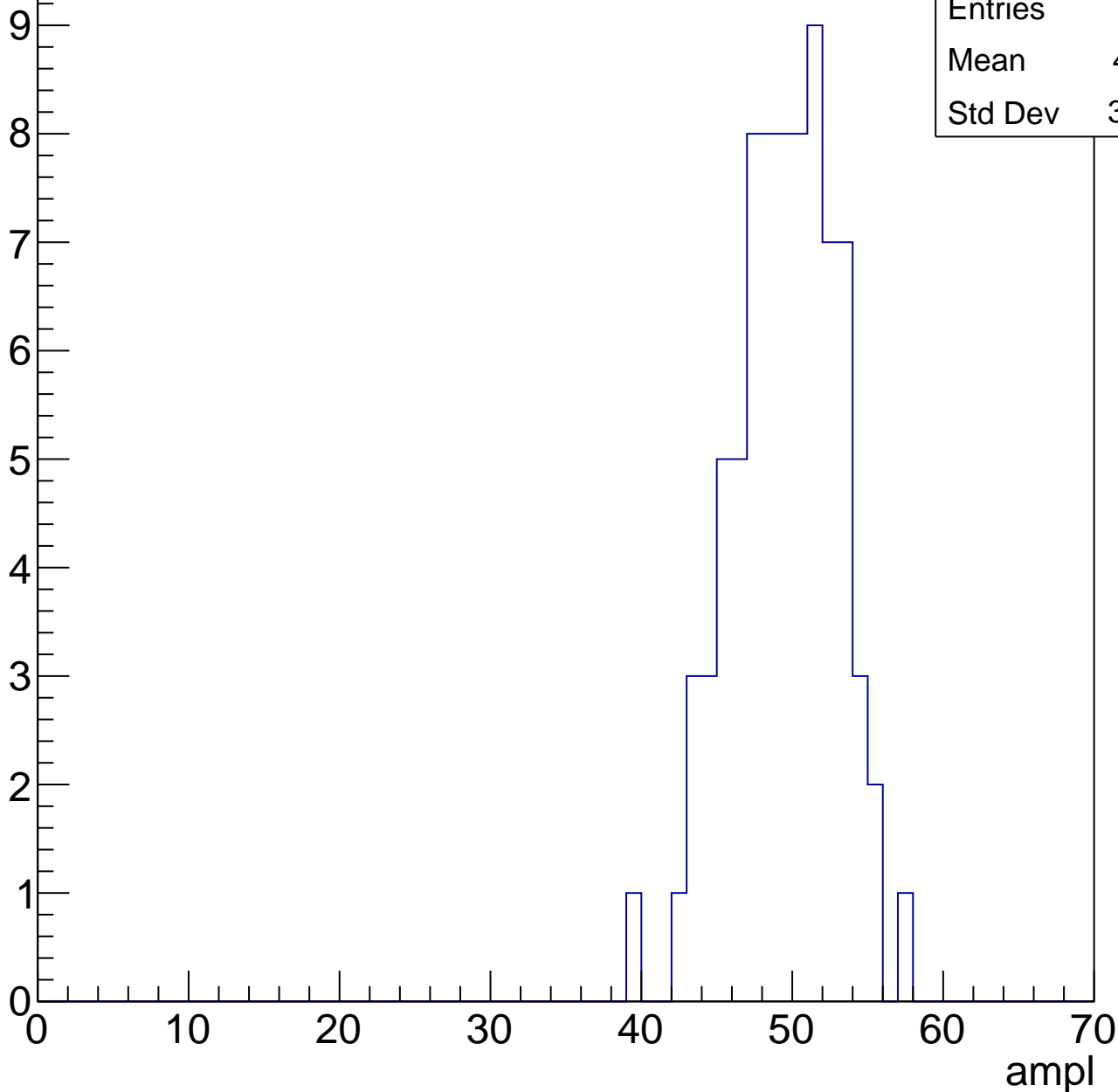


B1L003S, U20-ch7, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	49.01
Std Dev	3.425

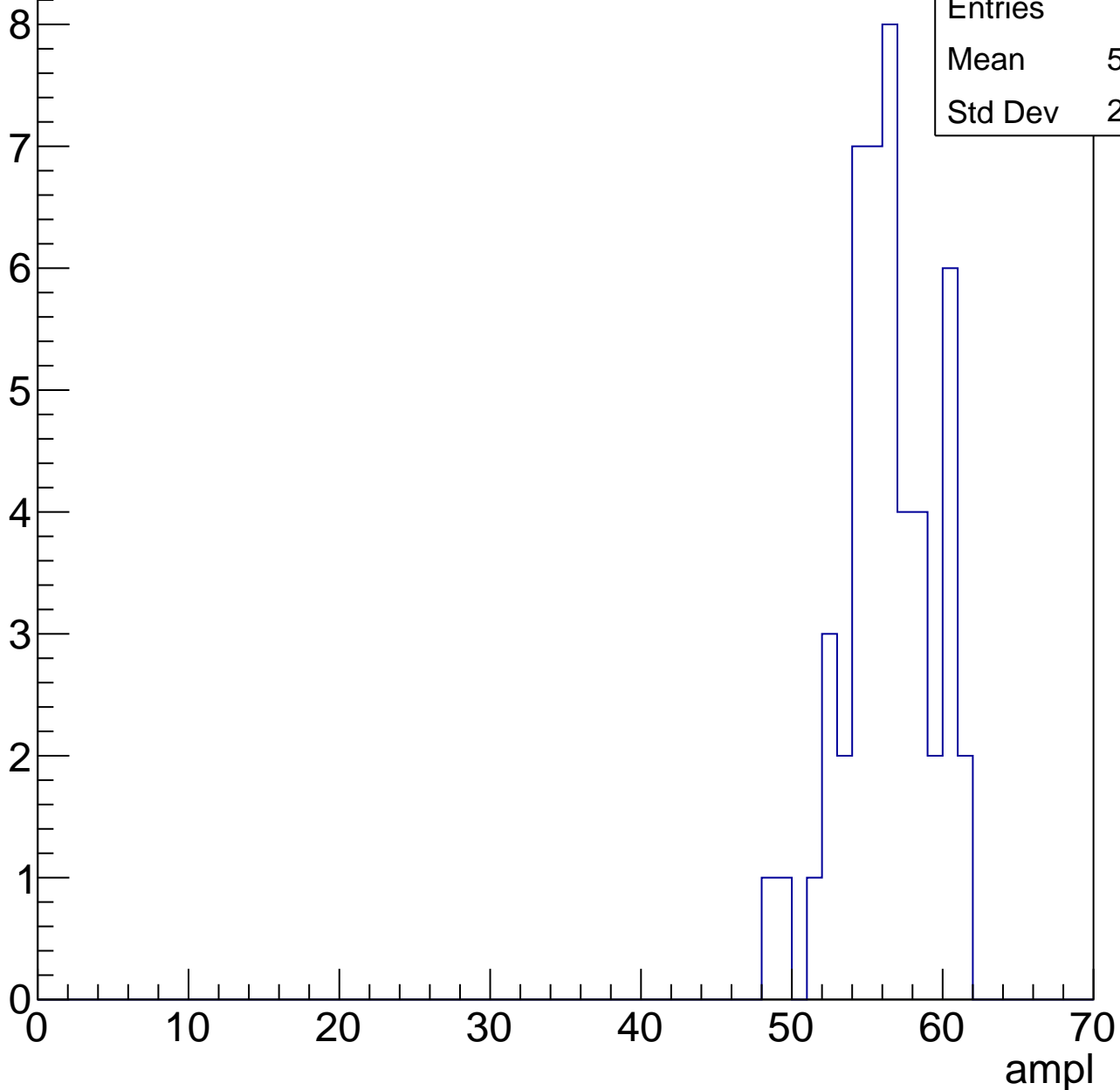


B1L003S, U20-ch7, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	55.85
Std Dev	2.965

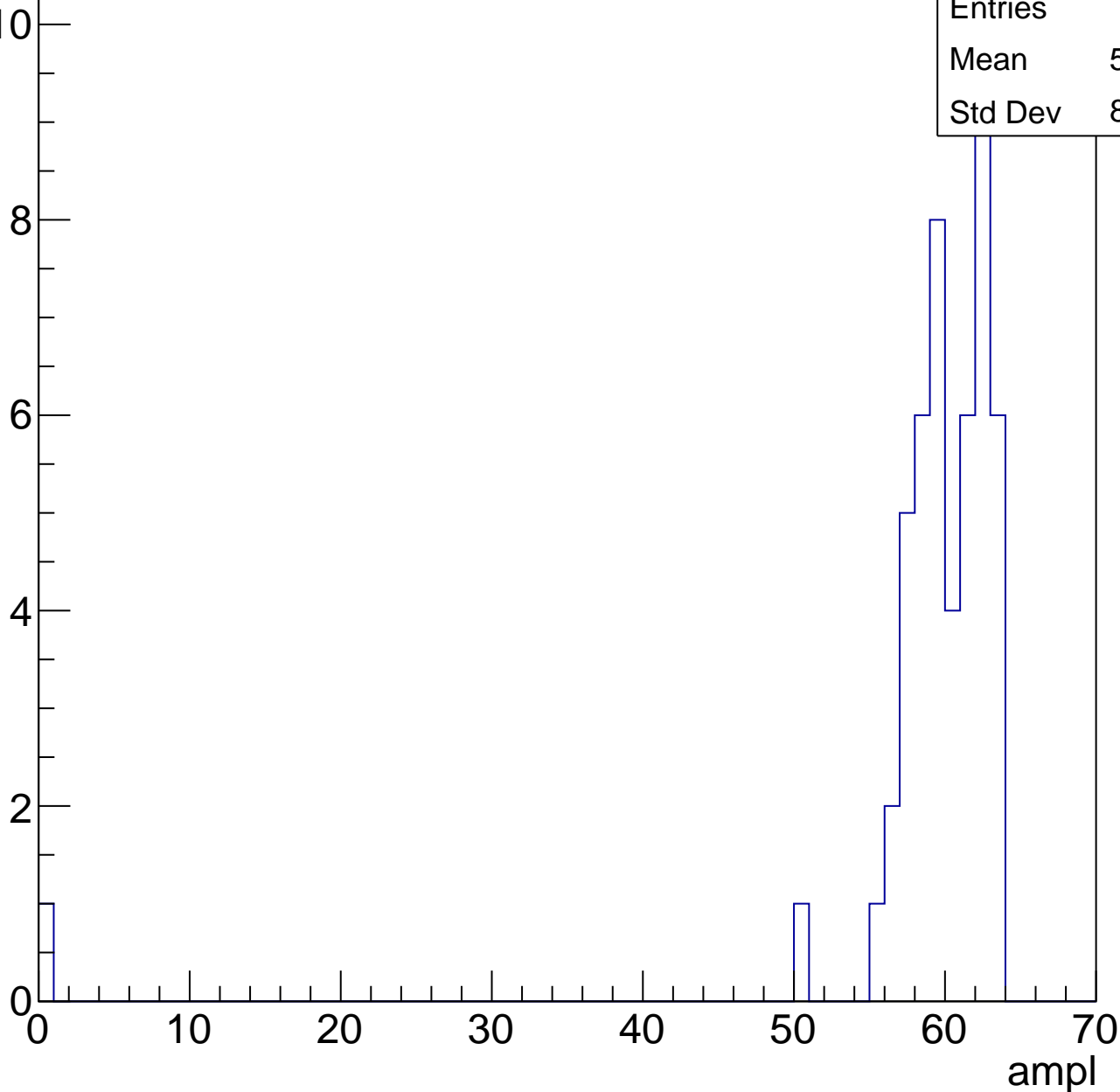


B1L003S, U20-ch7, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

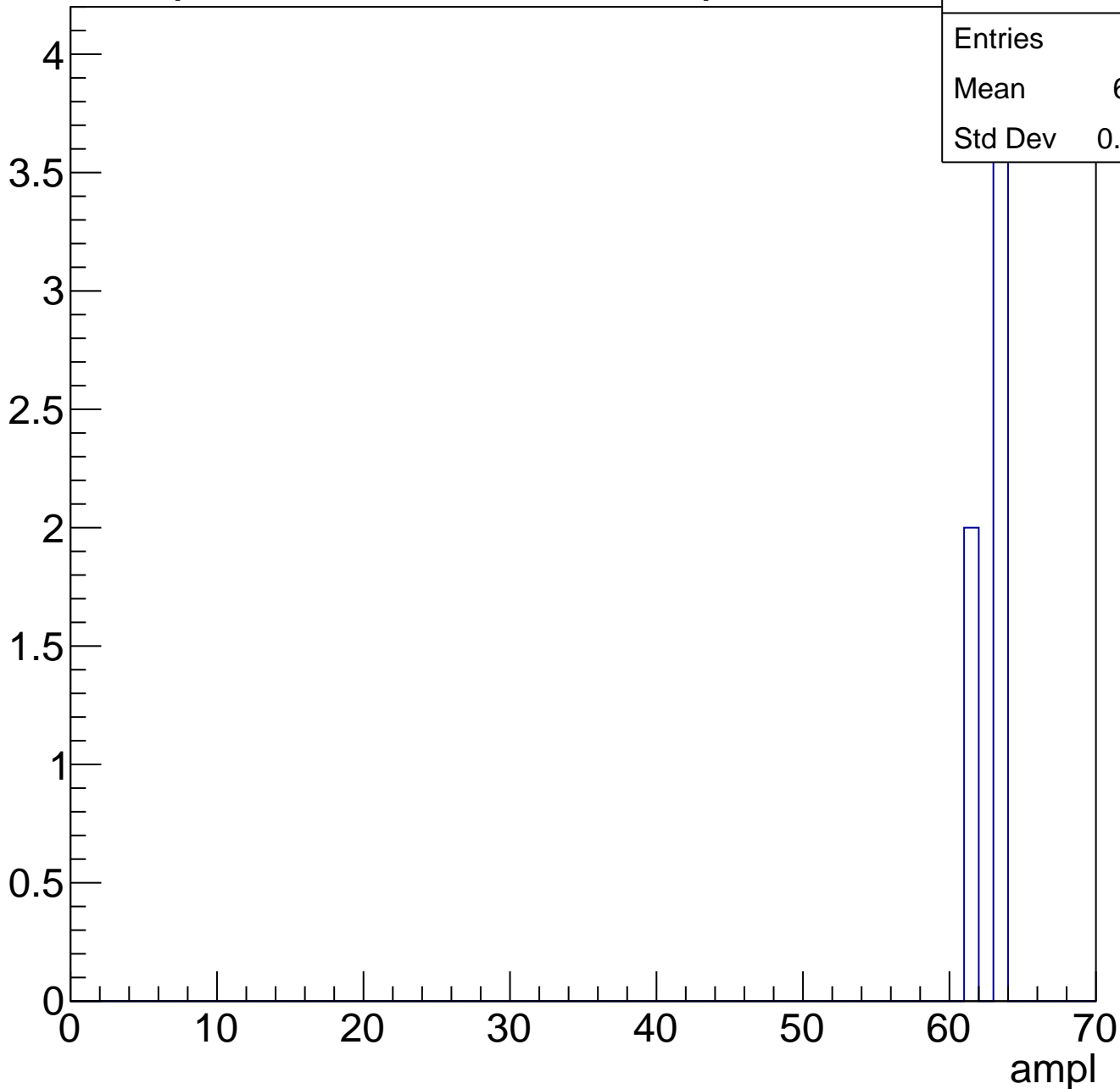
Entries	50
Mean	58.52
Std Dev	8.746



B1L003S, U20-ch7, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch7, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch8, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	28.63
Std Dev	4.689

Gaus mean : 29.6751

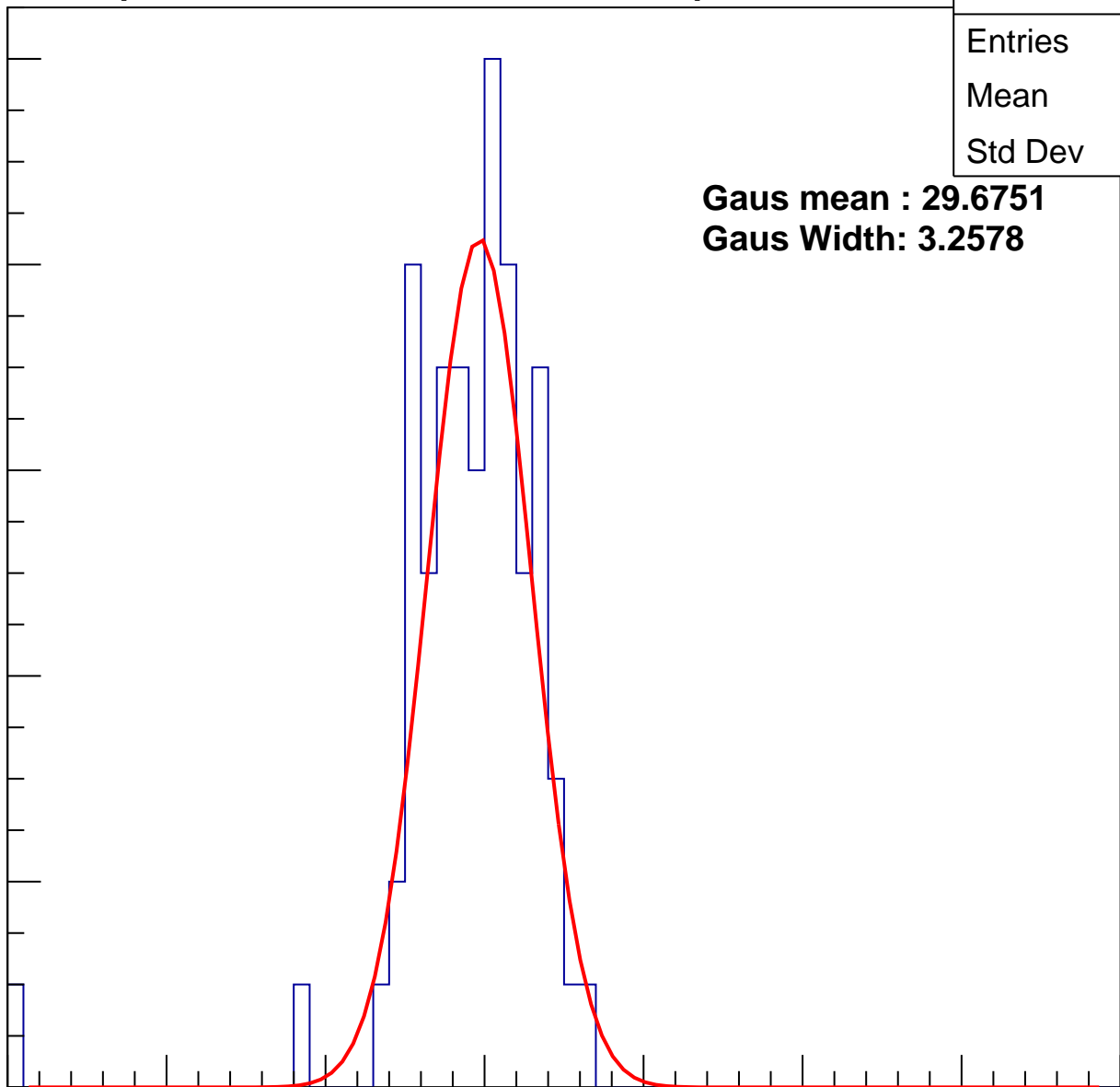
Gaus Width: 3.2578

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch8, adc1

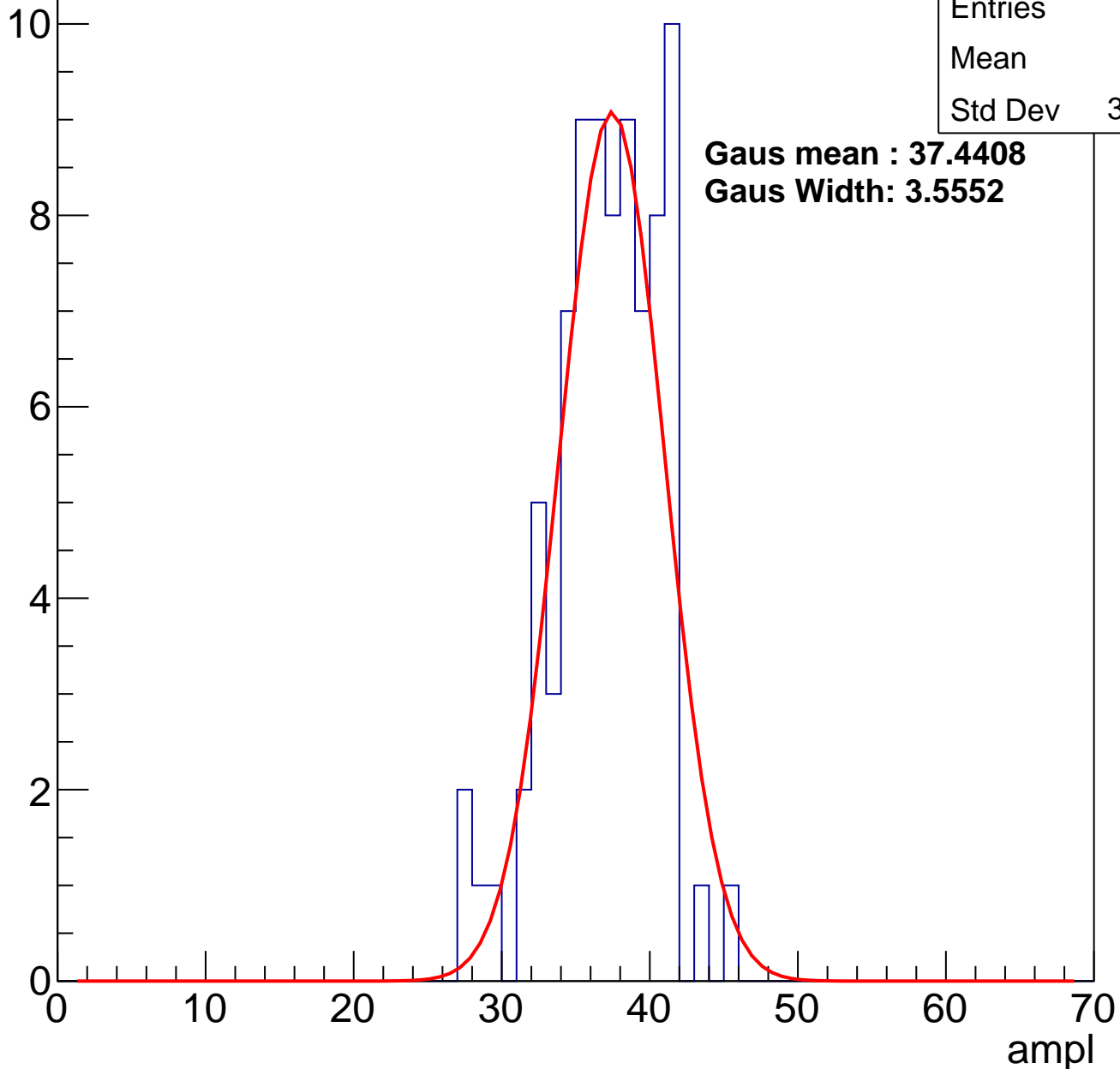
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	36.6
Std Dev	3.563

Gaus mean : 37.4408

Gaus Width: 3.5552

Entry



B1L003S, U20-ch8, adc2

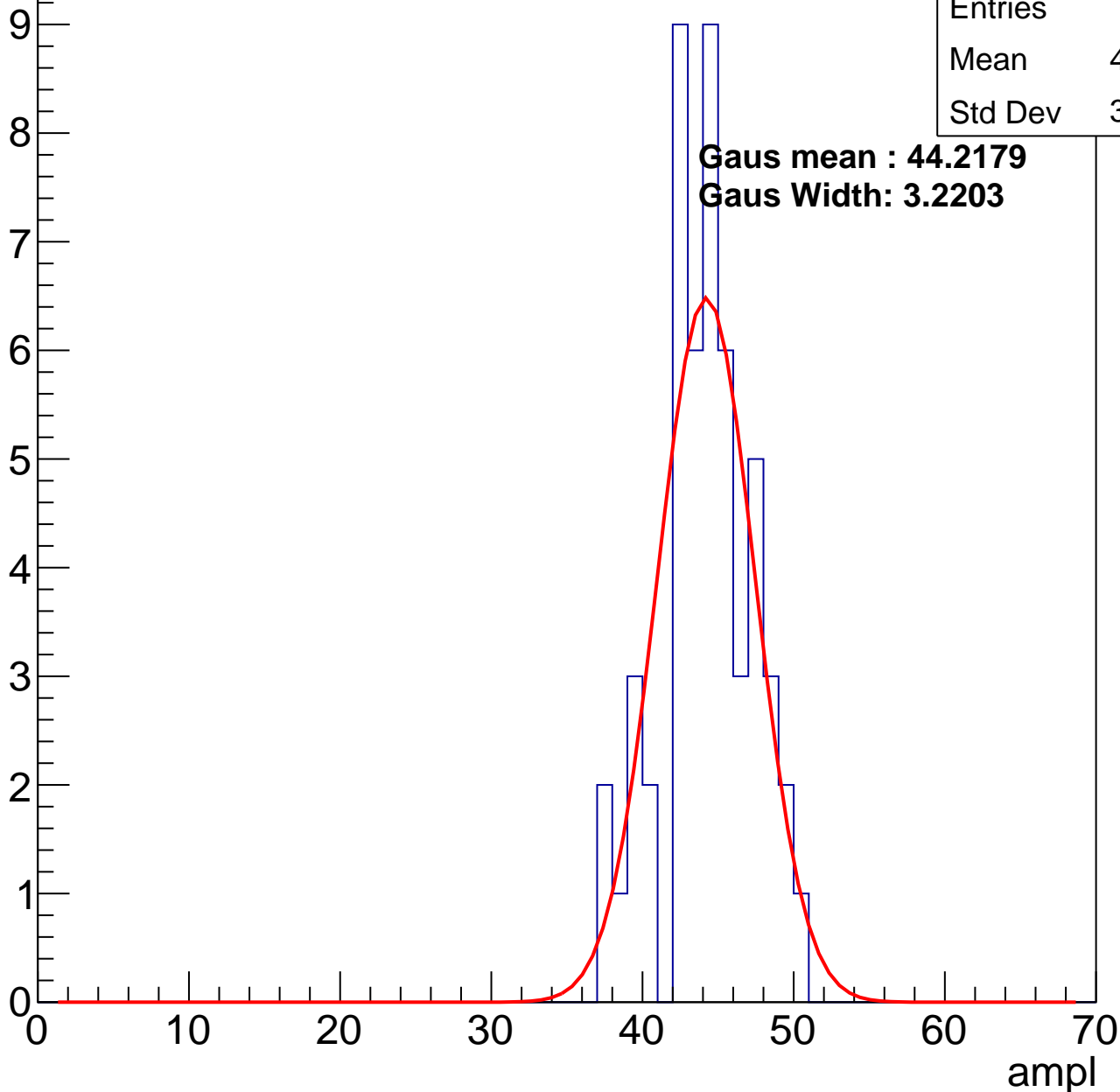
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.77
Std Dev	3.029

Gaus mean : 44.2179

Gaus Width: 3.2203

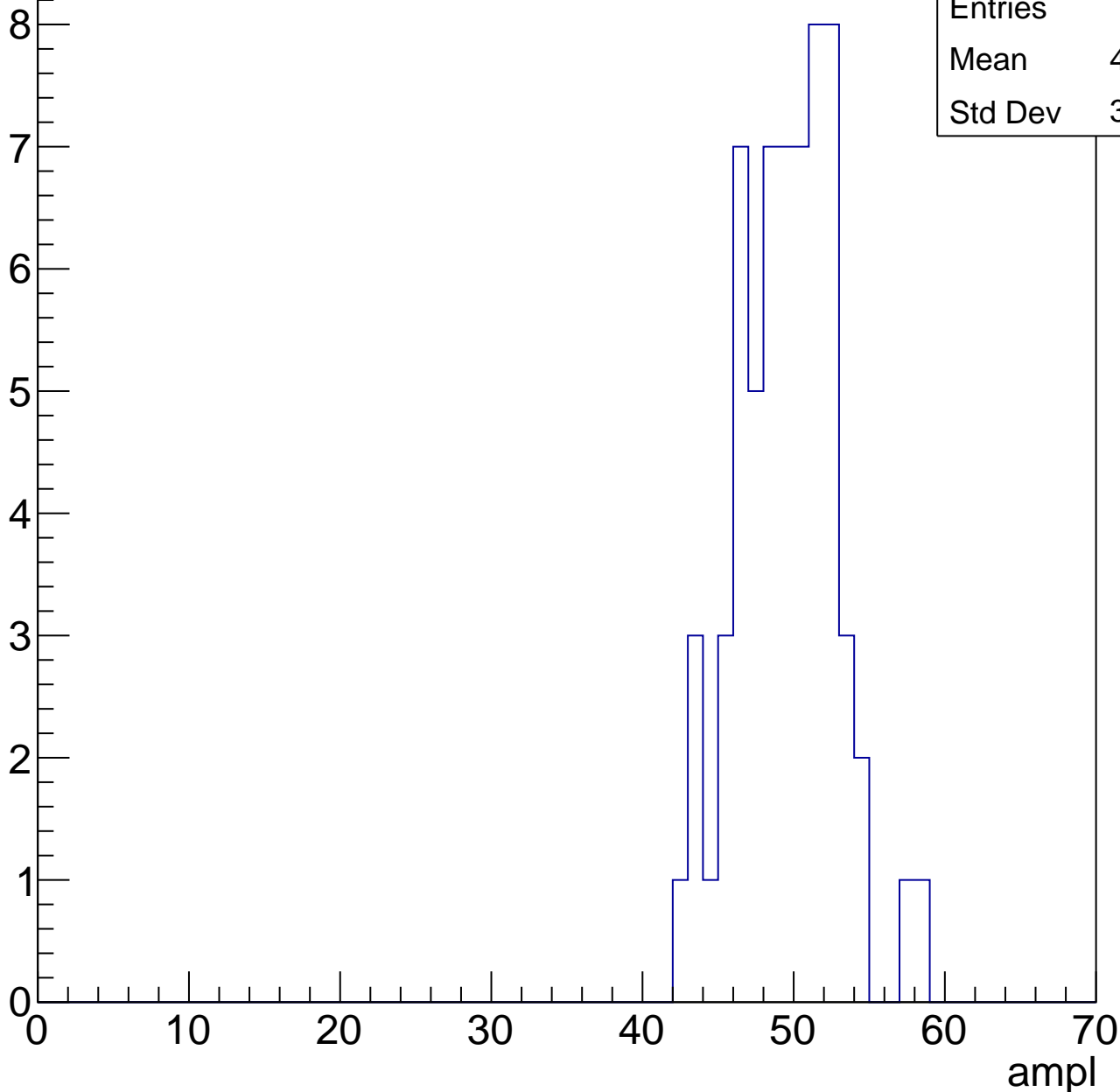


B1L003S, U20-ch8, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.09
Std Dev	3.244

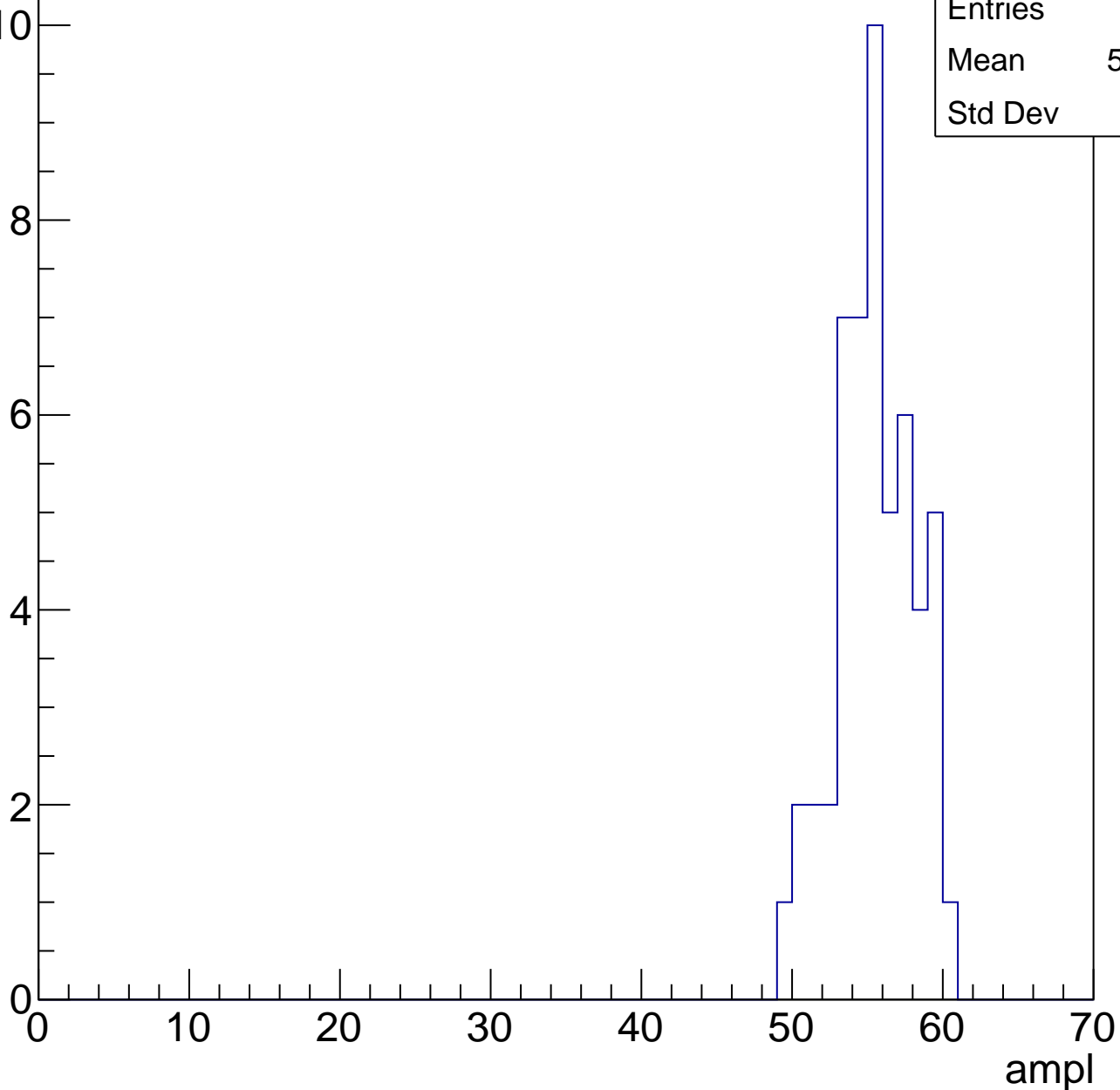


B1L003S, U20-ch8, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.06
Std Dev	2.56

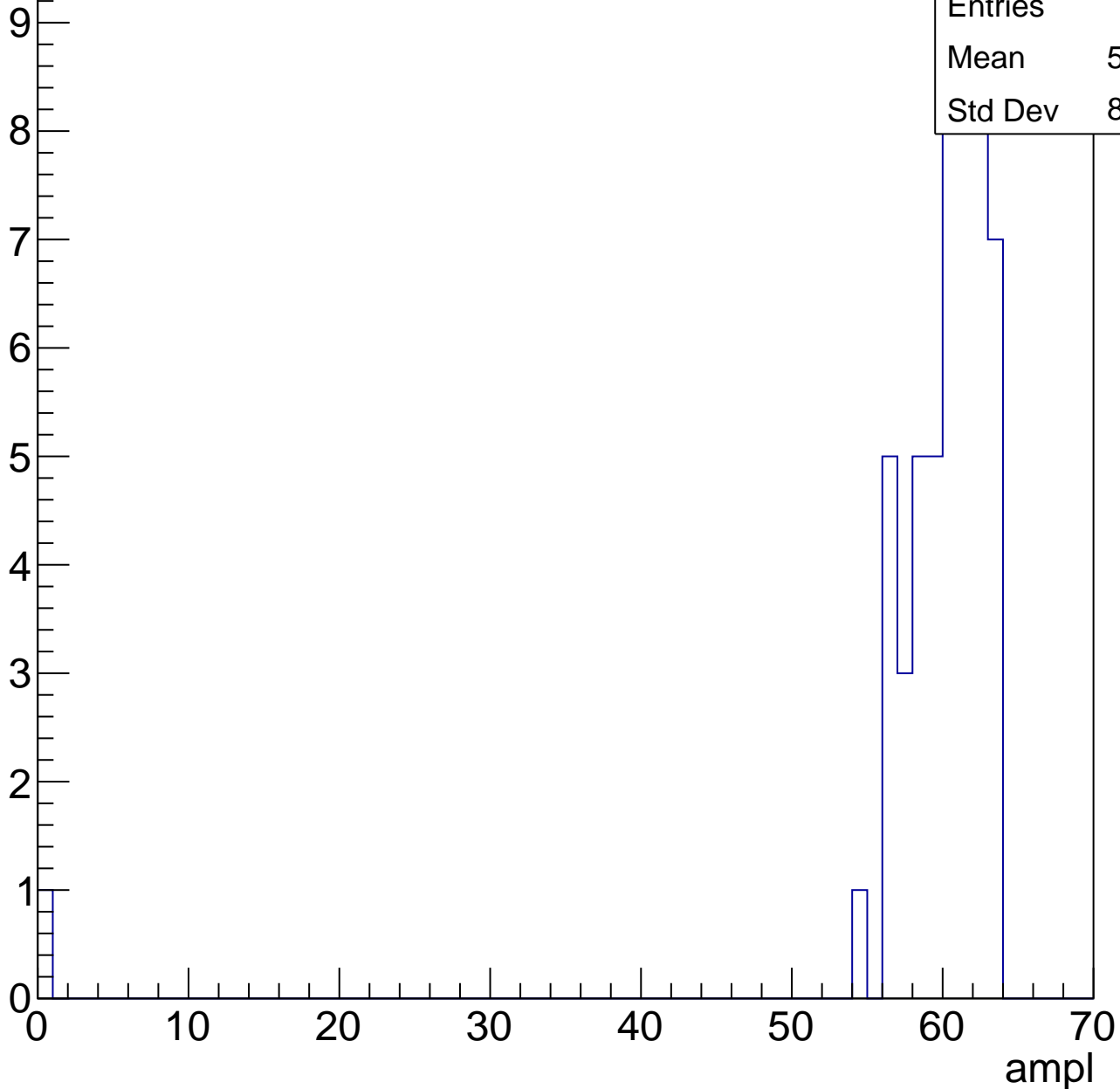


B1L003S, U20-ch8, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

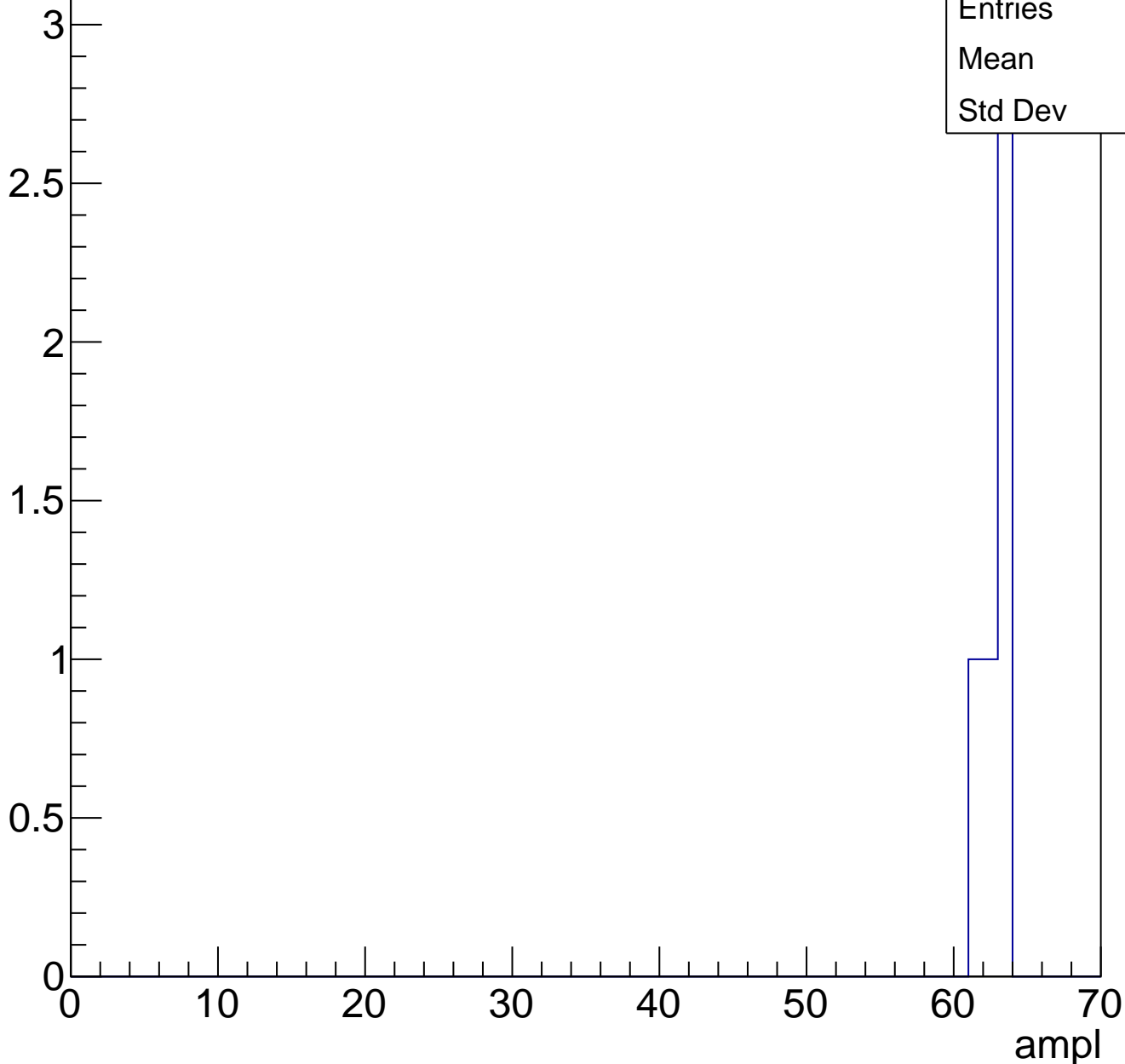
Entries	54
Mean	58.85
Std Dev	8.394



B1L003S, U20-ch8, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch8, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch9, adc0

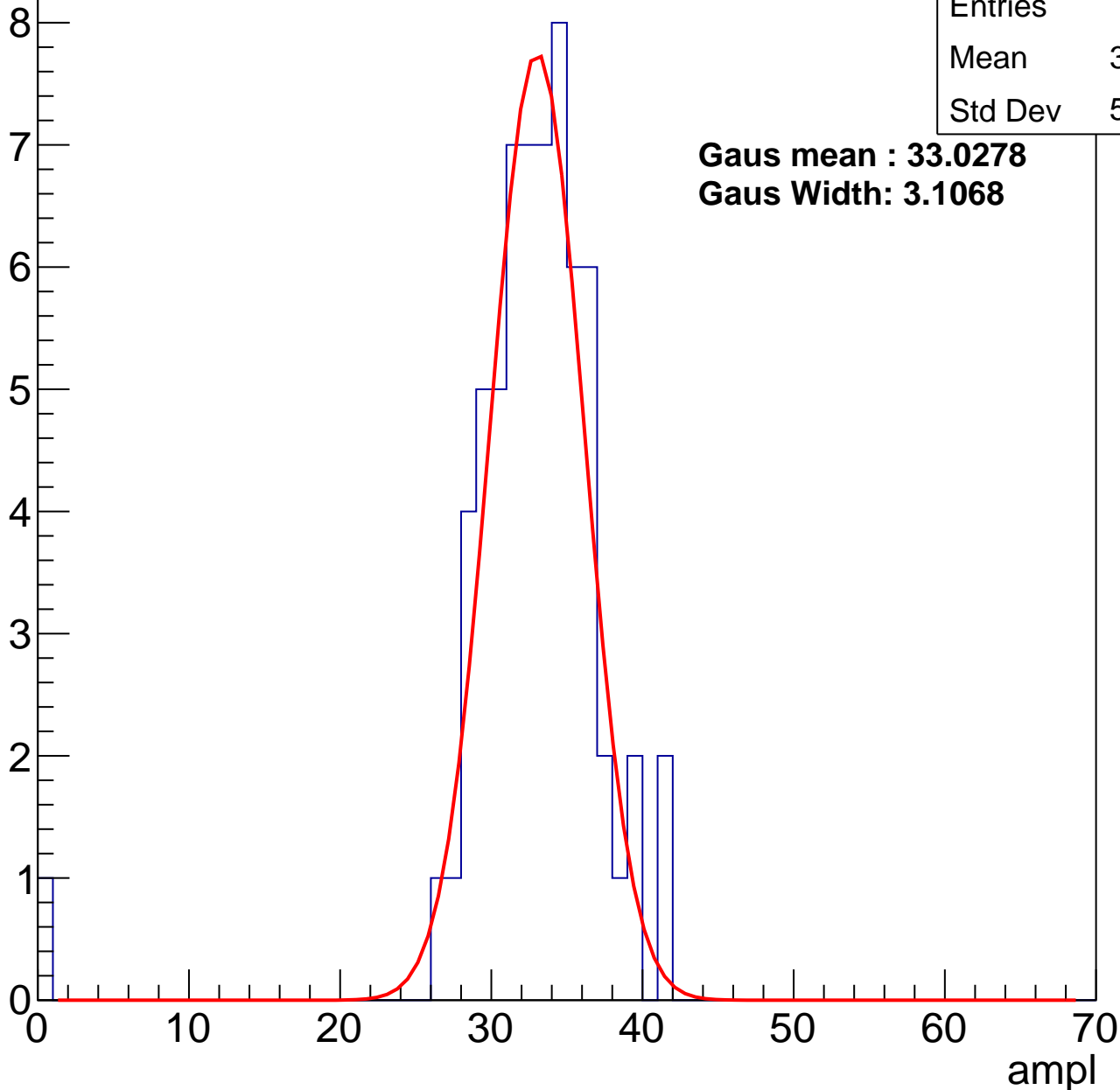
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	32.34
Std Dev	5.187

Gaus mean : 33.0278

Gaus Width: 3.1068



B1L003S, U20-ch9, adc1

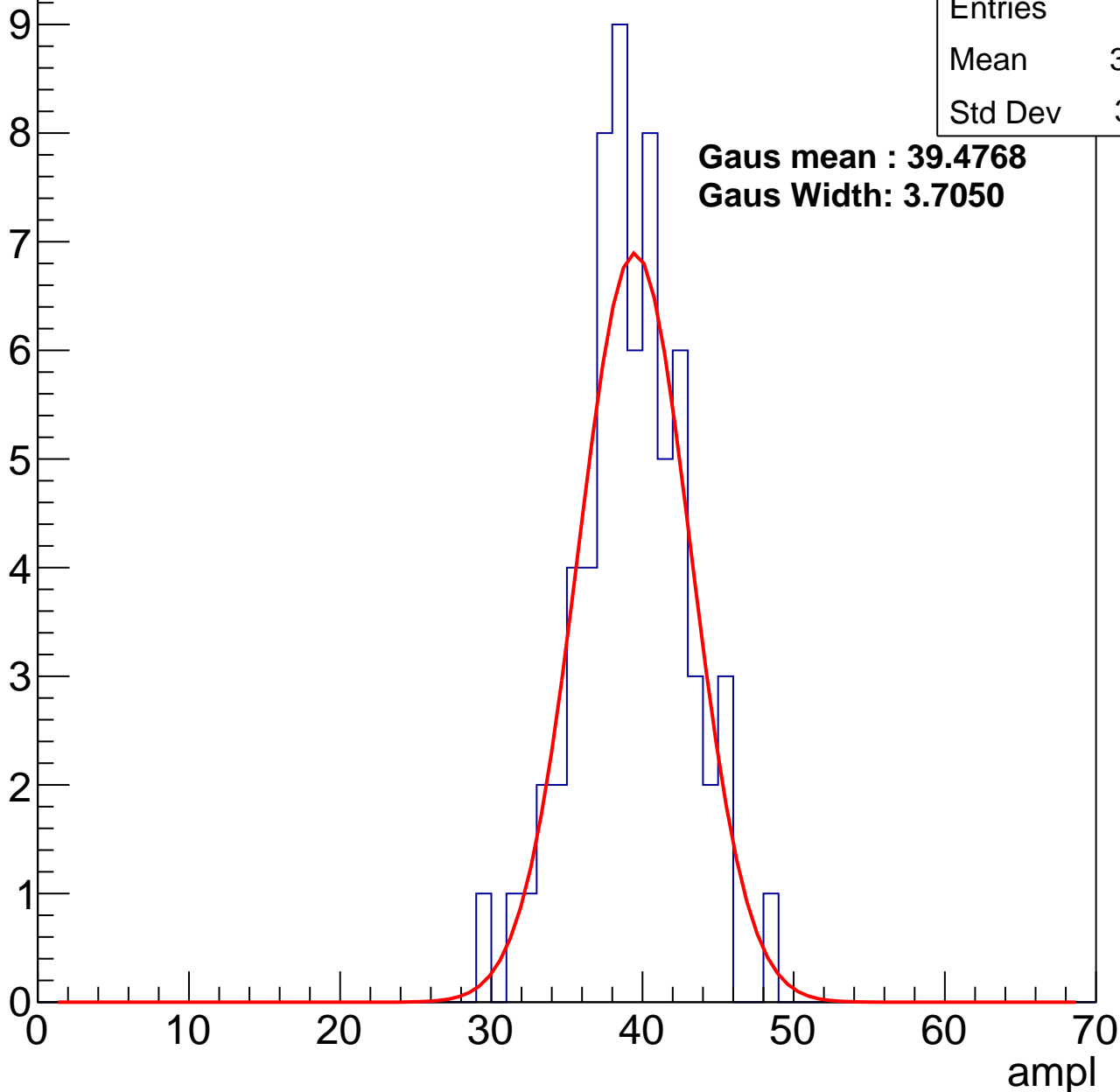
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	38.77
Std Dev	3.571

Gaus mean : 39.4768

Gaus Width: 3.7050



B1L003S, U20-ch9, adc2

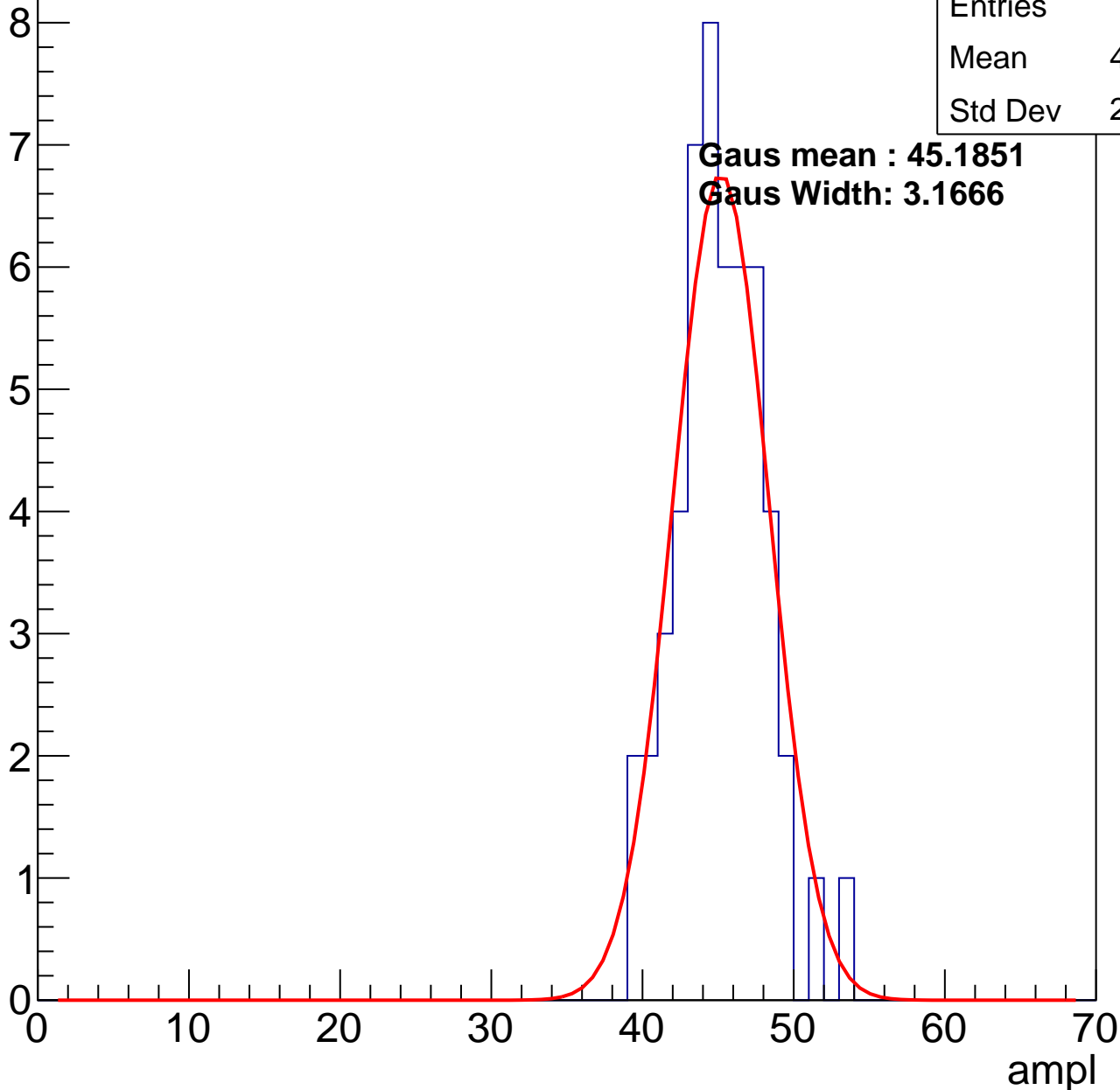
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	44.69
Std Dev	2.893

Gaus mean : 45.1851

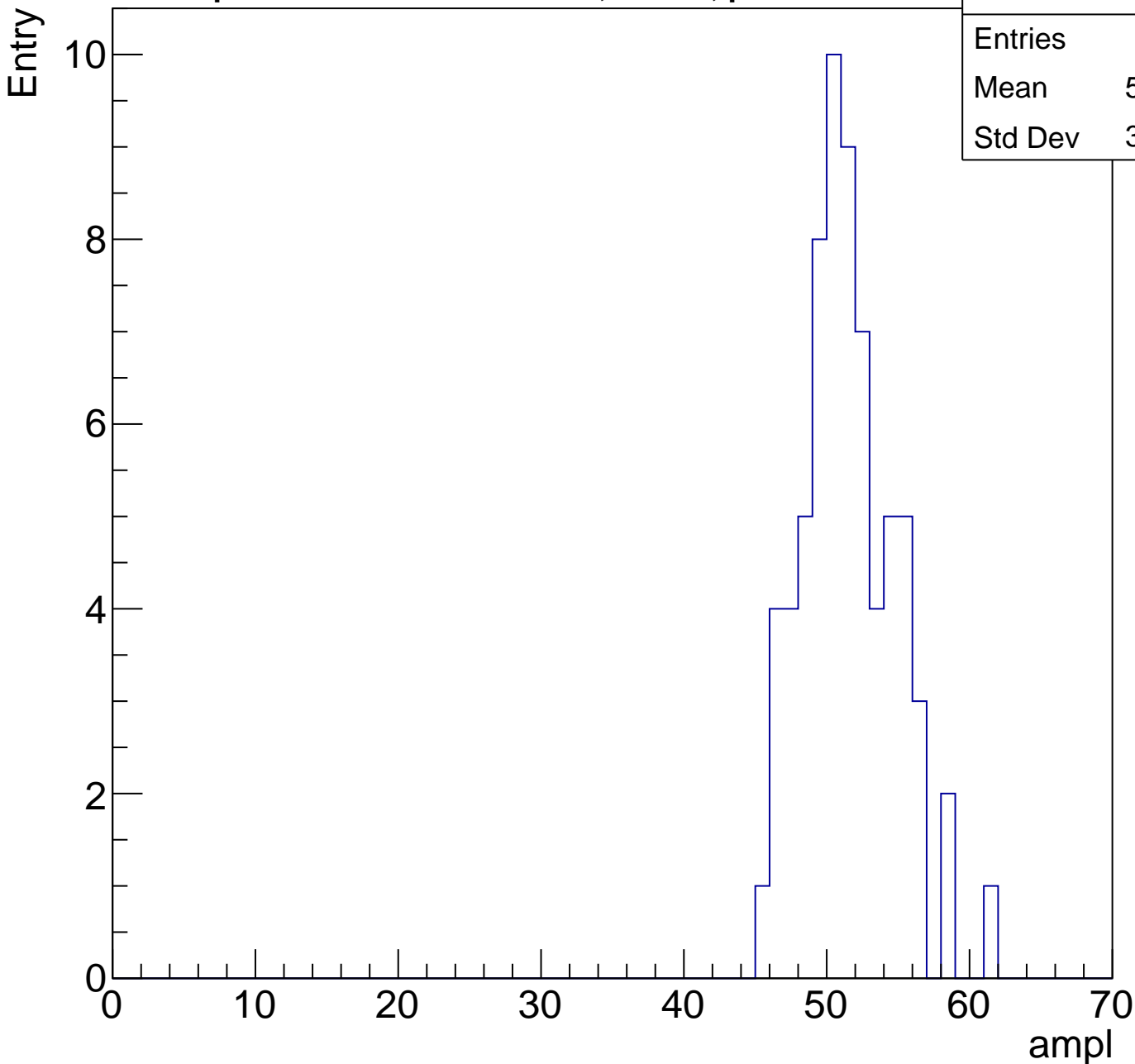
Gaus Width: 3.1666



B1L003S, U20-ch9, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	51.09
Std Dev	3.226



B1L003S, U20-ch9, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

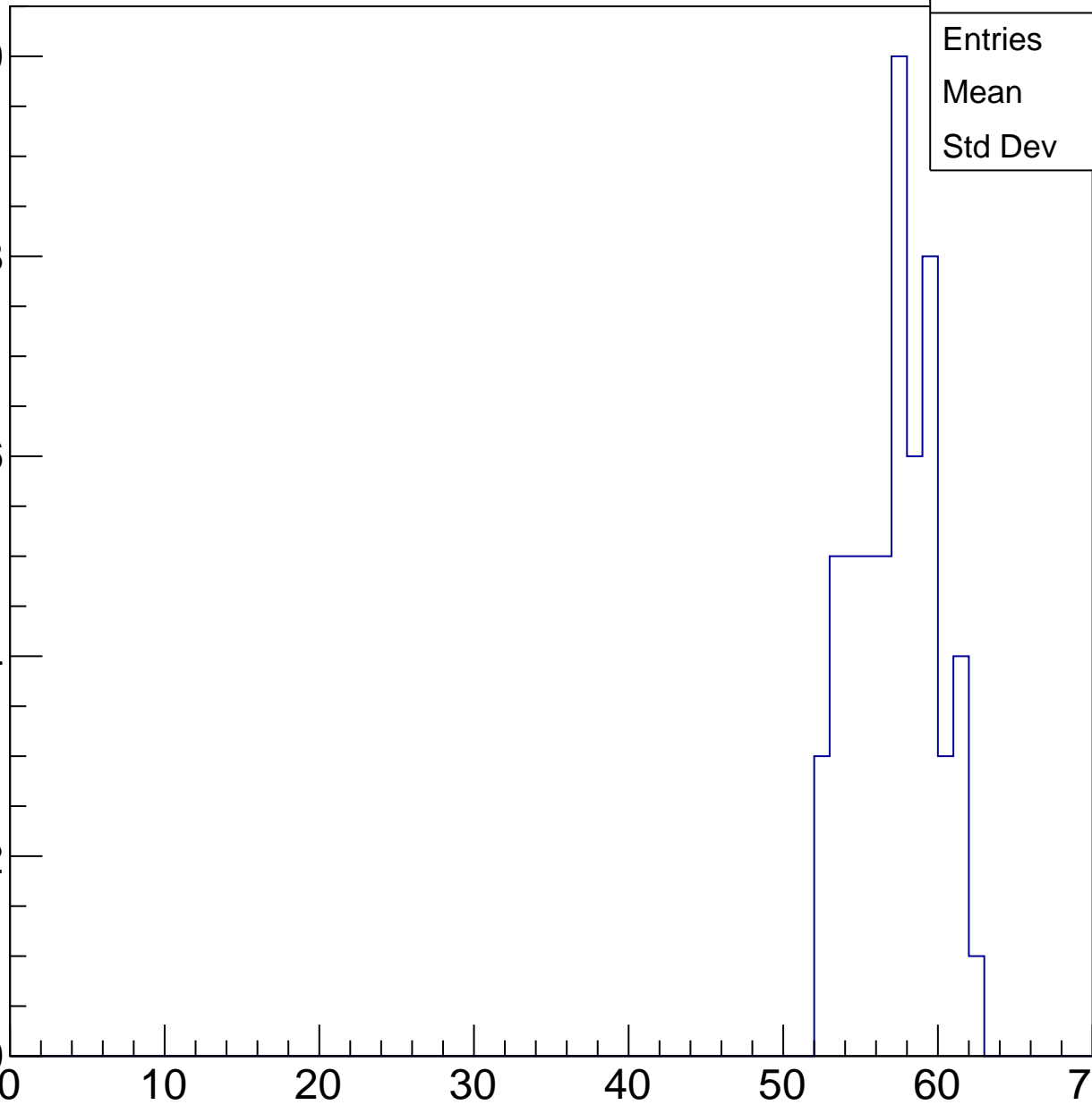
50

60

70

ampl

Entries	55
Mean	56.76
Std Dev	2.614

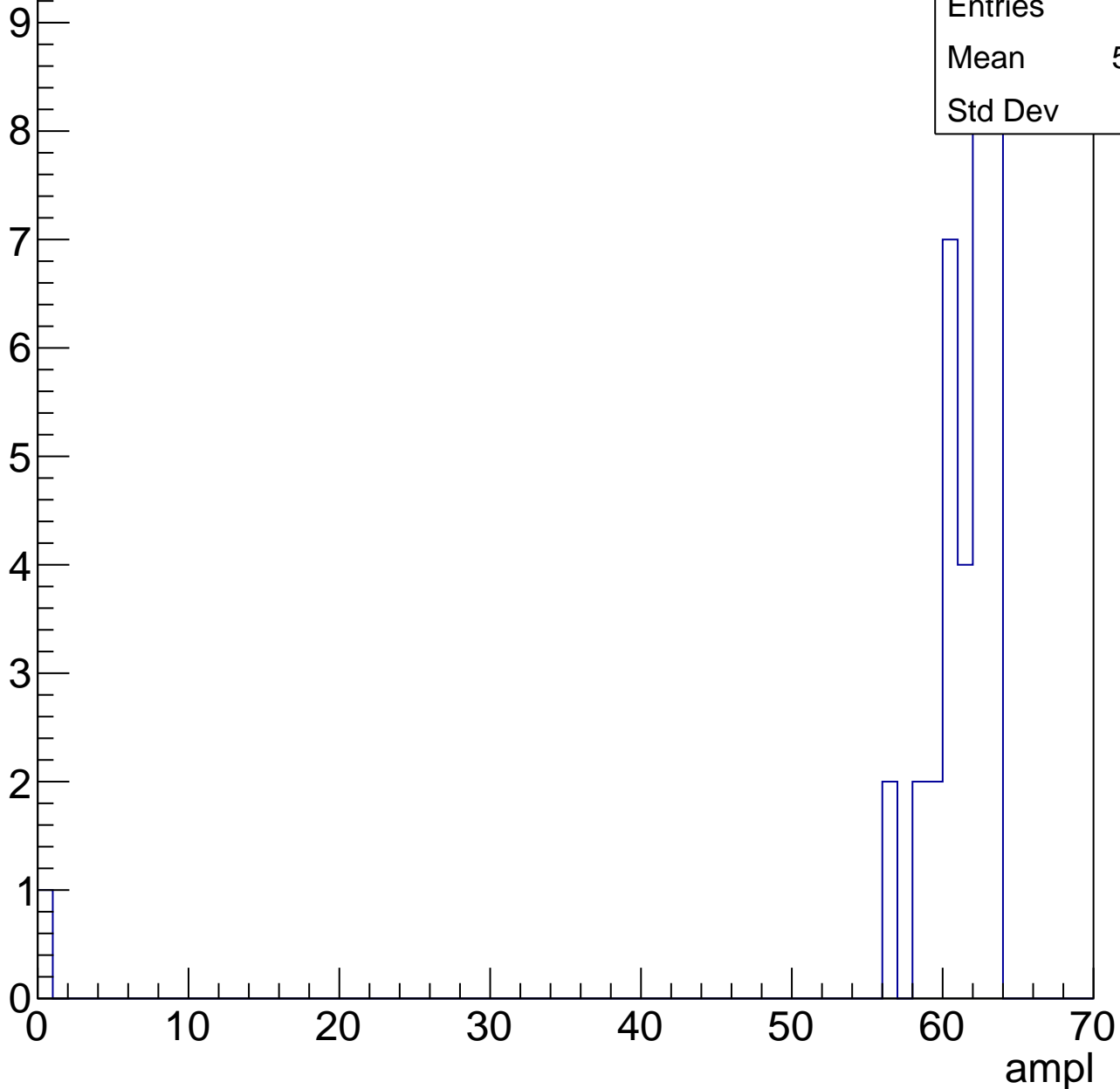


B1L003S, U20-ch9, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.31
Std Dev	10.2



B1L003S, U20-ch9, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch9, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch10, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	30.19
Std Dev	3.29

Gaus mean : 30.2418

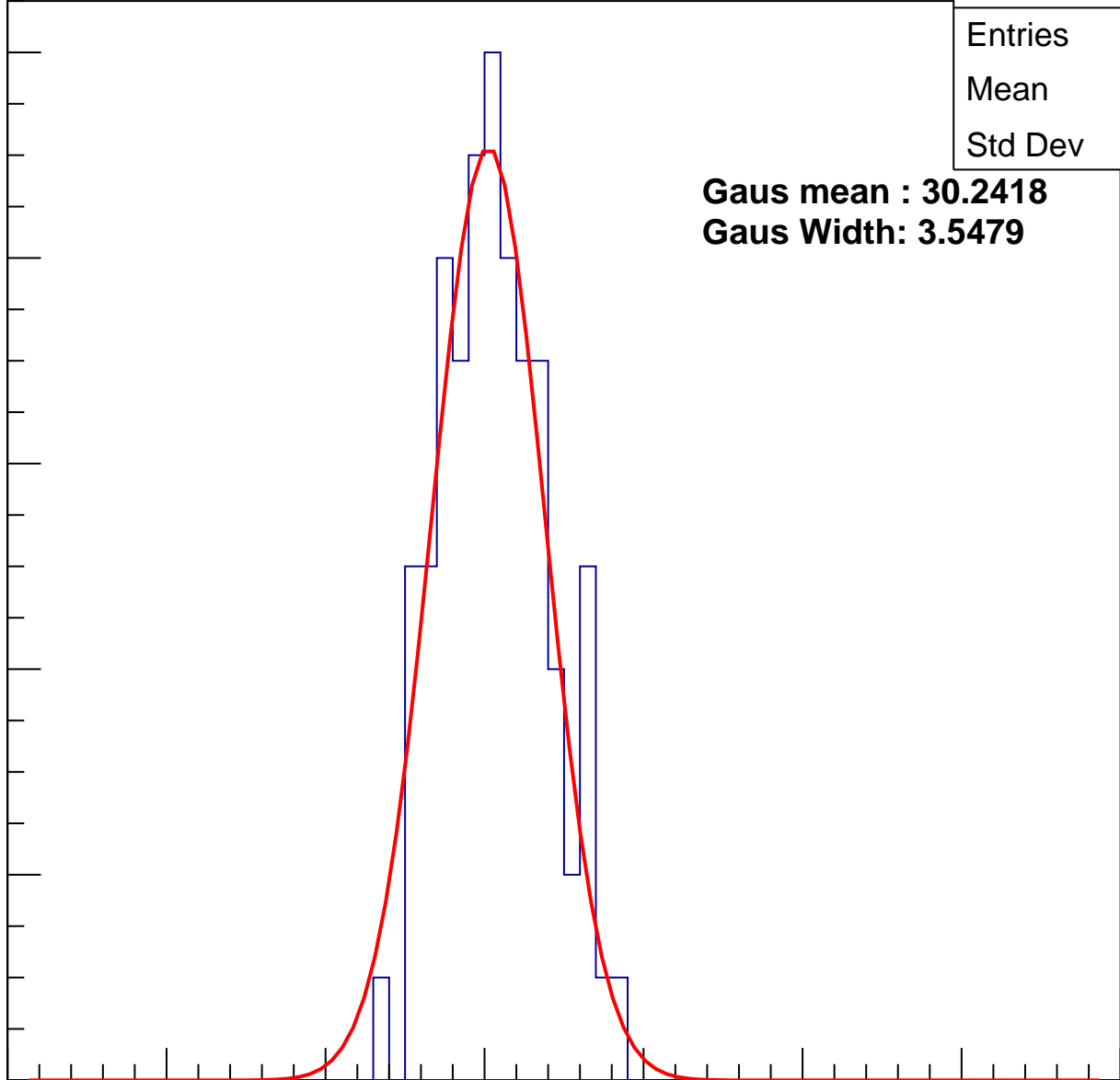
Gaus Width: 3.5479

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



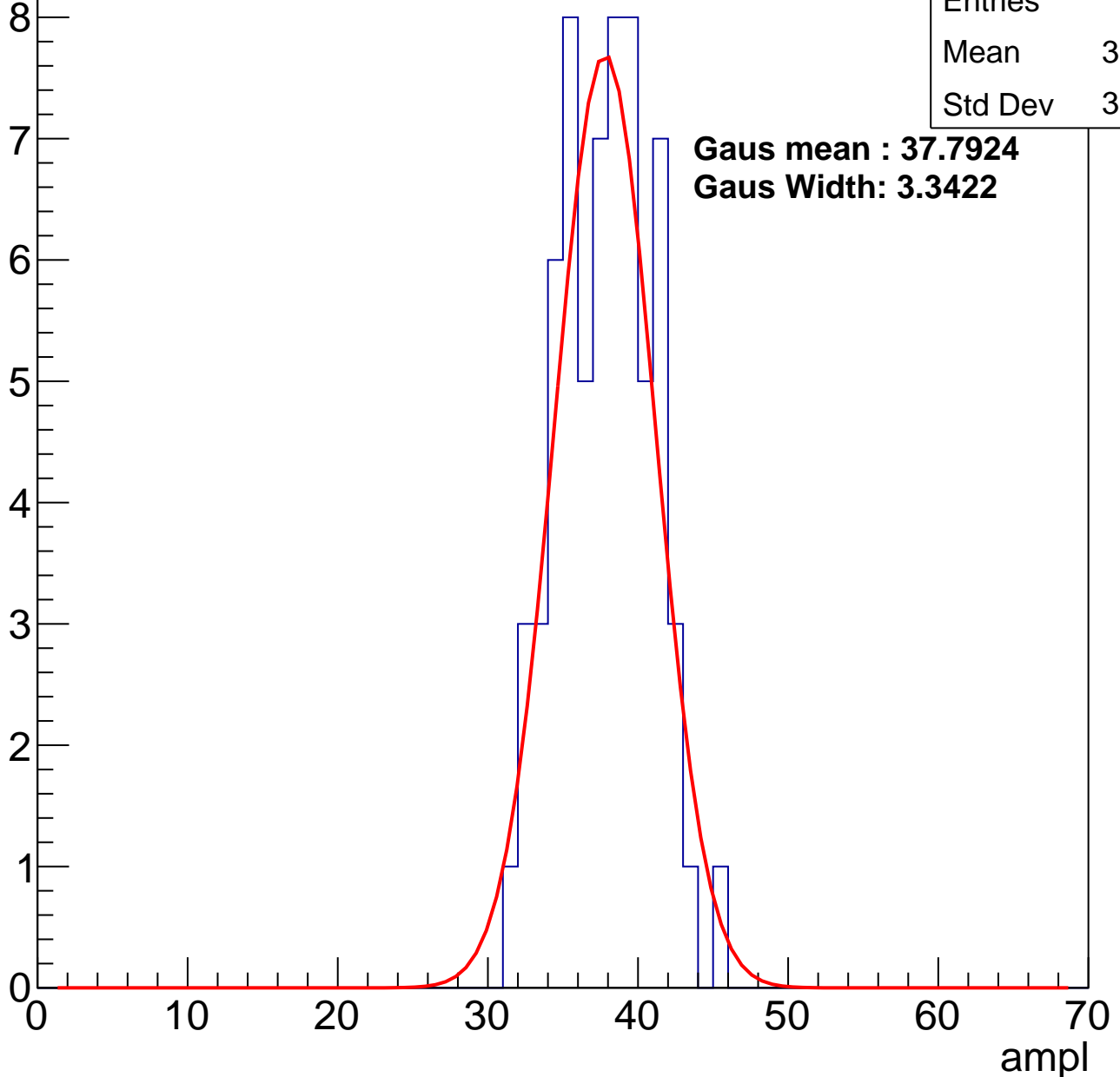
B1L003S, U20-ch10, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	37.36
Std Dev	3.048

Gaus mean : 37.7924
Gaus Width: 3.3422



B1L003S, U20-ch10, adc2

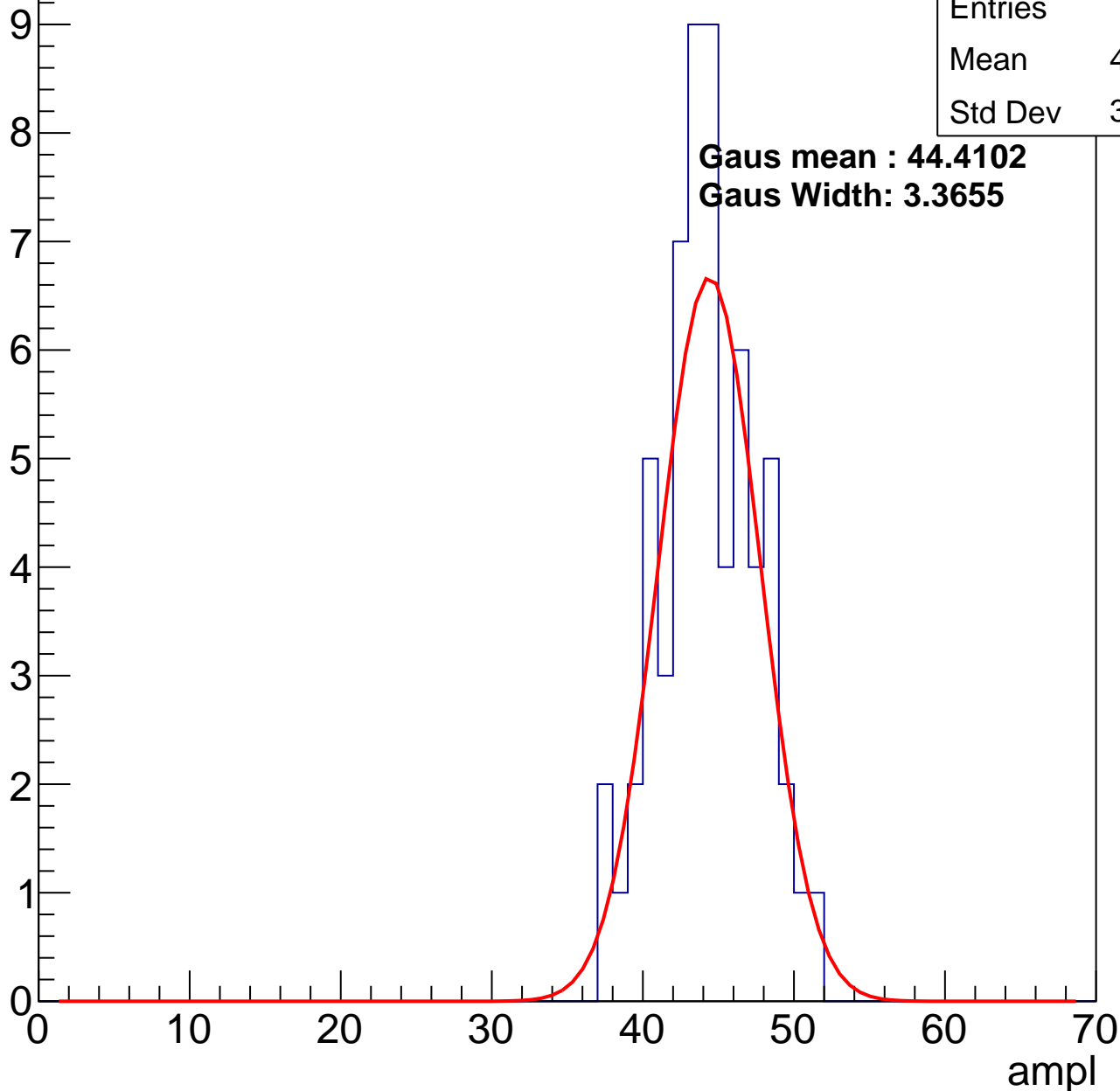
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.82
Std Dev	3.149

Gaus mean : 44.4102

Gaus Width: 3.3655

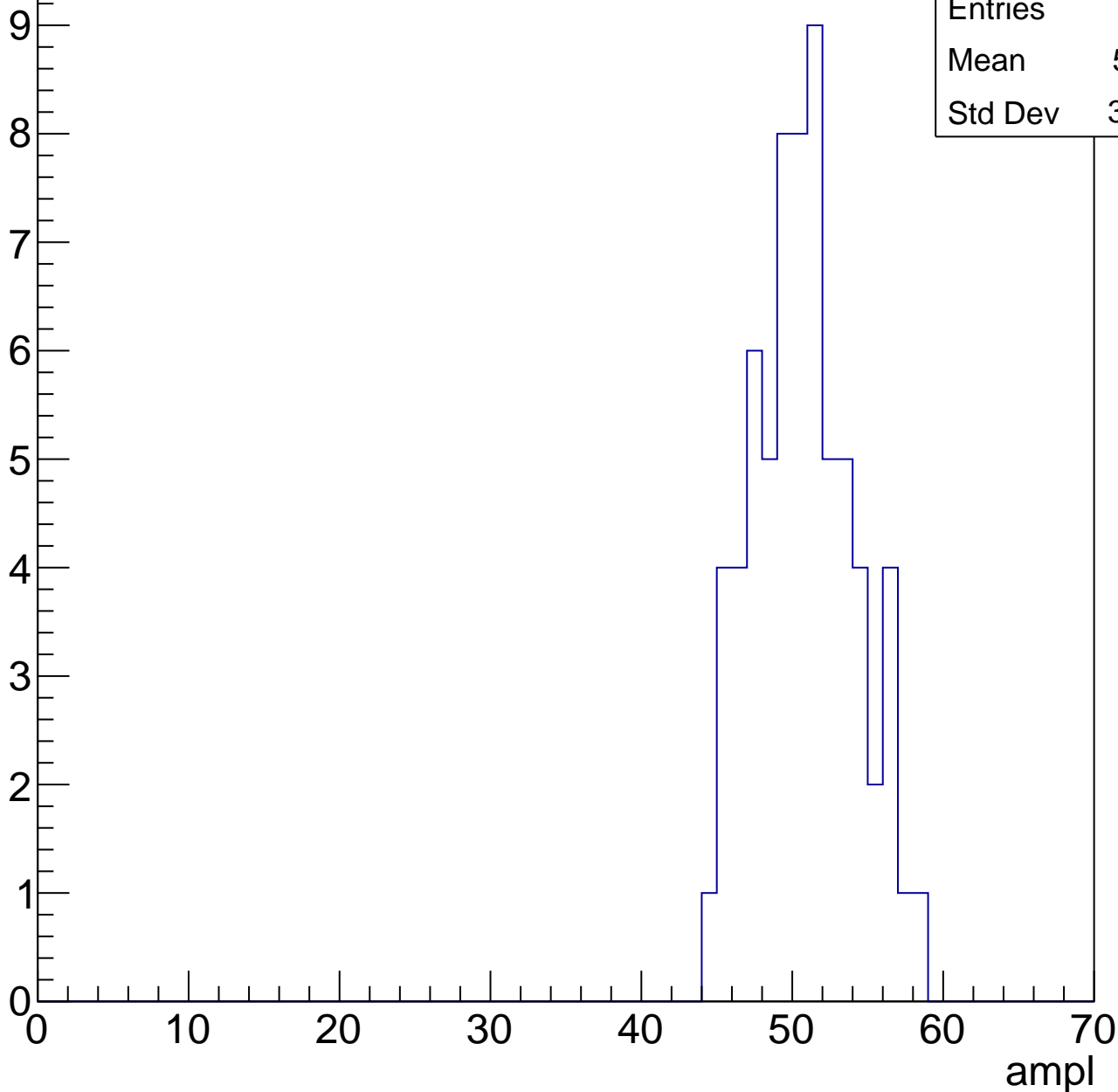


B1L003S, U20-ch10, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	50.31
Std Dev	3.279

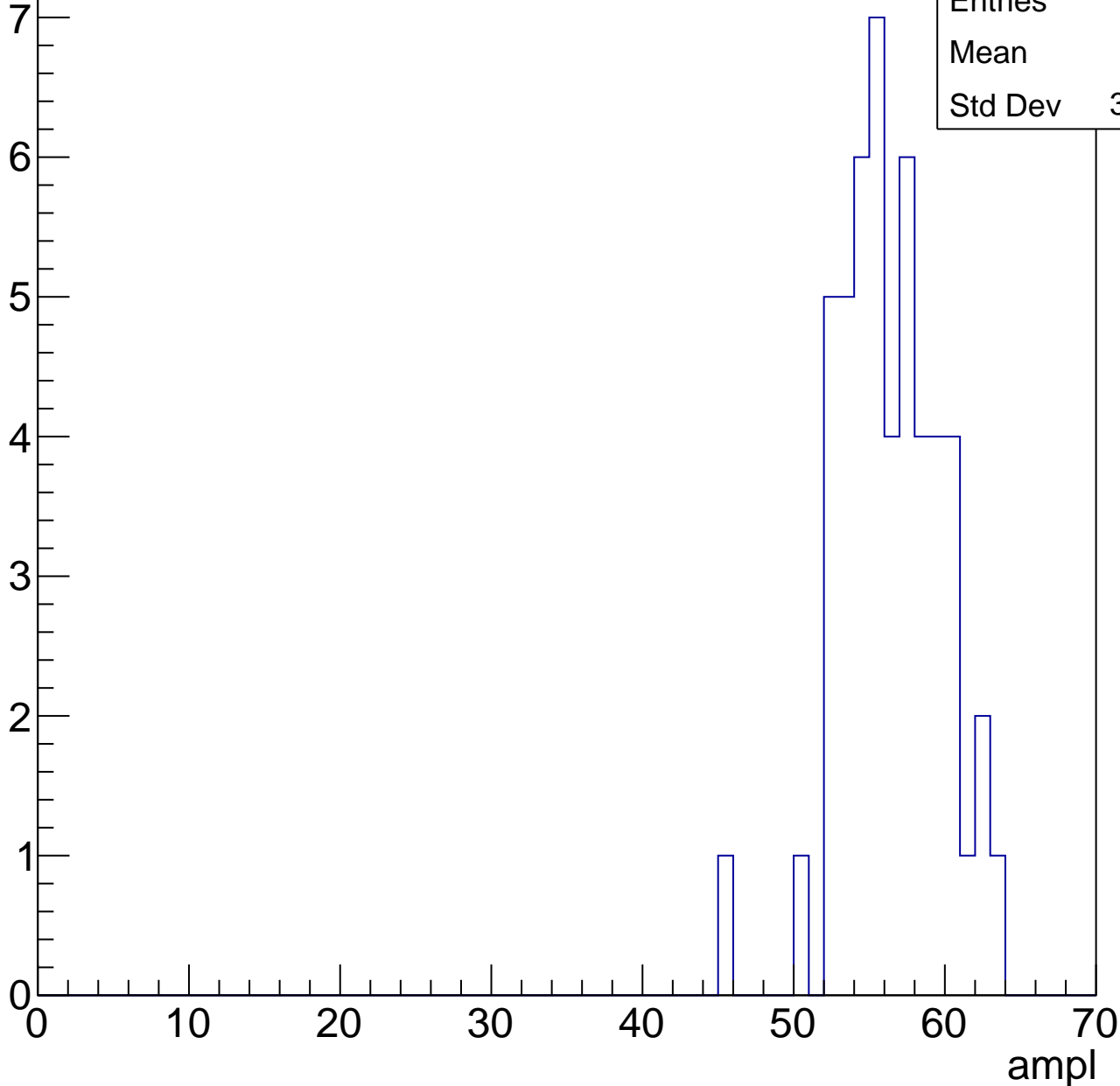


B1L003S, U20-ch10, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	55.9
Std Dev	3.374

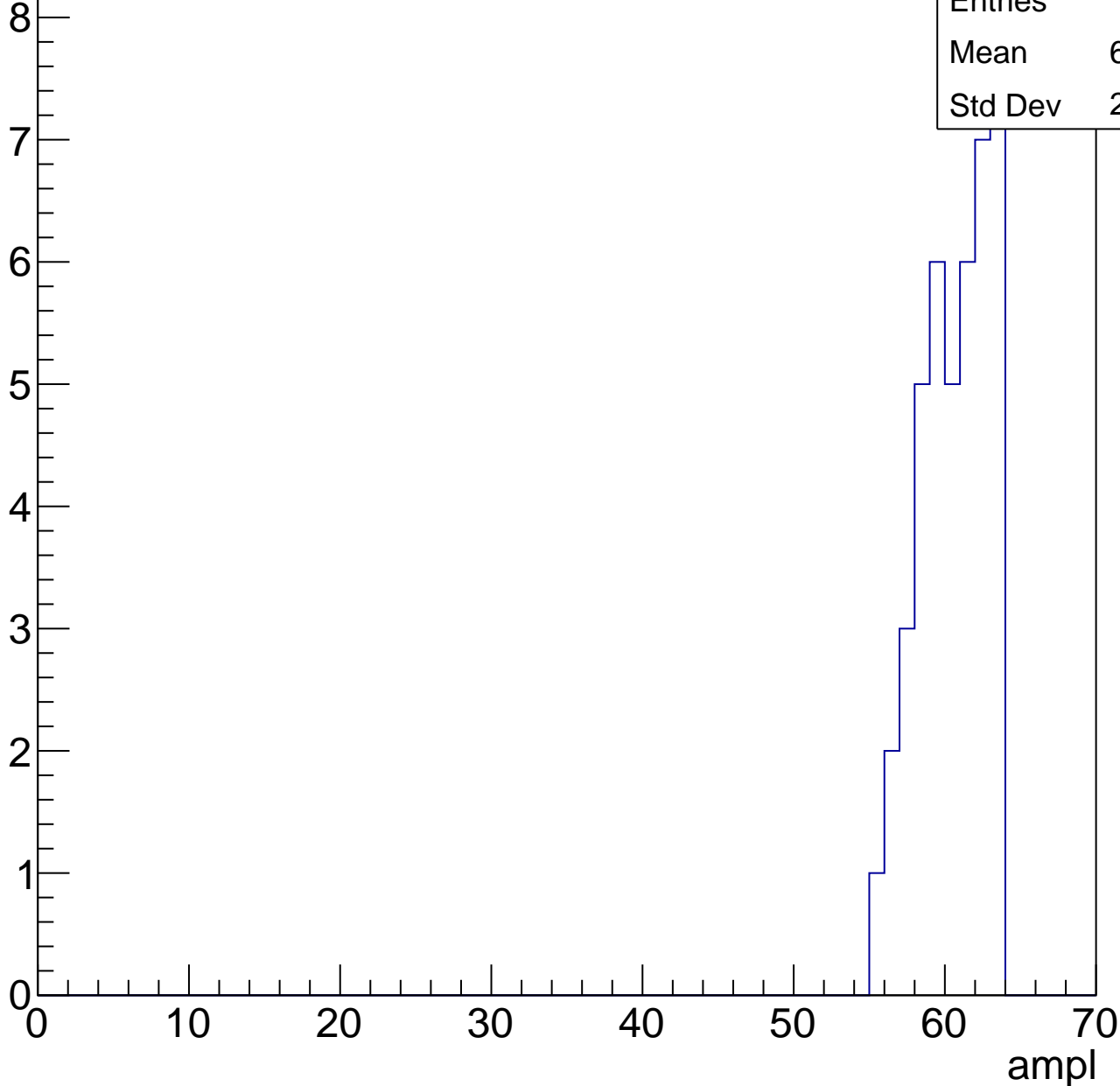


B1L003S, U20-ch10, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

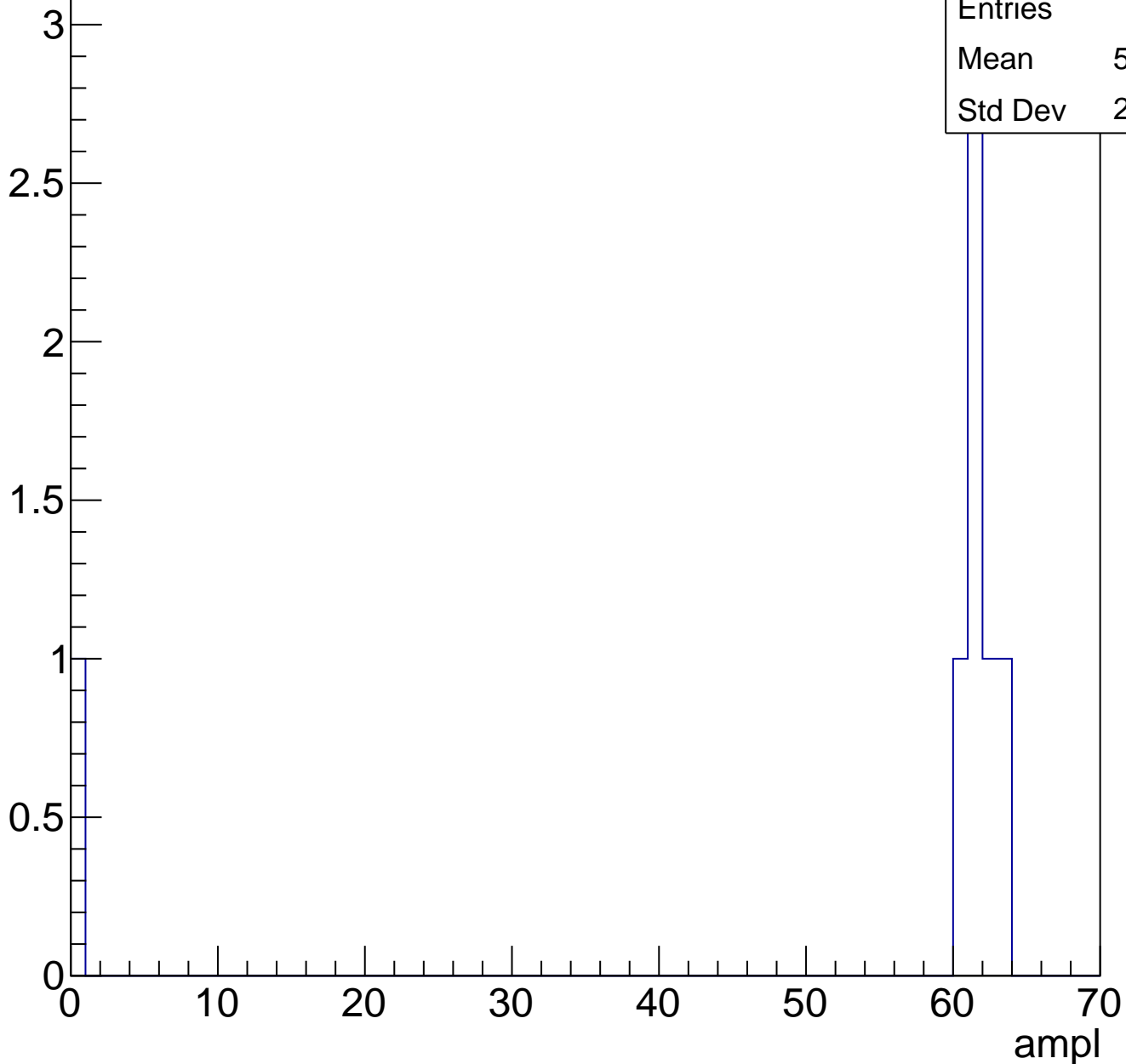
Entries	43
Mean	60.14
Std Dev	2.237



B1L003S, U20-ch10, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch10, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch11, adc0

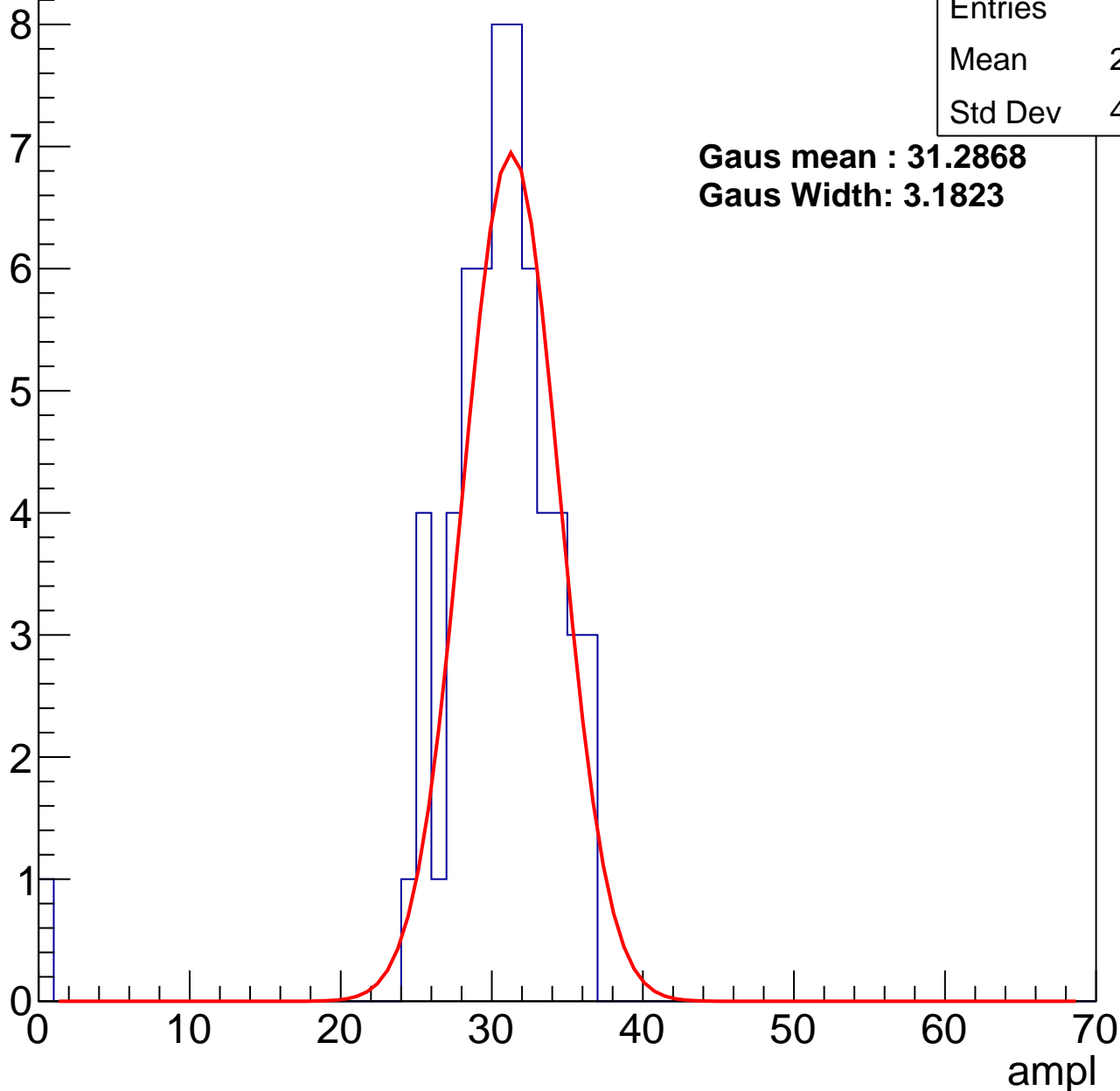
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.85
Std Dev	4.926

Gaus mean : 31.2868

Gaus Width: 3.1823



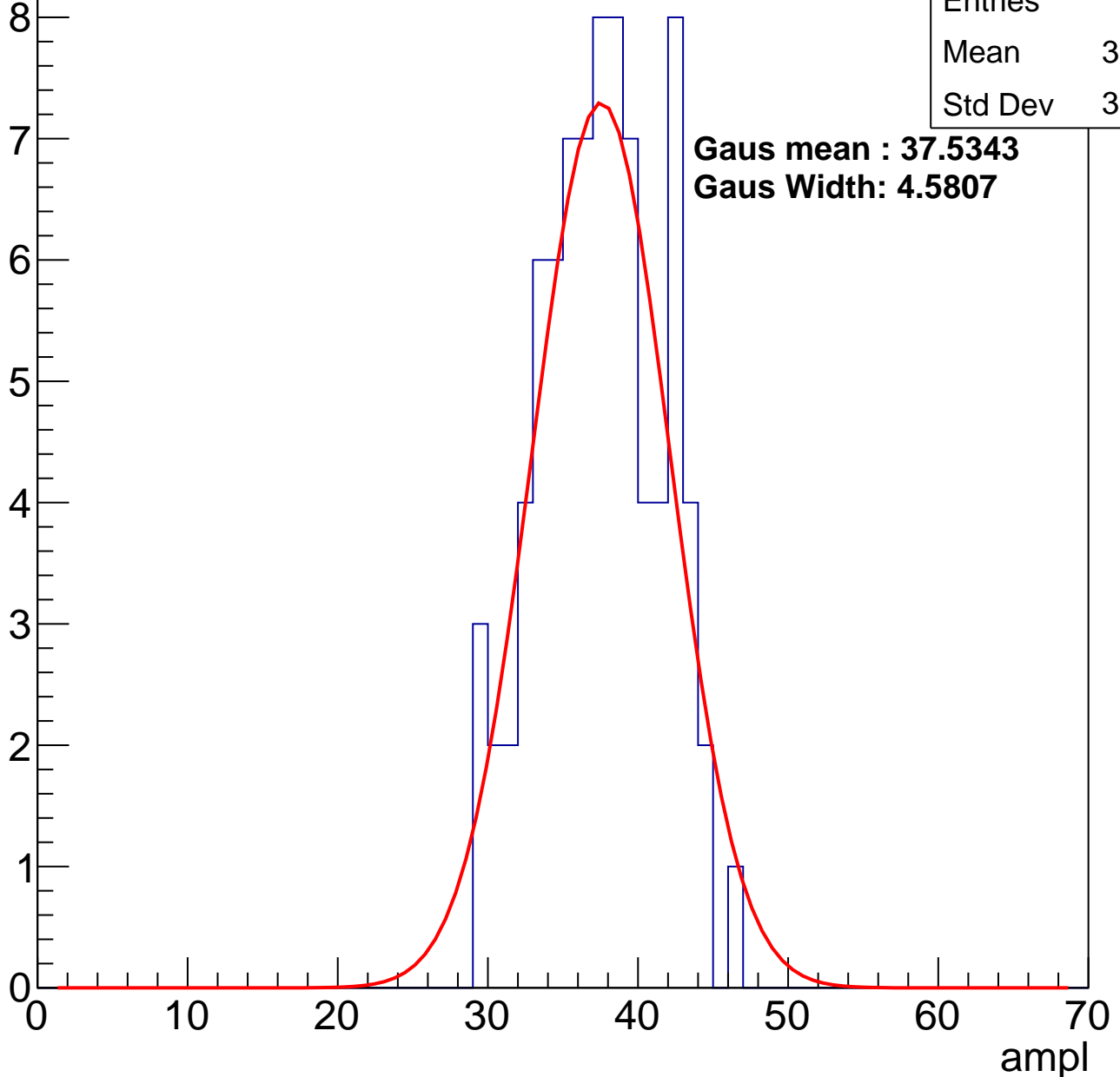
B1L003S, U20-ch11, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	37.05
Std Dev	3.966

Gaus mean : 37.5343
Gaus Width: 4.5807



B1L003S, U20-ch11, adc2

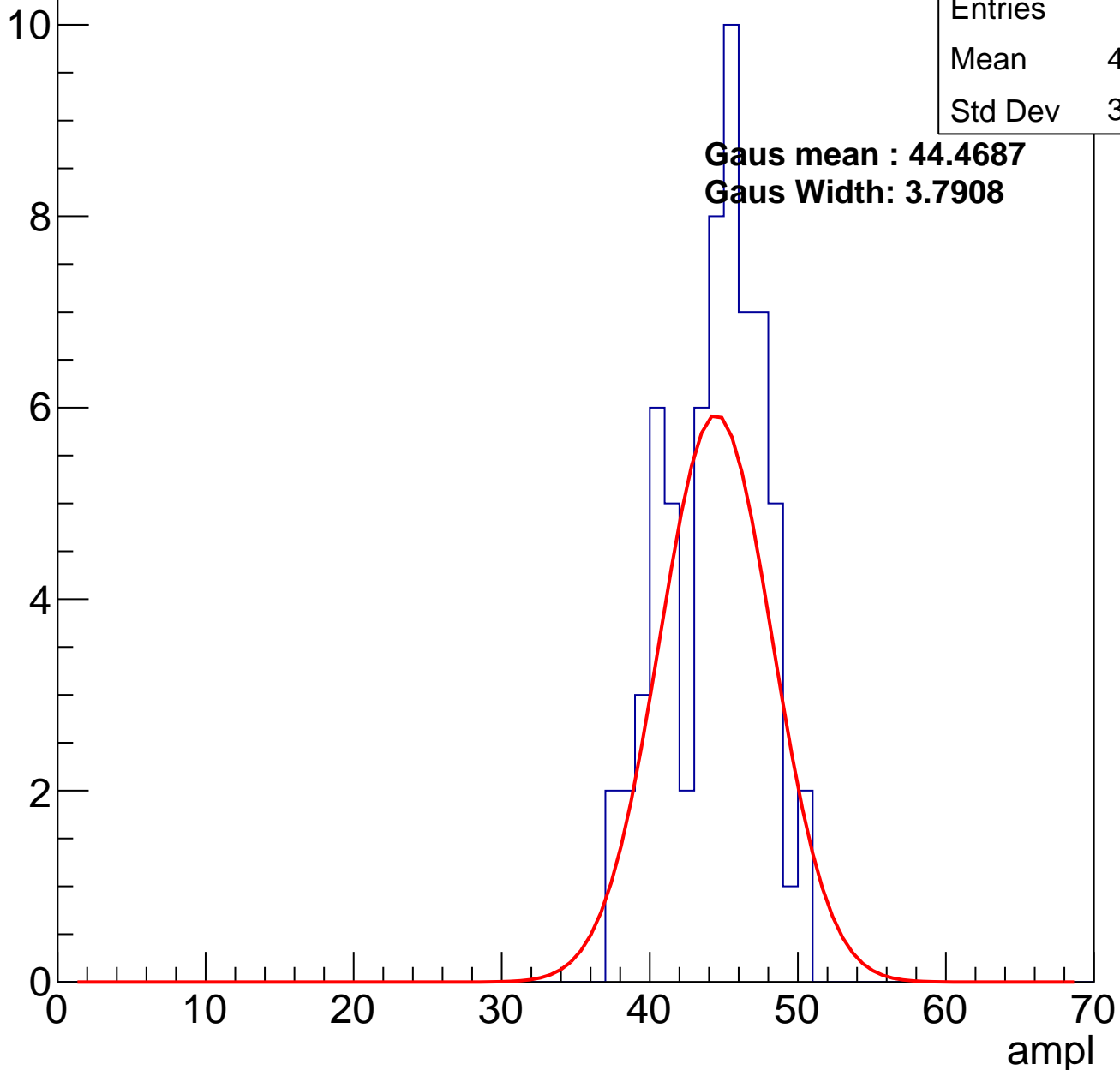
calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	43.88
Std Dev	3.203

Gaus mean : 44.4687

Gaus Width: 3.7908

Entry

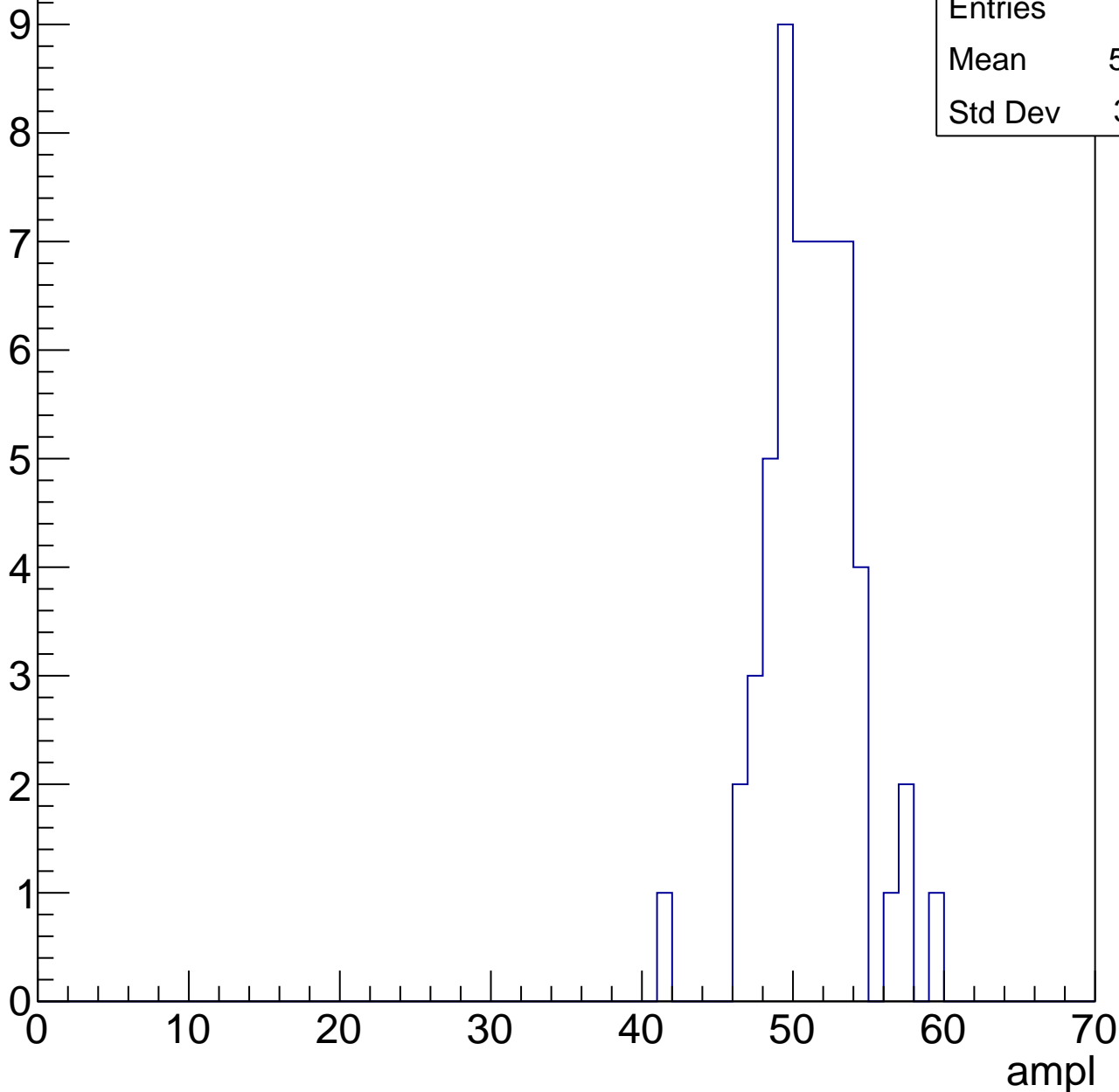


B1L003S, U20-ch11, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	50.75
Std Dev	3.031

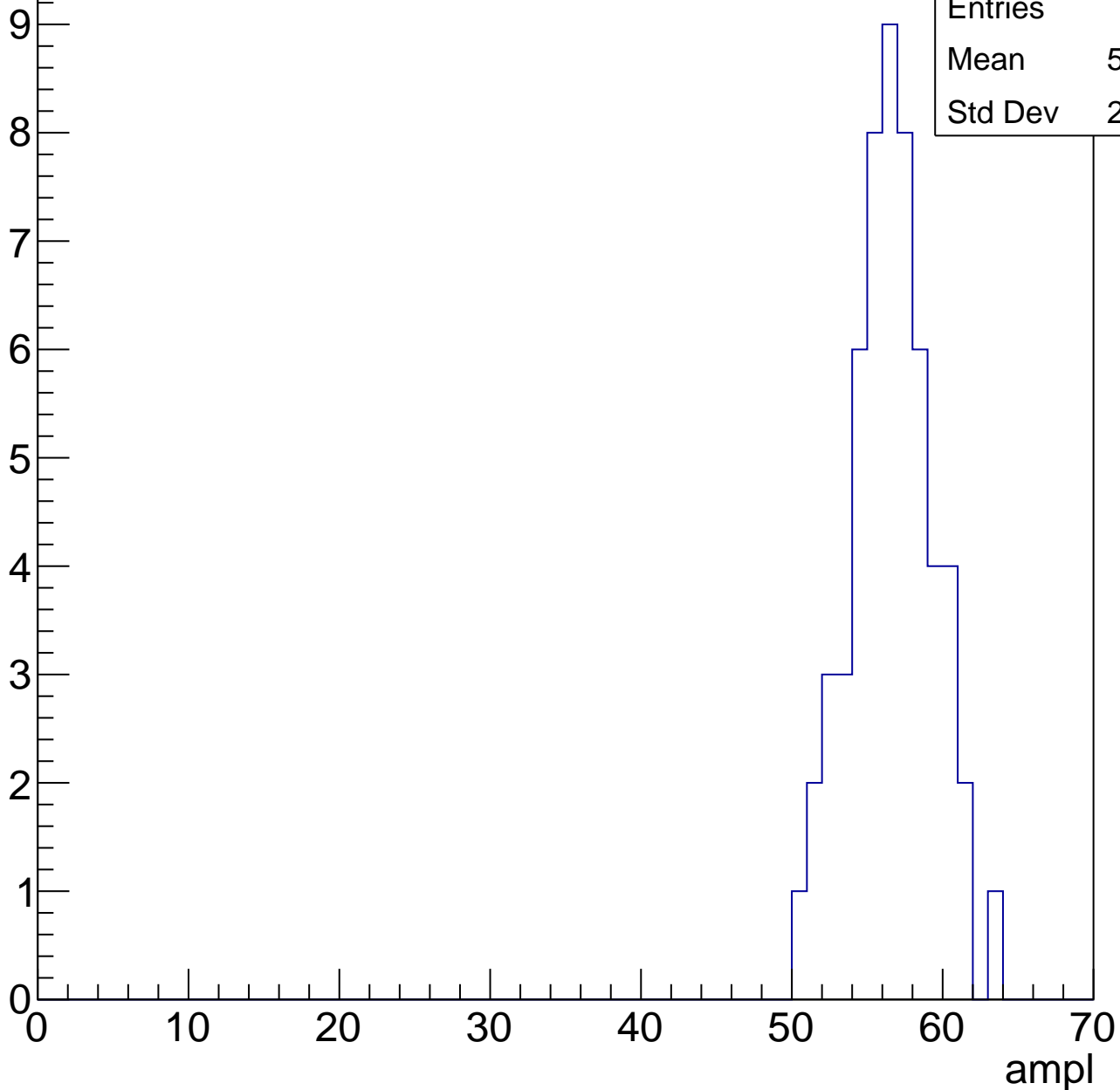


B1L003S, U20-ch11, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.14
Std Dev	2.724

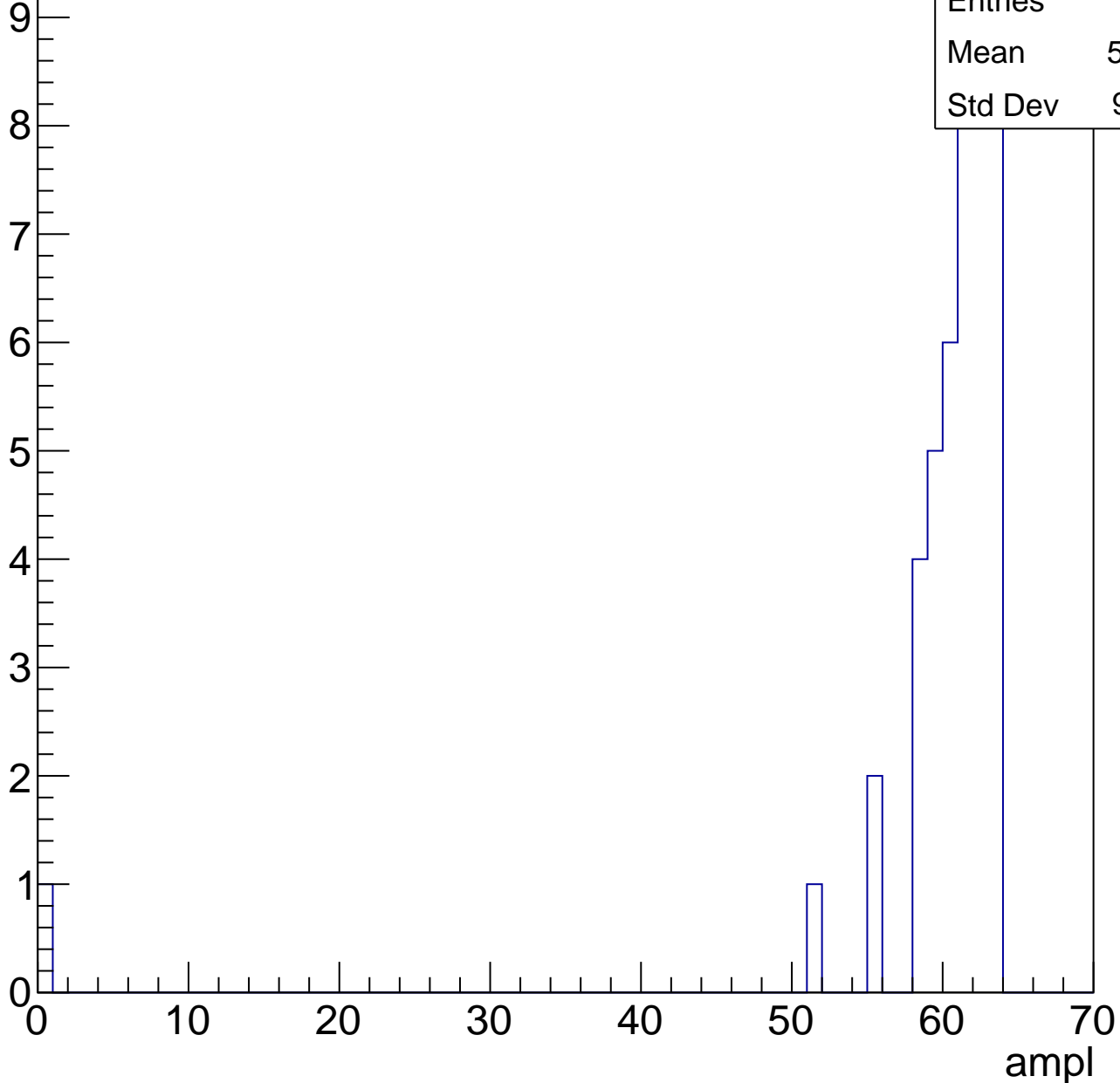


B1L003S, U20-ch11, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.13
Std Dev	9.241



B1L003S, U20-ch11, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch11, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch12, adc0

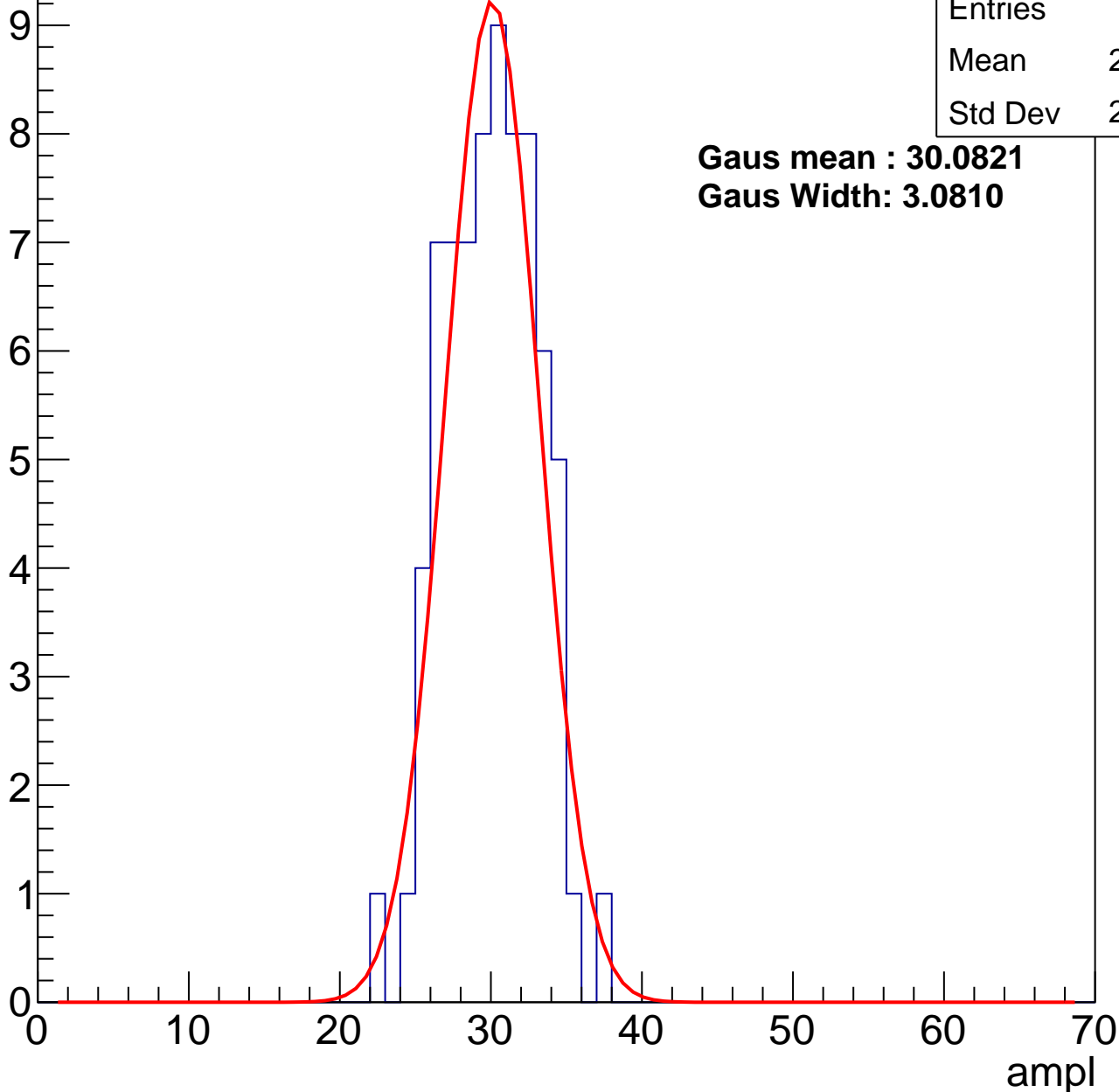
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.58
Std Dev	2.974

Gaus mean : 30.0821

Gaus Width: 3.0810



B1L003S, U20-ch12, adc1

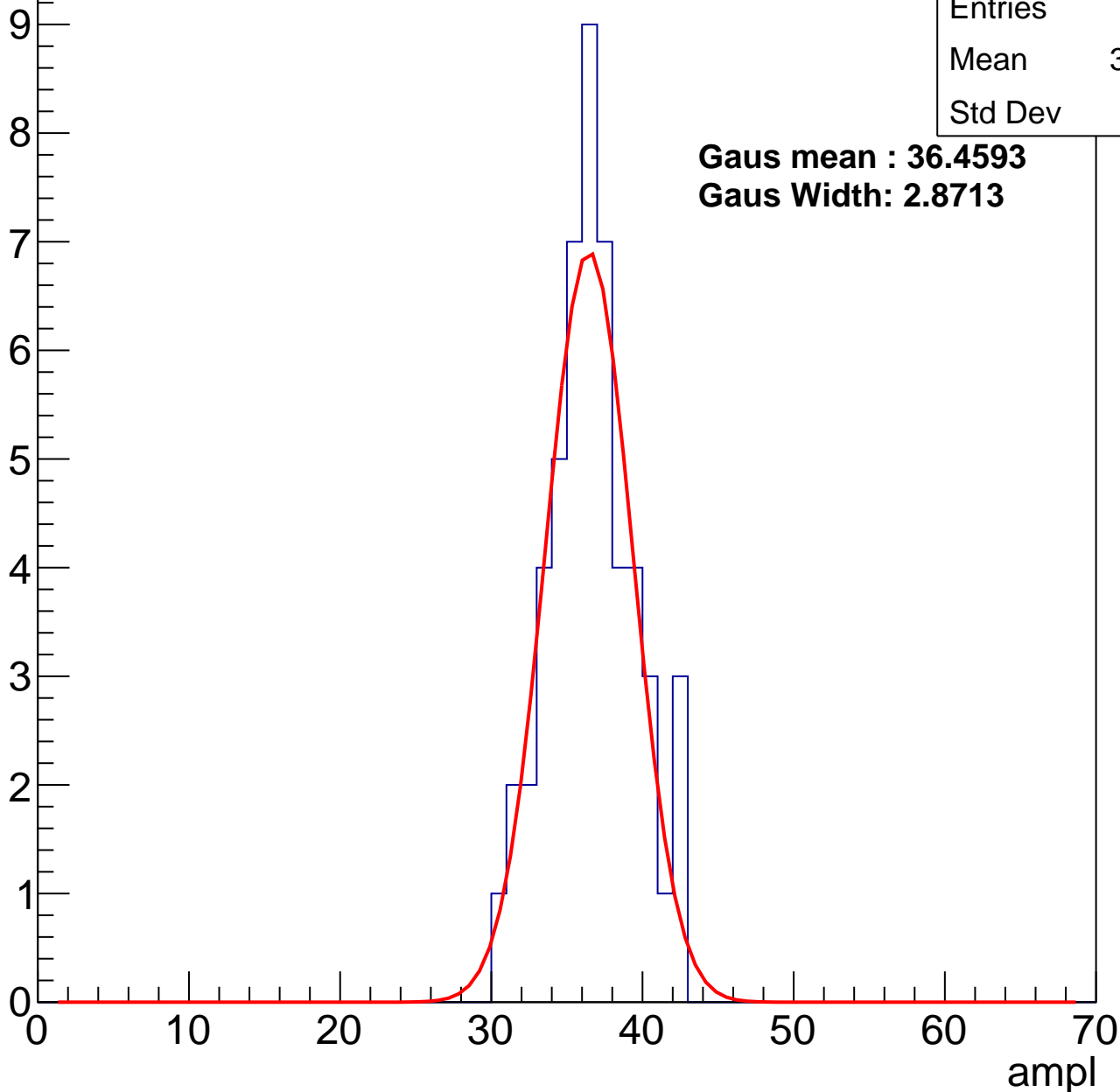
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	36.17
Std Dev	2.84

Gaus mean : 36.4593

Gaus Width: 2.8713



B1L003S, U20-ch12, adc2

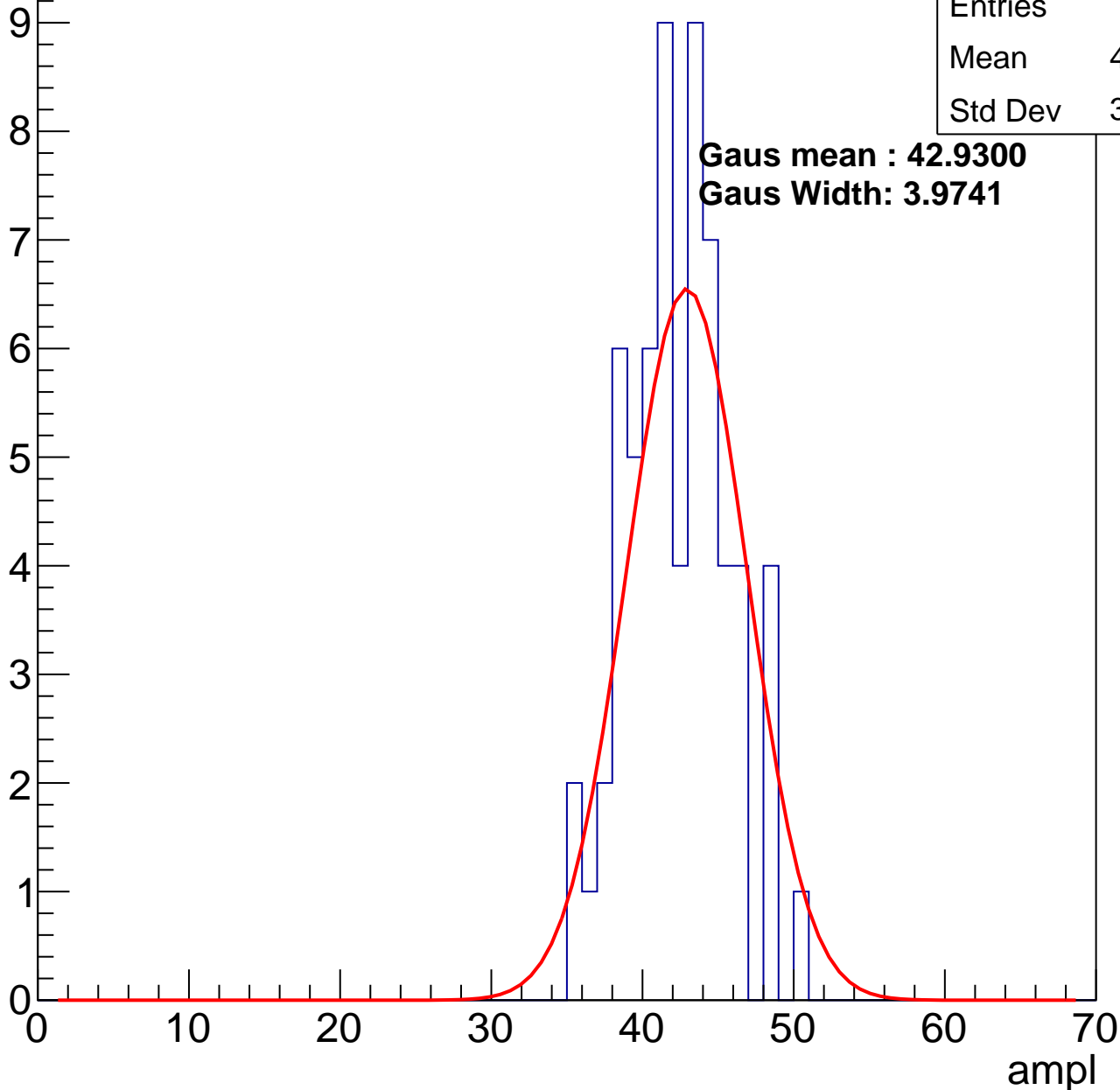
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.89
Std Dev	3.312

Gaus mean : 42.9300

Gaus Width: 3.9741



B1L003S, U20-ch12, adc3

calib_packv5_042523_0143.root, FC#13, port D2

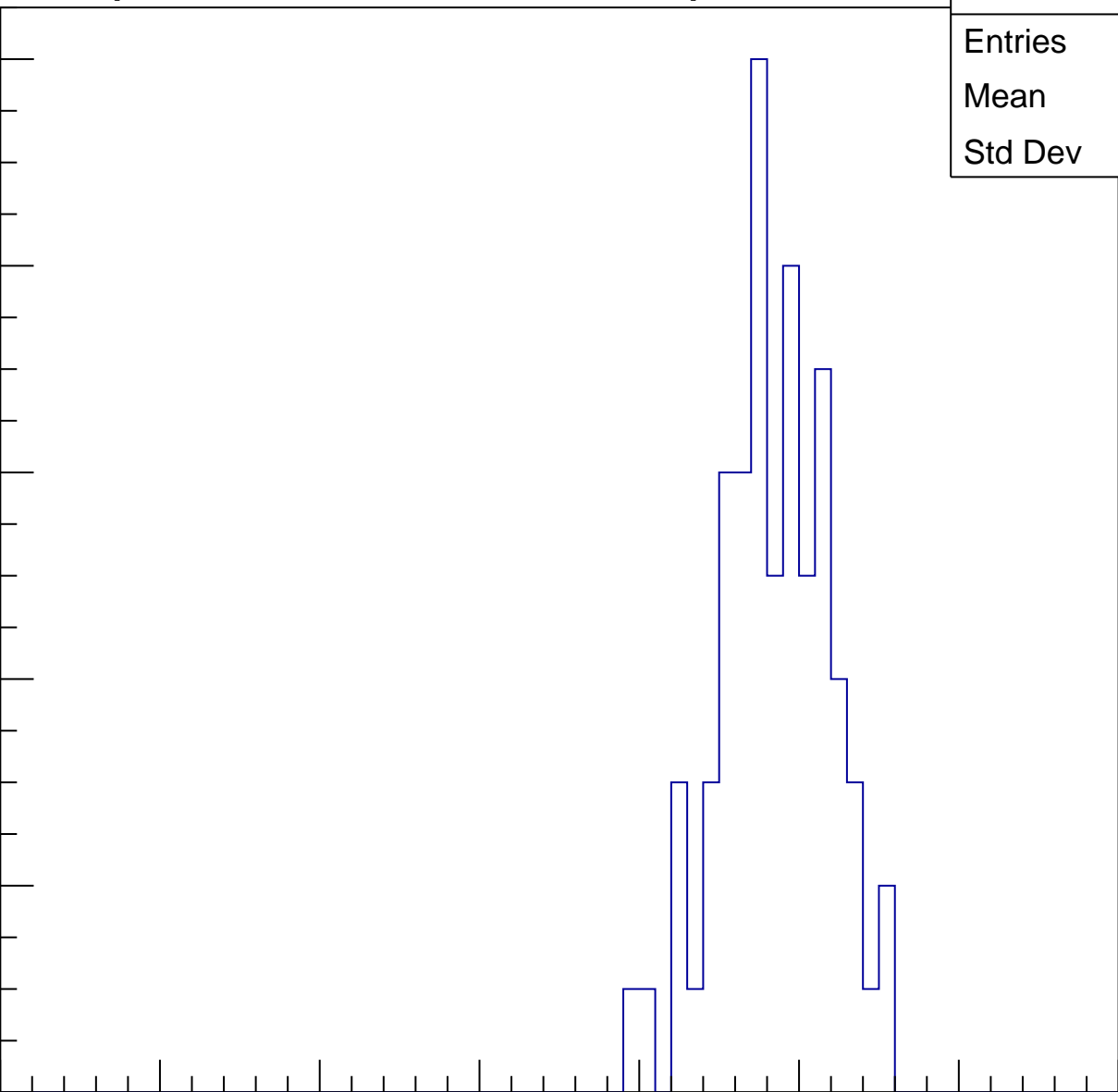
Entries	66
Mean	47.97
Std Dev	3.433

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

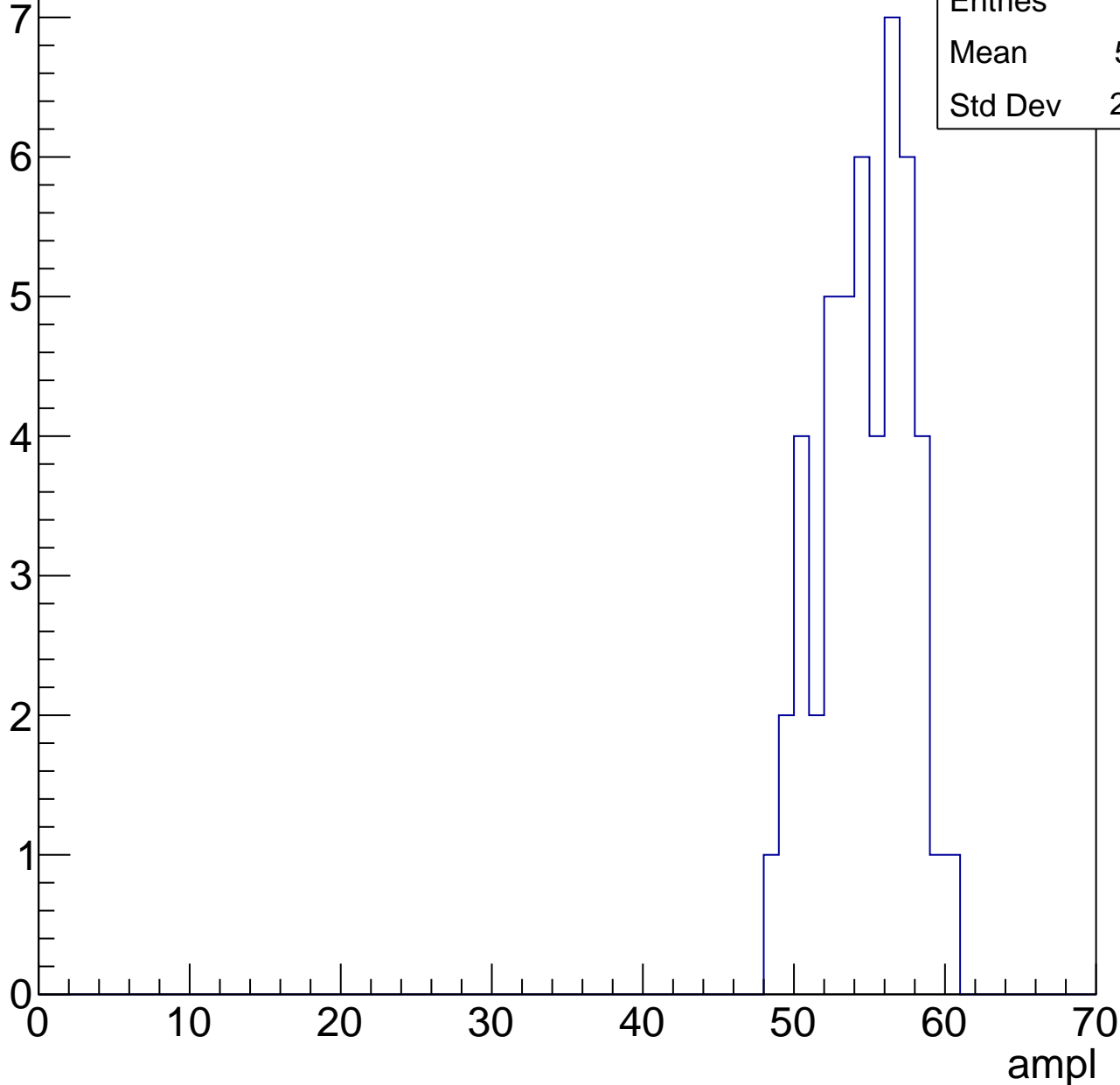
ampl



B1L003S, U20-ch12, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

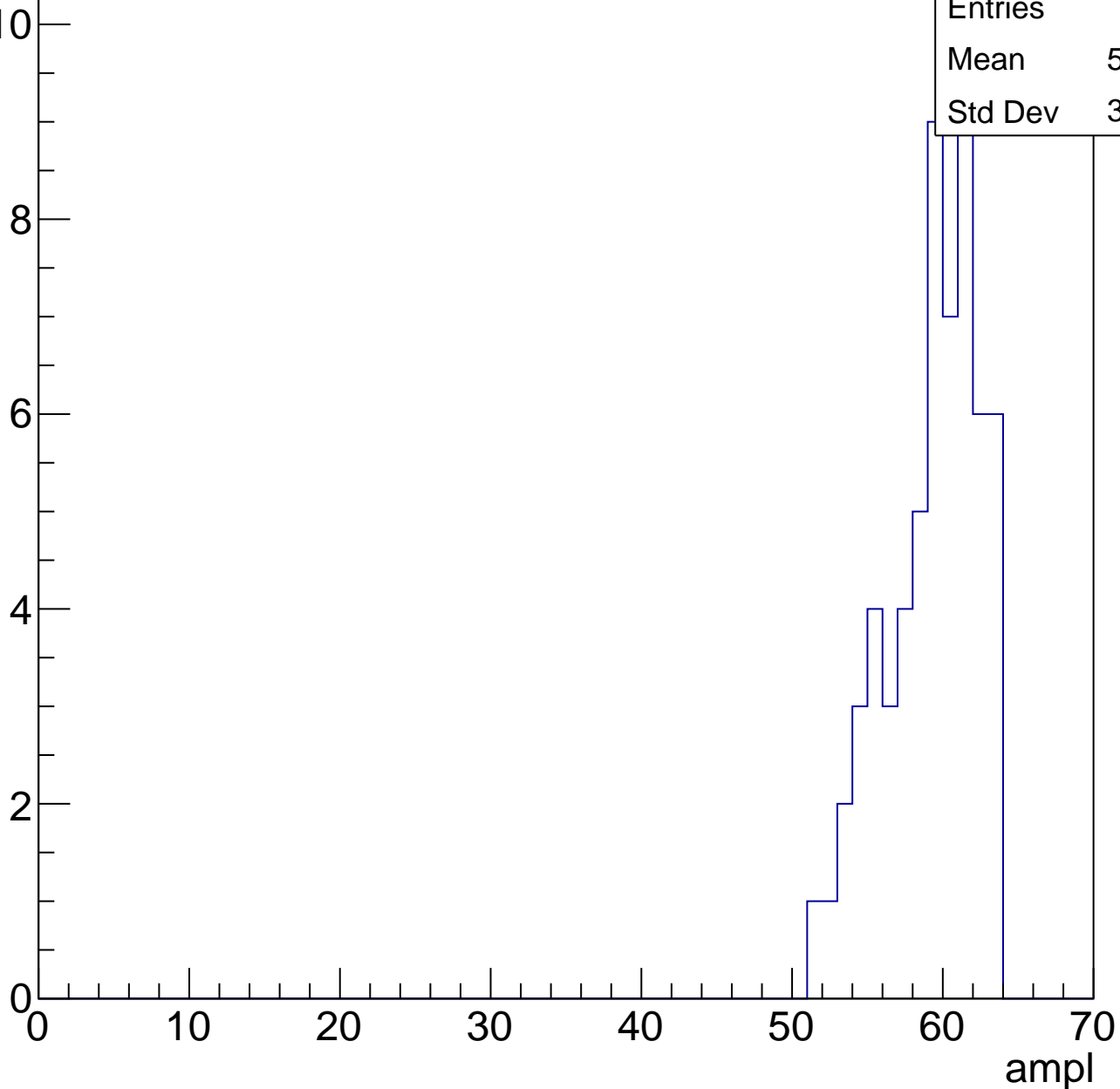


B1L003S, U20-ch12, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

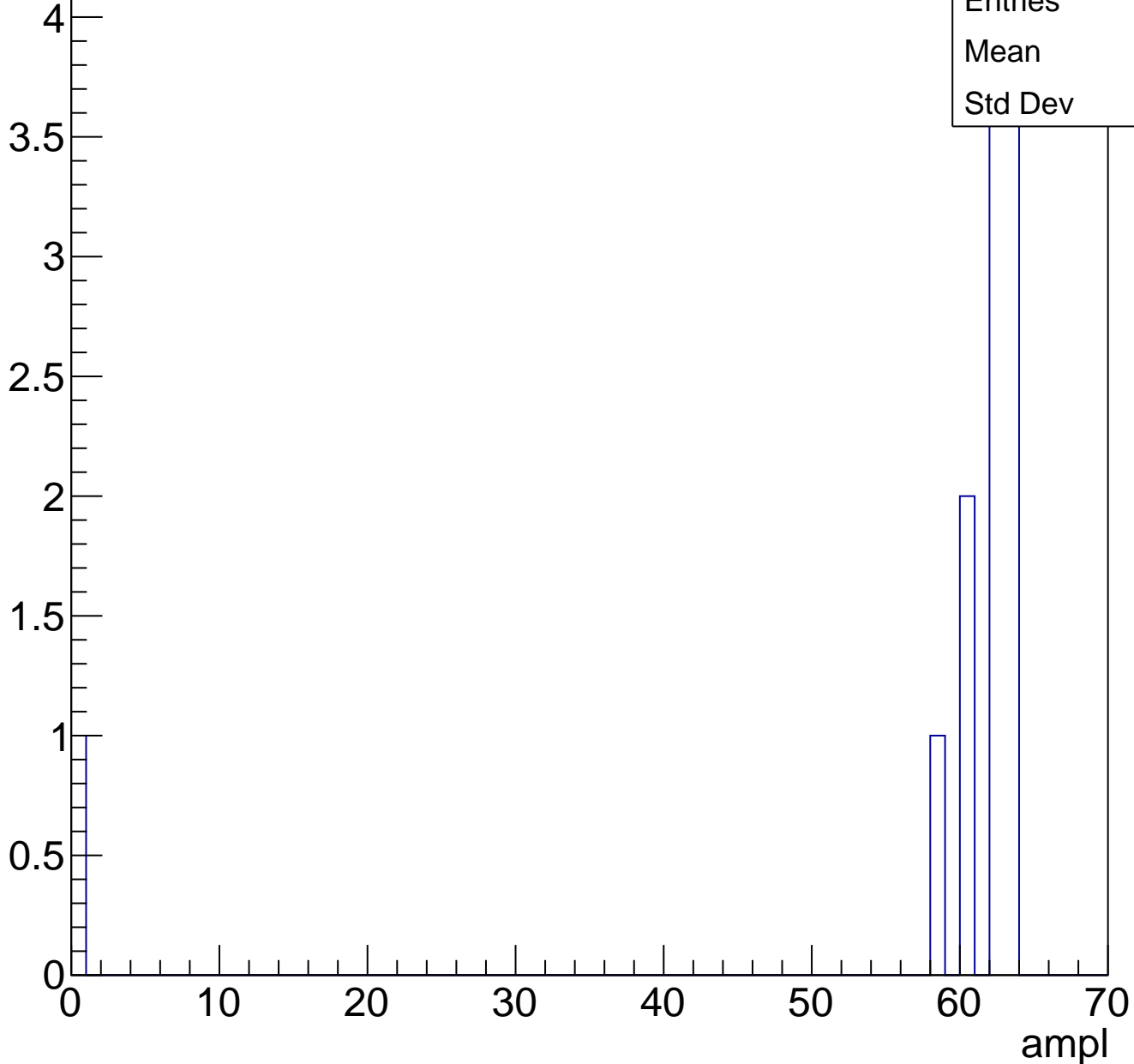
Entries	61
Mean	58.82
Std Dev	3.049



B1L003S, U20-ch12, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch12, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch13, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	28.67
Std Dev	5.751

Gaus mean : 29.6339

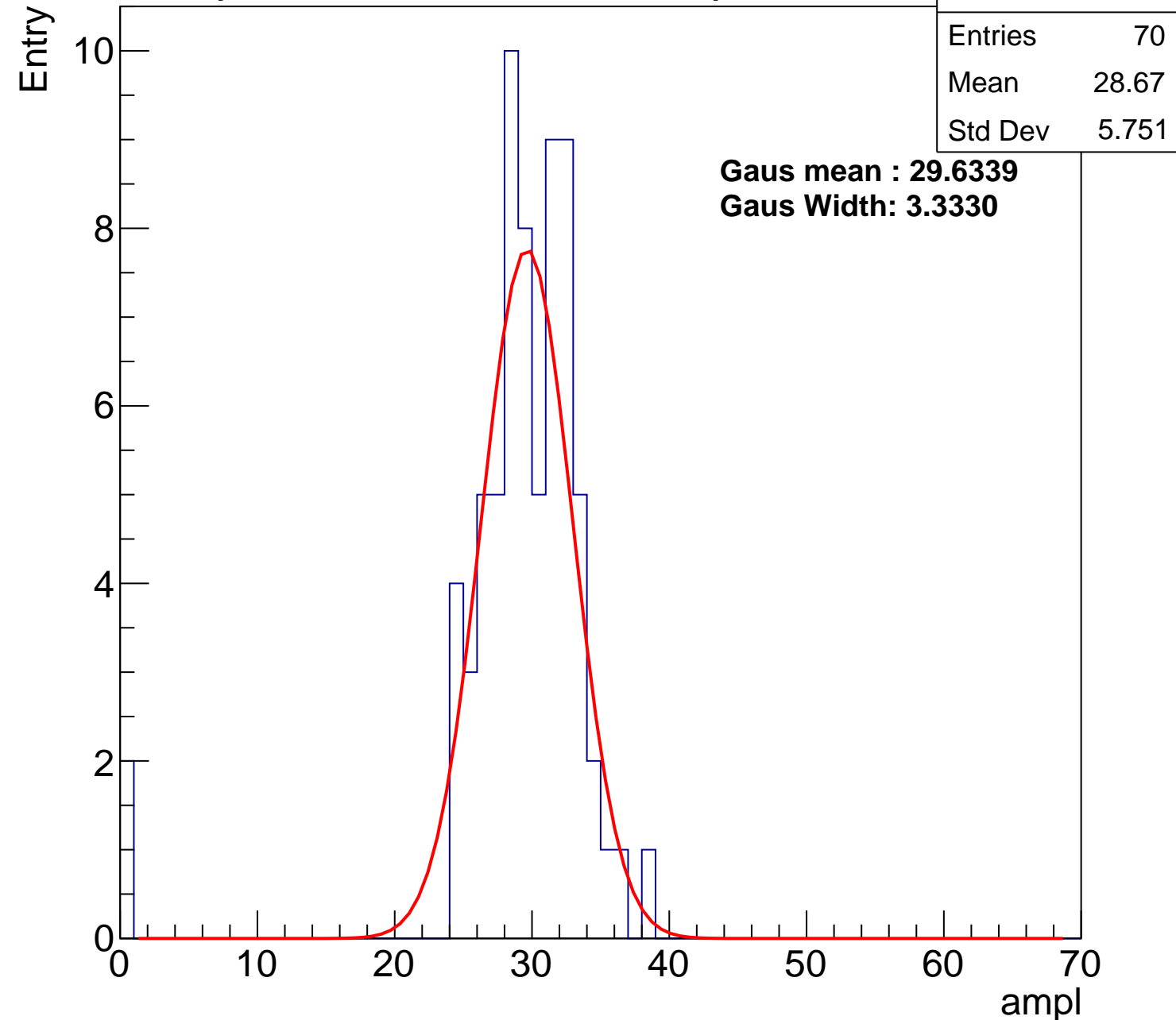
Gaus Width: 3.3330

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch13, adc1

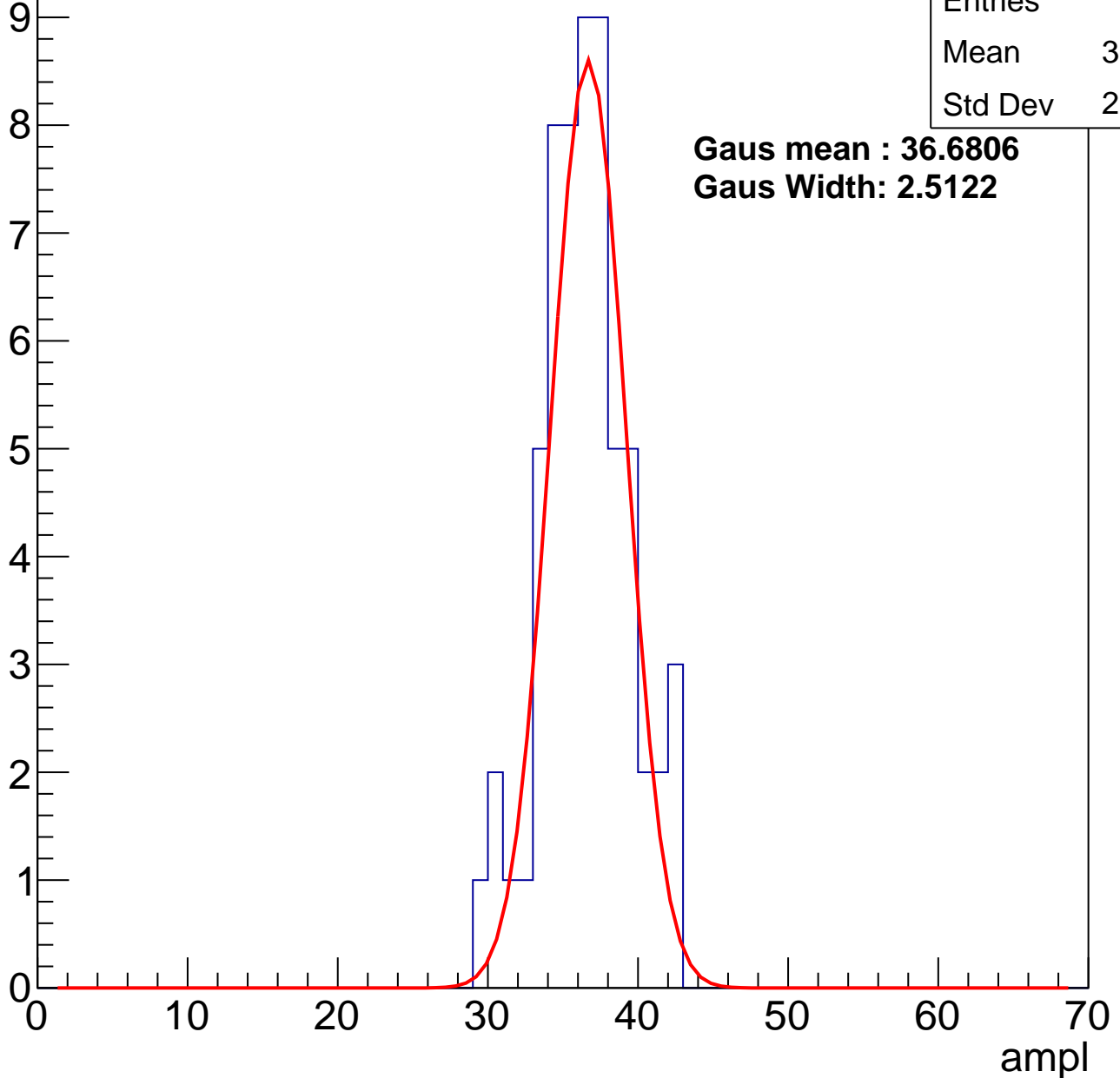
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.05
Std Dev	2.894

Gaus mean : 36.6806

Gaus Width: 2.5122



B1L003S, U20-ch13, adc2

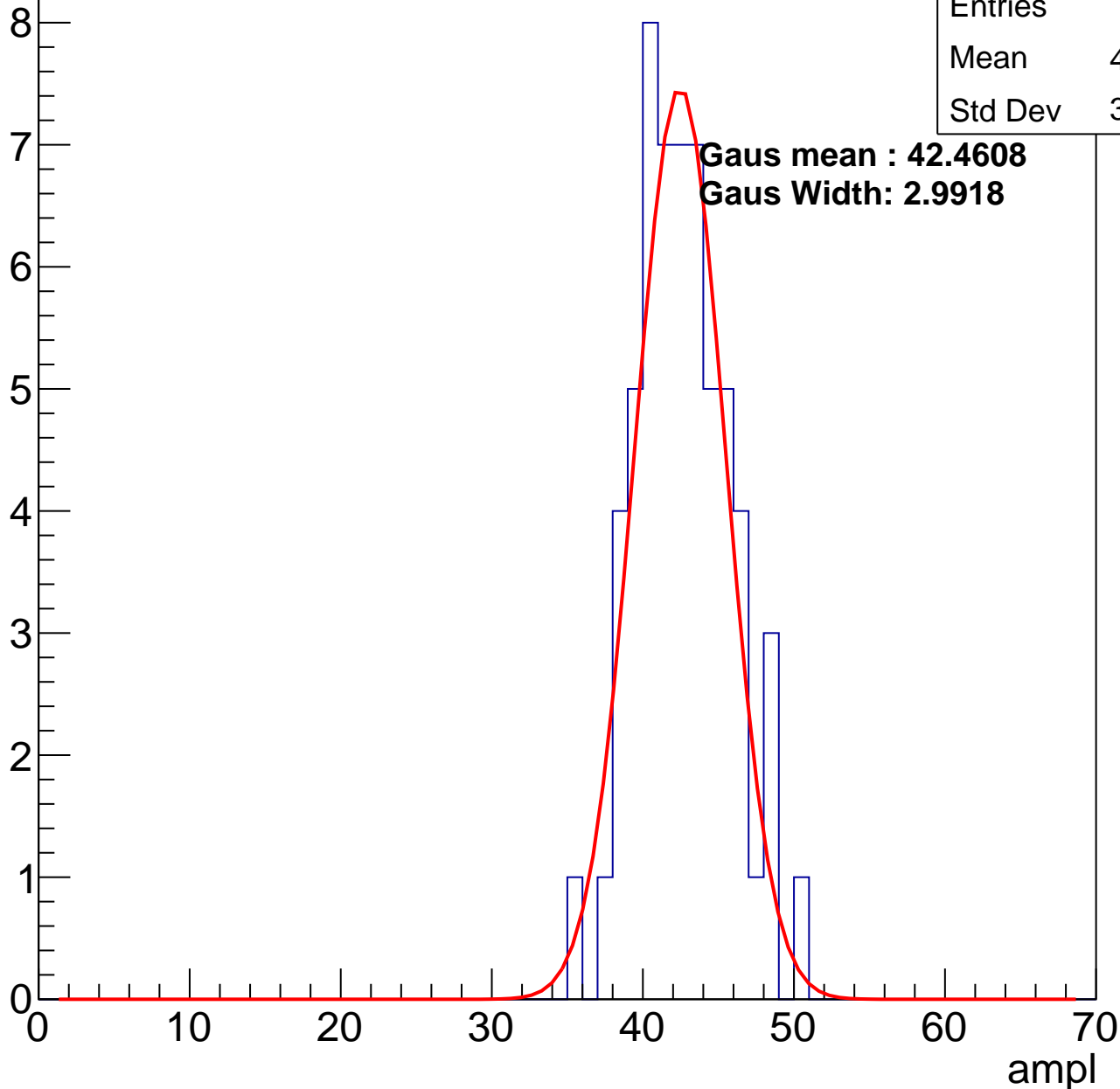
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.22
Std Dev	3.059

Gaus mean : 42.4608

Gaus Width: 2.9918

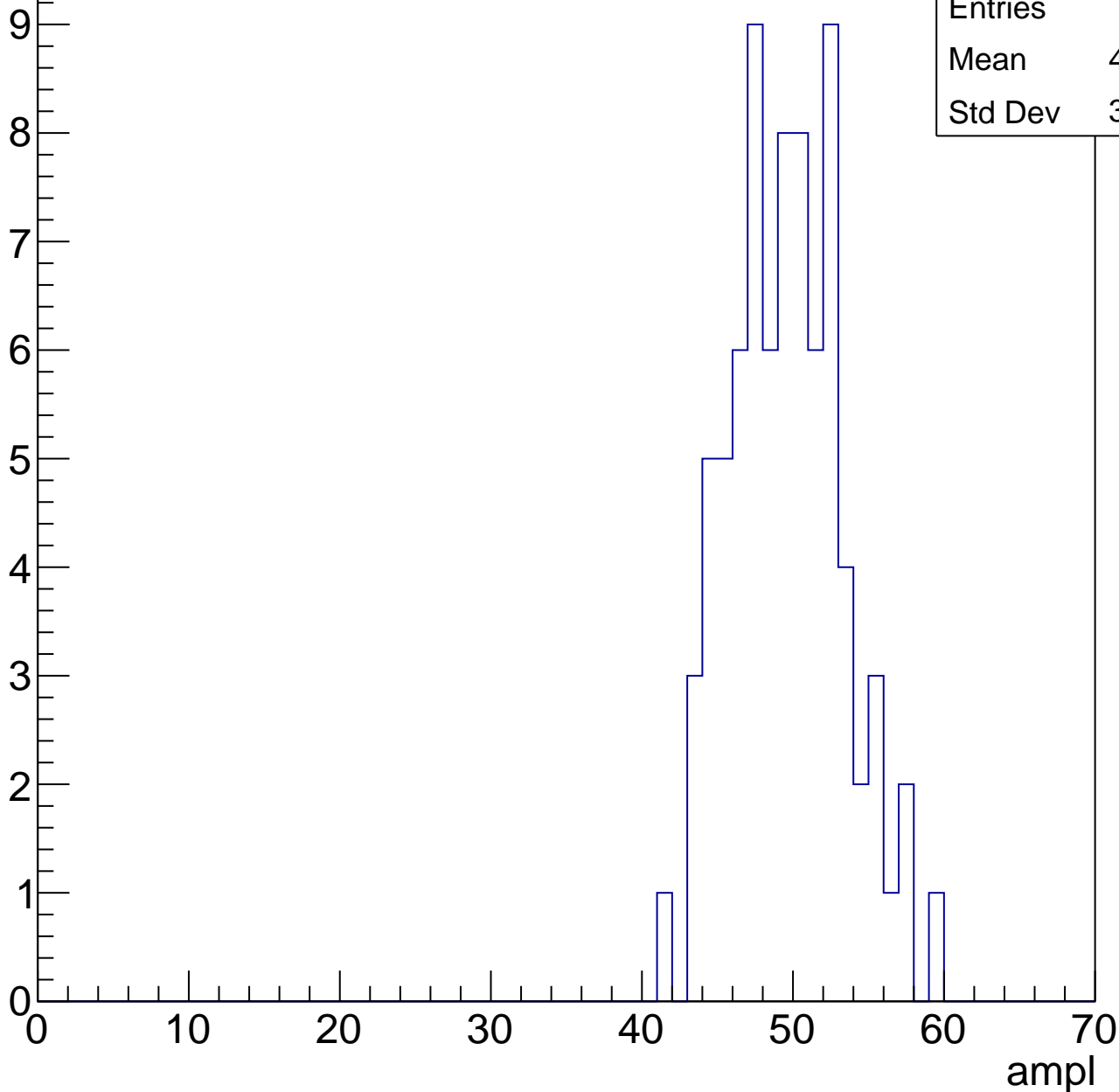


B1L003S, U20-ch13, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

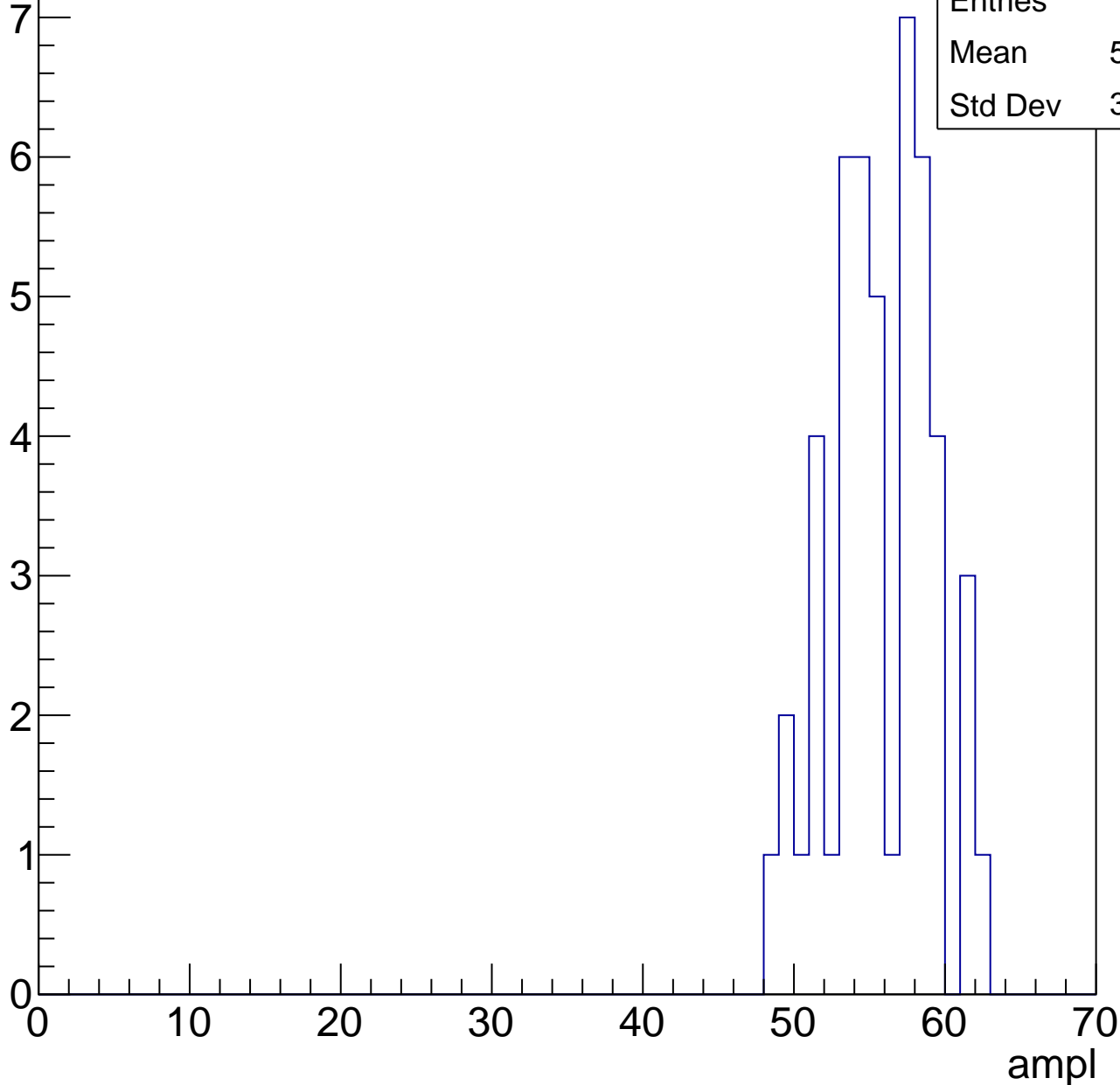
Entries	79
Mean	49.14
Std Dev	3.703



B1L003S, U20-ch13, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

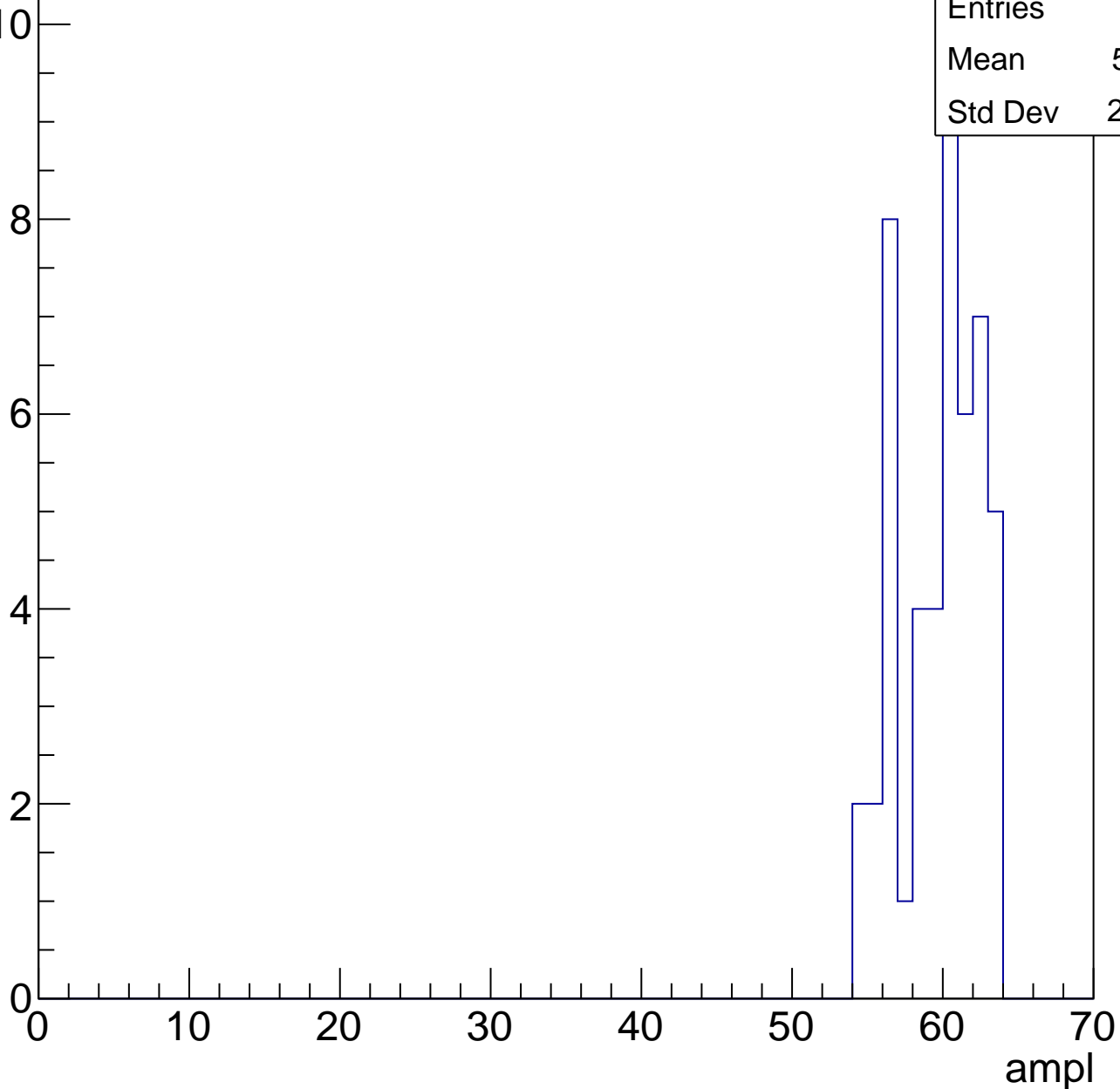


B1L003S, U20-ch13, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.31
Std Dev	2.612

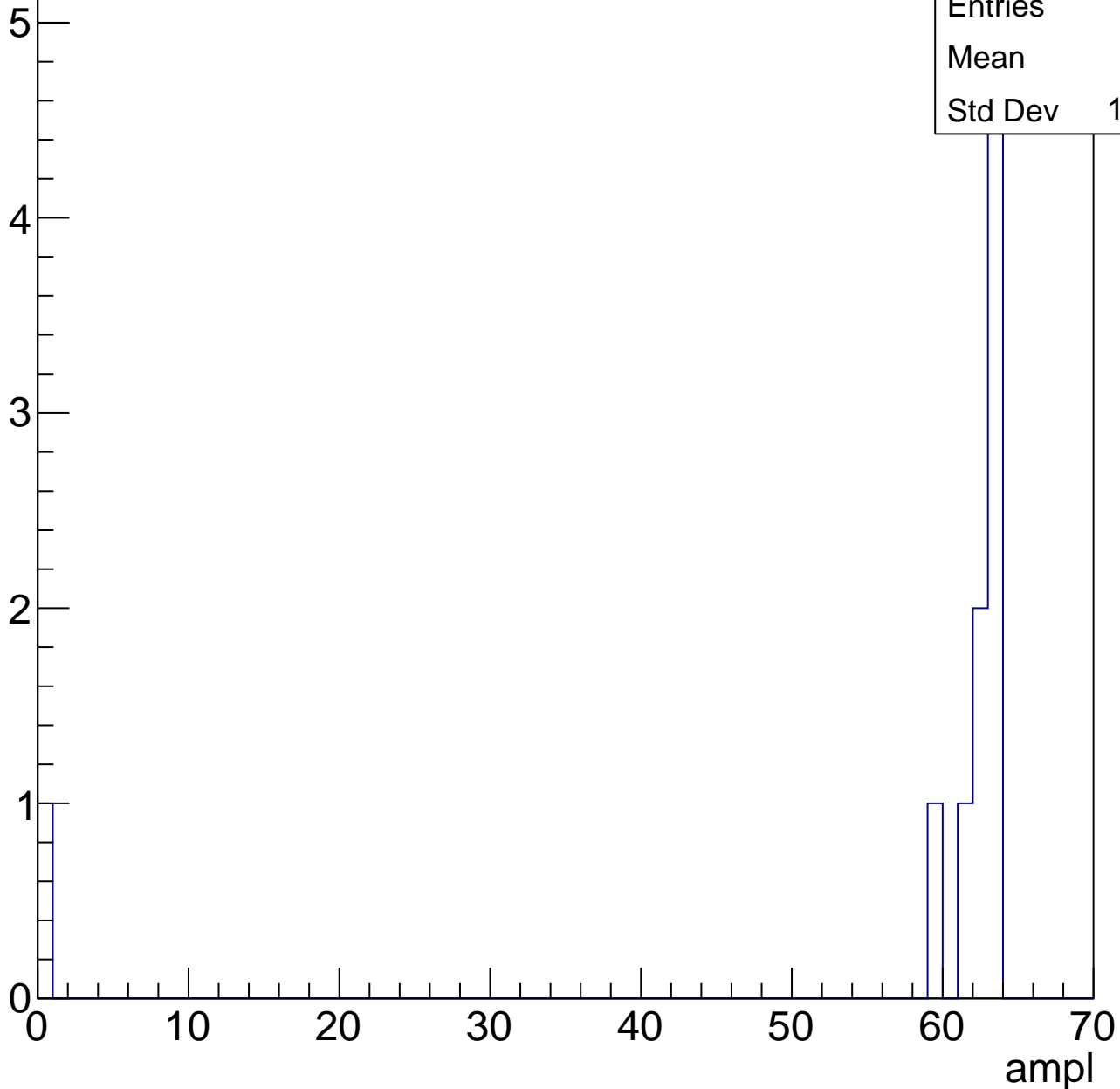


B1L003S, U20-ch13, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

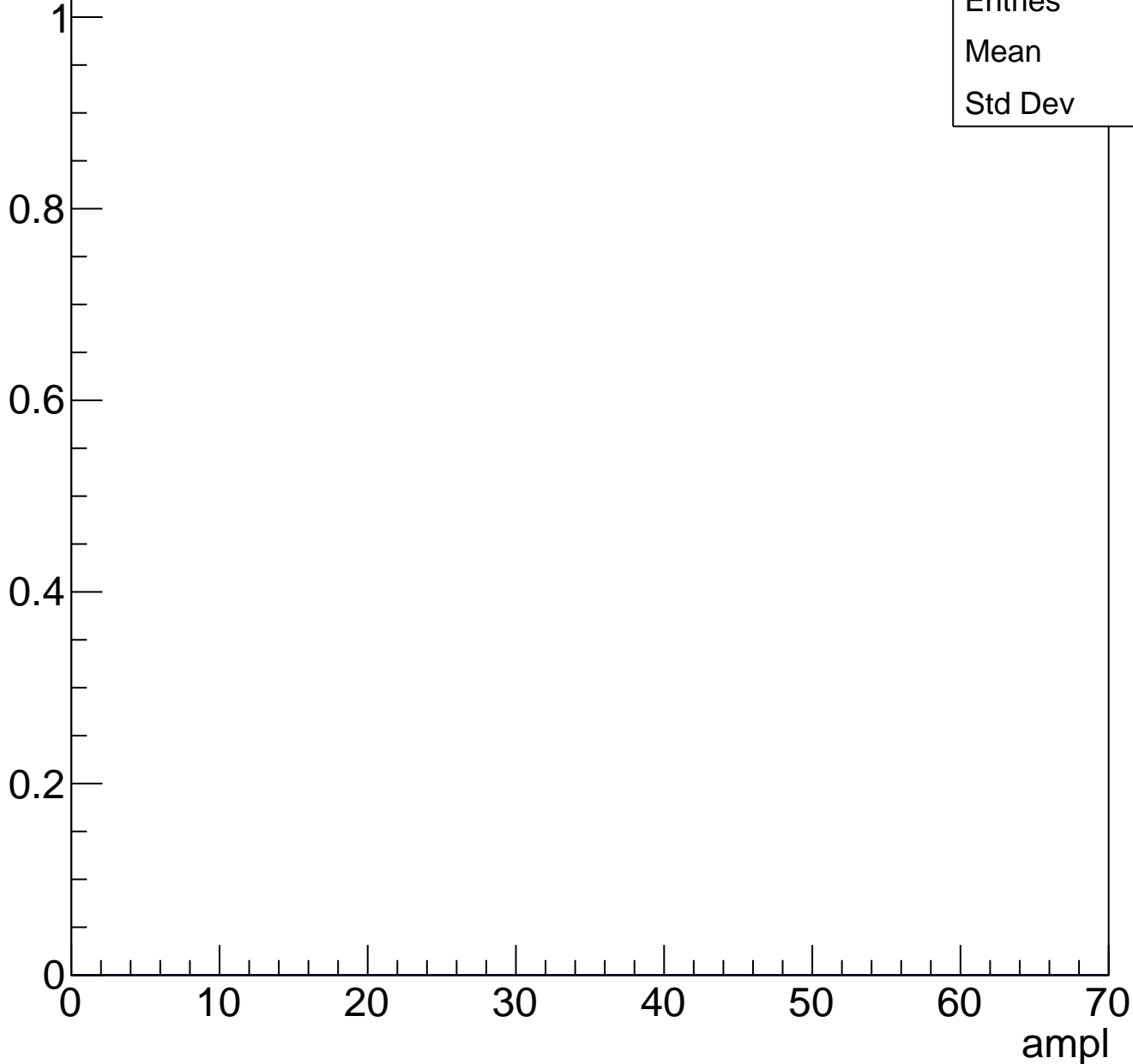
Entries	10
Mean	55.9
Std Dev	18.67



B1L003S, U20-ch13, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch14, adc0

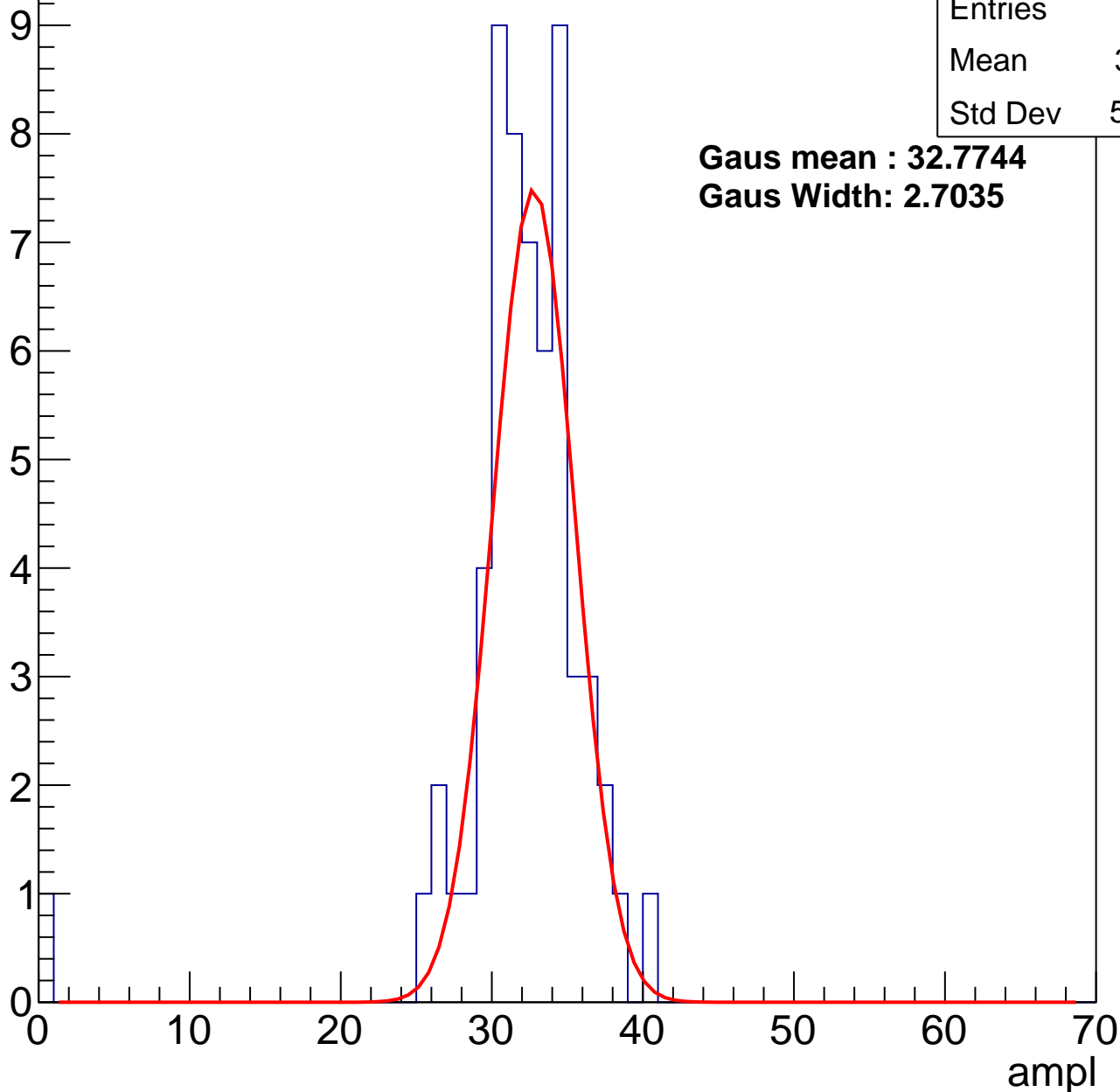
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	31.51
Std Dev	5.074

Gaus mean : 32.7744

Gaus Width: 2.7035



B1L003S, U20-ch14, adc1

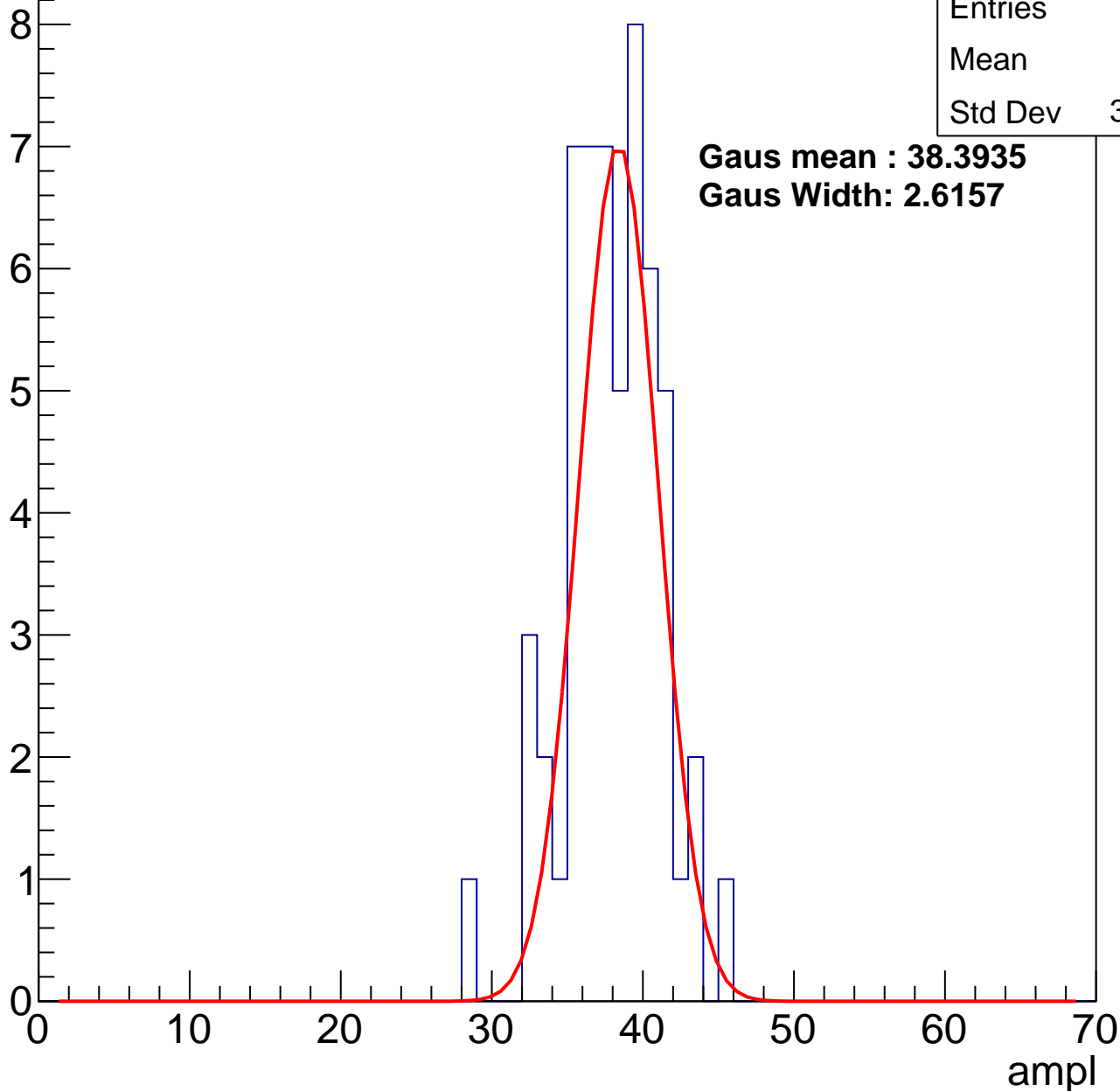
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	37.5
Std Dev	3.134

Gaus mean : 38.3935

Gaus Width: 2.6157



B1L003S, U20-ch14, adc2

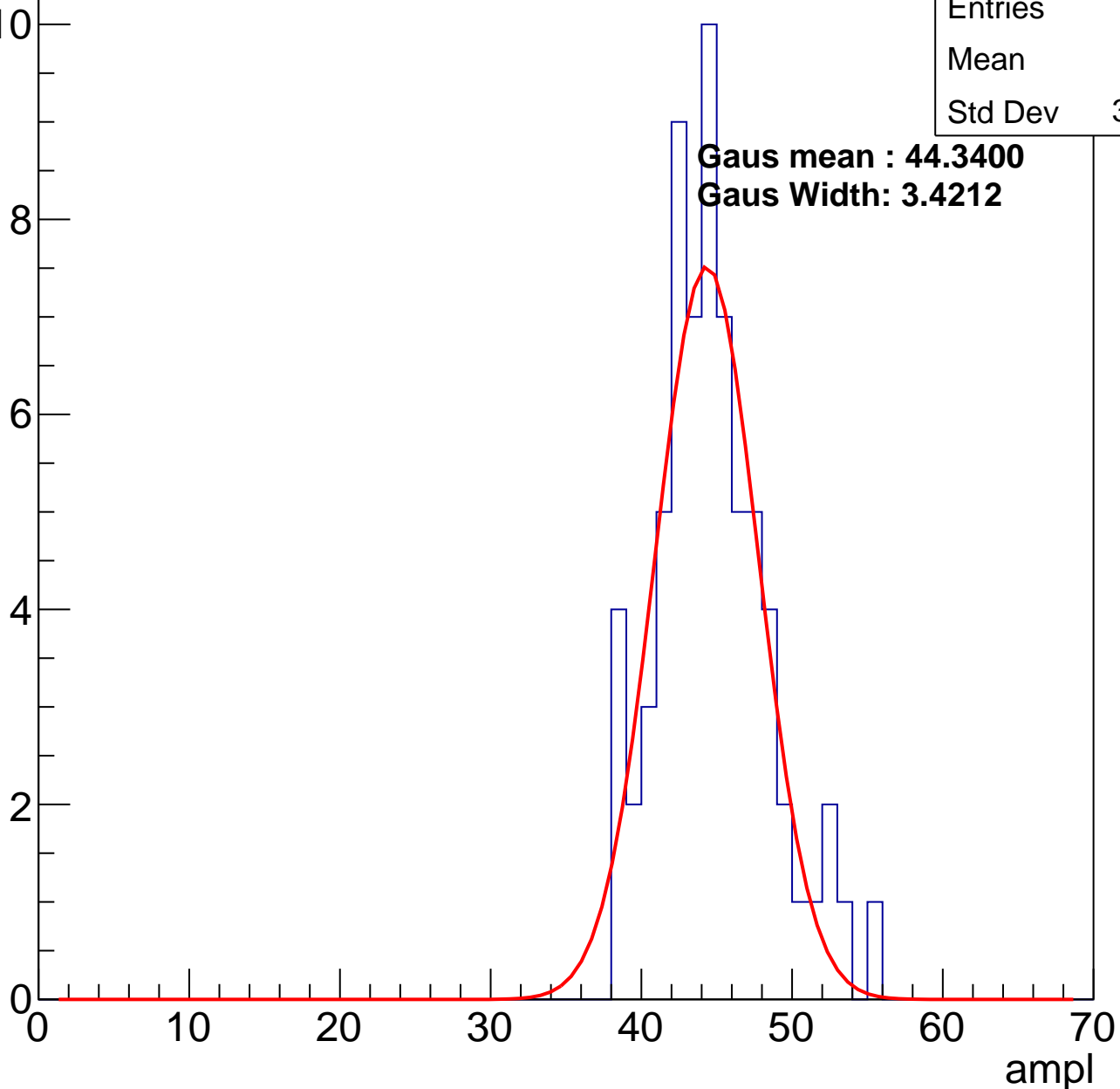
calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	44.3
Std Dev	3.661

Gaus mean : 44.3400

Gaus Width: 3.4212

Entry

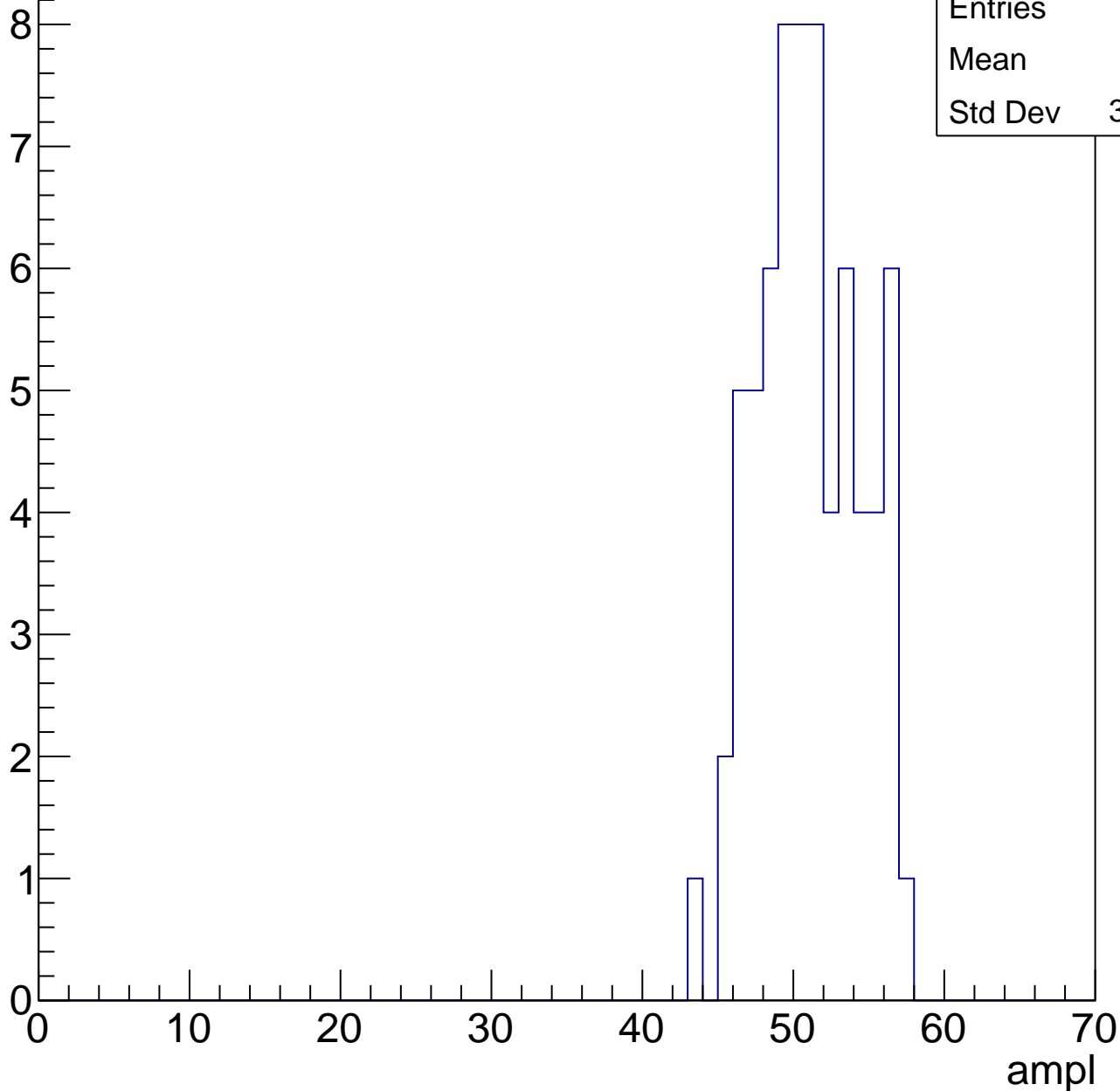


B1L003S, U20-ch14, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

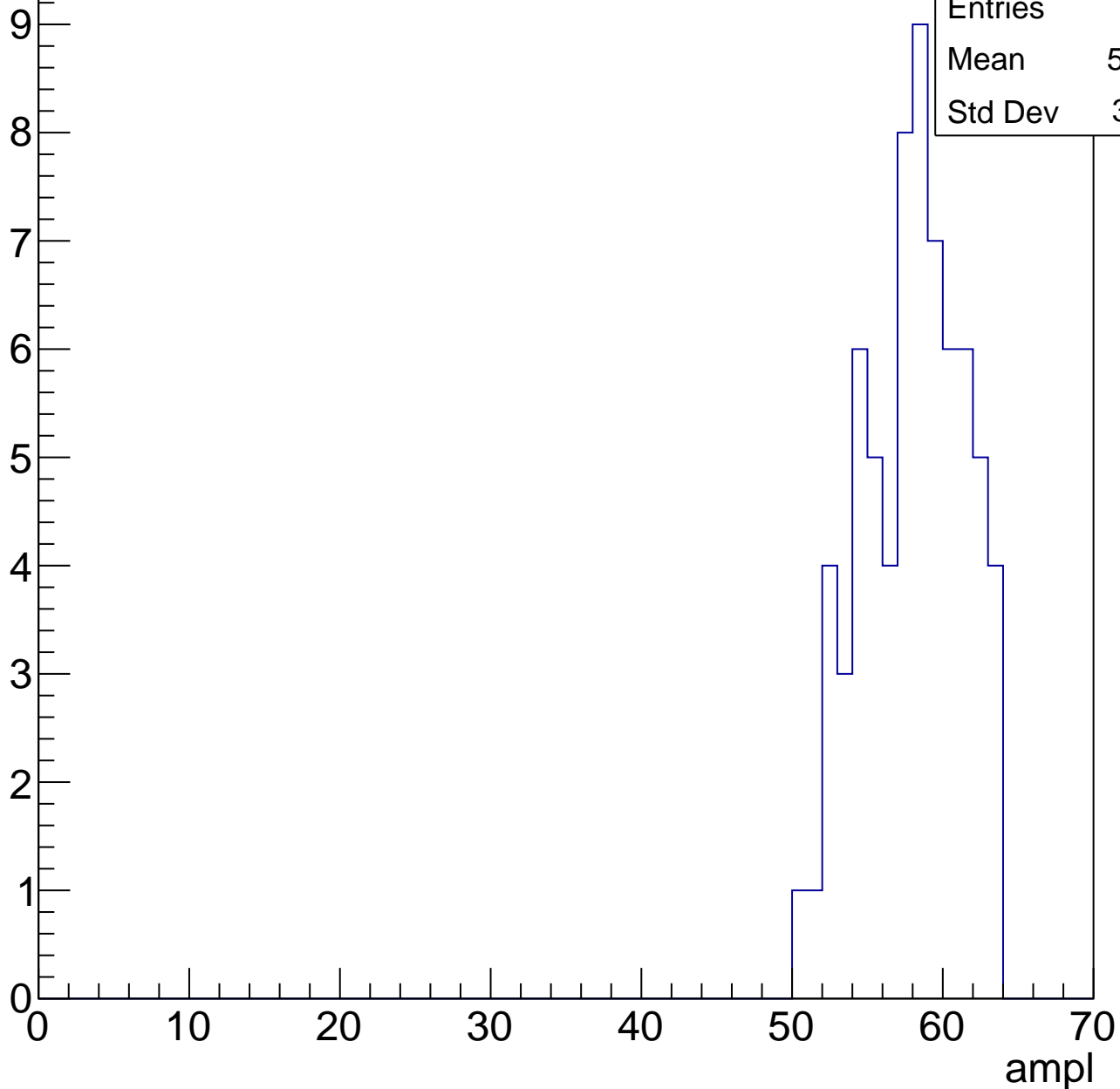
Entries	68
Mean	50.6
Std Dev	3.295



B1L003S, U20-ch14, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

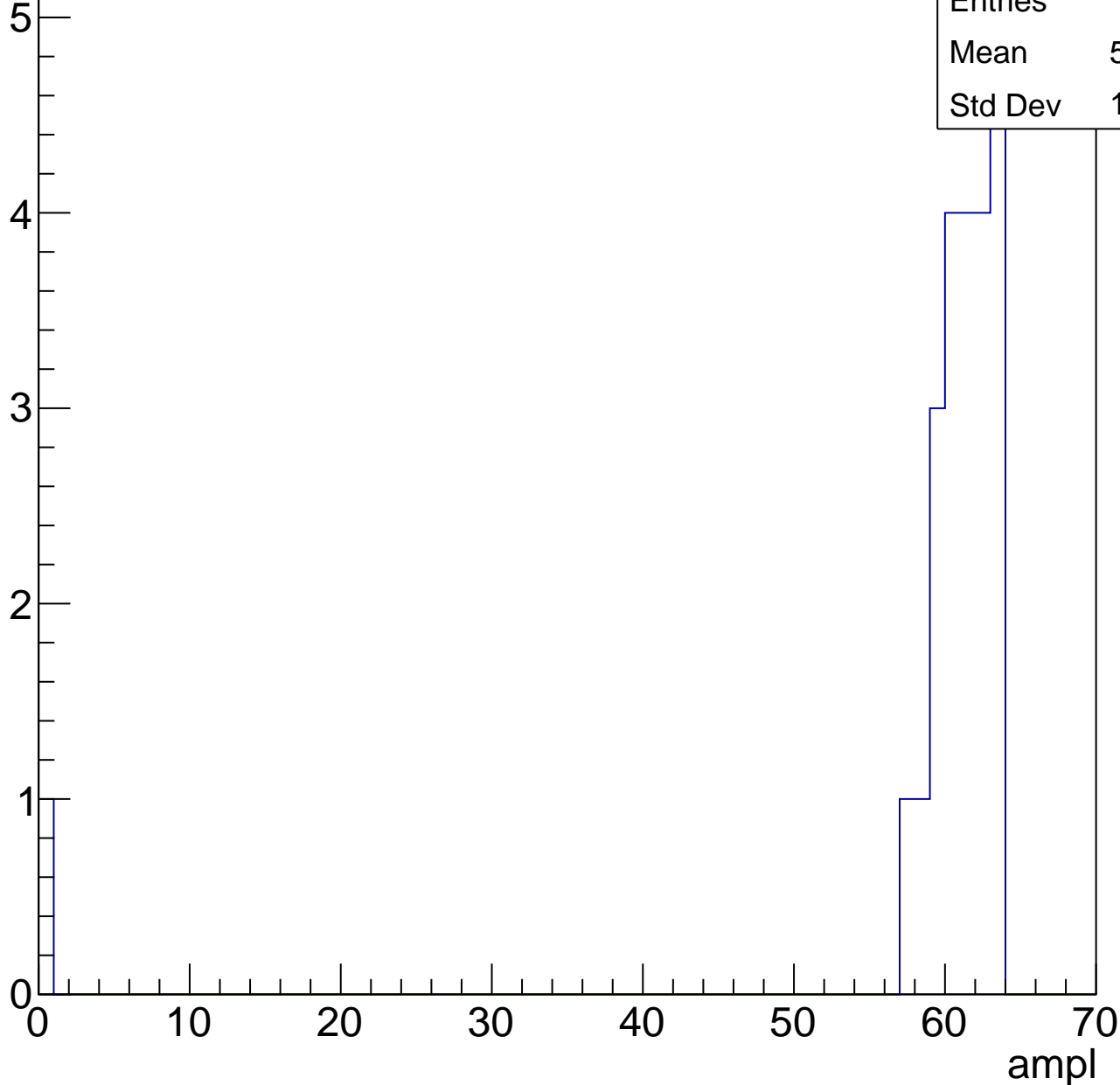


B1L003S, U20-ch14, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.22
Std Dev	12.52



B1L003S, U20-ch14, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch14, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch15, adc0

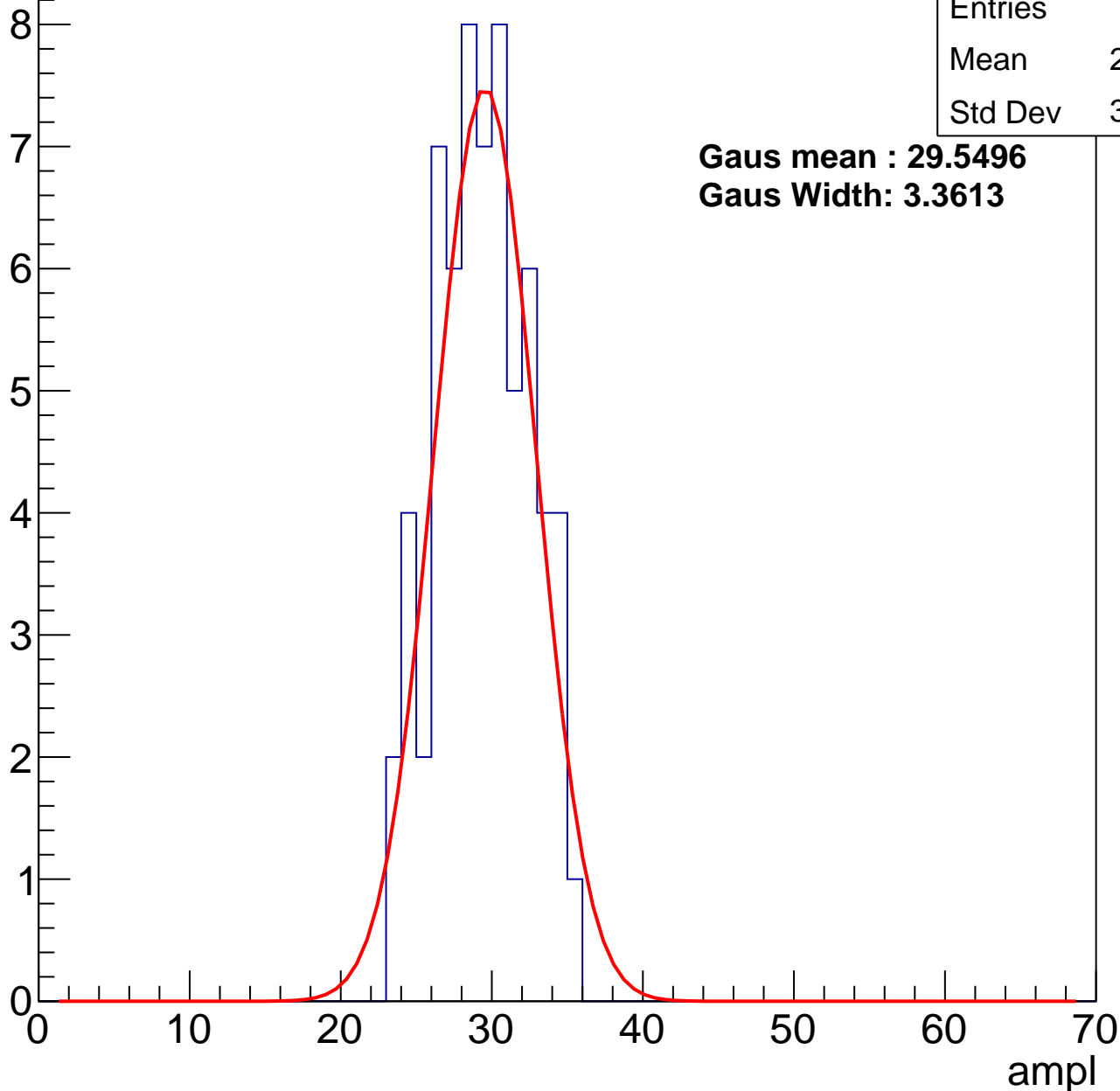
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.95
Std Dev	3.013

Gaus mean : 29.5496

Gaus Width: 3.3613



B1L003S, U20-ch15, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	36.78
Std Dev	3.558

Gaus mean : 37.1006

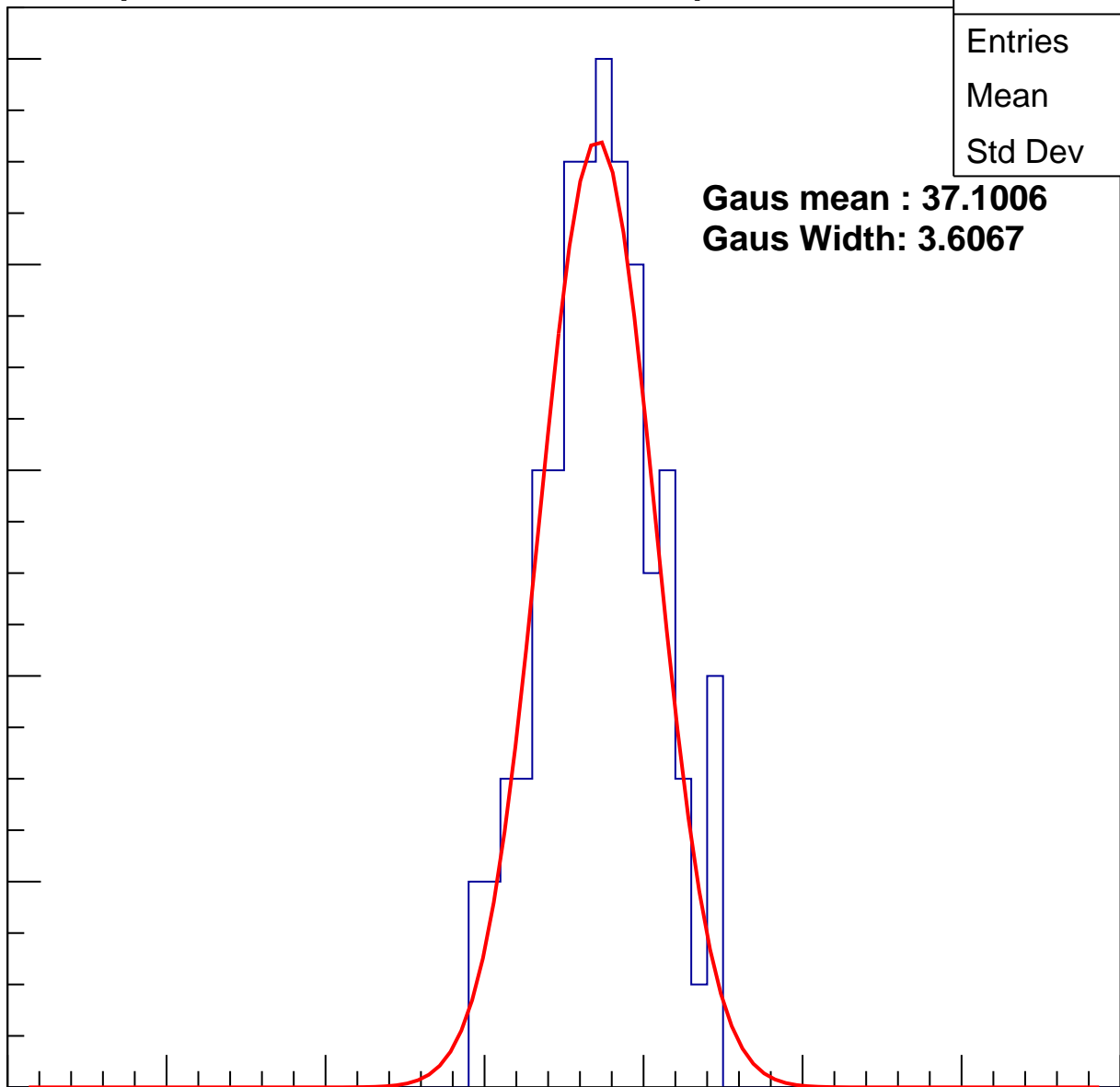
Gaus Width: 3.6067

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch15, adc2

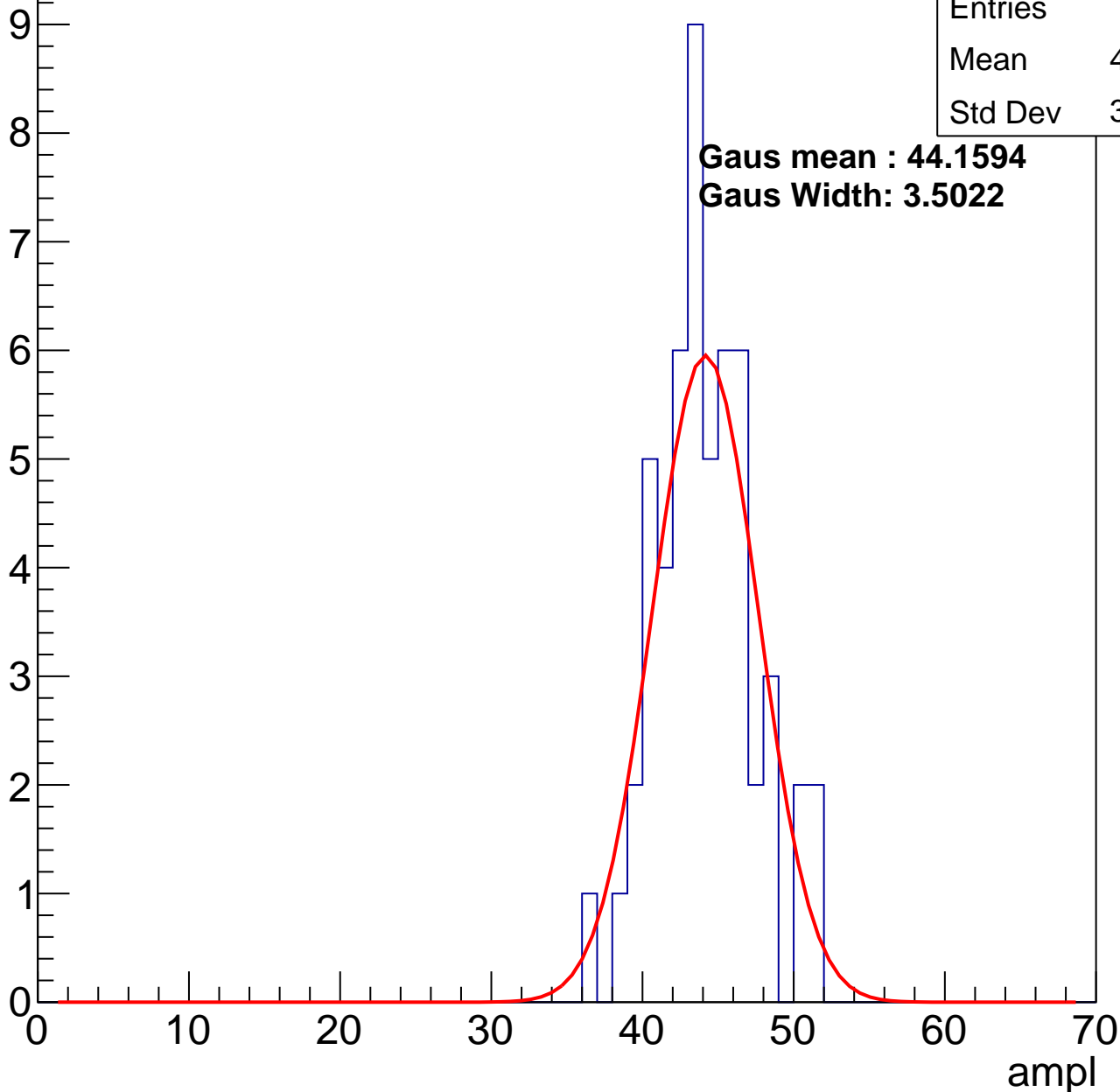
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.72
Std Dev	3.223

Gaus mean : 44.1594

Gaus Width: 3.5022

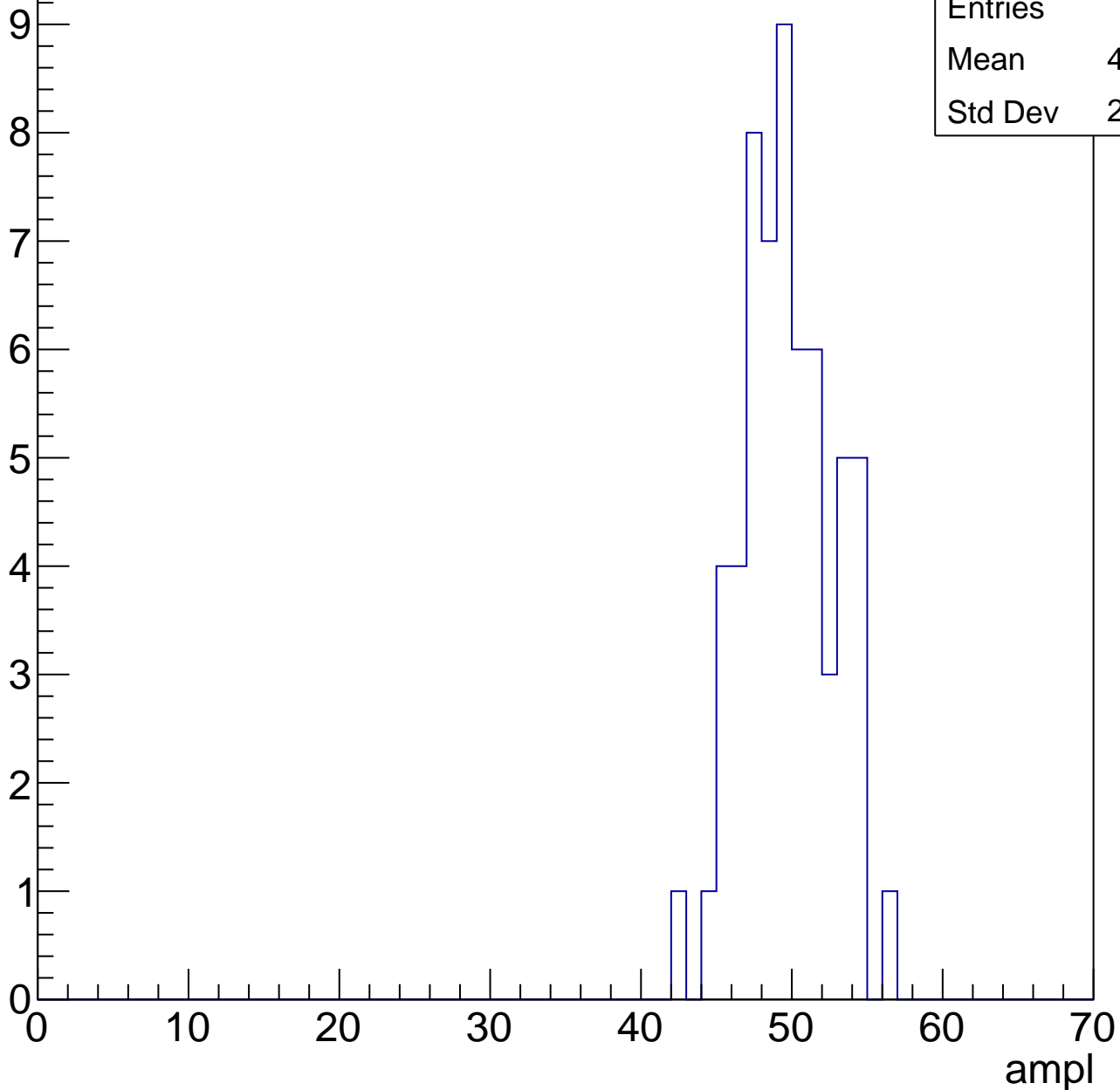


B1L003S, U20-ch15, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.27
Std Dev	2.943

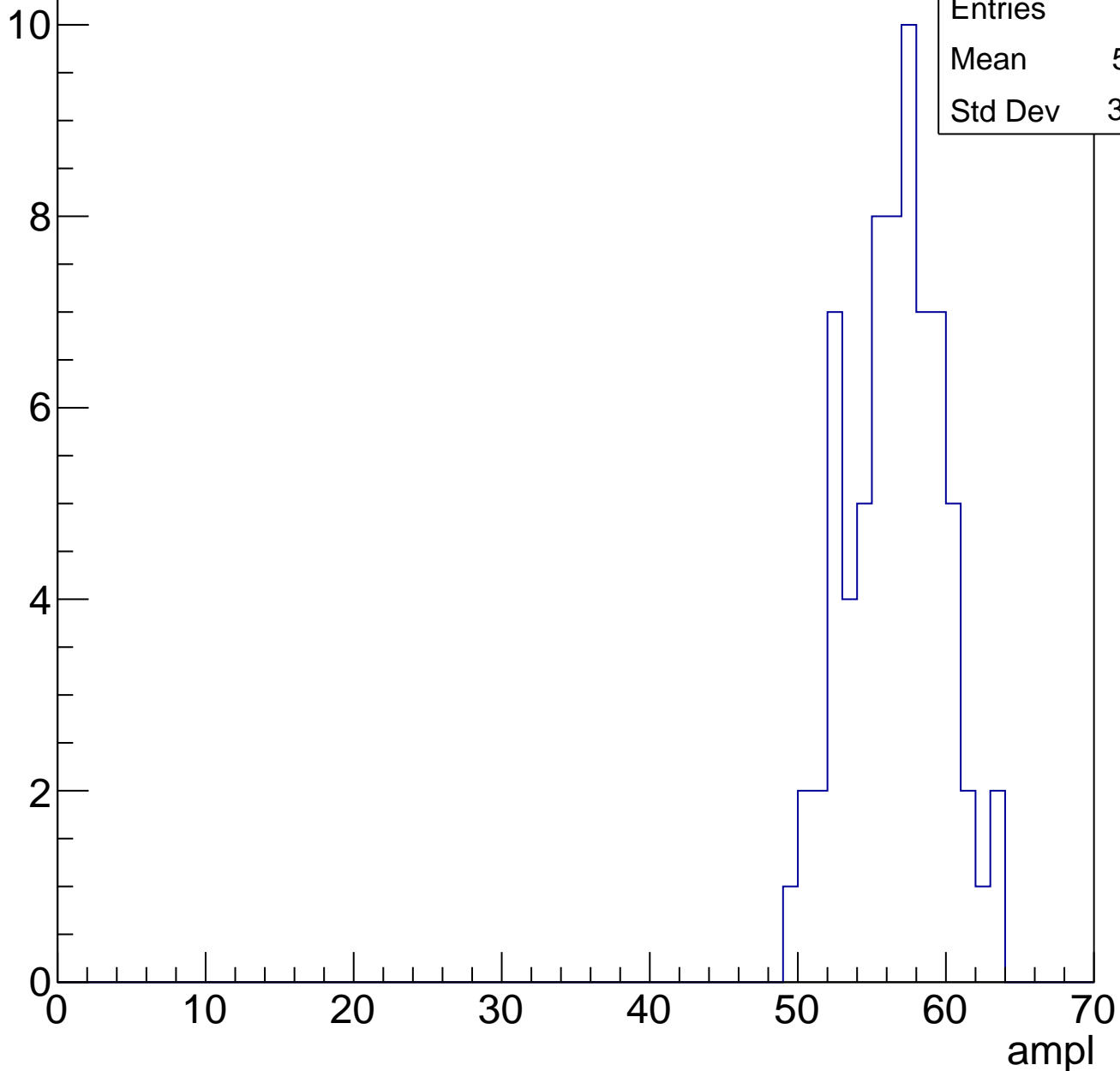


B1L003S, U20-ch15, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	56.11
Std Dev	3.165

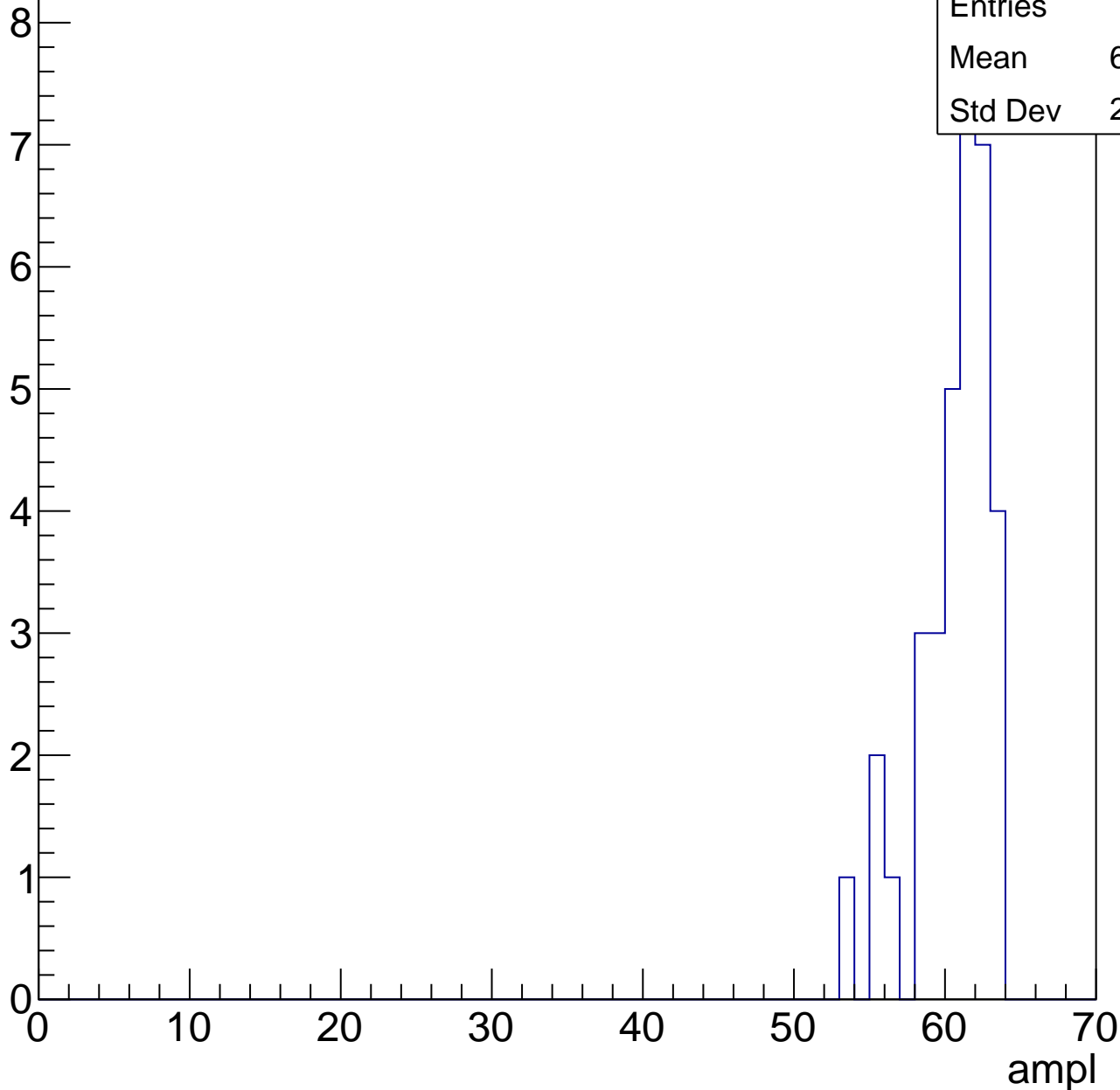


B1L003S, U20-ch15, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

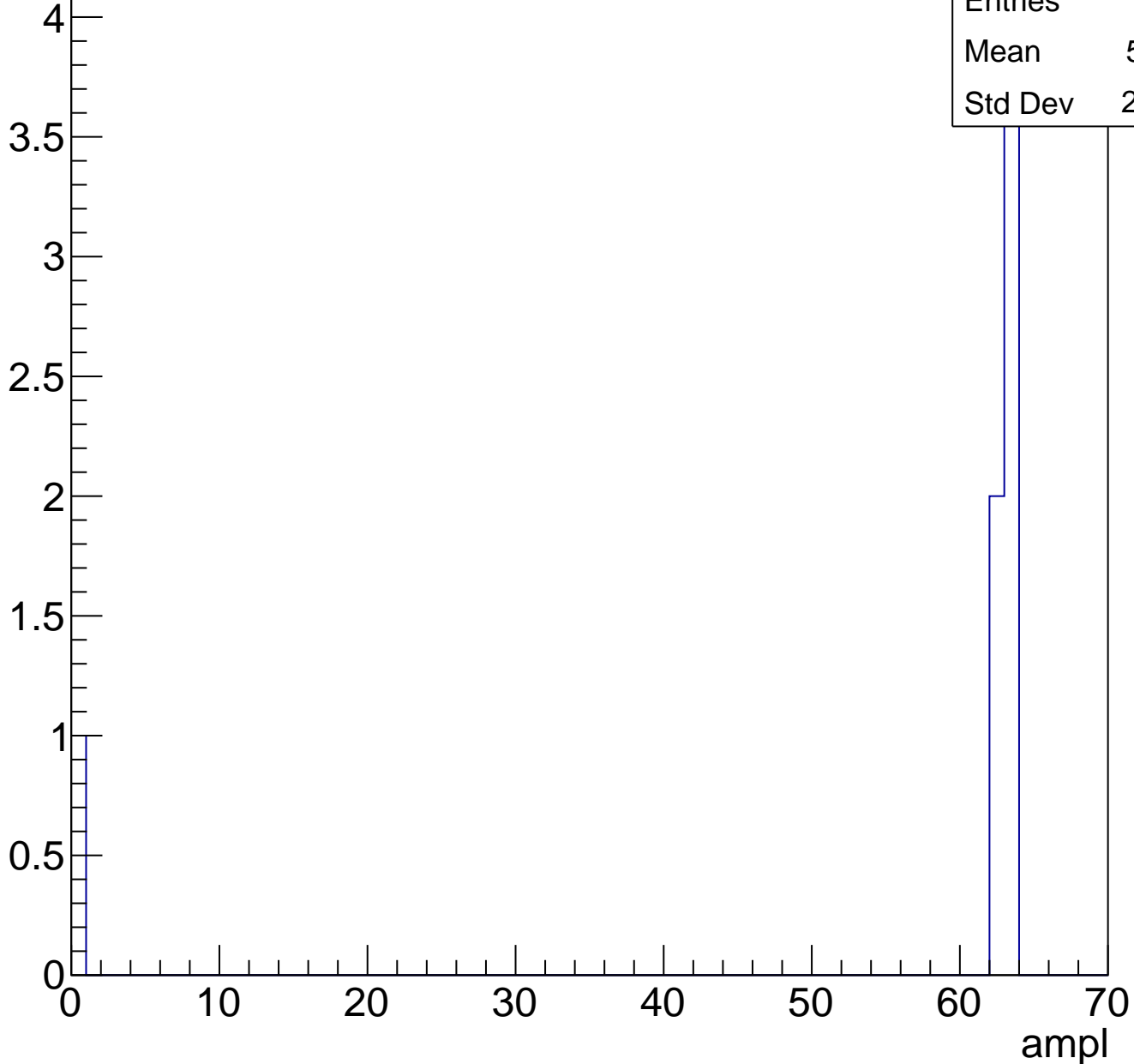
Entries	34
Mean	60.12
Std Dev	2.435



B1L003S, U20-ch15, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch15, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch16, adc0

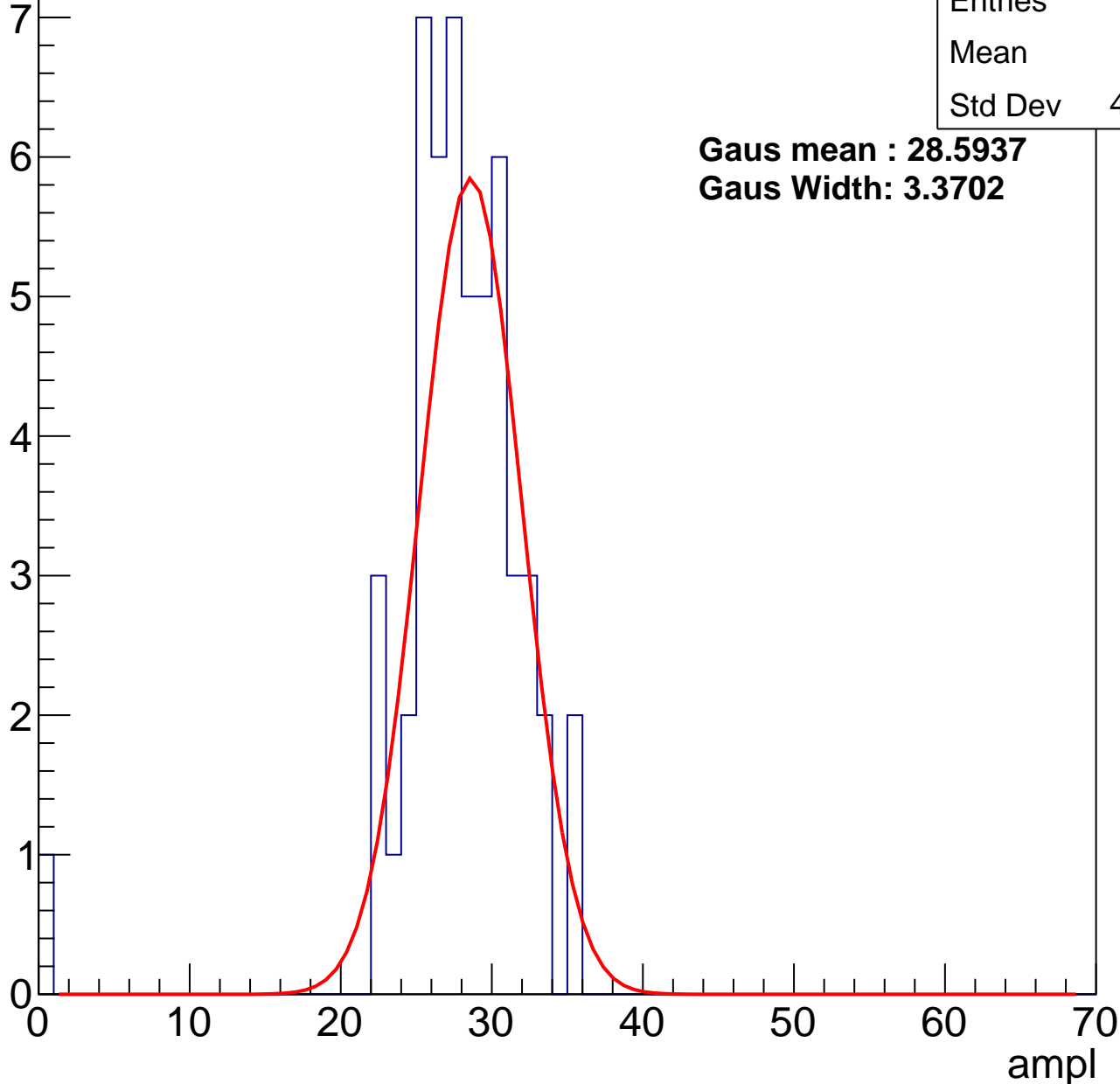
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	27.3
Std Dev	4.894

Gaus mean : 28.5937

Gaus Width: 3.3702



B1L003S, U20-ch16, adc1

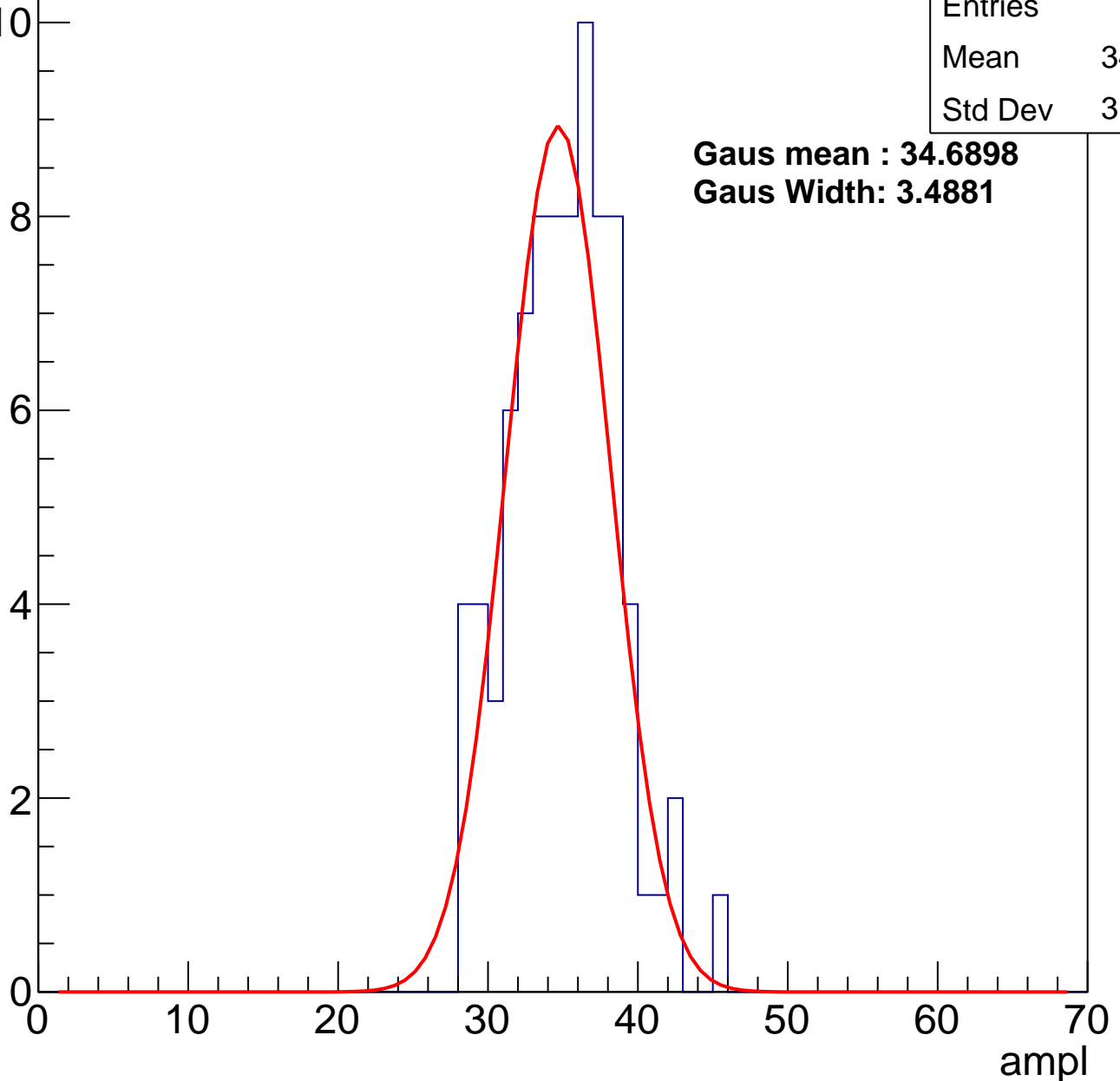
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	34.58
Std Dev	3.533

Gaus mean : 34.6898

Gaus Width: 3.4881



B1L003S, U20-ch16, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	42.19
Std Dev	3.273

Gaus mean : 42.5914

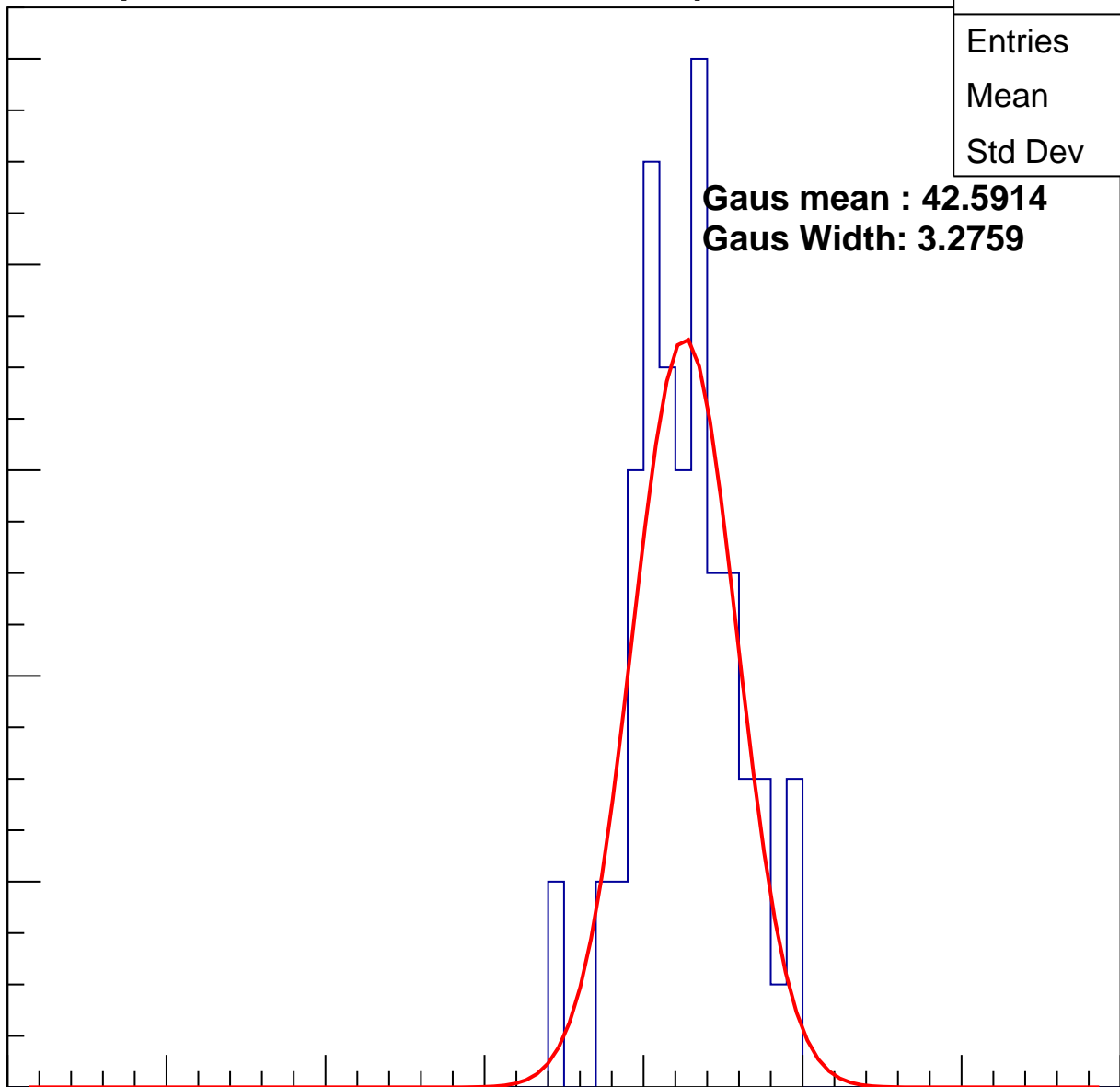
Gaus Width: 3.2759

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

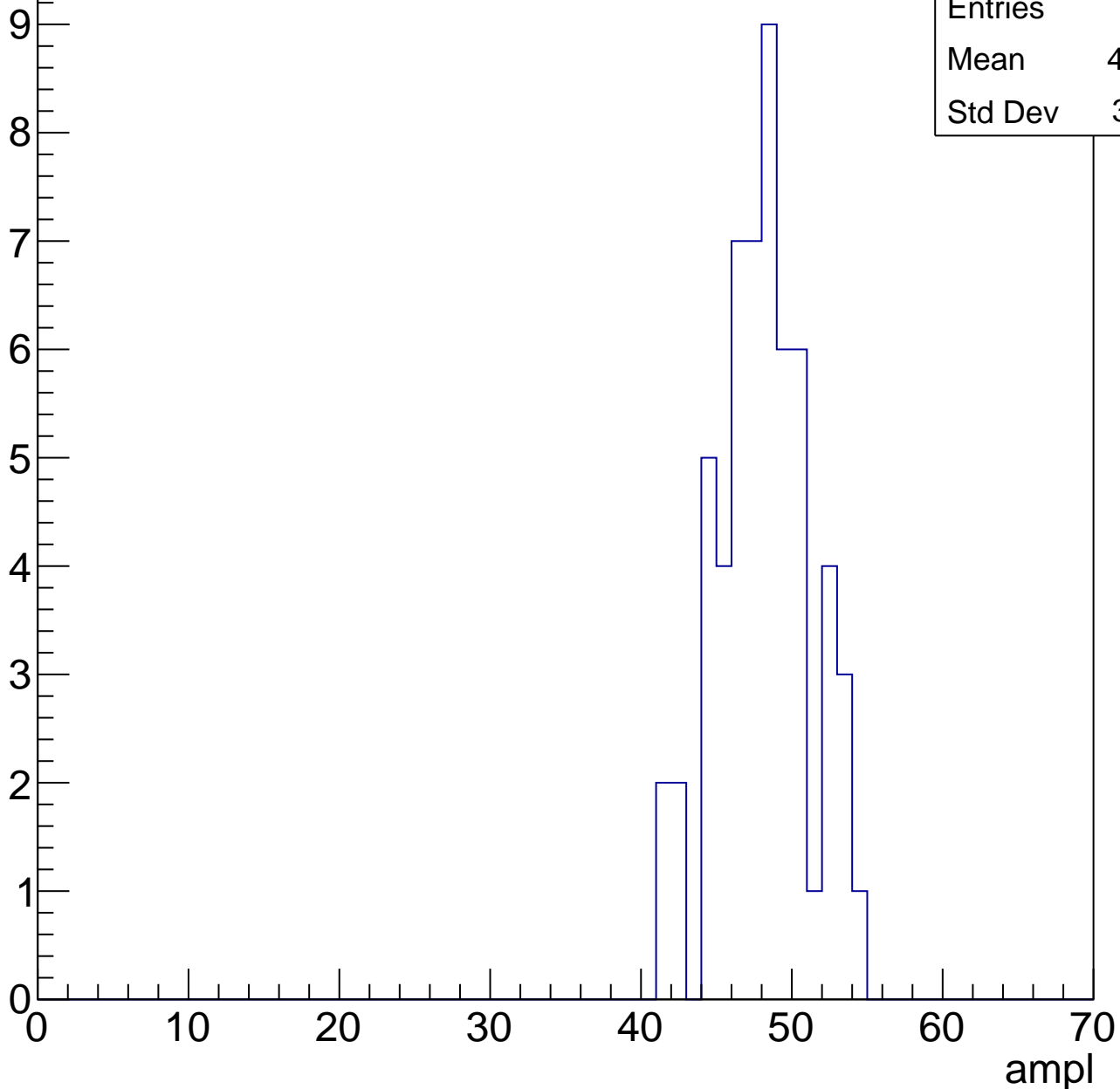


B1L003S, U20-ch16, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.63
Std Dev	3.041

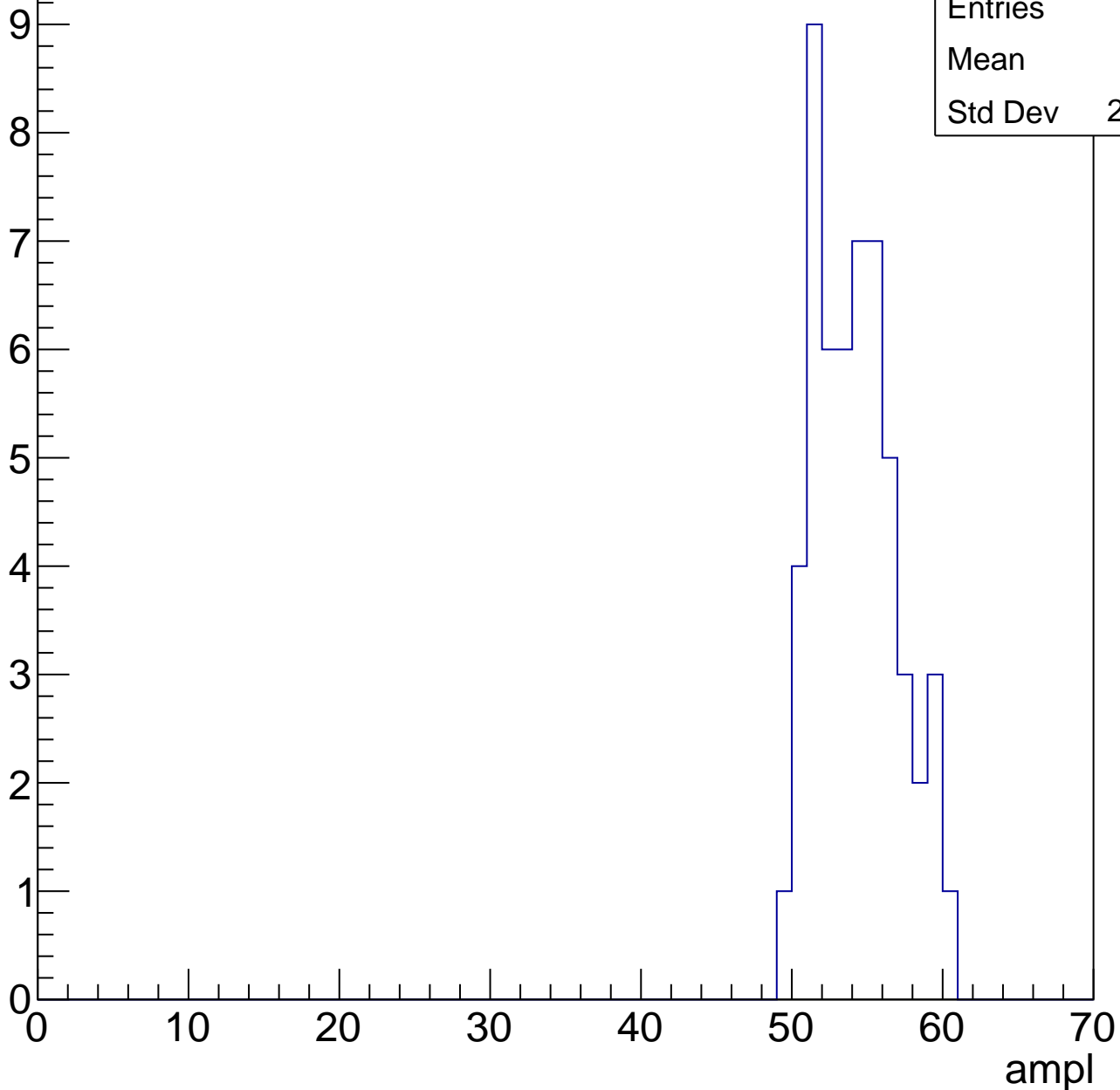


B1L003S, U20-ch16, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	53.8
Std Dev	2.704

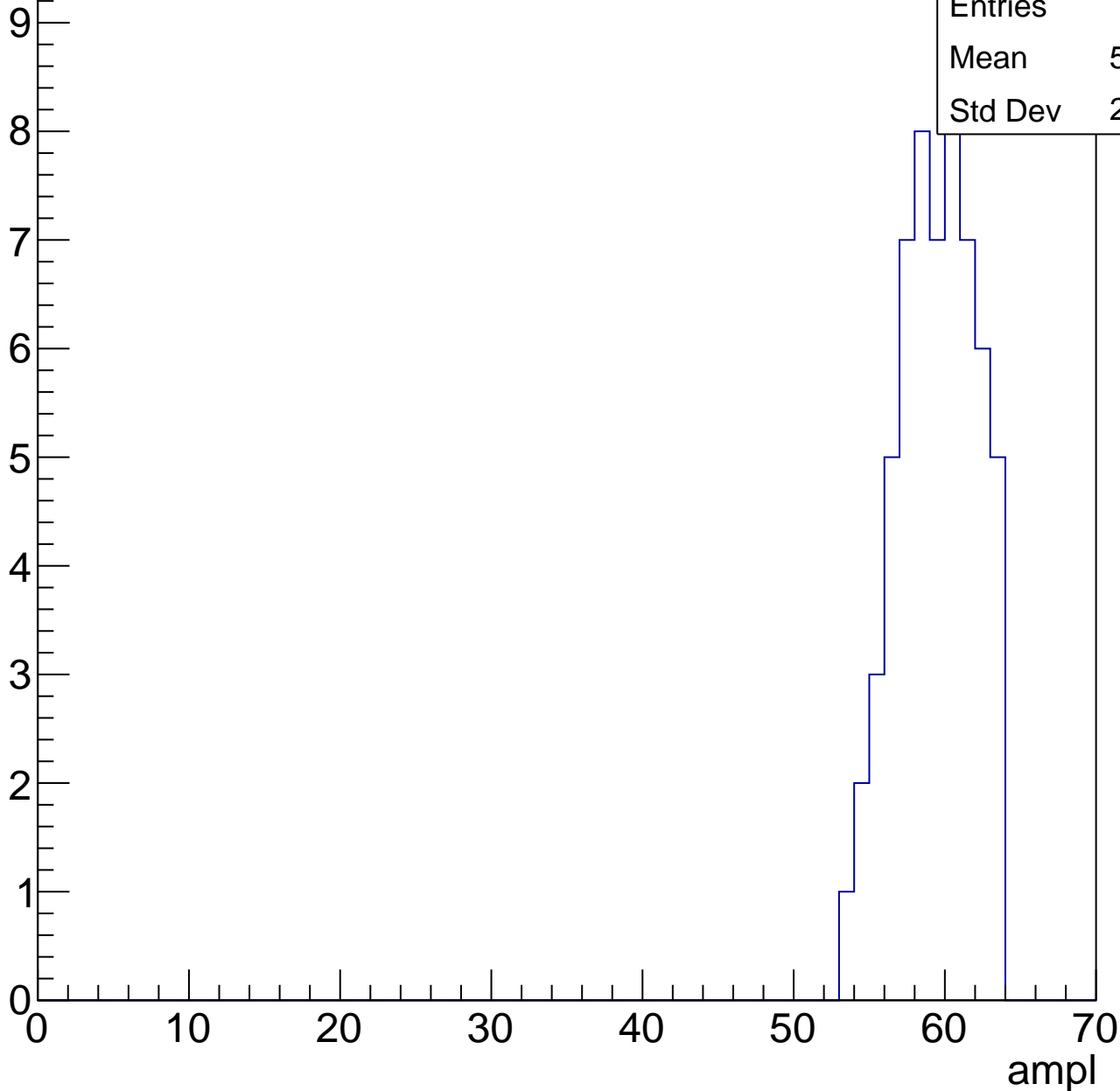


B1L003S, U20-ch16, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	58.93
Std Dev	2.536

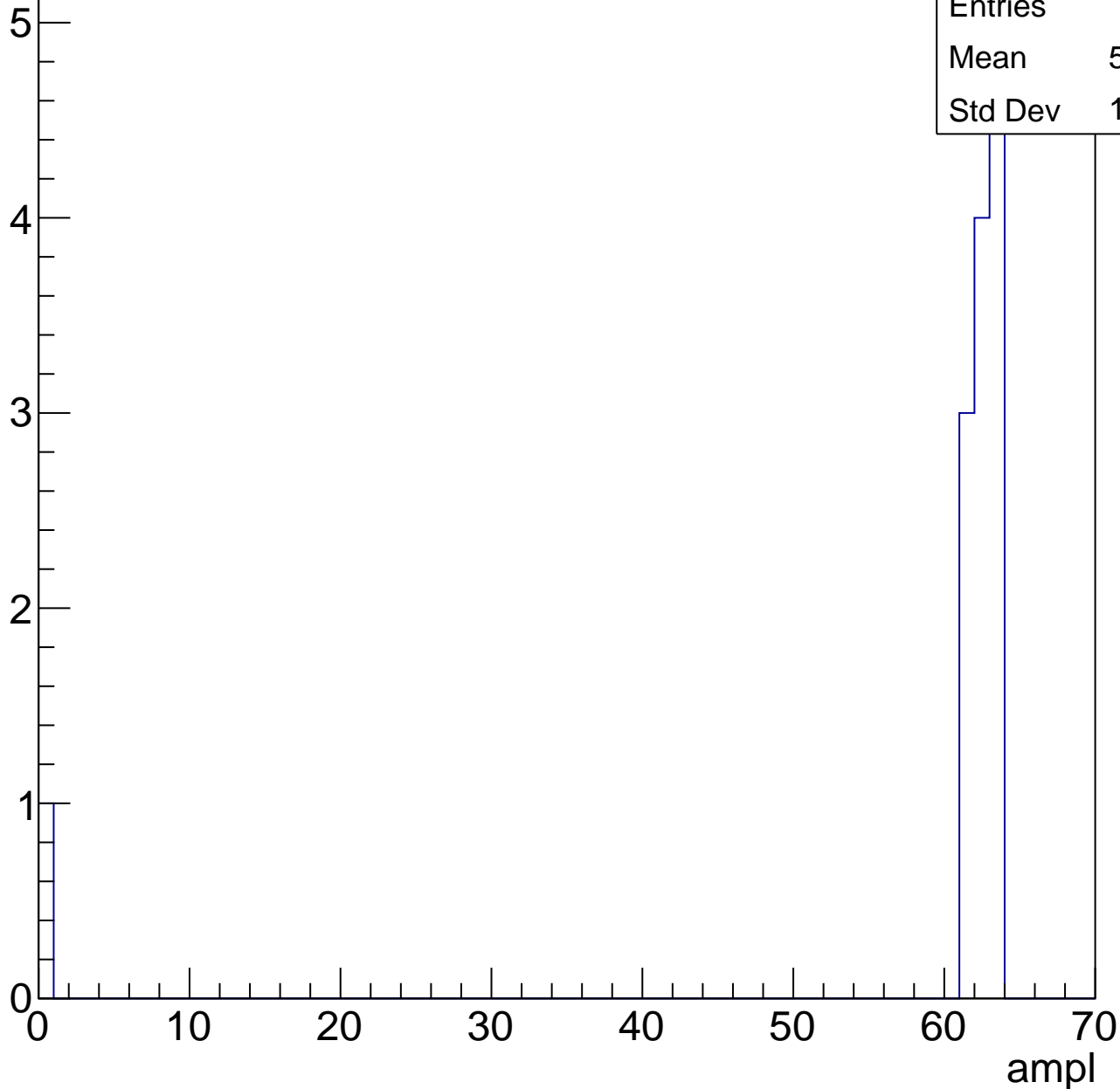


B1L003S, U20-ch16, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

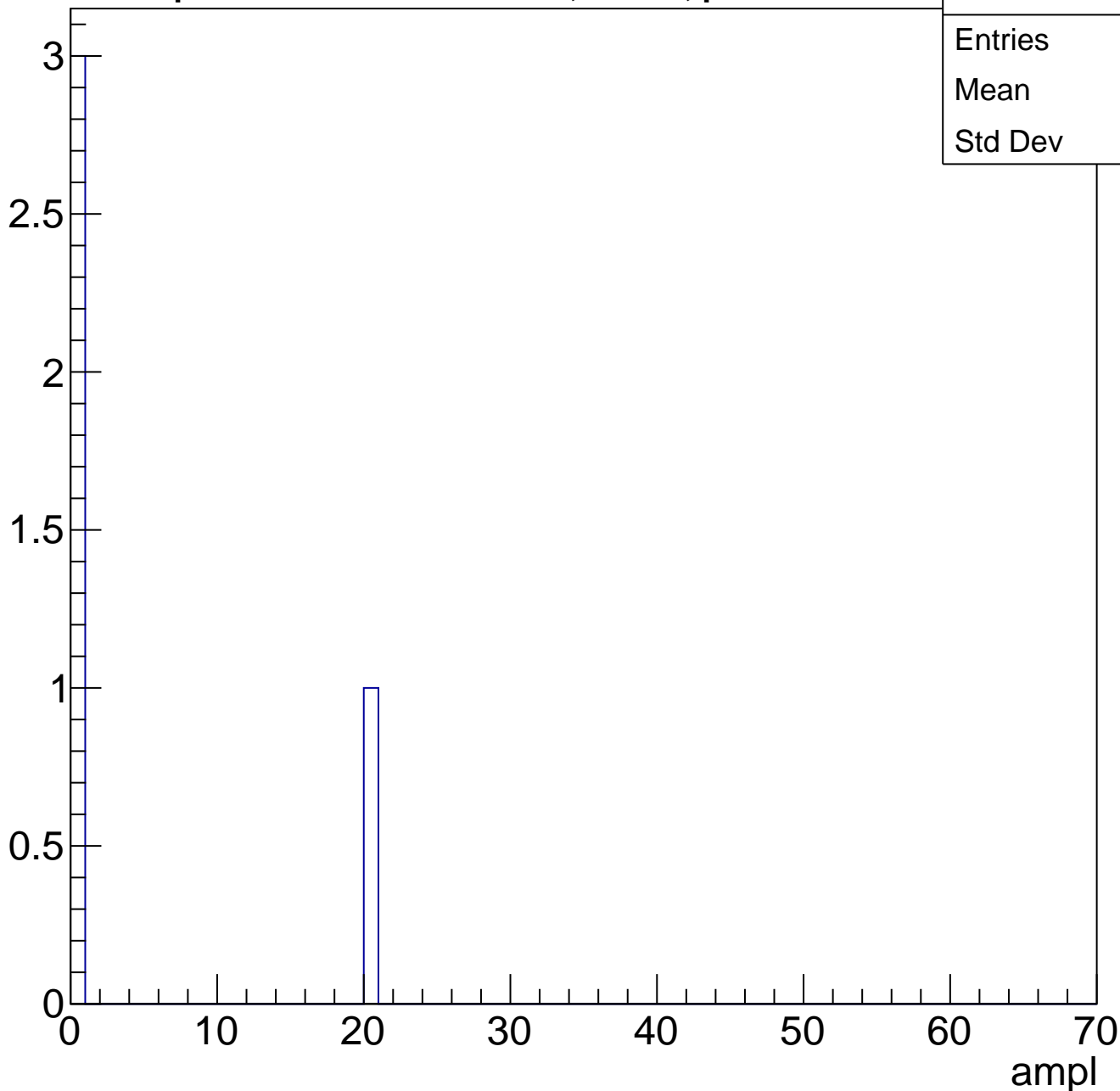
Entries	13
Mean	57.38
Std Dev	16.58



B1L003S, U20-ch16, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	5
Std Dev	8.66

B1L003S, U20-ch17, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	30.62
Std Dev	2.876

Gaus mean : 30.8658

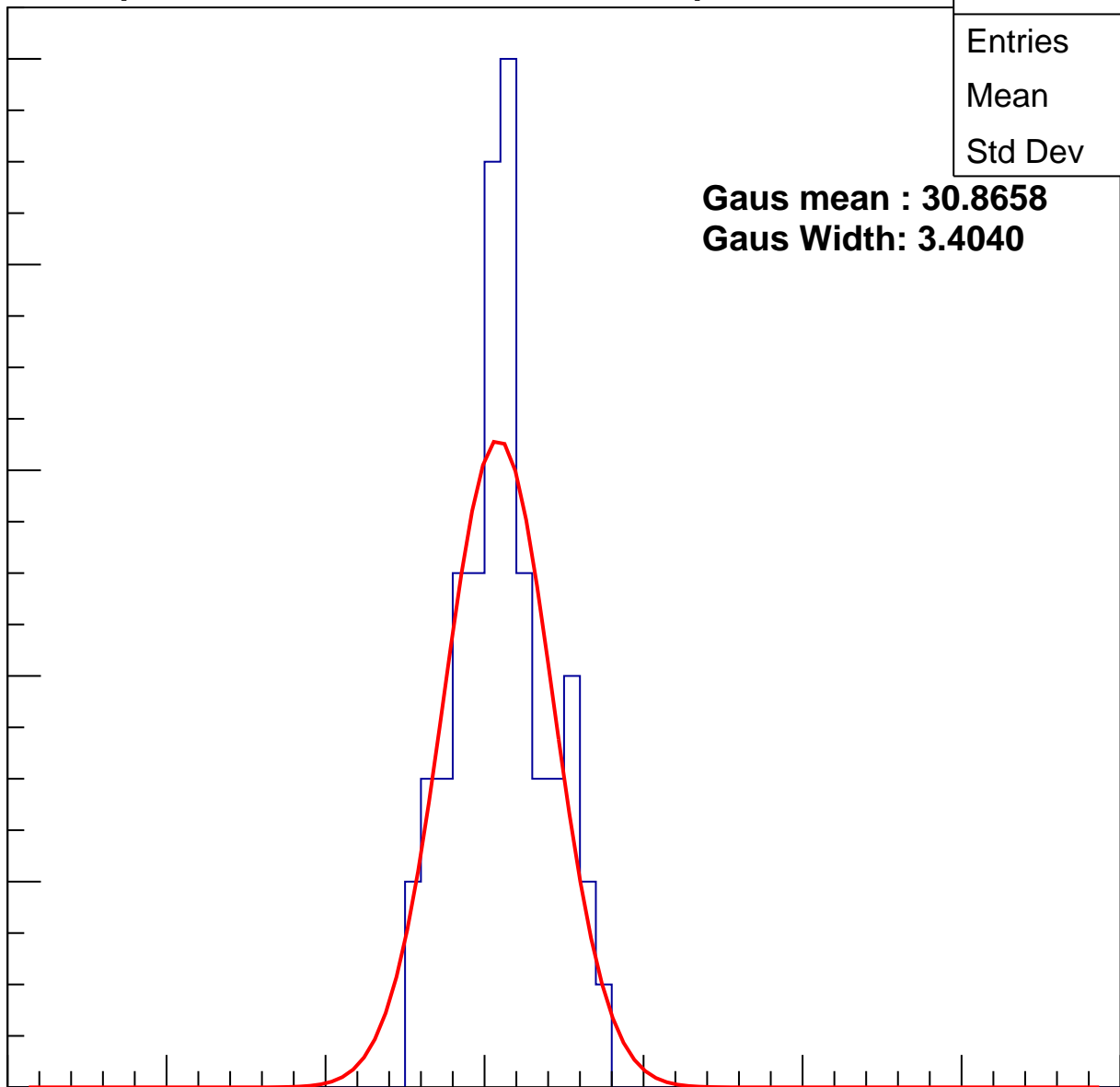
Gaus Width: 3.4040

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch17, adc1

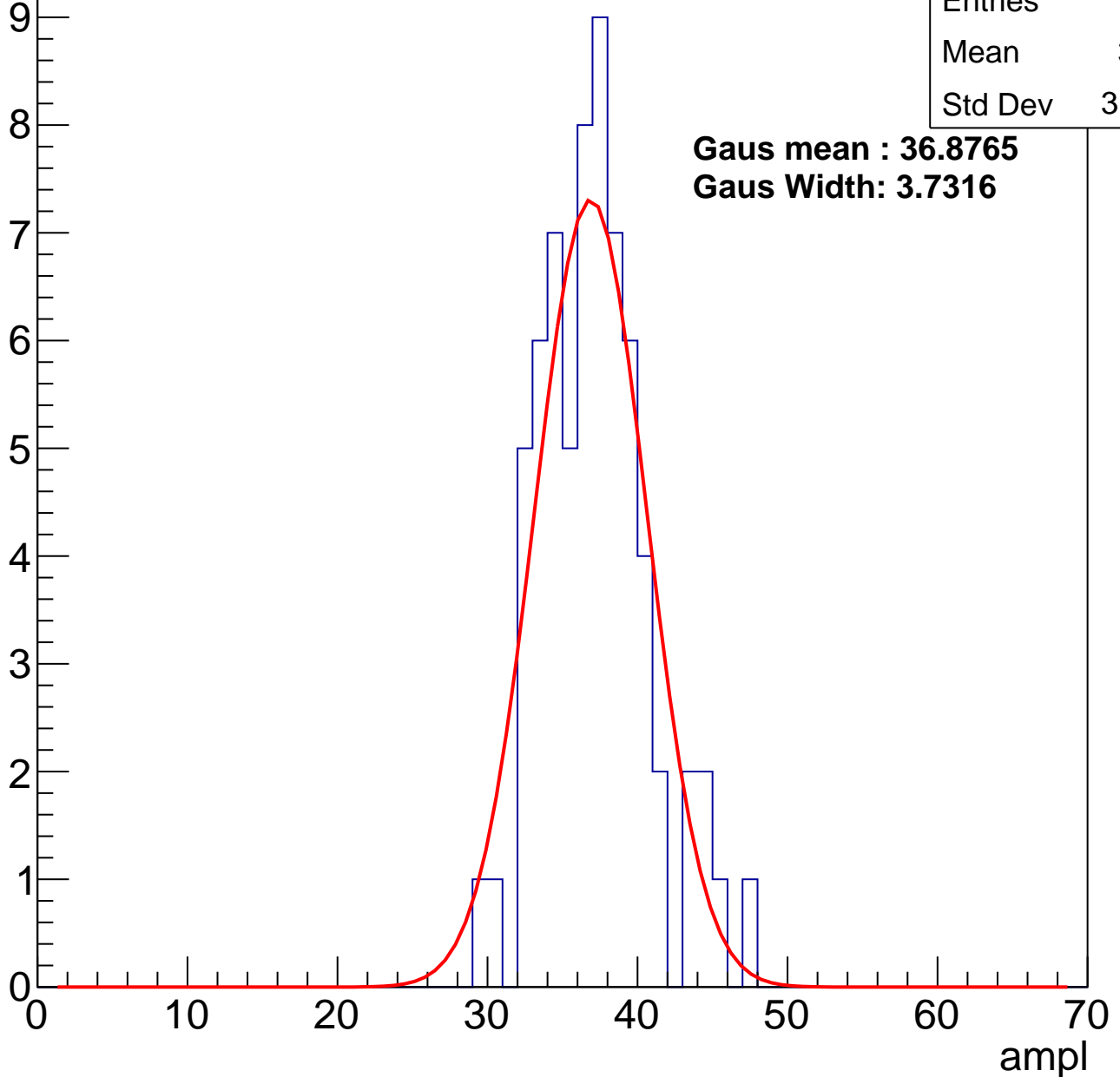
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.7
Std Dev	3.566

Gaus mean : 36.8765

Gaus Width: 3.7316



B1L003S, U20-ch17, adc2

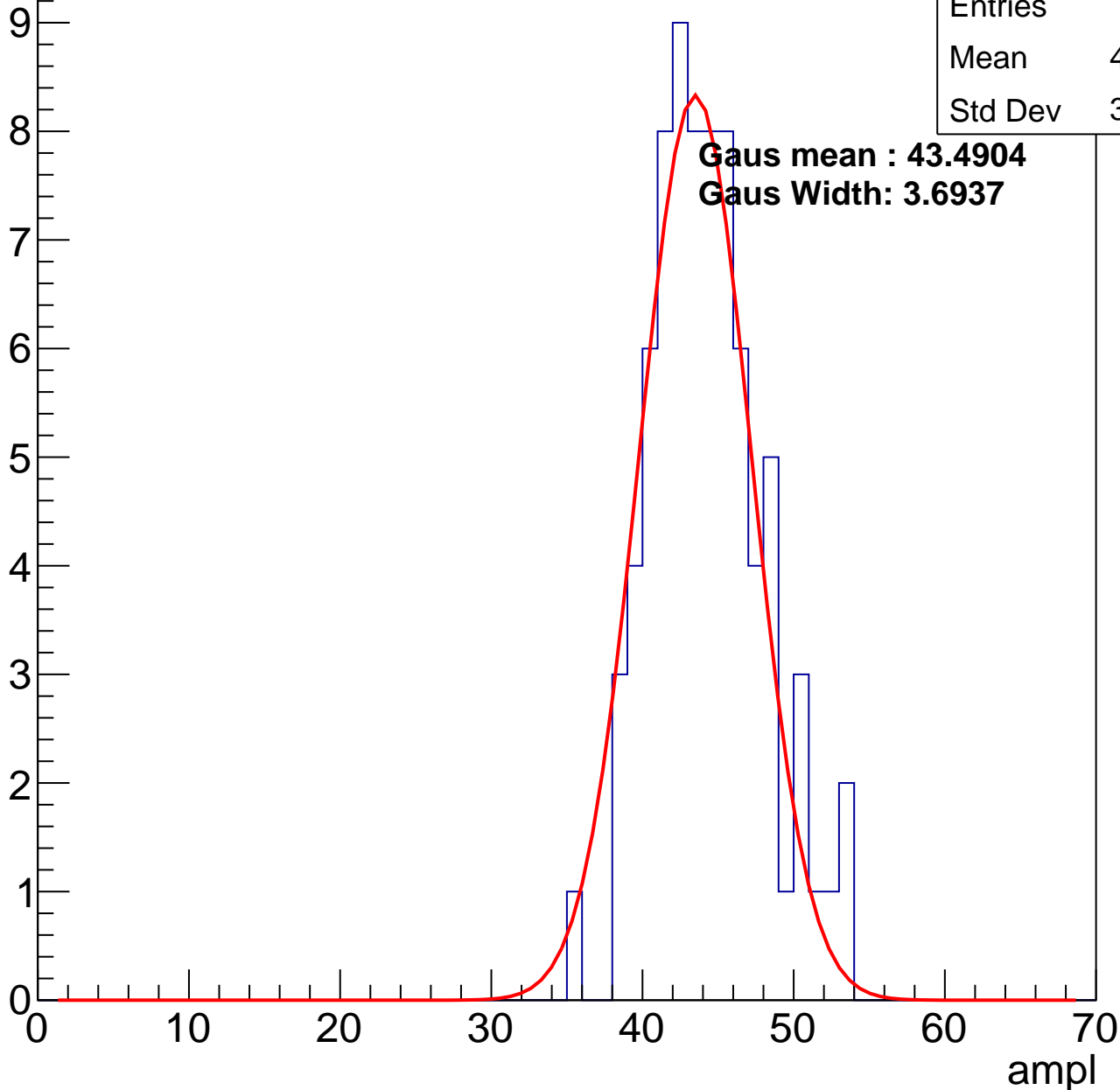
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	43.83
Std Dev	3.677

Gaus mean : 43.4904

Gaus Width: 3.6937

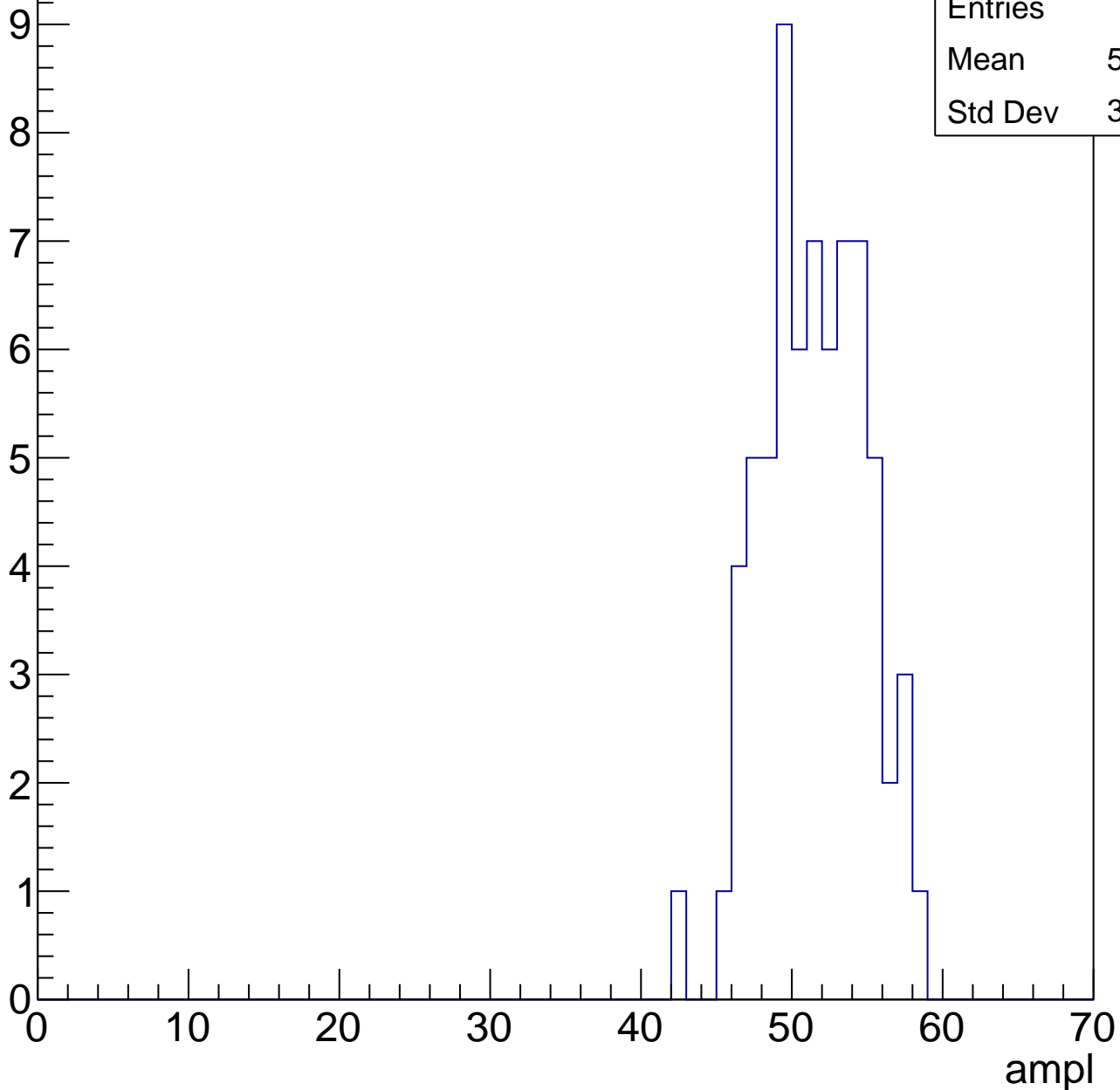


B1L003S, U20-ch17, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	51.03
Std Dev	3.336

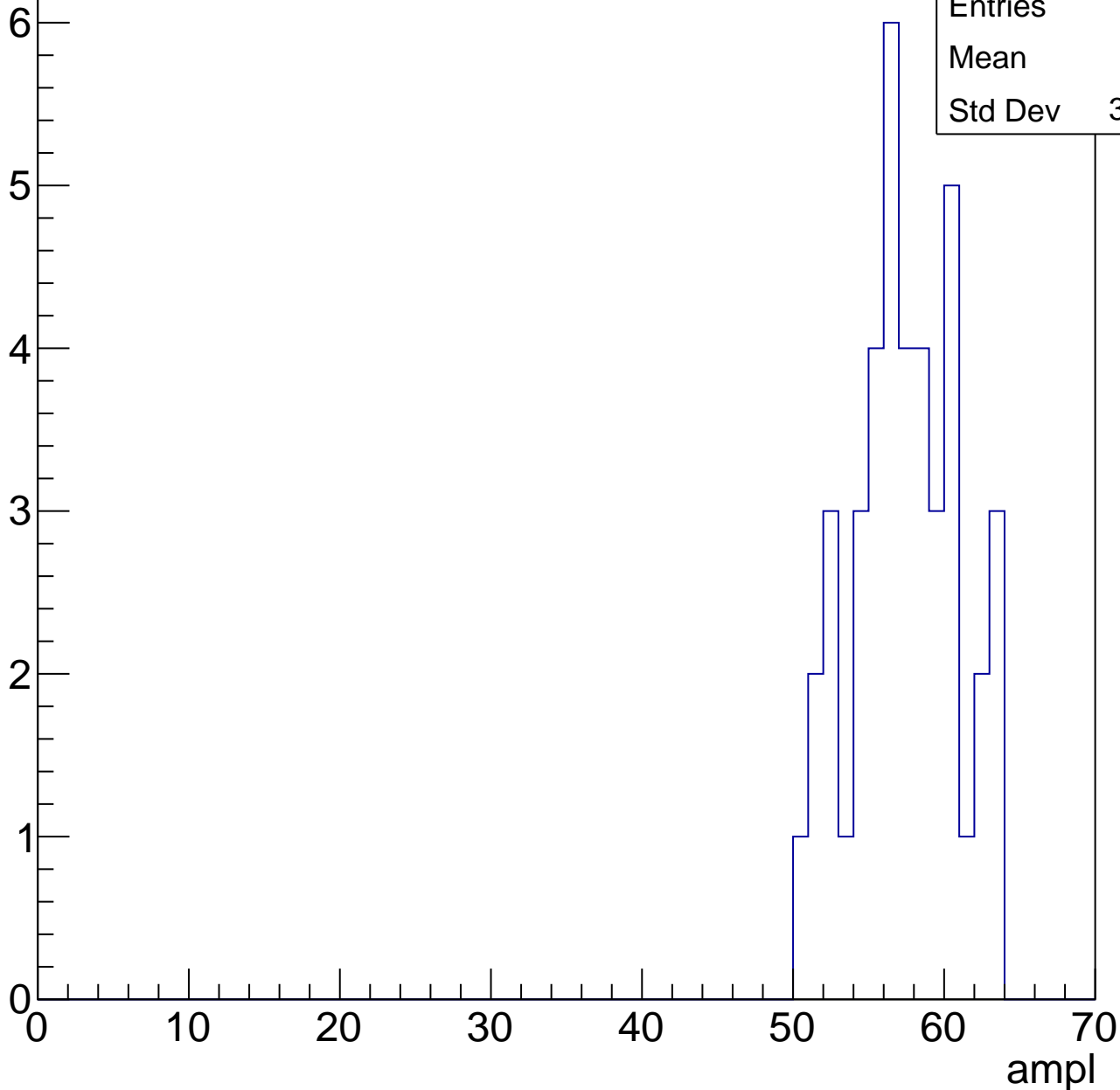


B1L003S, U20-ch17, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	56.9
Std Dev	3.435

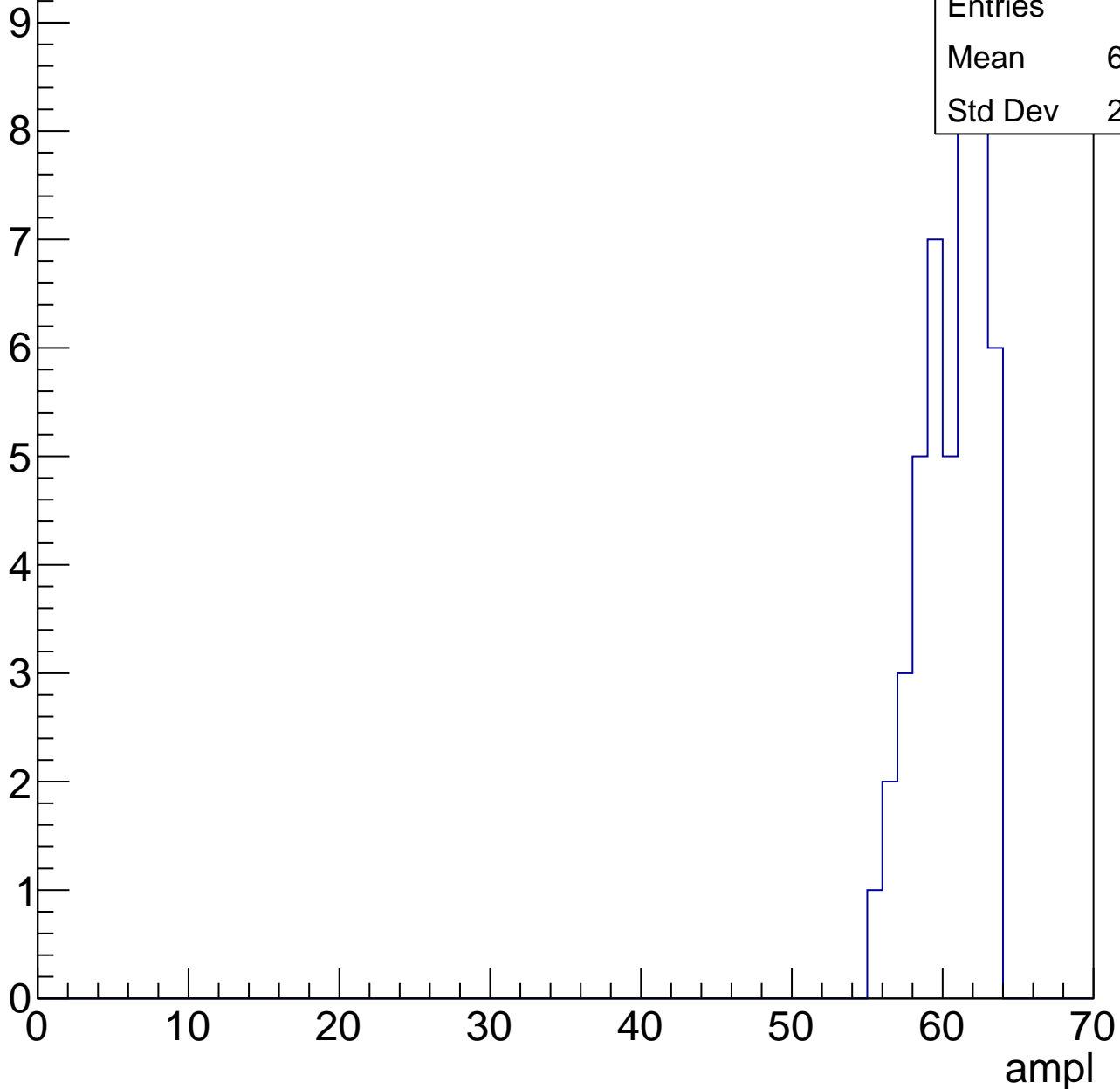


B1L003S, U20-ch17, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	60.09
Std Dev	2.114



B1L003S, U20-ch17, adc6

calib_packv5_042523_0143.root, FC#13, port D2

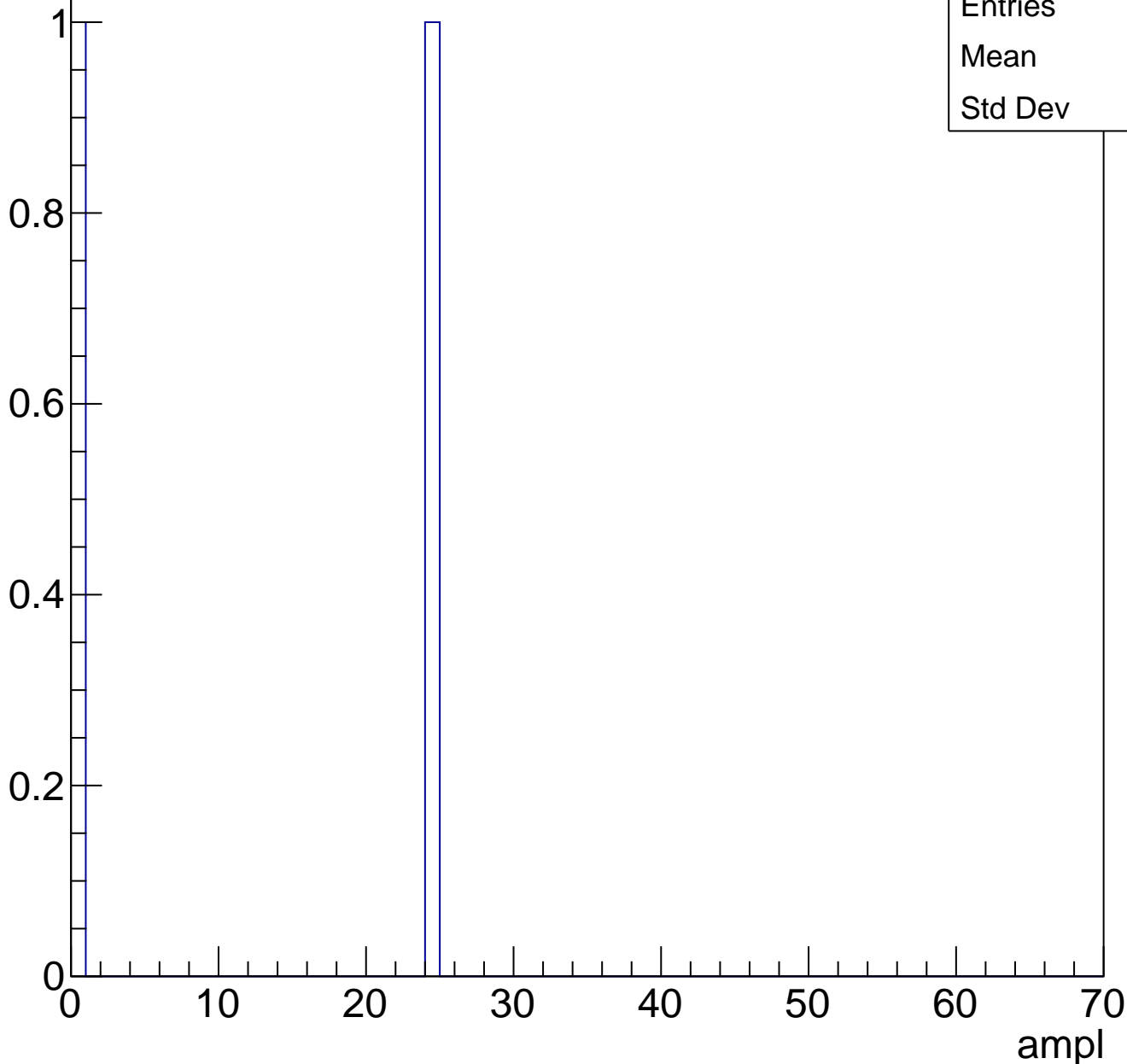
Entry



B1L003S, U20-ch17, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch18, adc0

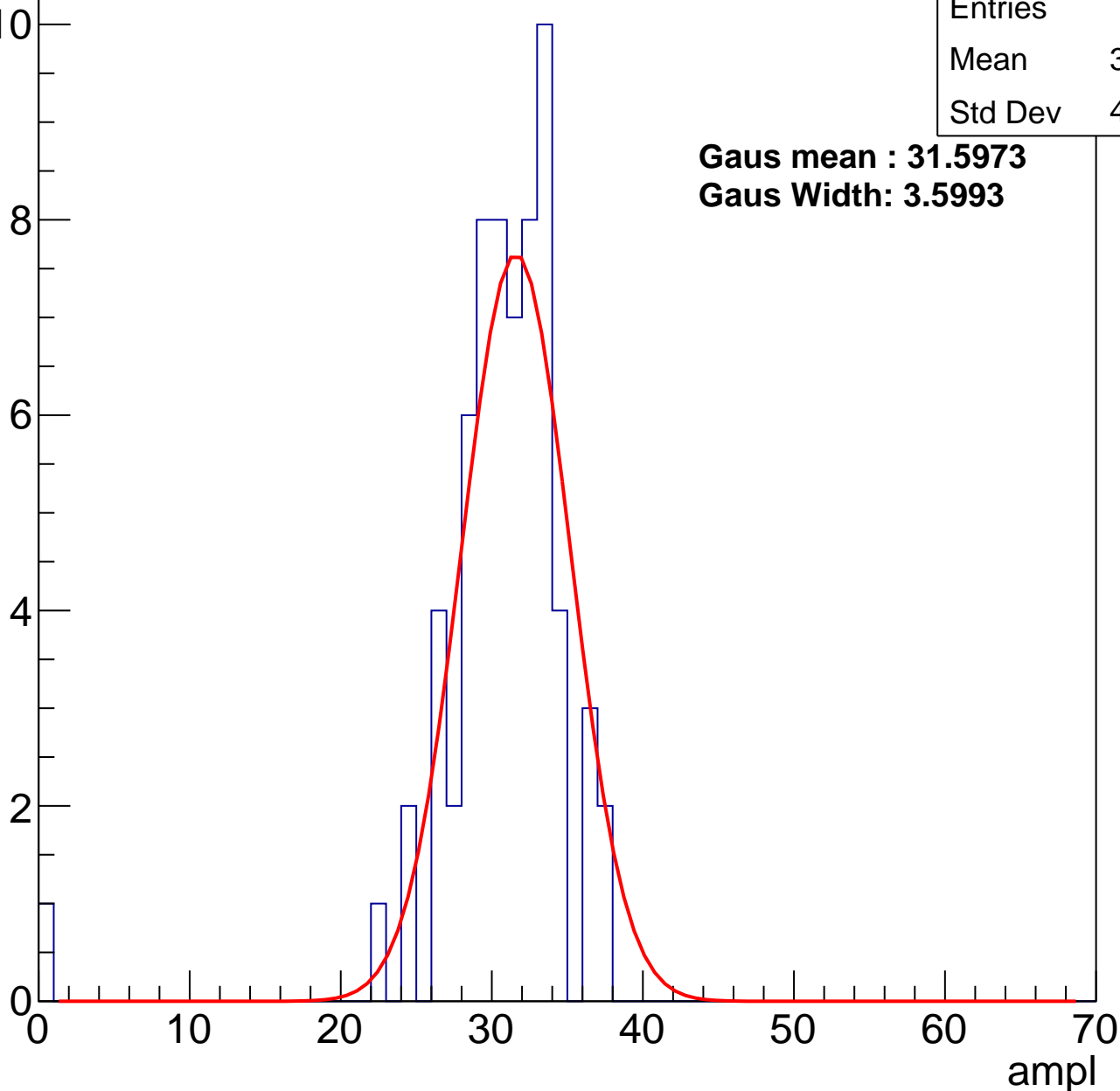
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.14
Std Dev	4.843

Gaus mean : 31.5973

Gaus Width: 3.5993



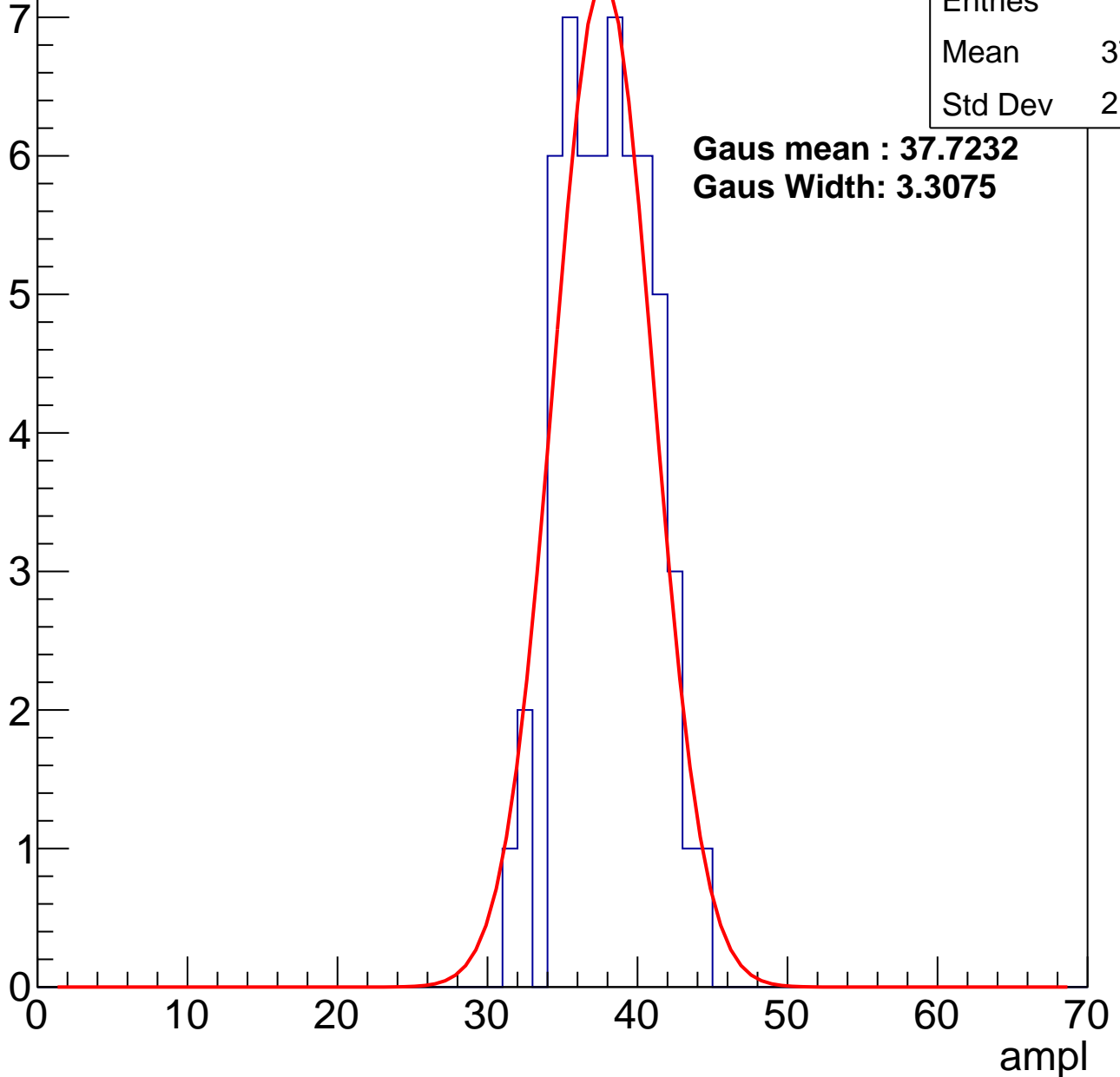
B1L003S, U20-ch18, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	37.54
Std Dev	2.908

Gaus mean : 37.7232
Gaus Width: 3.3075



B1L003S, U20-ch18, adc2

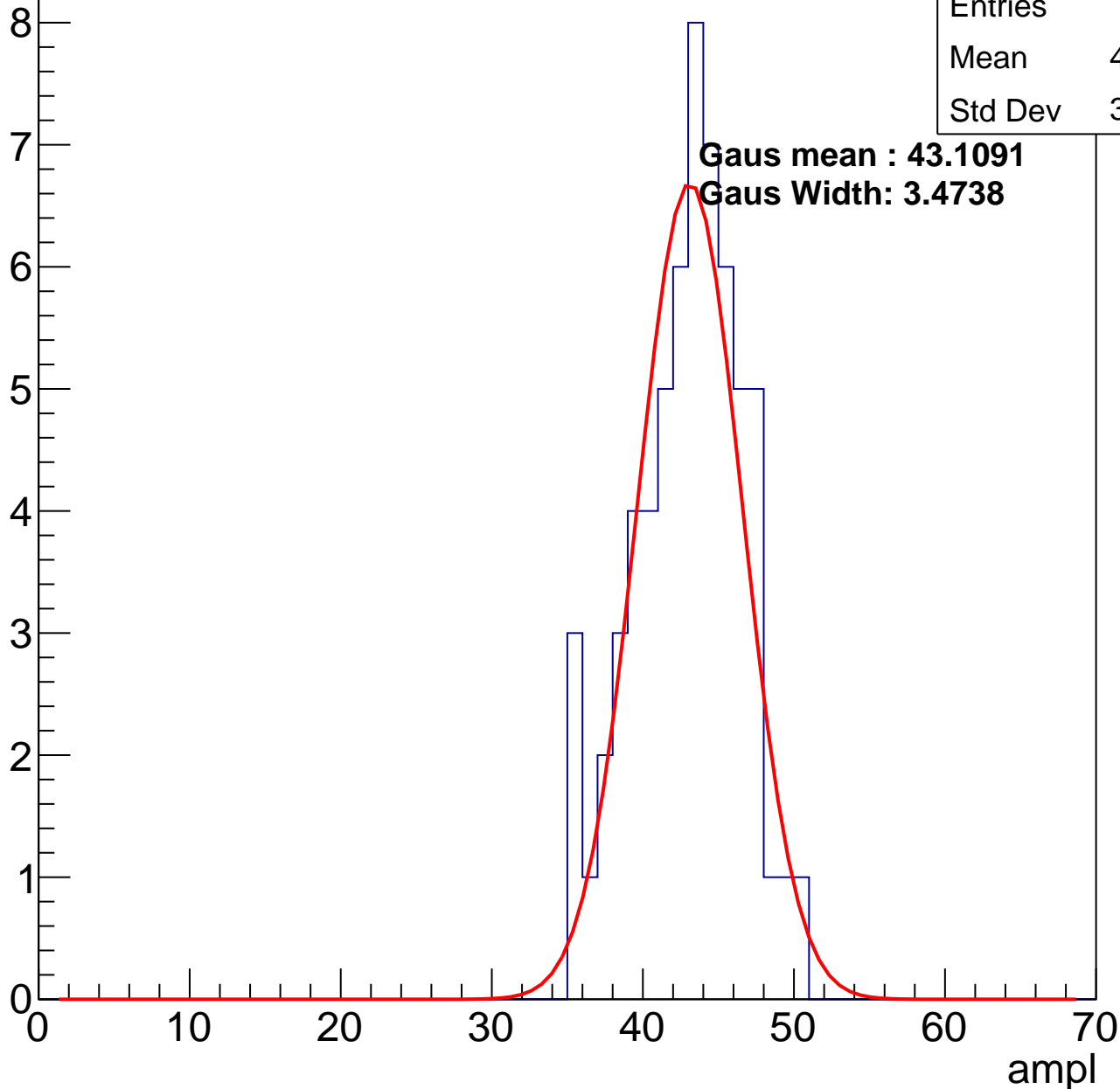
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.52
Std Dev	3.514

Gaus mean : 43.1091

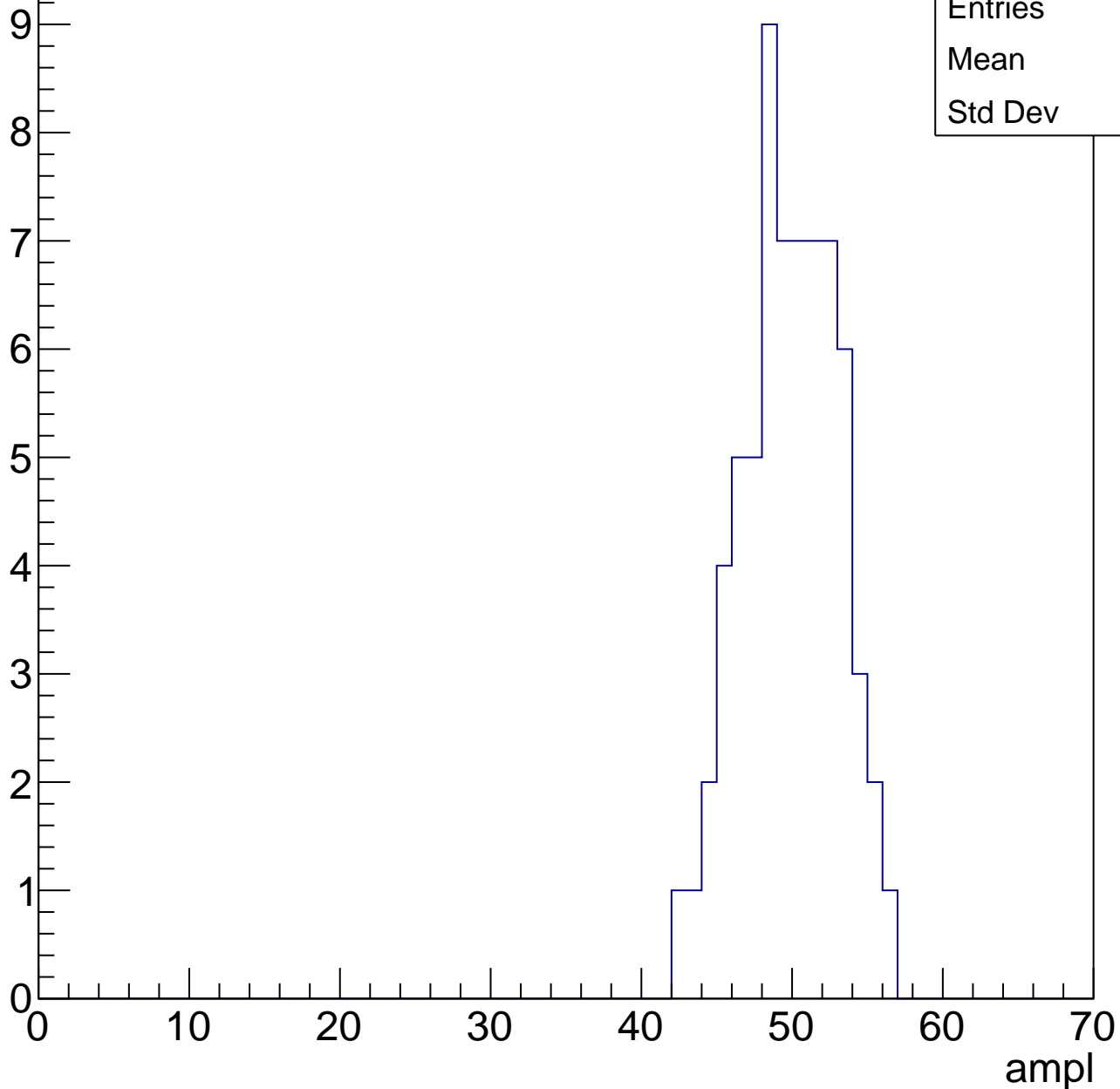
Gaus Width: 3.4738



B1L003S, U20-ch18, adc3

calib_packv5_042523_0143.root, FC#13, port D2

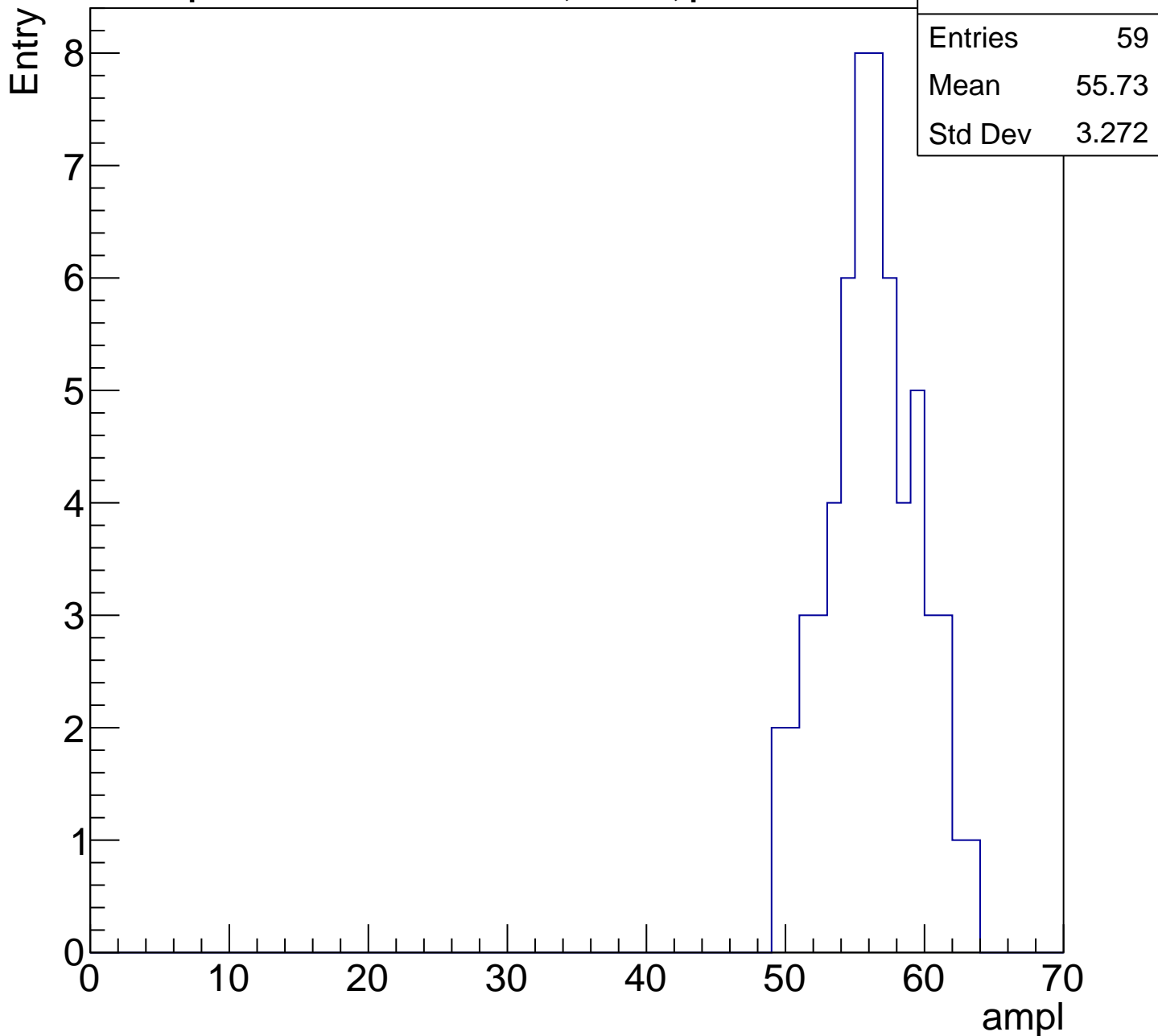
Entry



Entries	67
Mean	49.4
Std Dev	3.12

B1L003S, U20-ch18, adc4

calib_packv5_042523_0143.root, FC#13, port D2

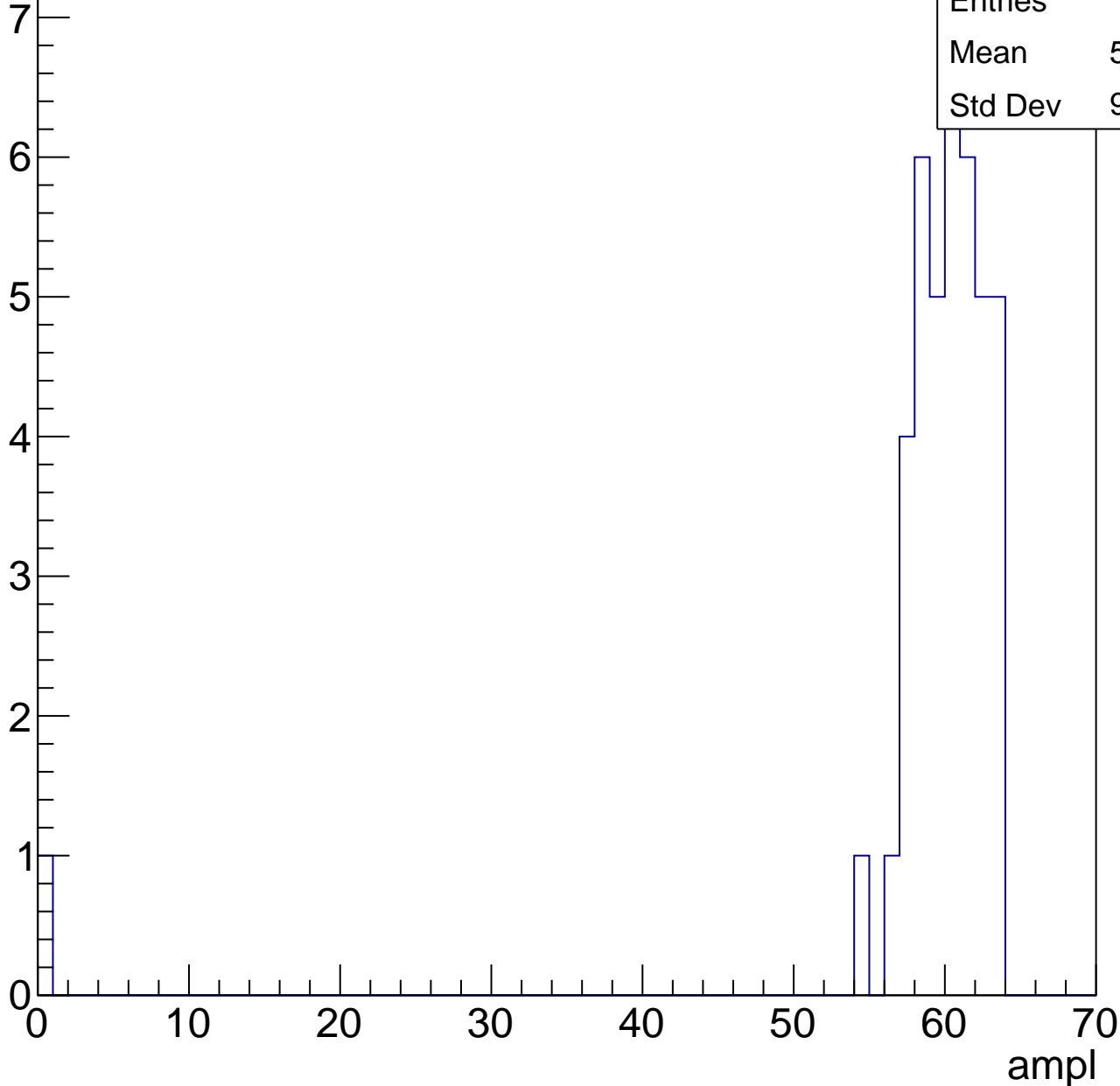


B1L003S, U20-ch18, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

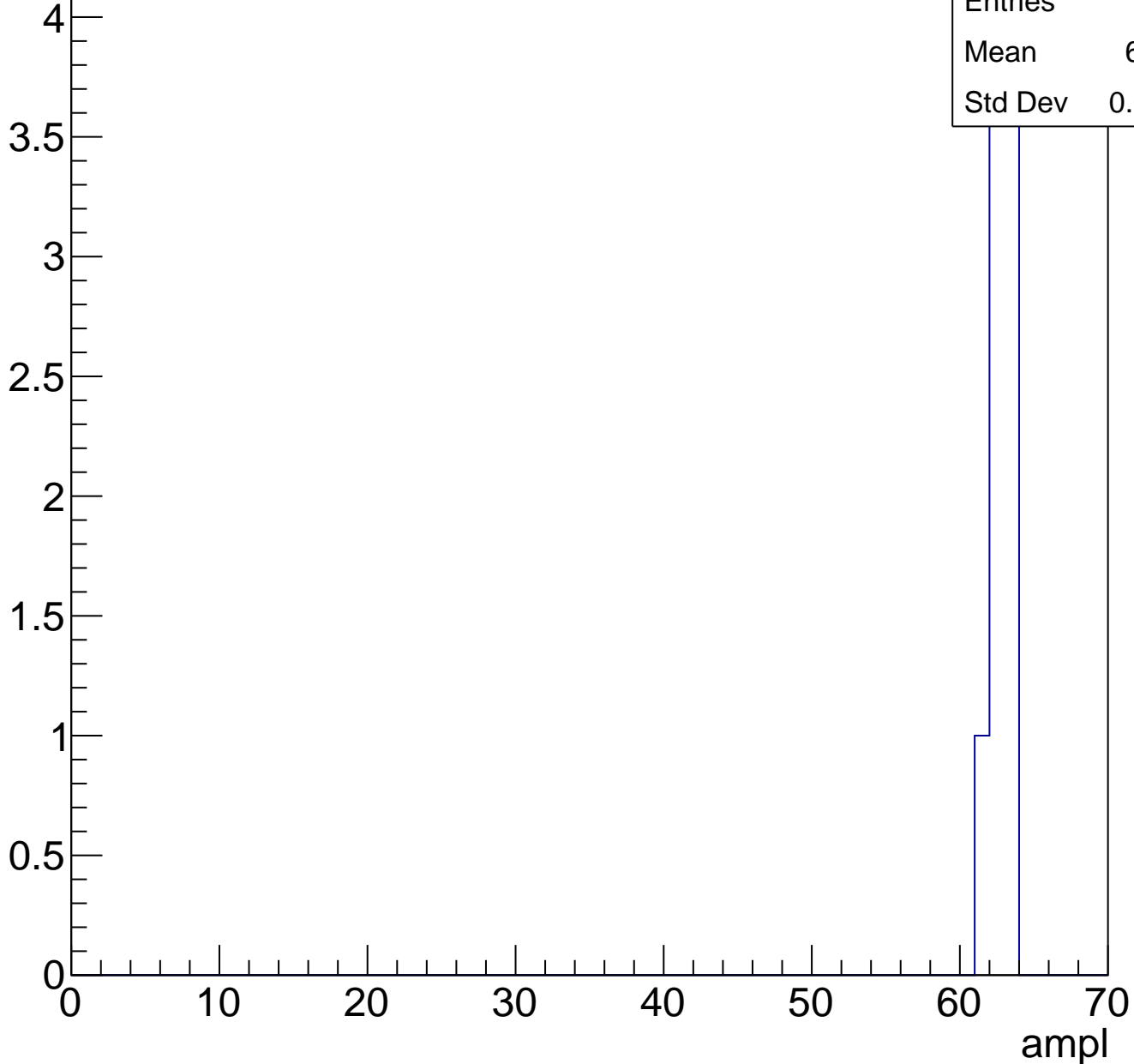
Entries	41
Mean	58.34
Std Dev	9.468



B1L003S, U20-ch18, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch18, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch19, adc0

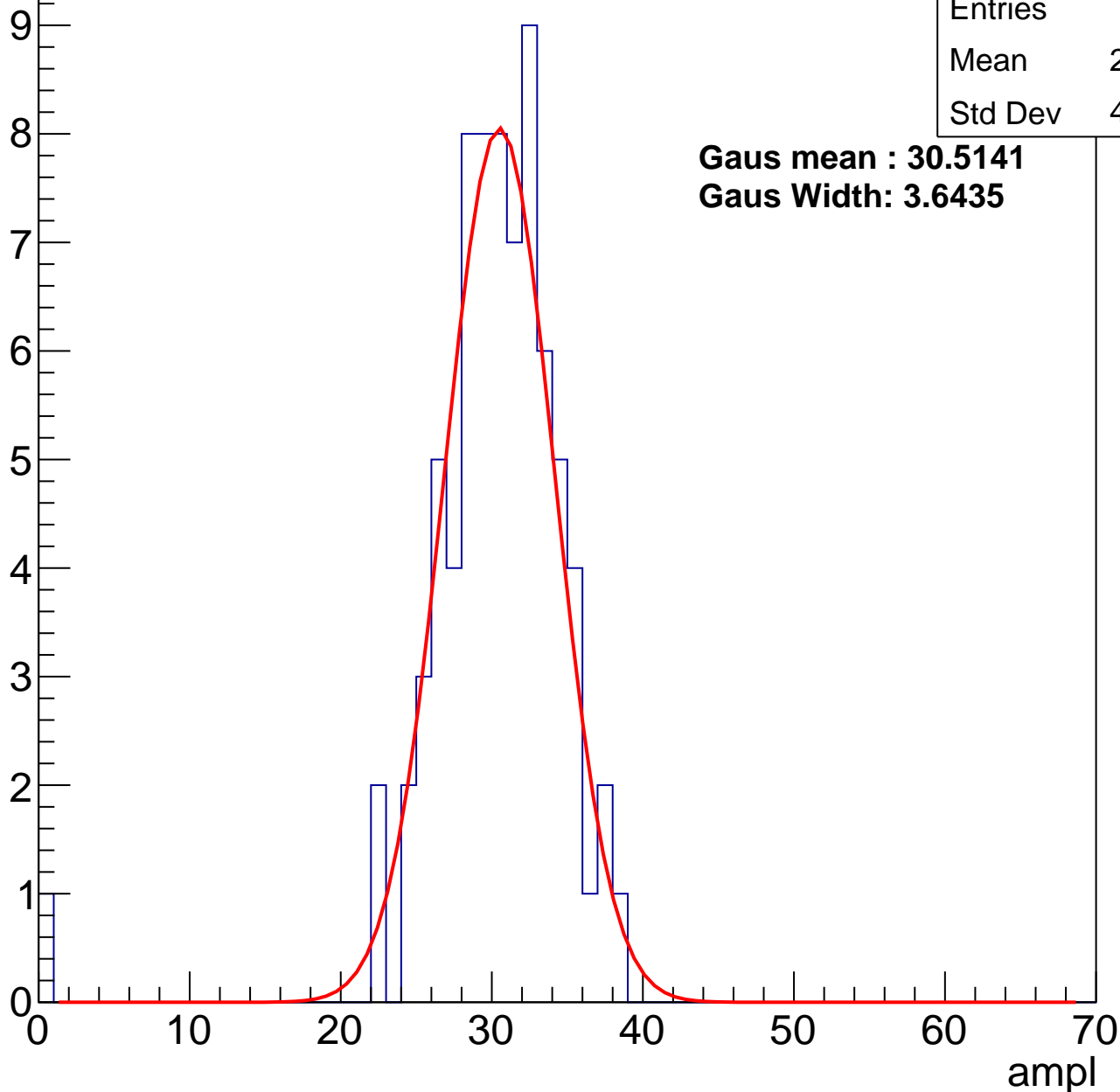
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	29.76
Std Dev	4.869

Gaus mean : 30.5141

Gaus Width: 3.6435



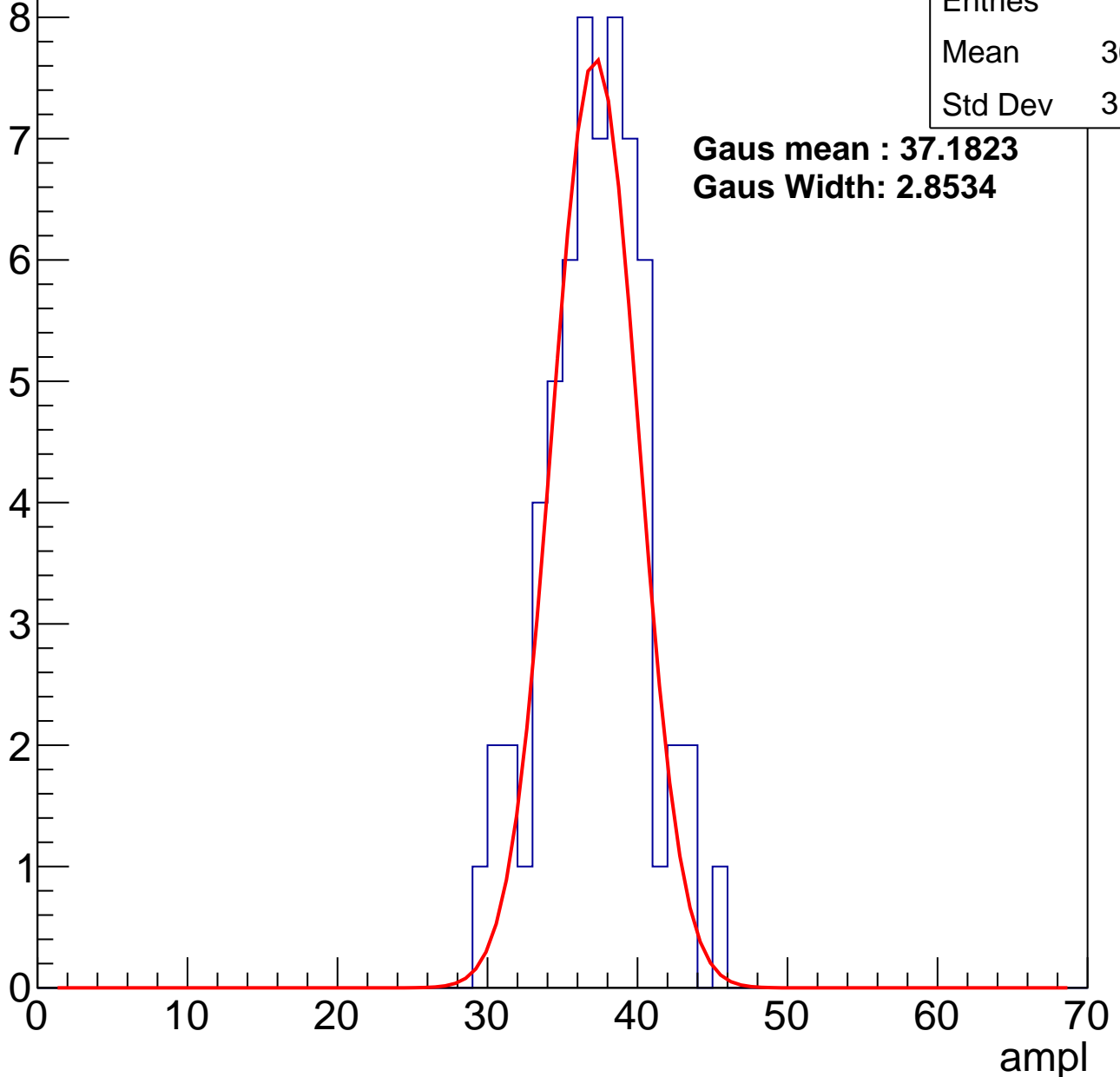
B1L003S, U20-ch19, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.75
Std Dev	3.304

Gaus mean : 37.1823
Gaus Width: 2.8534



B1L003S, U20-ch19, adc2

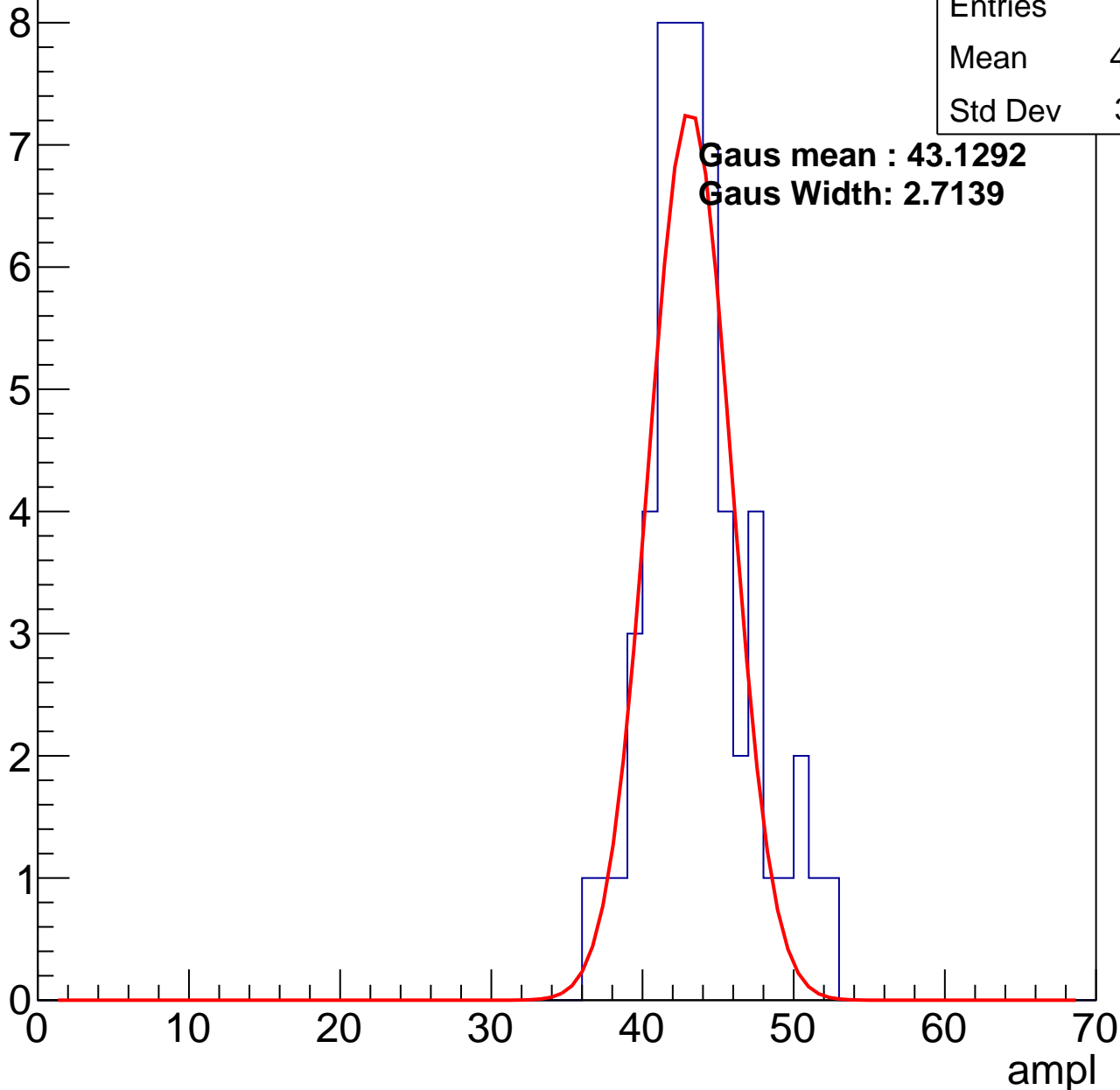
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.23
Std Dev	3.351

Gaus mean : 43.1292

Gaus Width: 2.7139

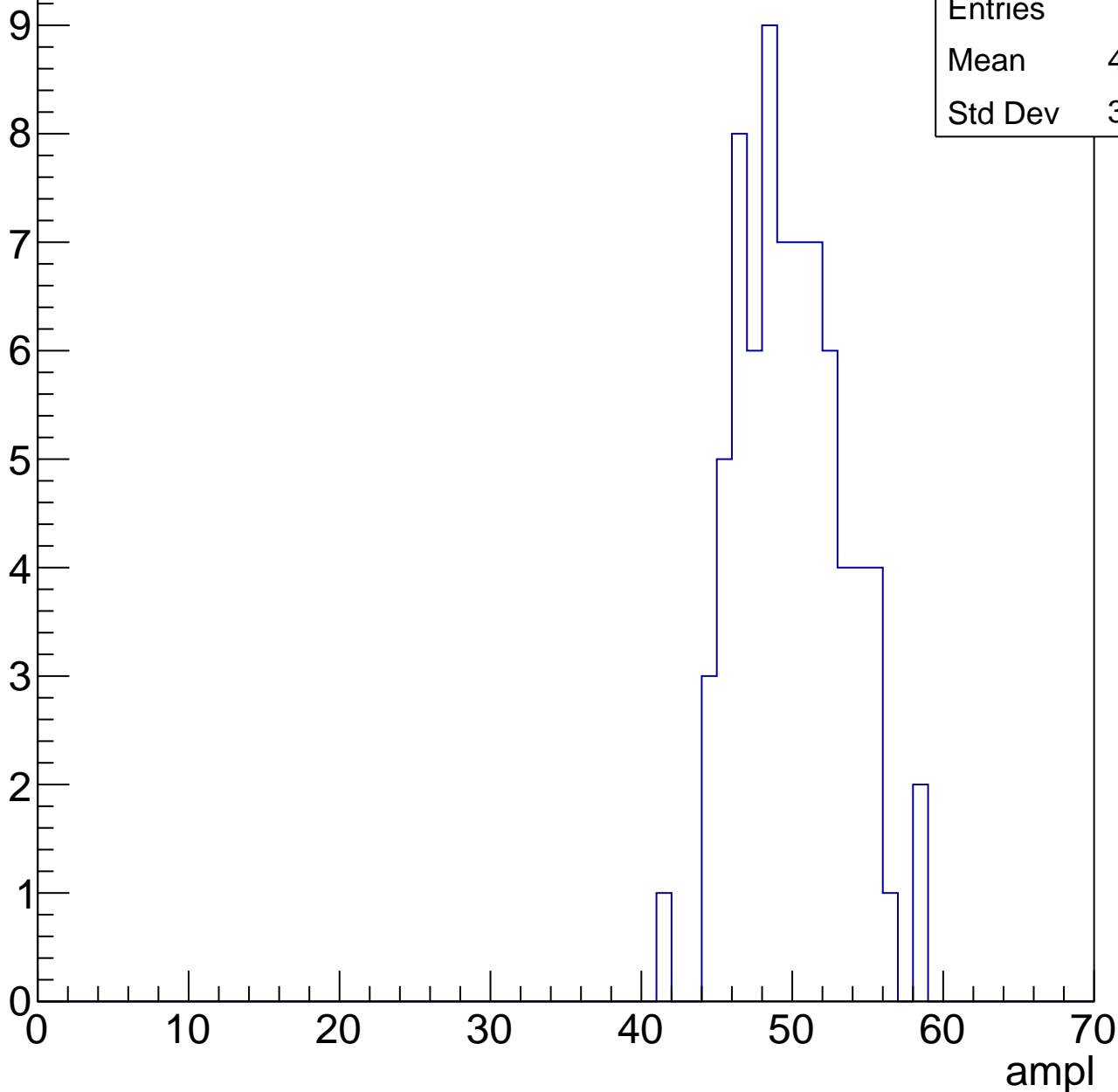


B1L003S, U20-ch19, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.49
Std Dev	3.515

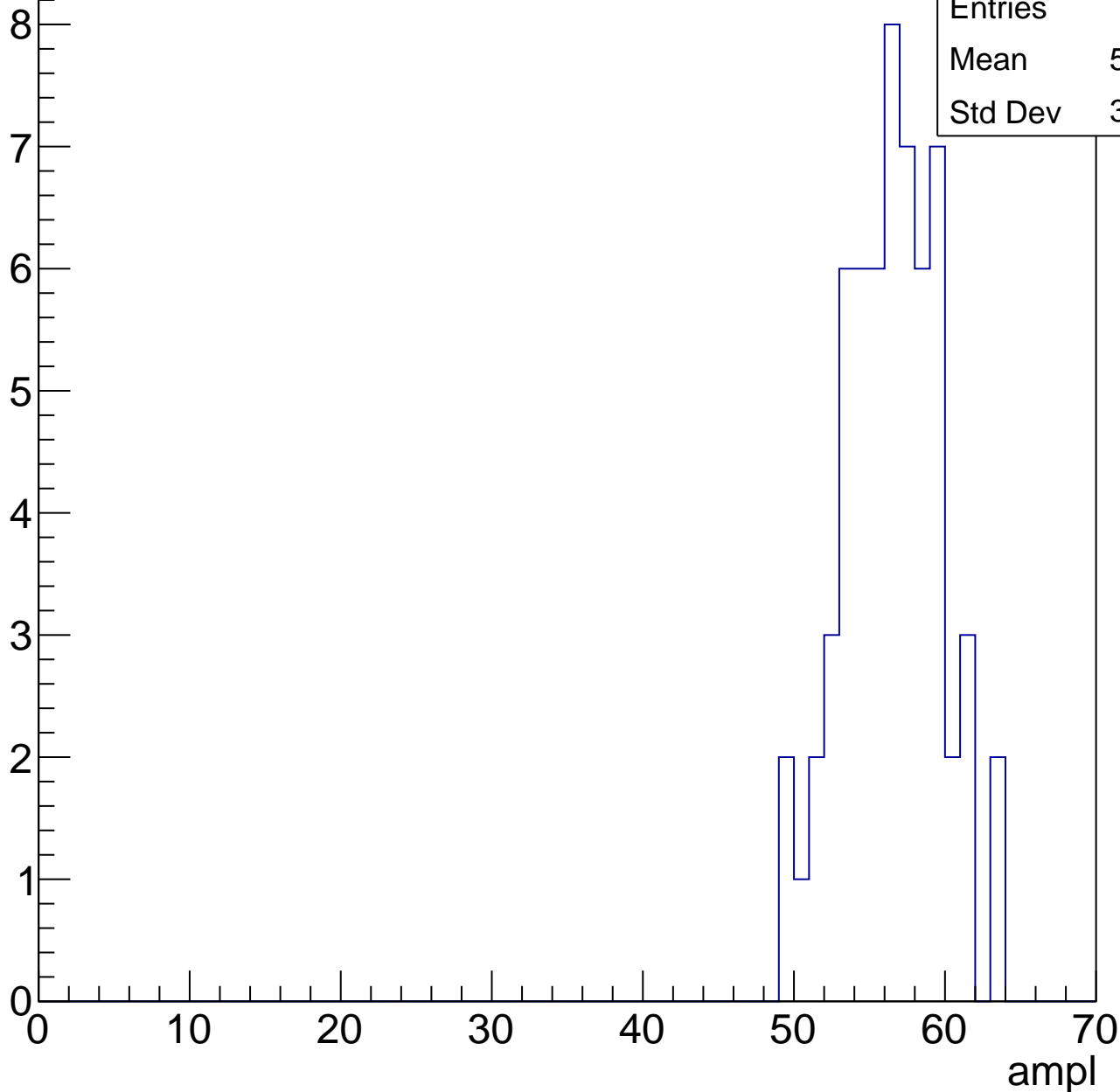


B1L003S, U20-ch19, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.98
Std Dev	3.175

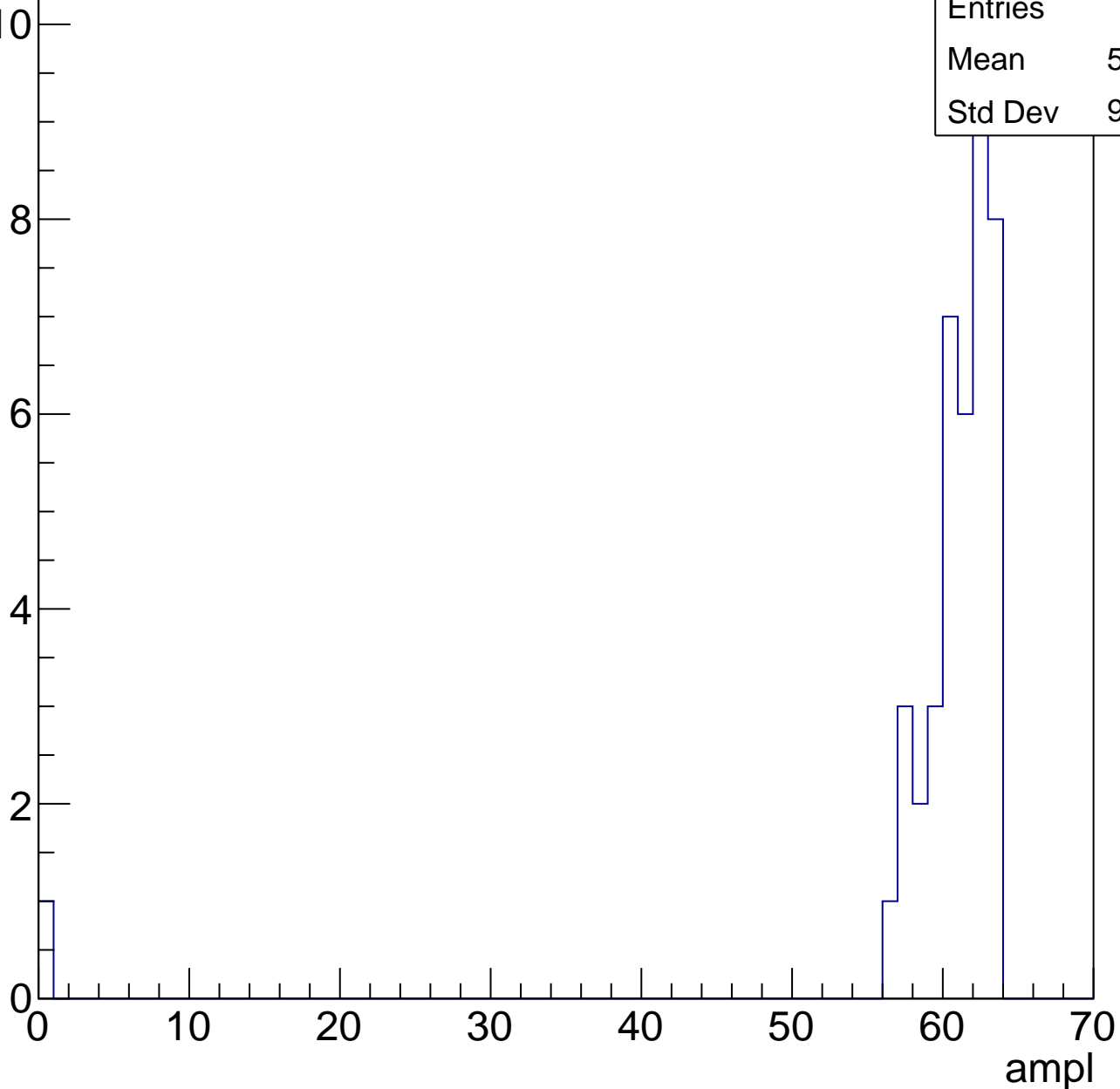


B1L003S, U20-ch19, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	59.27
Std Dev	9.564



B1L003S, U20-ch19, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch19, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch20, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	28.76
Std Dev	5.175

Gaus mean : 29.3330

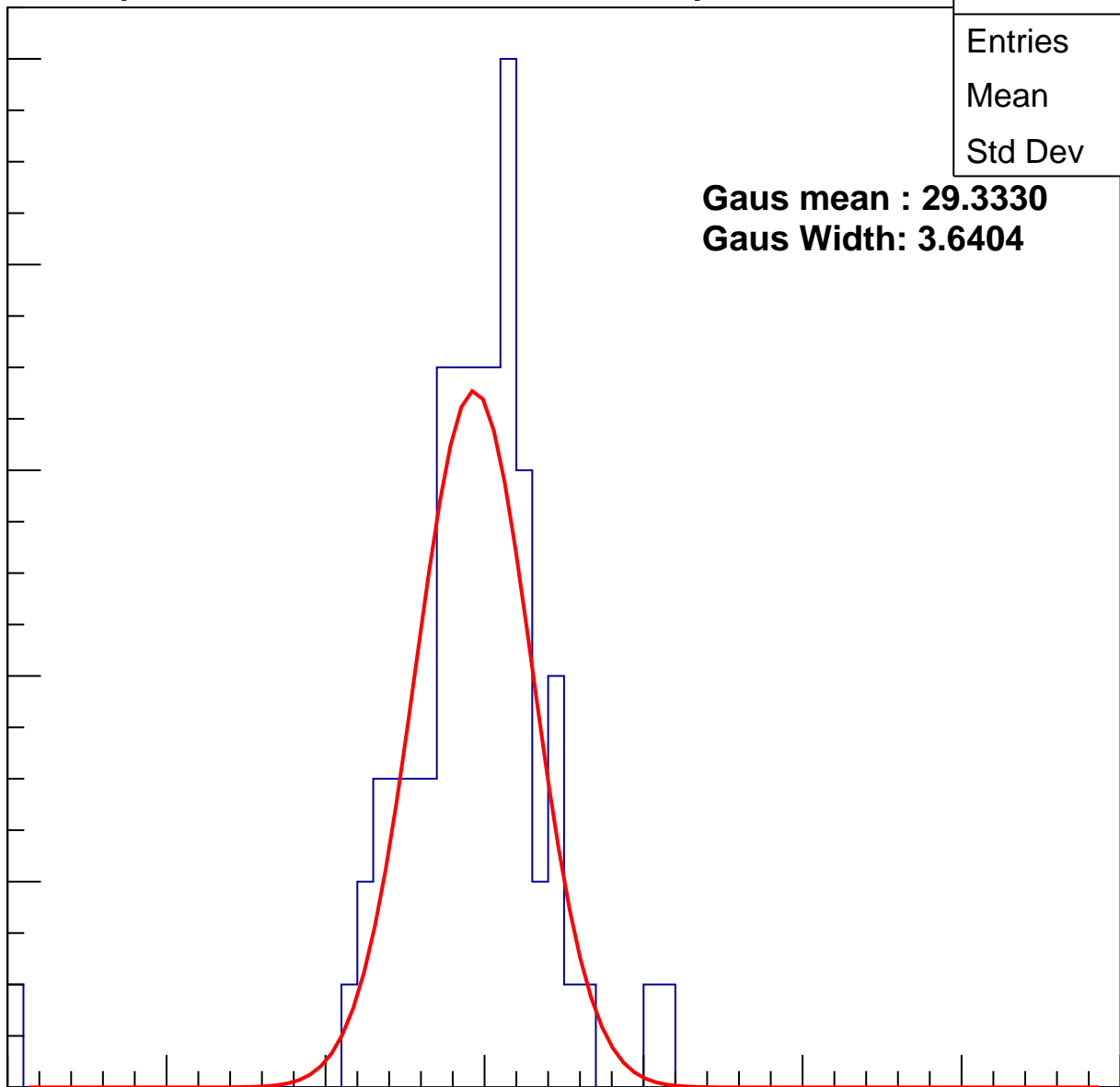
Gaus Width: 3.6404

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch20, adc1

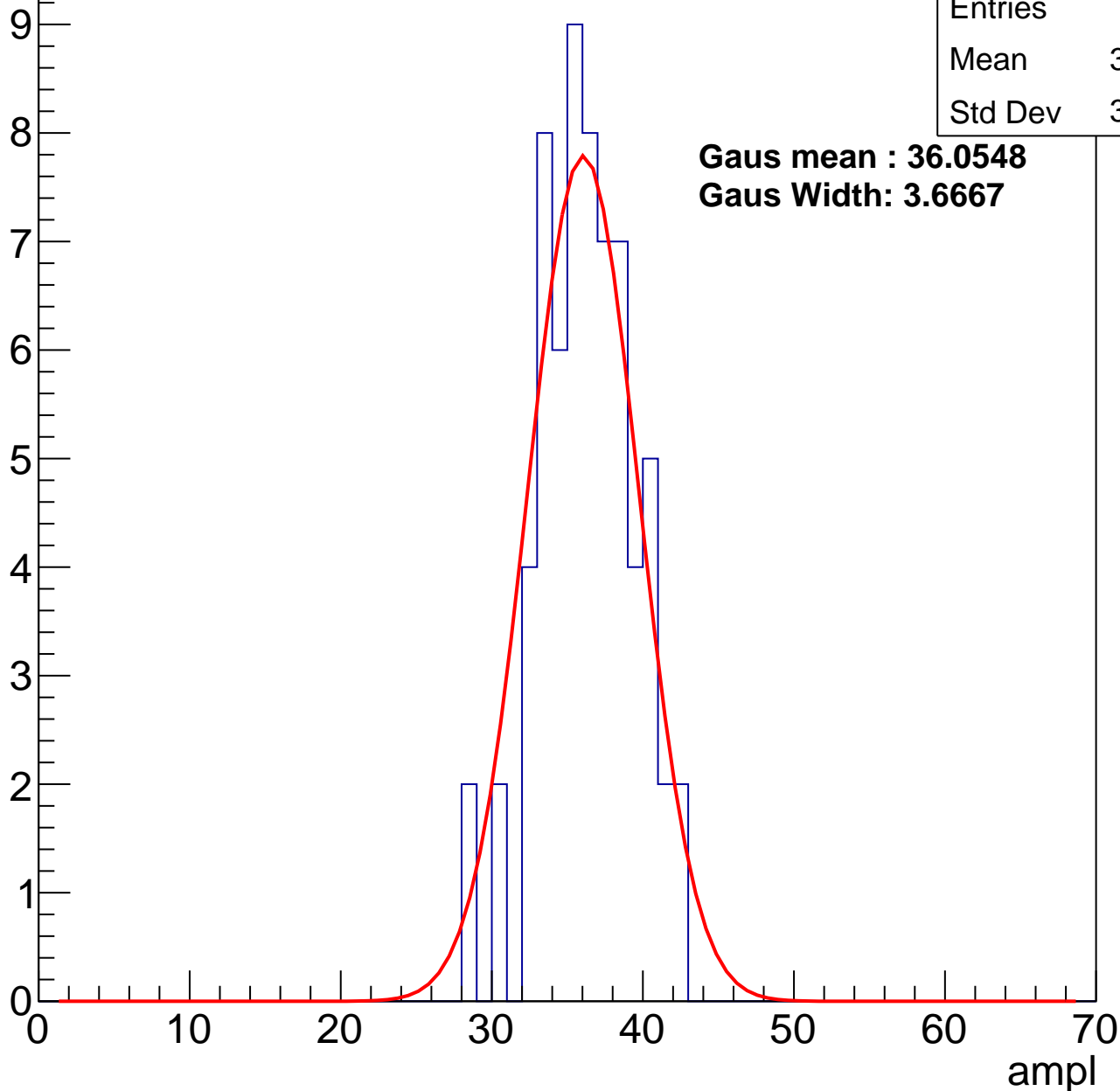
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.79
Std Dev	3.112

Gaus mean : 36.0548

Gaus Width: 3.6667



B1L003S, U20-ch20, adc2

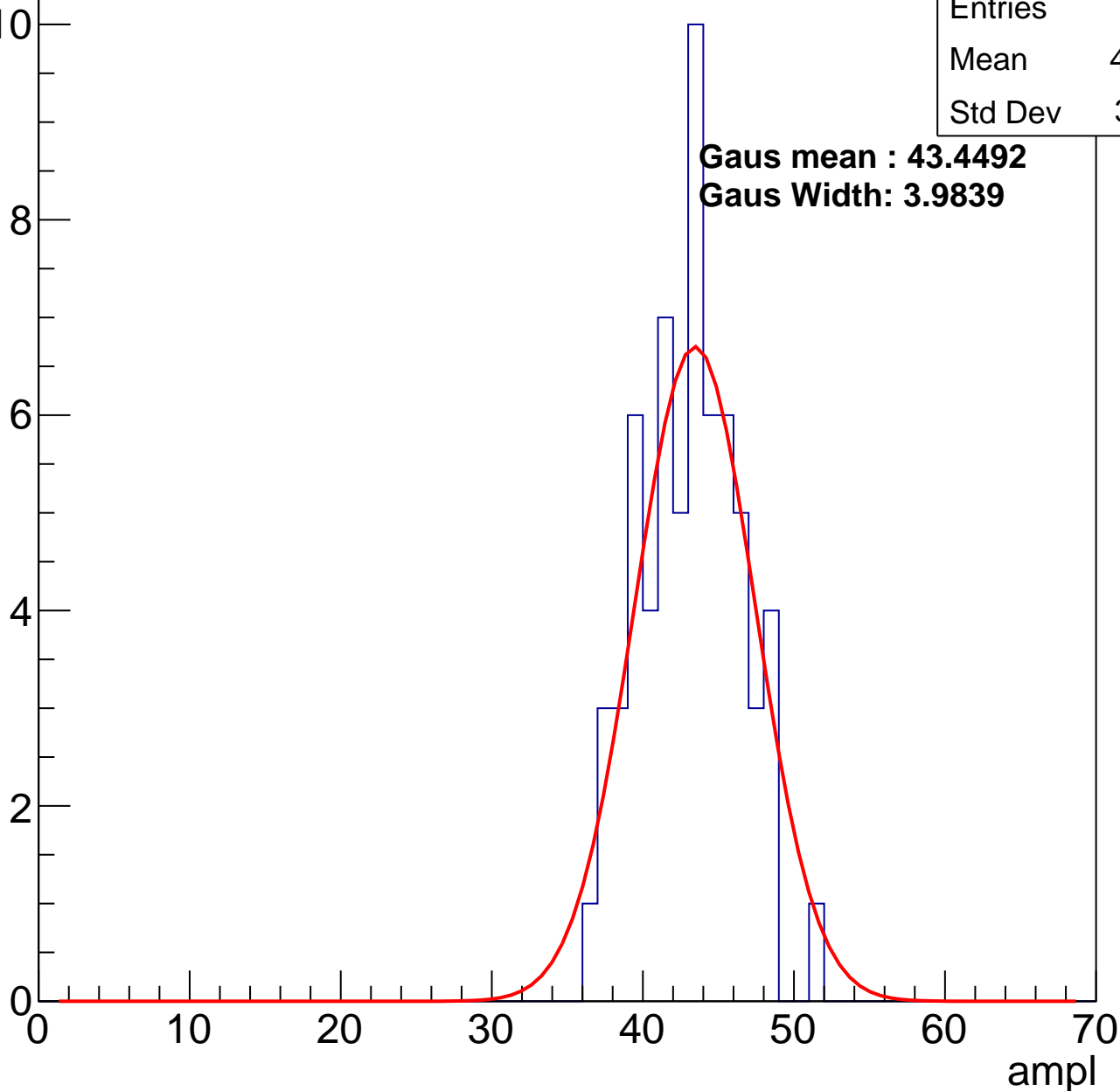
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.66
Std Dev	3.251

Gaus mean : 43.4492

Gaus Width: 3.9839

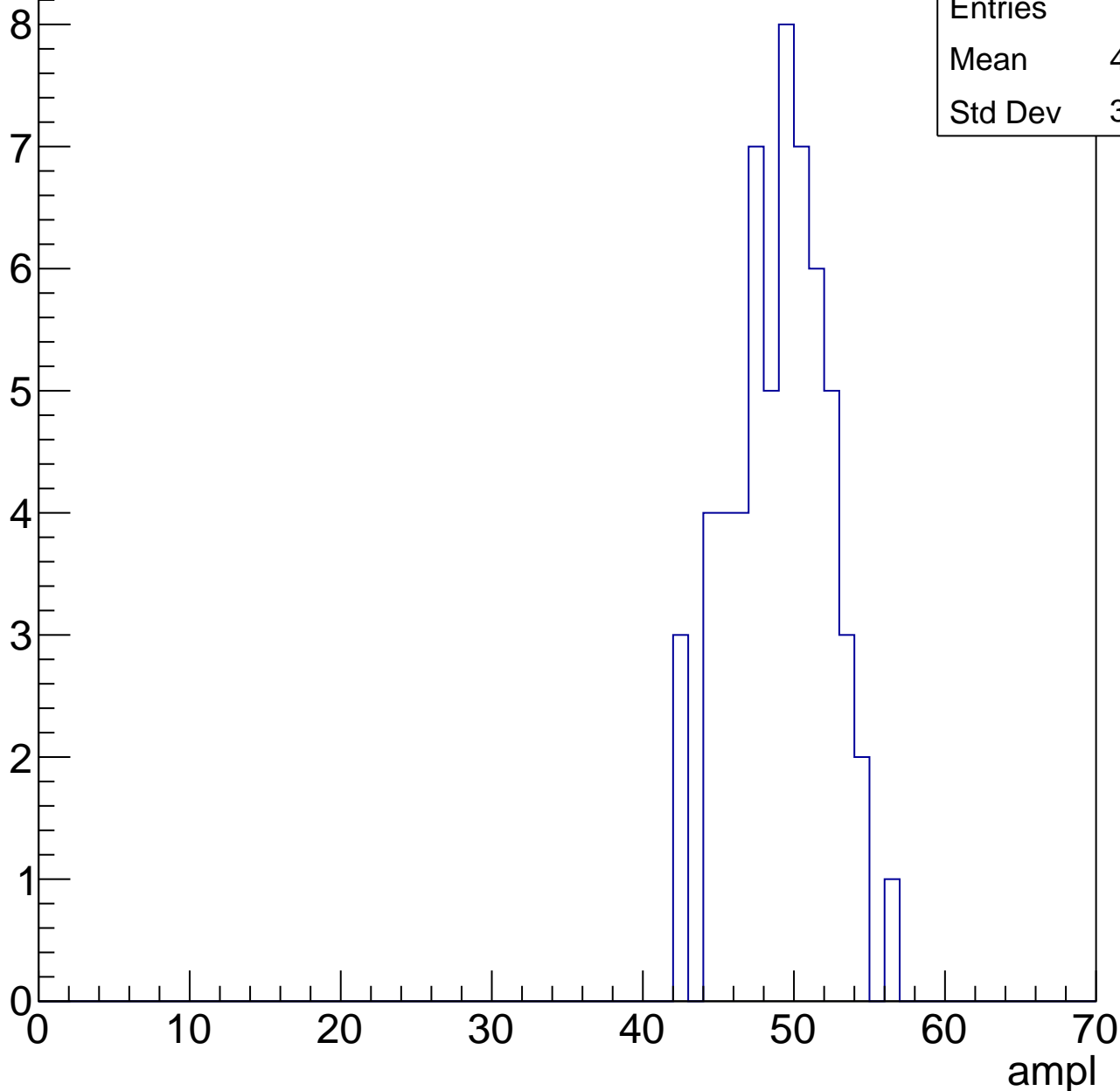


B1L003S, U20-ch20, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.58
Std Dev	3.169

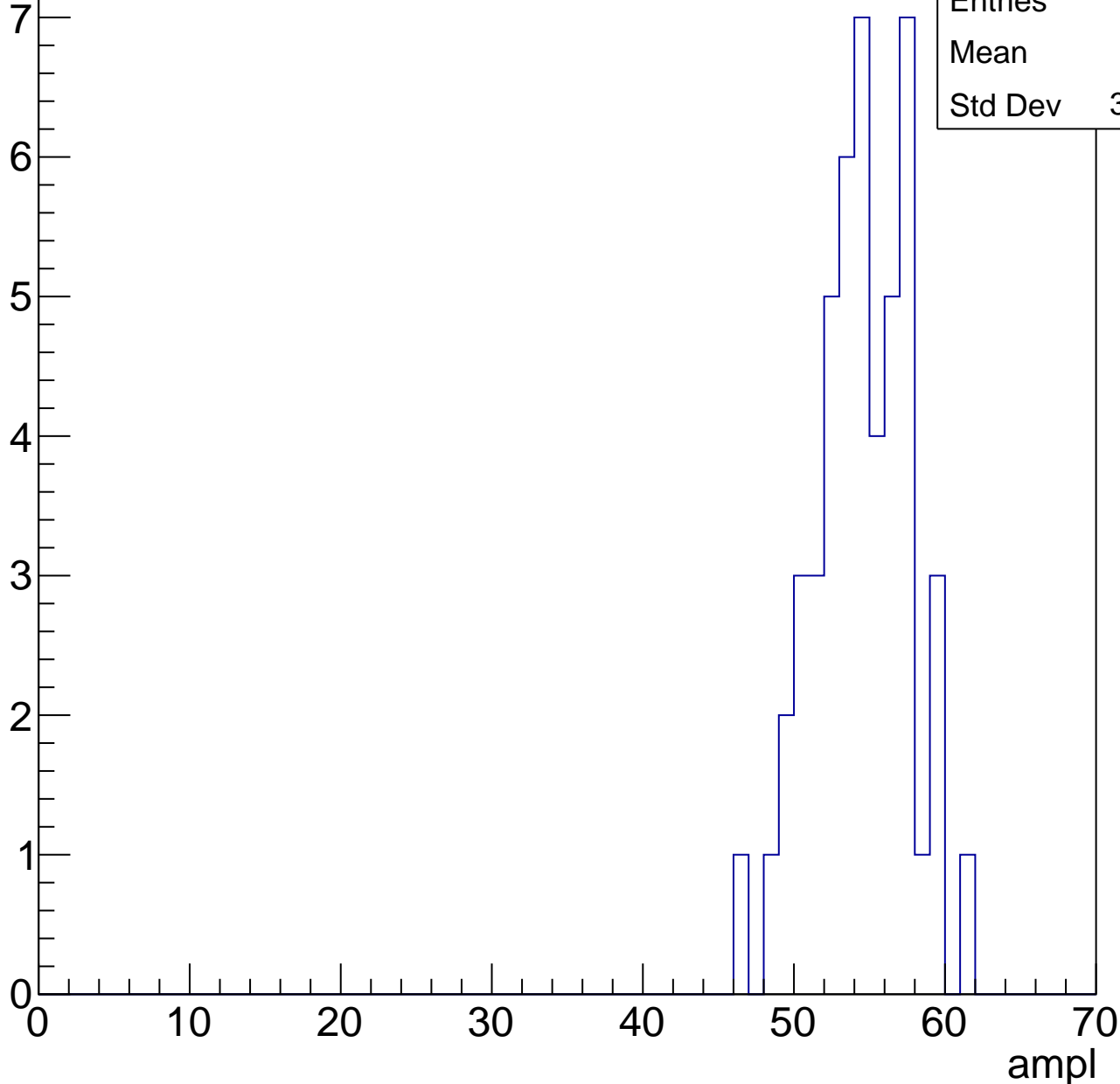


B1L003S, U20-ch20, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

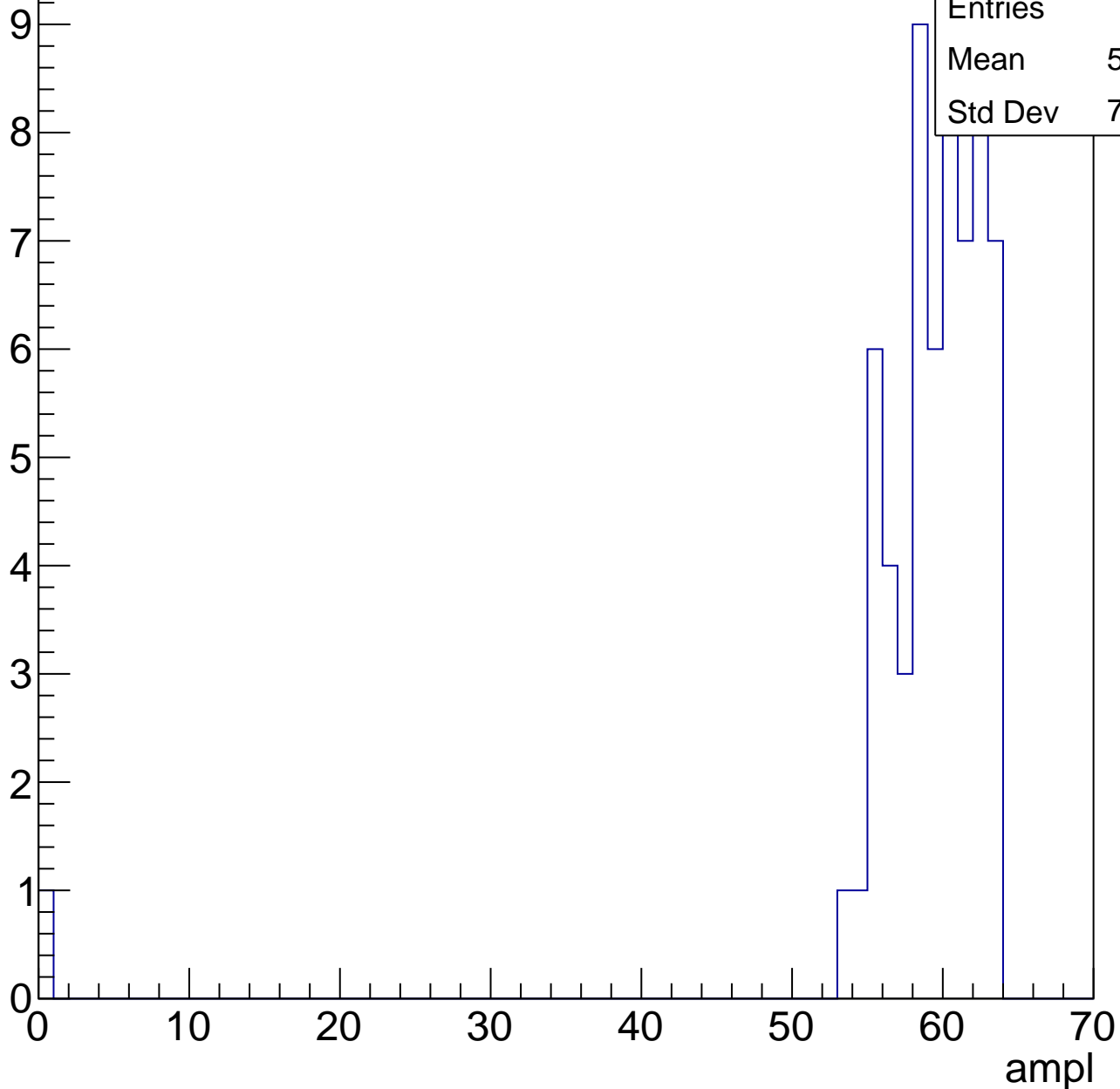
Entries	49
Mean	54
Std Dev	3.123



B1L003S, U20-ch20, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch20, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.56
Std Dev	1.343

B1L003S, U20-ch20, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch21, adc0

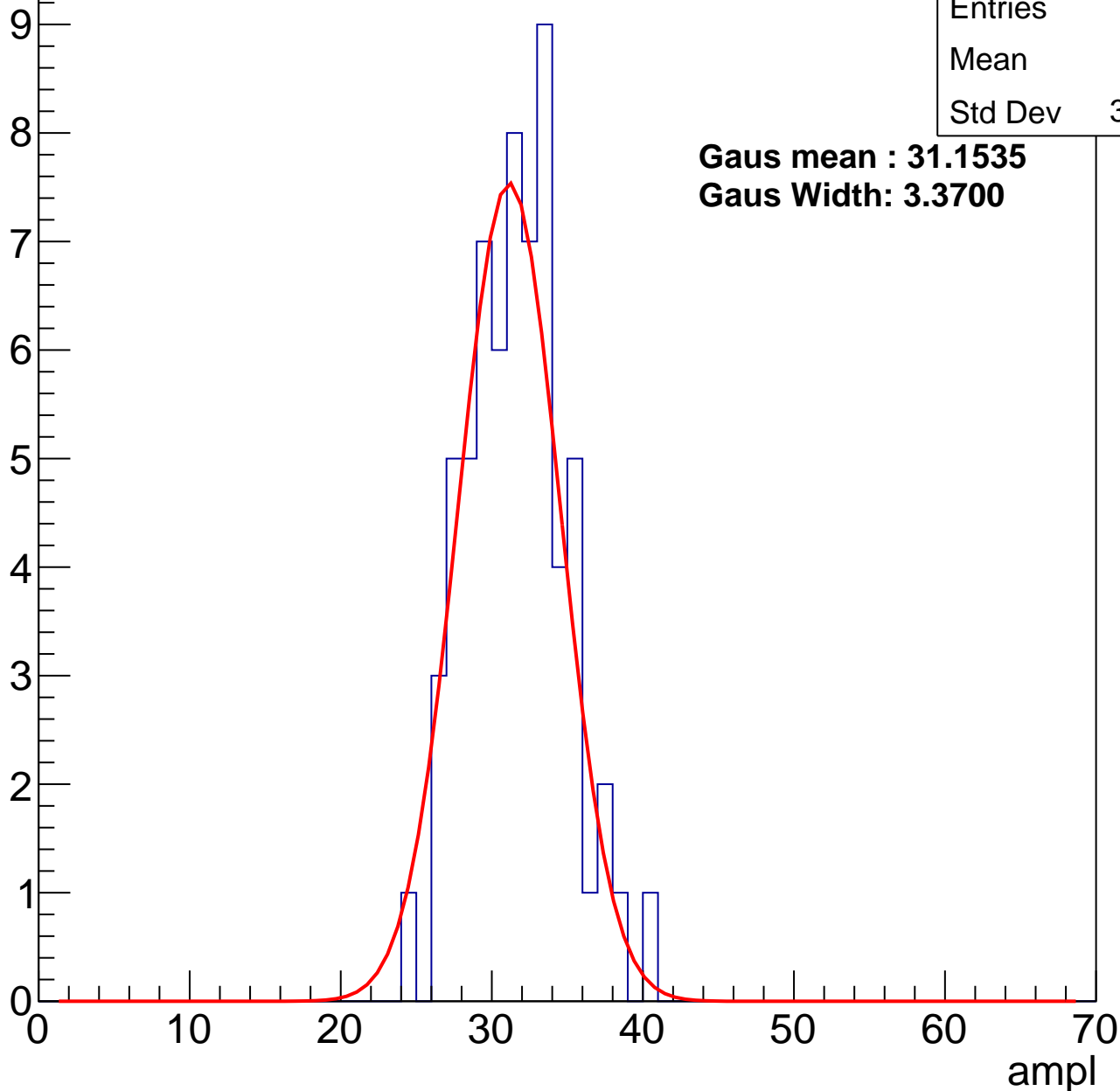
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	31.2
Std Dev	3.202

Gaus mean : 31.1535

Gaus Width: 3.3700



B1L003S, U20-ch21, adc1

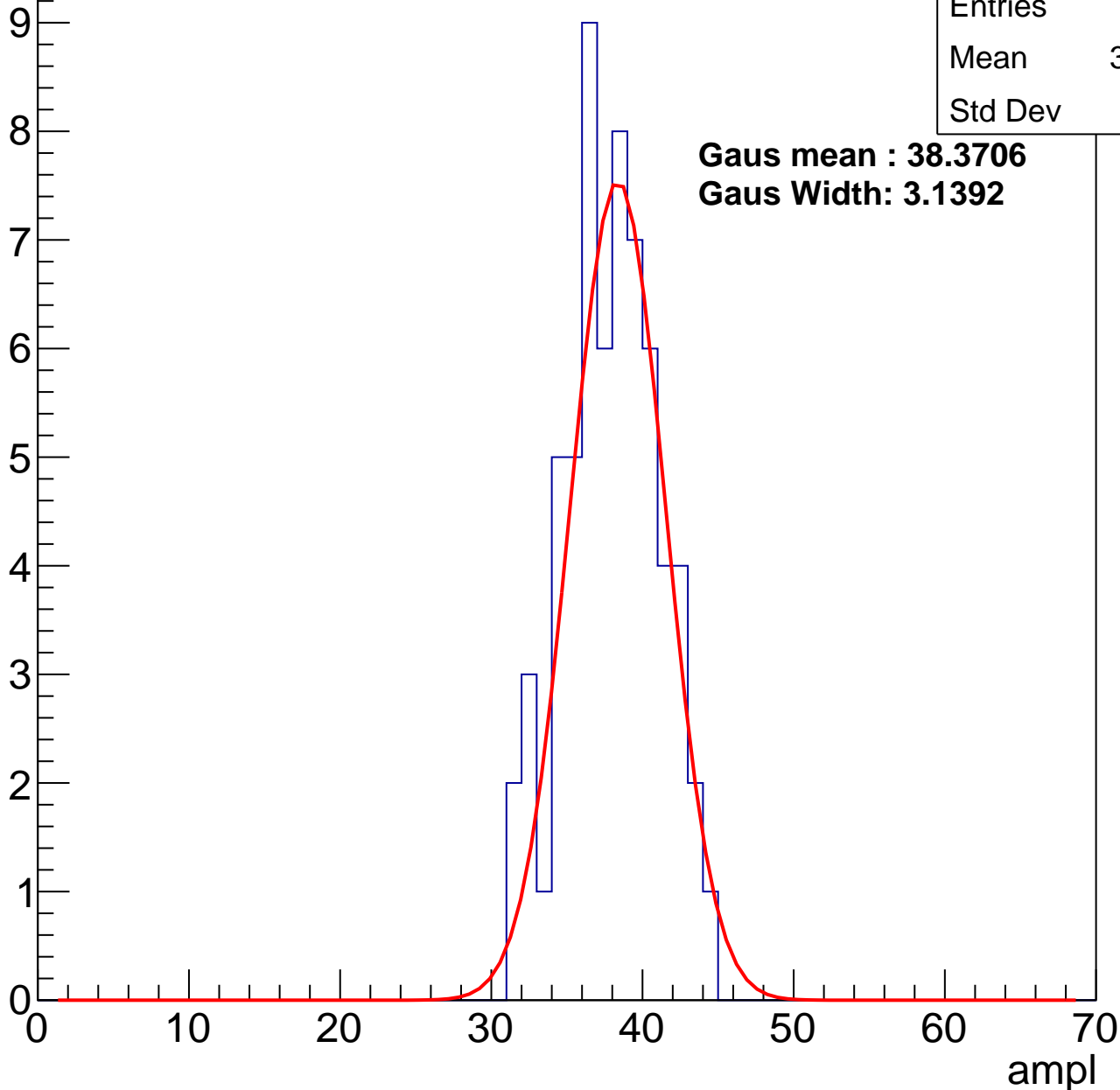
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	37.48
Std Dev	3.08

Gaus mean : 38.3706

Gaus Width: 3.1392



B1L003S, U20-ch21, adc2

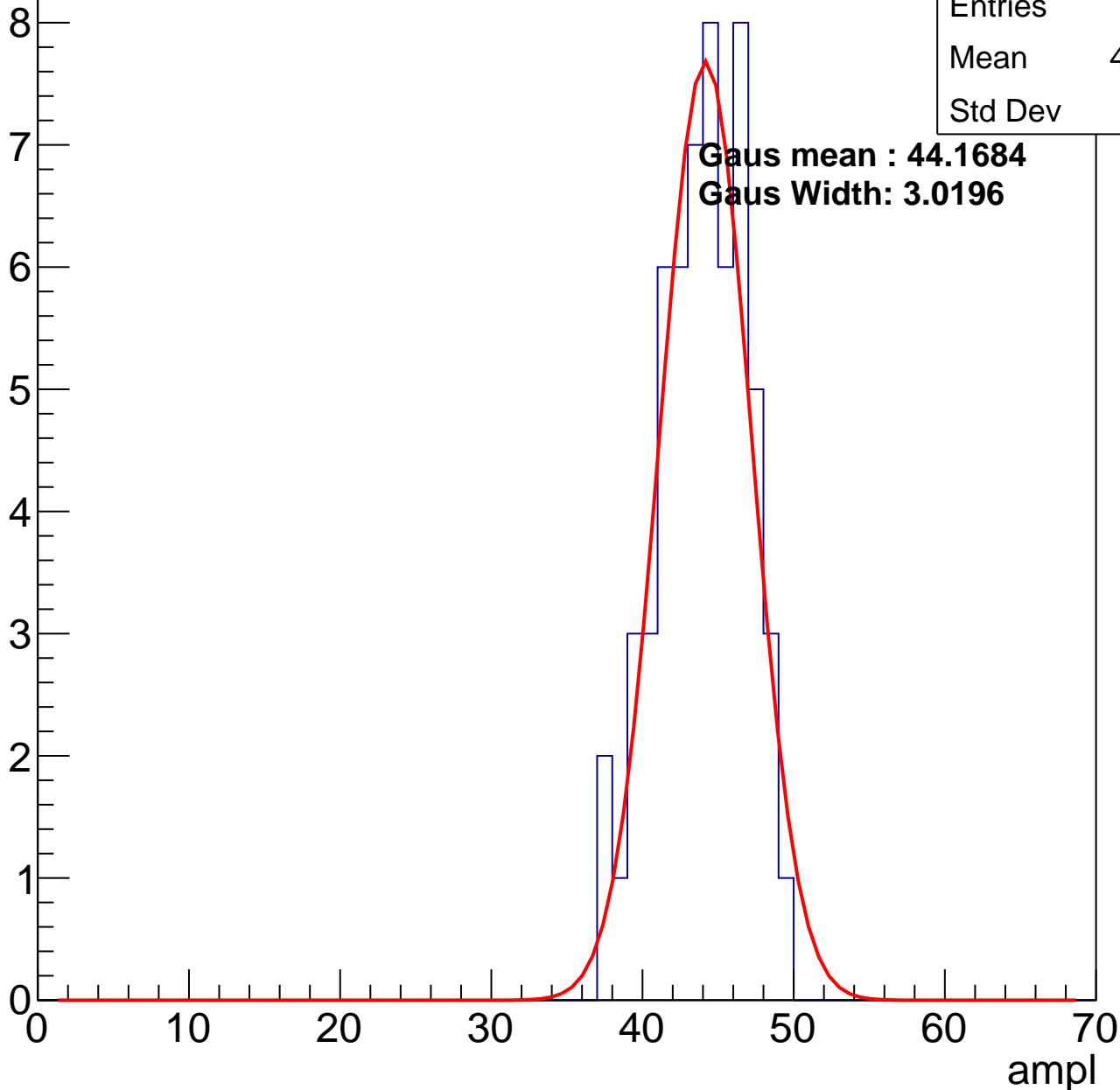
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.49
Std Dev	2.86

Gaus mean : 44.1684

Gaus Width: 3.0196

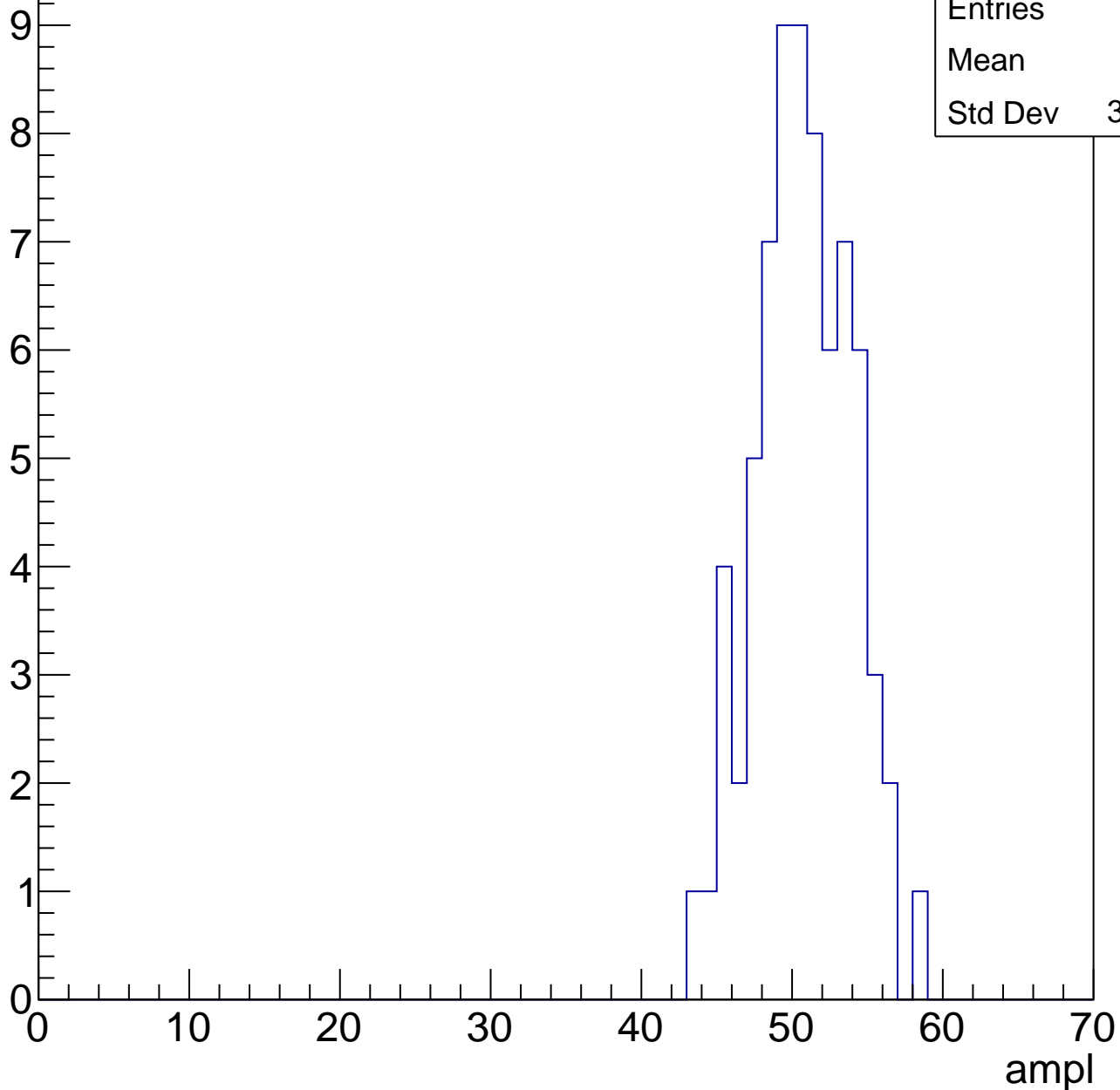


B1L003S, U20-ch21, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	50.3
Std Dev	3.128

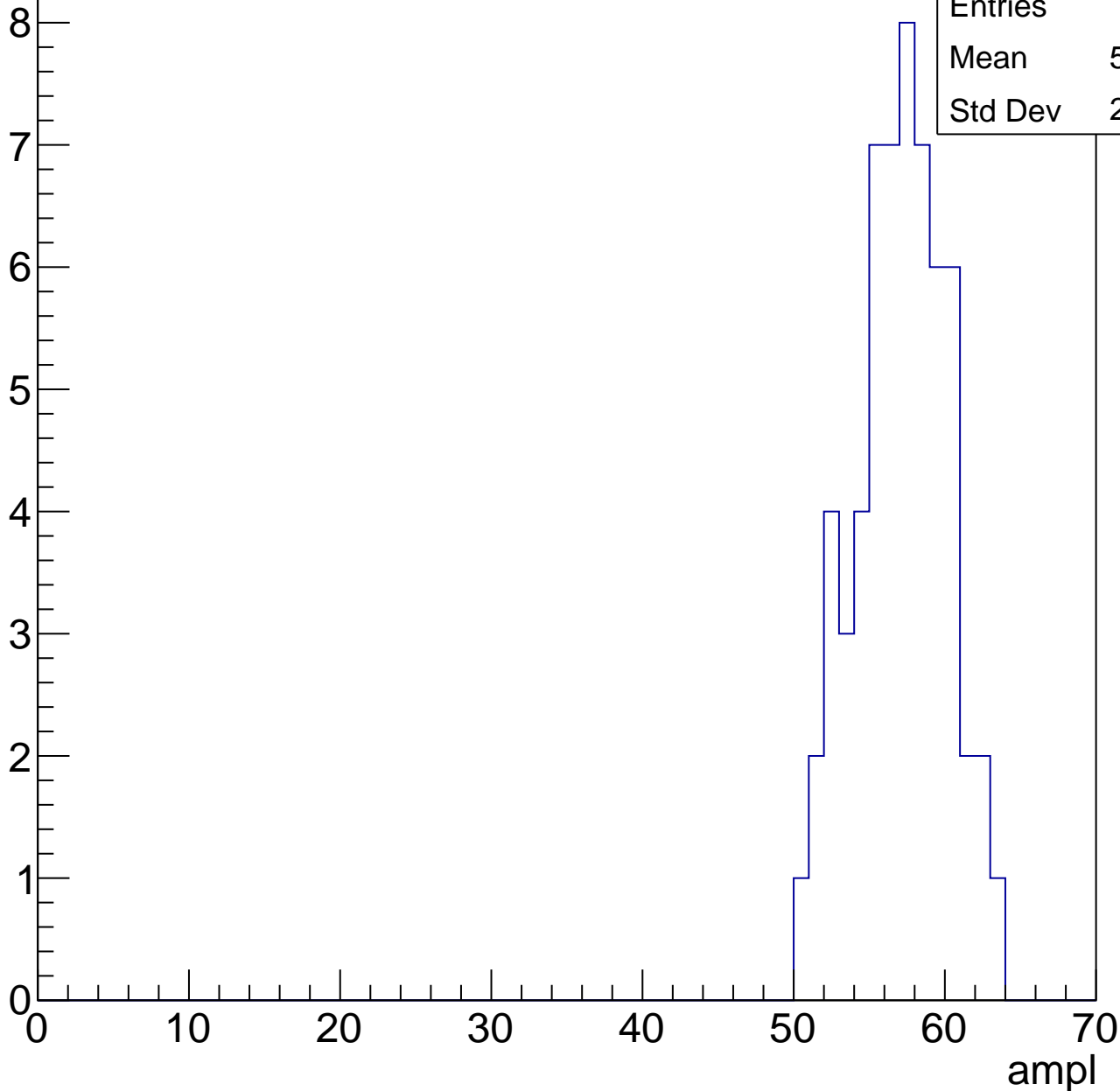


B1L003S, U20-ch21, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	56.62
Std Dev	2.984



B1L003S, U20-ch21, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	58.82
Std Dev	10.41

ampl

0

10

20

30

40

50

60

70

B1L003S, U20-ch21, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch21, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L003S, U20-ch22, adc0

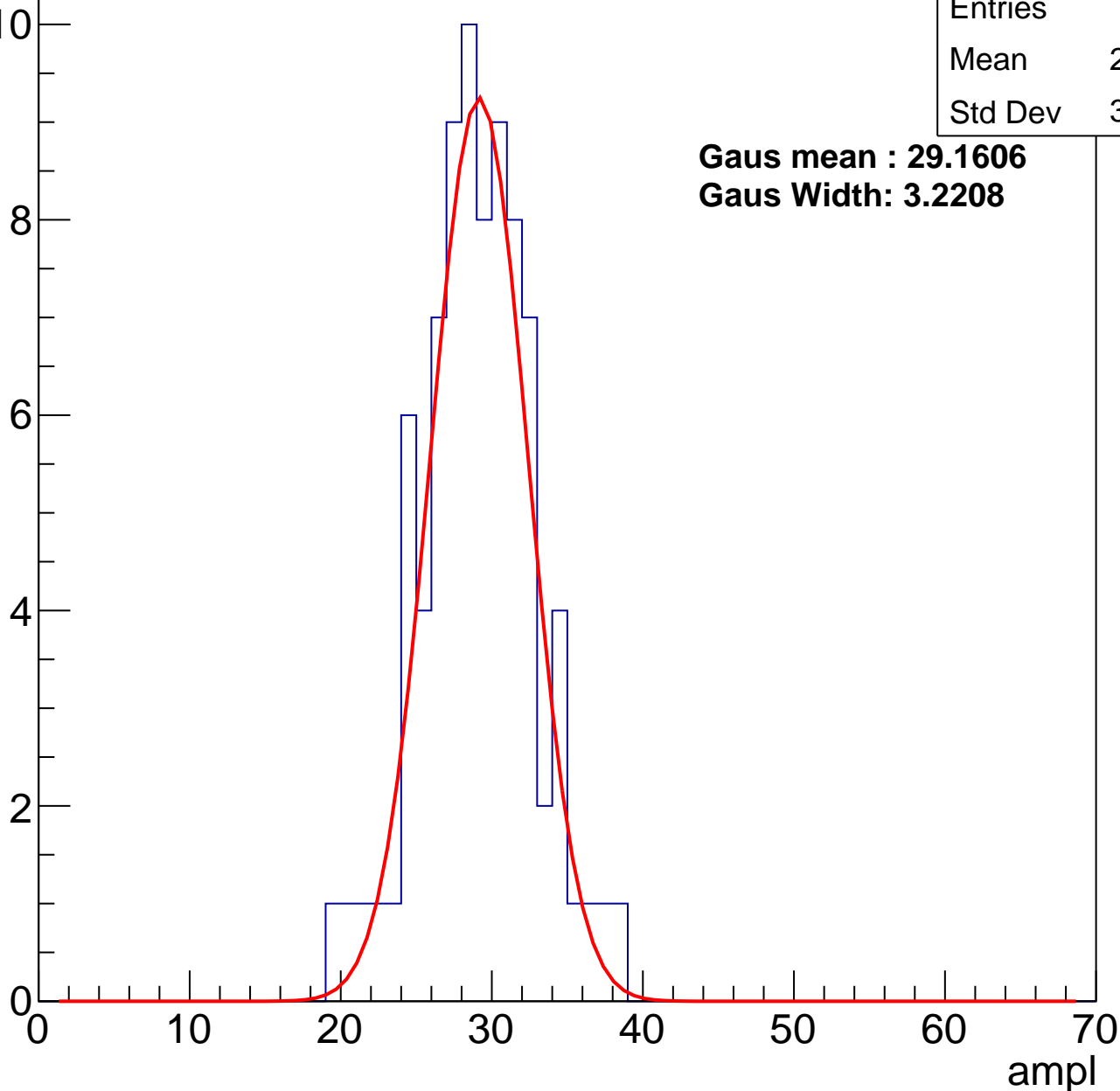
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	28.63
Std Dev	3.653

Gaus mean : 29.1606

Gaus Width: 3.2208



B1L003S, U20-ch22, adc1

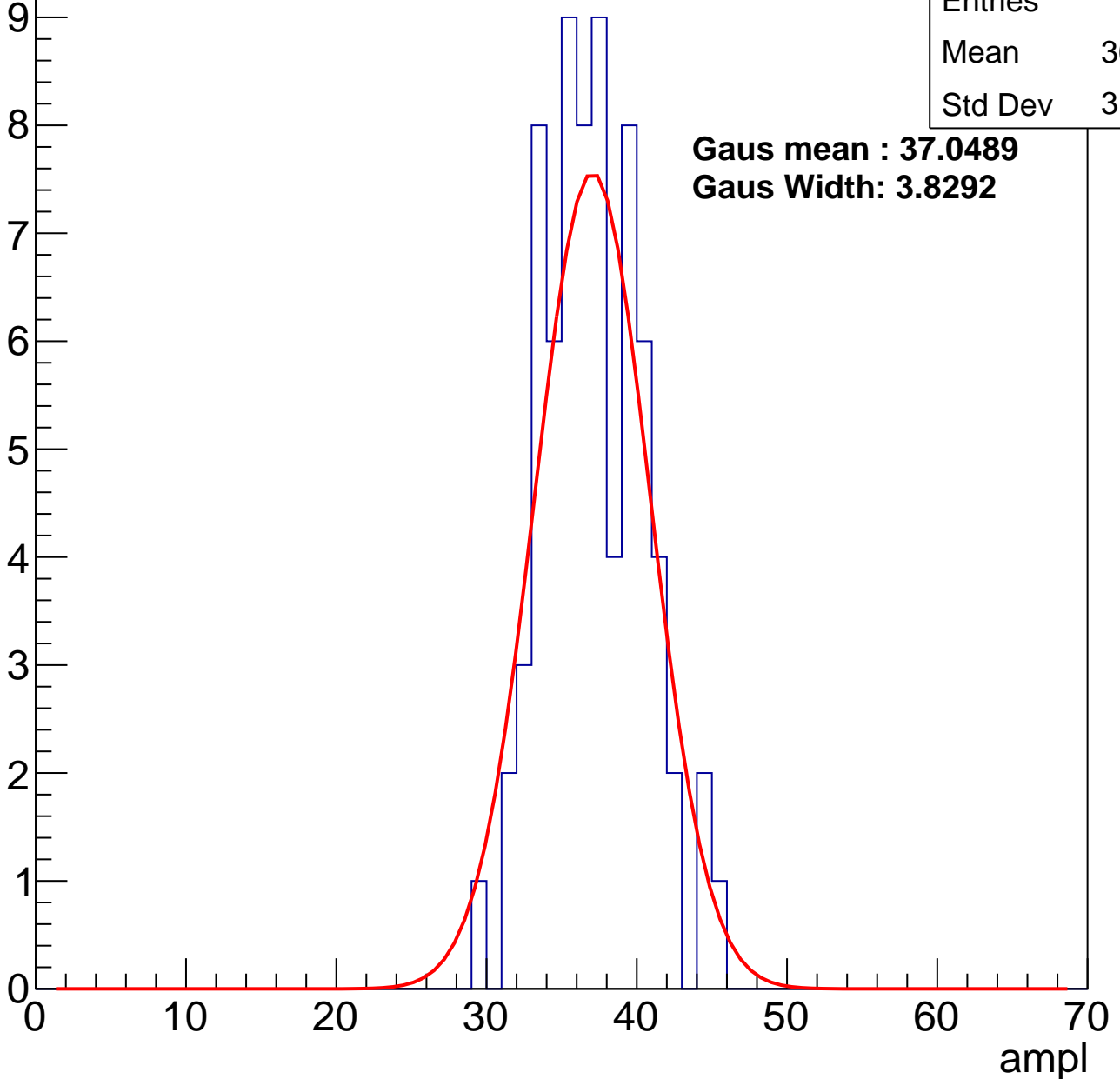
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	36.66
Std Dev	3.299

Gaus mean : 37.0489

Gaus Width: 3.8292



B1L003S, U20-ch22, adc2

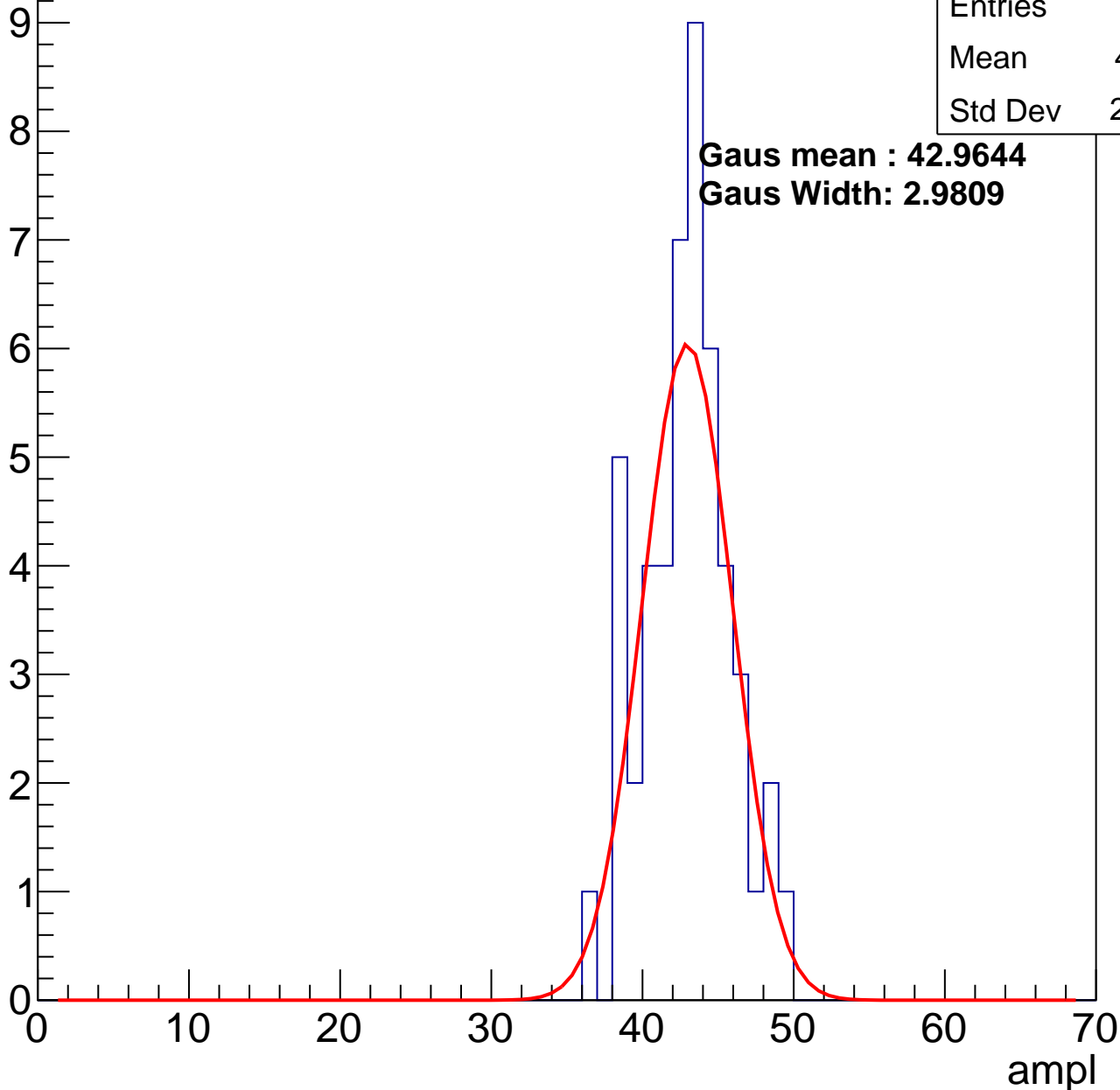
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.51
Std Dev	2.872

Gaus mean : 42.9644

Gaus Width: 2.9809

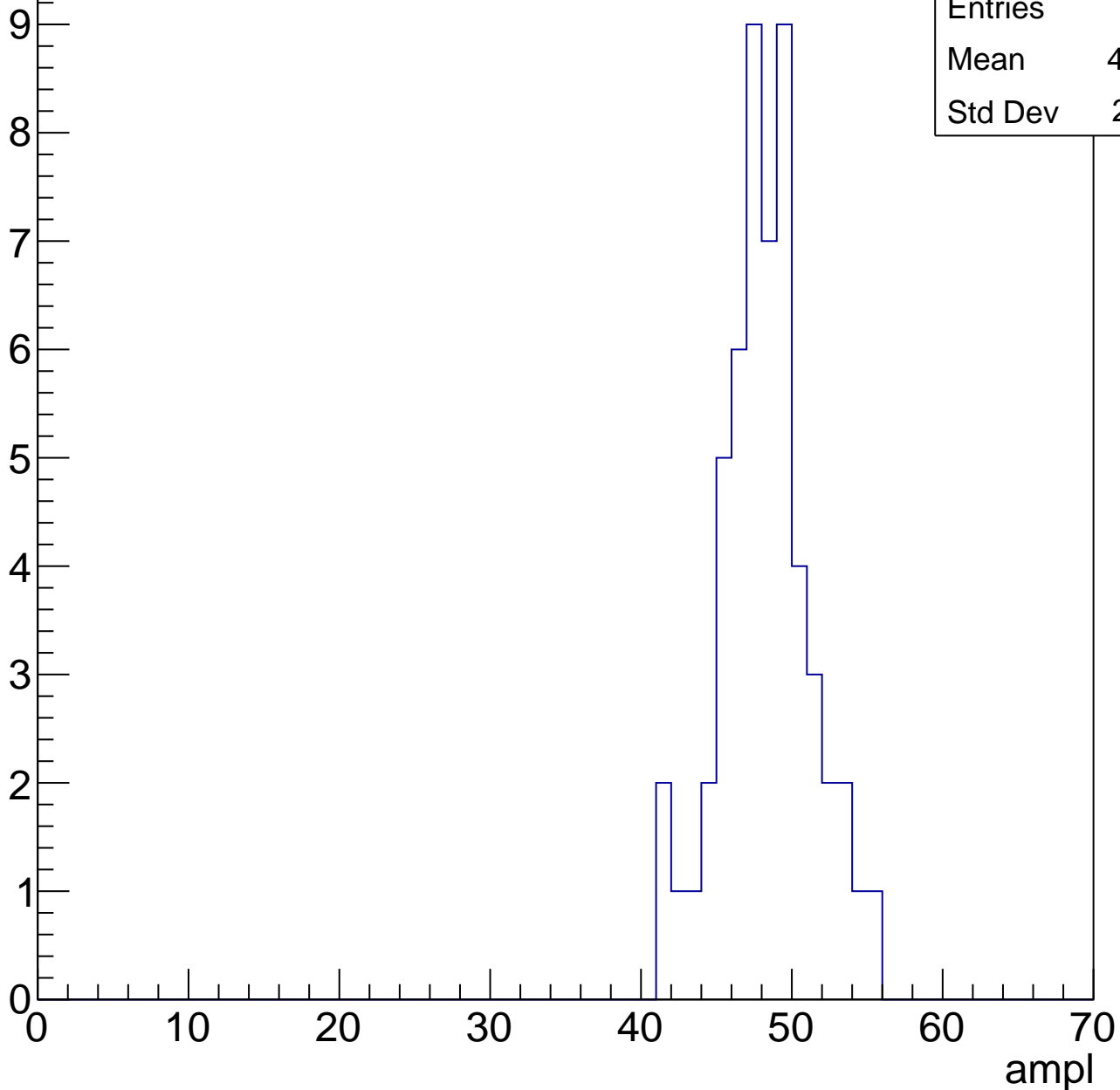


B1L003S, U20-ch22, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

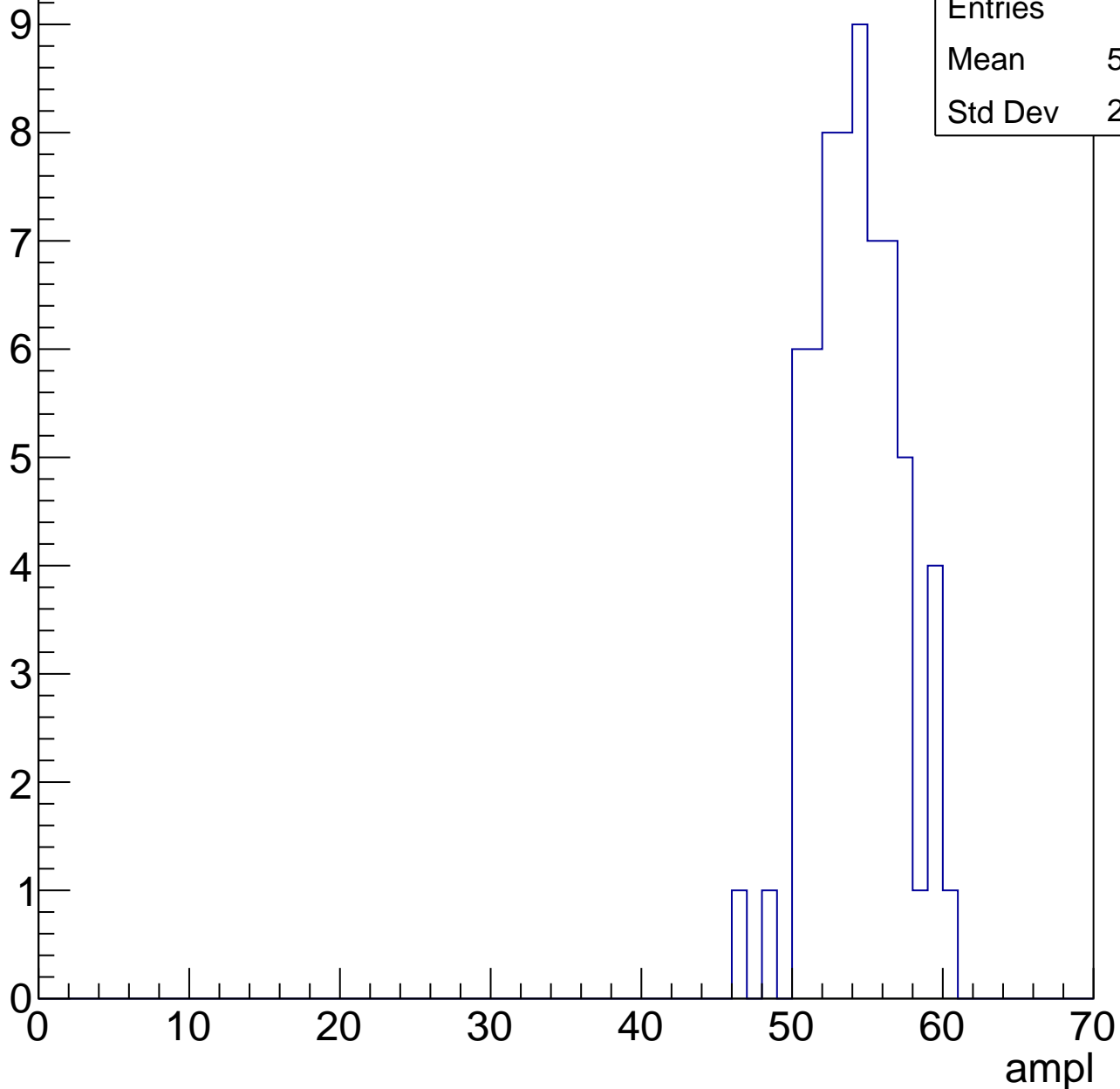
Entries	55
Mean	47.78
Std Dev	2.971



B1L003S, U20-ch22, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



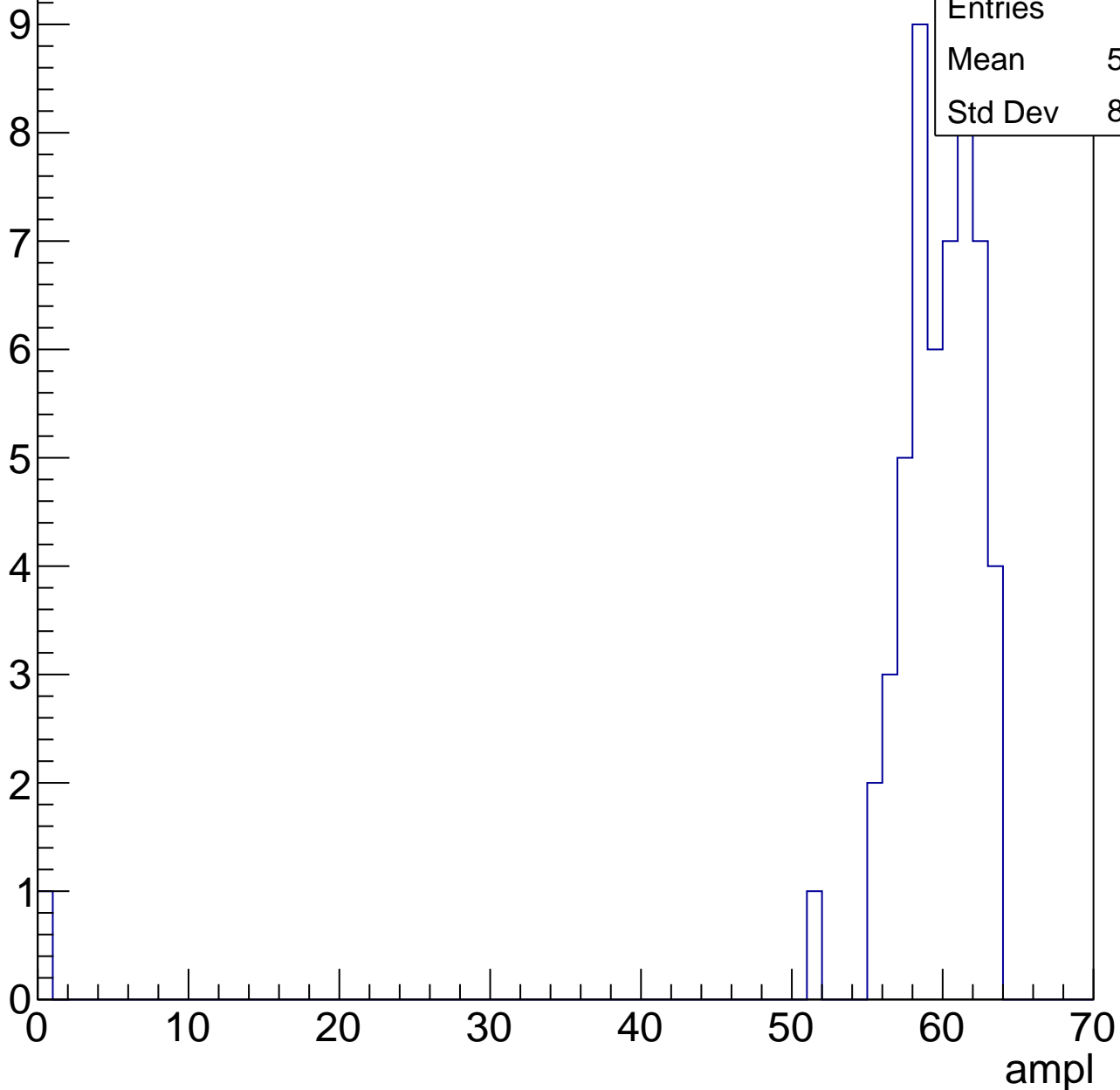
Entries	64
Mean	53.78
Std Dev	2.848

B1L003S, U20-ch22, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.24
Std Dev	8.358

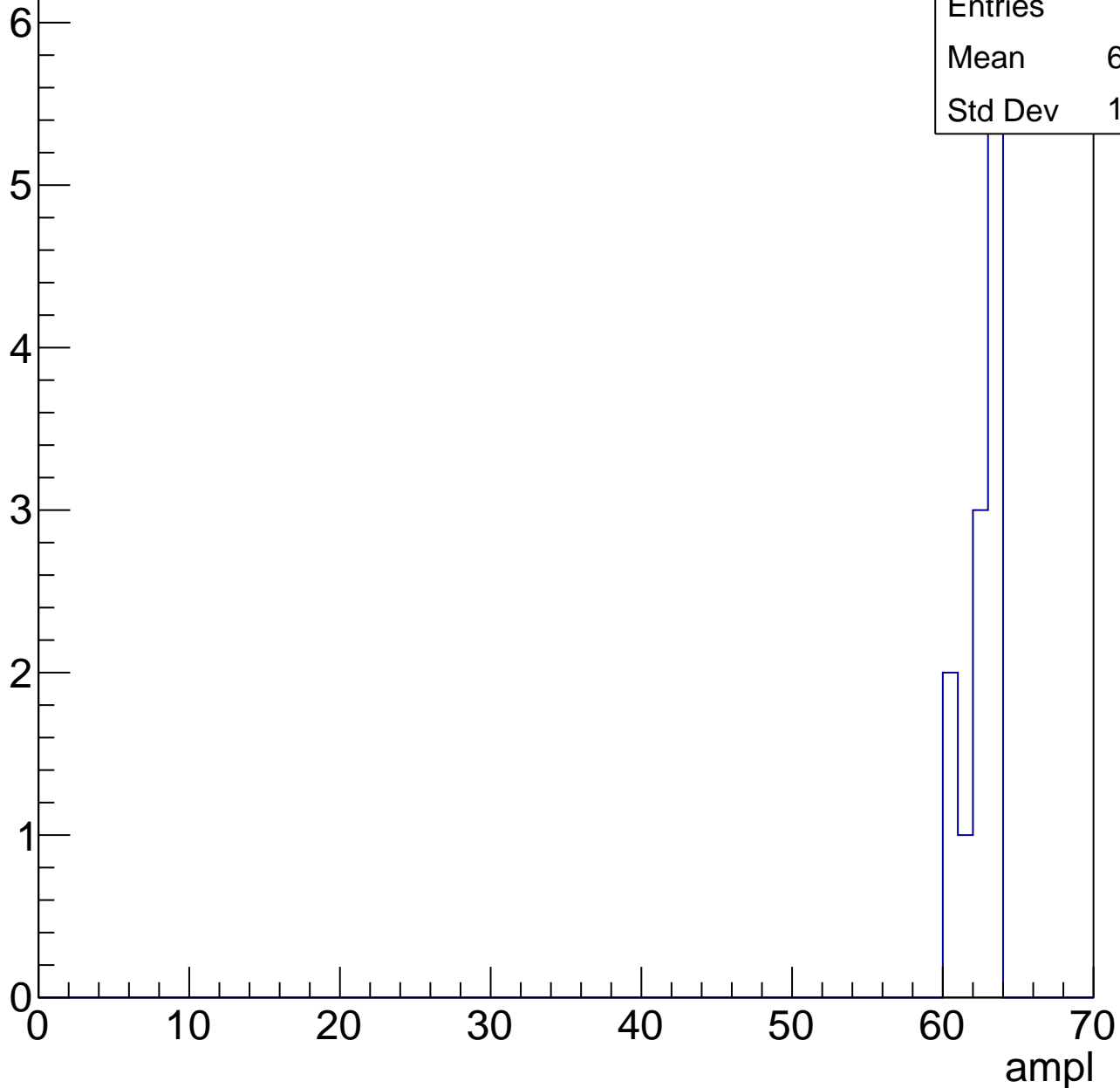


B1L003S, U20-ch22, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	62.08
Std Dev	1.115



B1L003S, U20-ch22, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch23, adc0

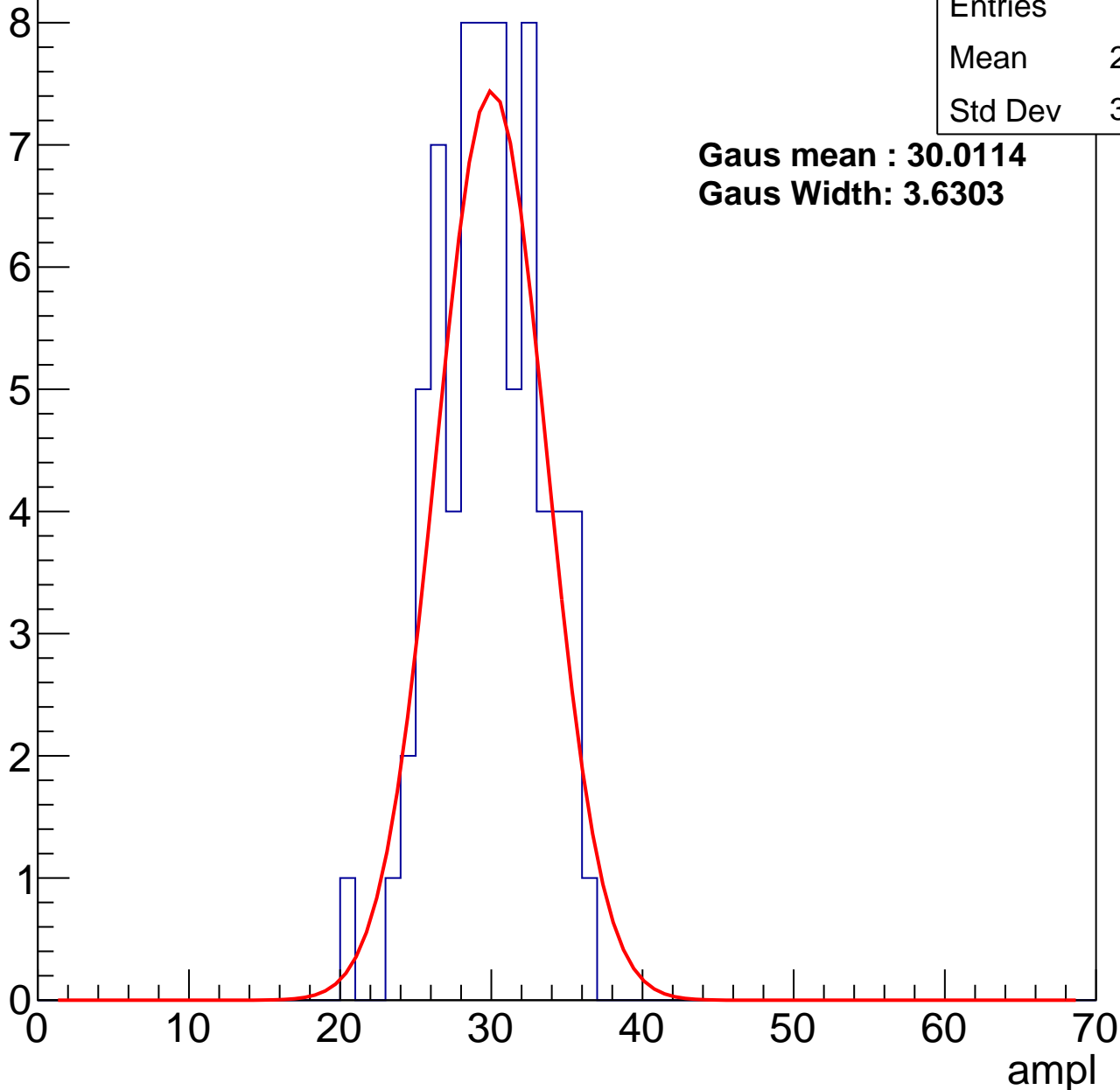
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.39
Std Dev	3.339

Gaus mean : 30.0114

Gaus Width: 3.6303



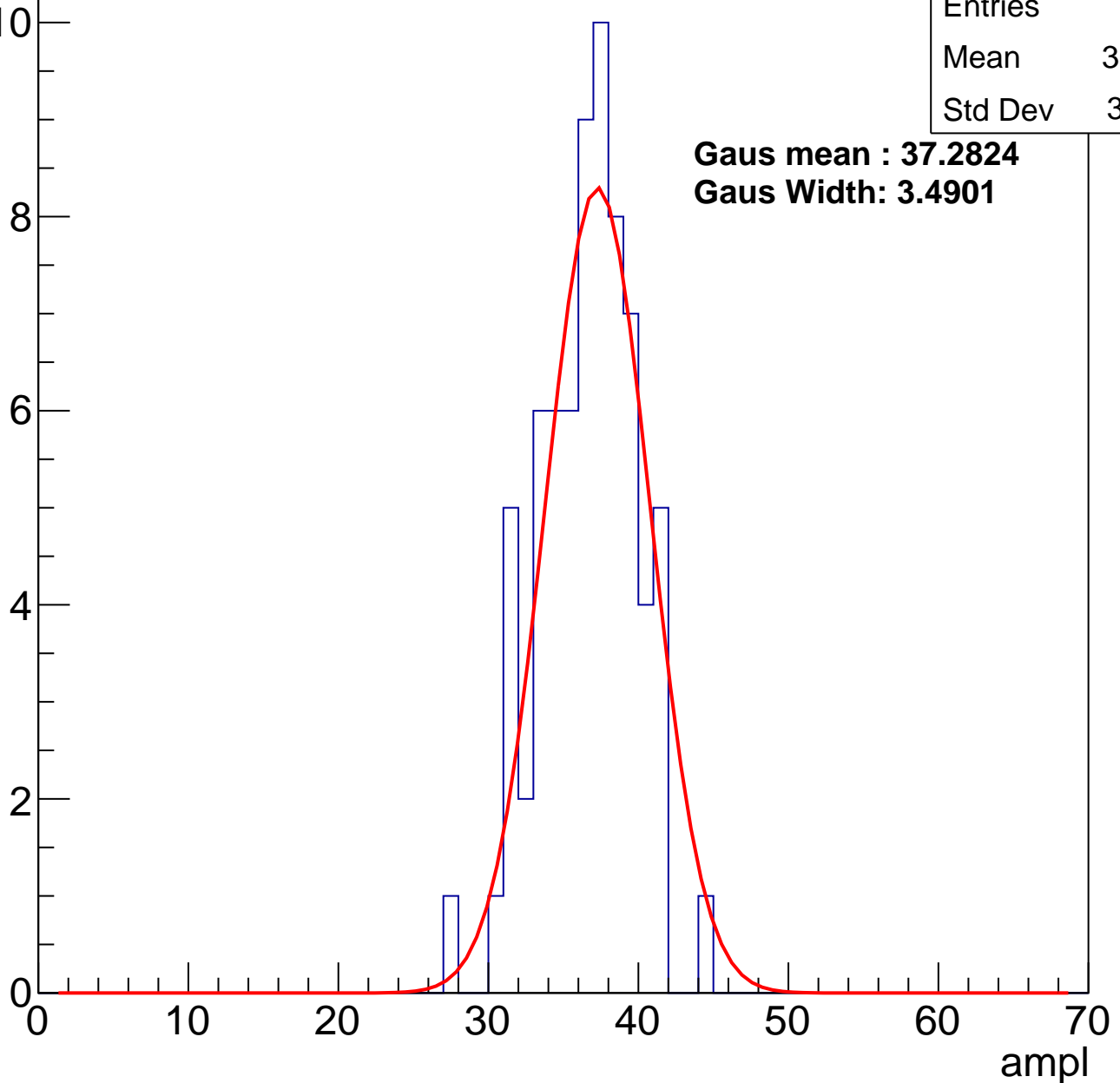
B1L003S, U20-ch23, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.17
Std Dev	3.171

Gaus mean : 37.2824
Gaus Width: 3.4901



B1L003S, U20-ch23, adc2

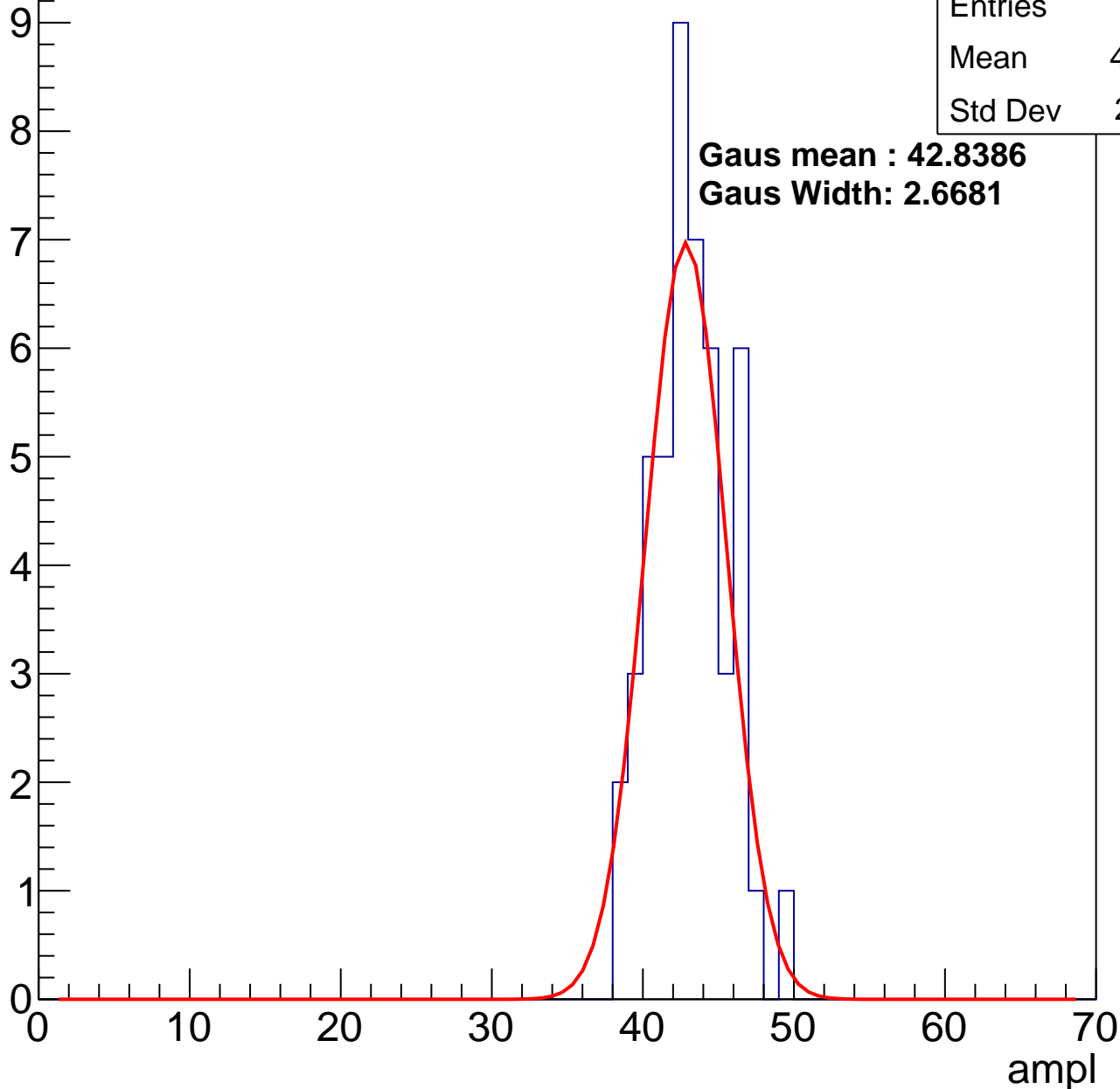
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	42.67
Std Dev	2.461

Gaus mean : 42.8386

Gaus Width: 2.6681



B1L003S, U20-ch23, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	48.62
Std Dev	3.33

Entry

10

8

6

4

2

0

0

10

20

30

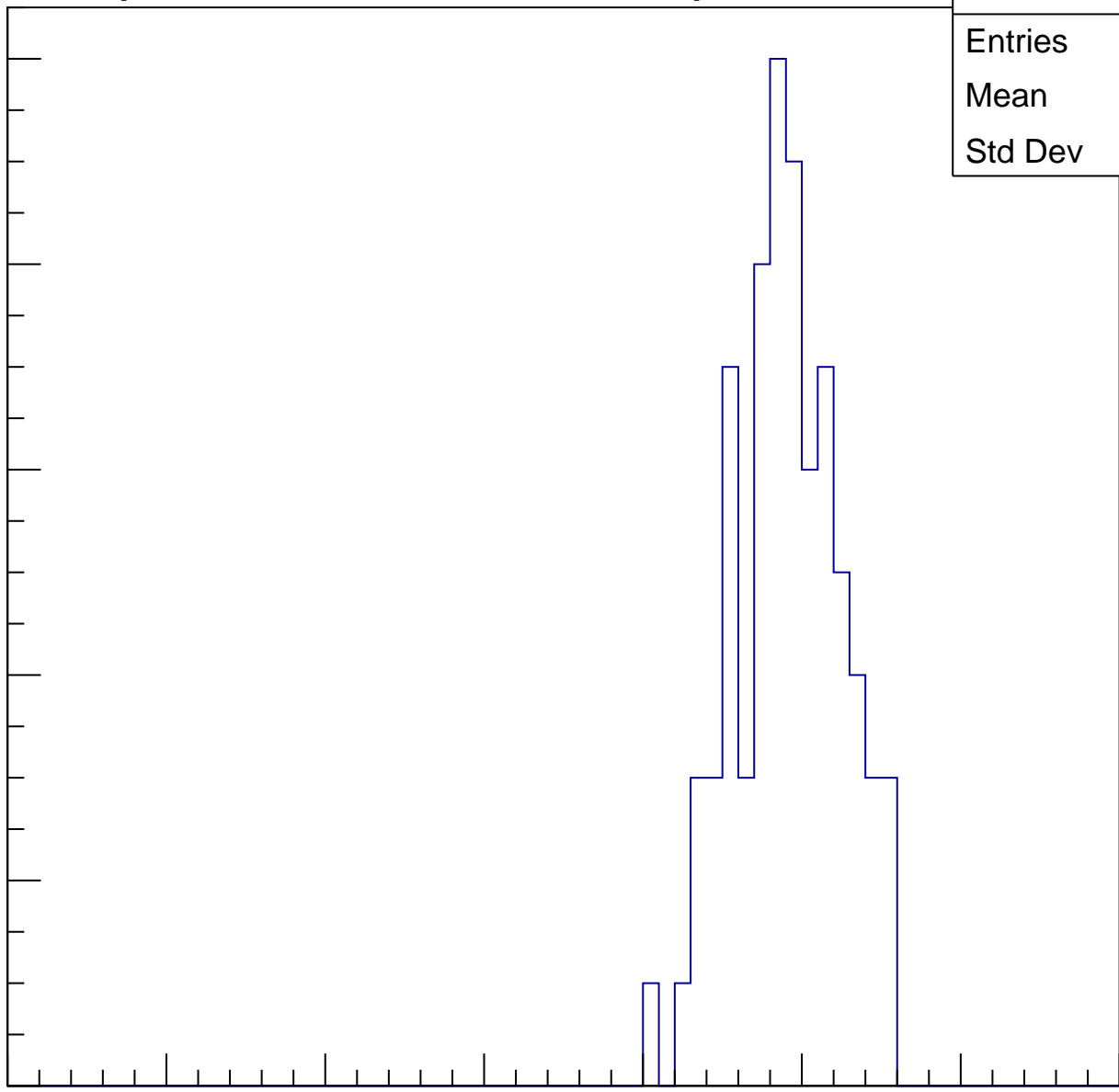
40

50

60

70

ampl

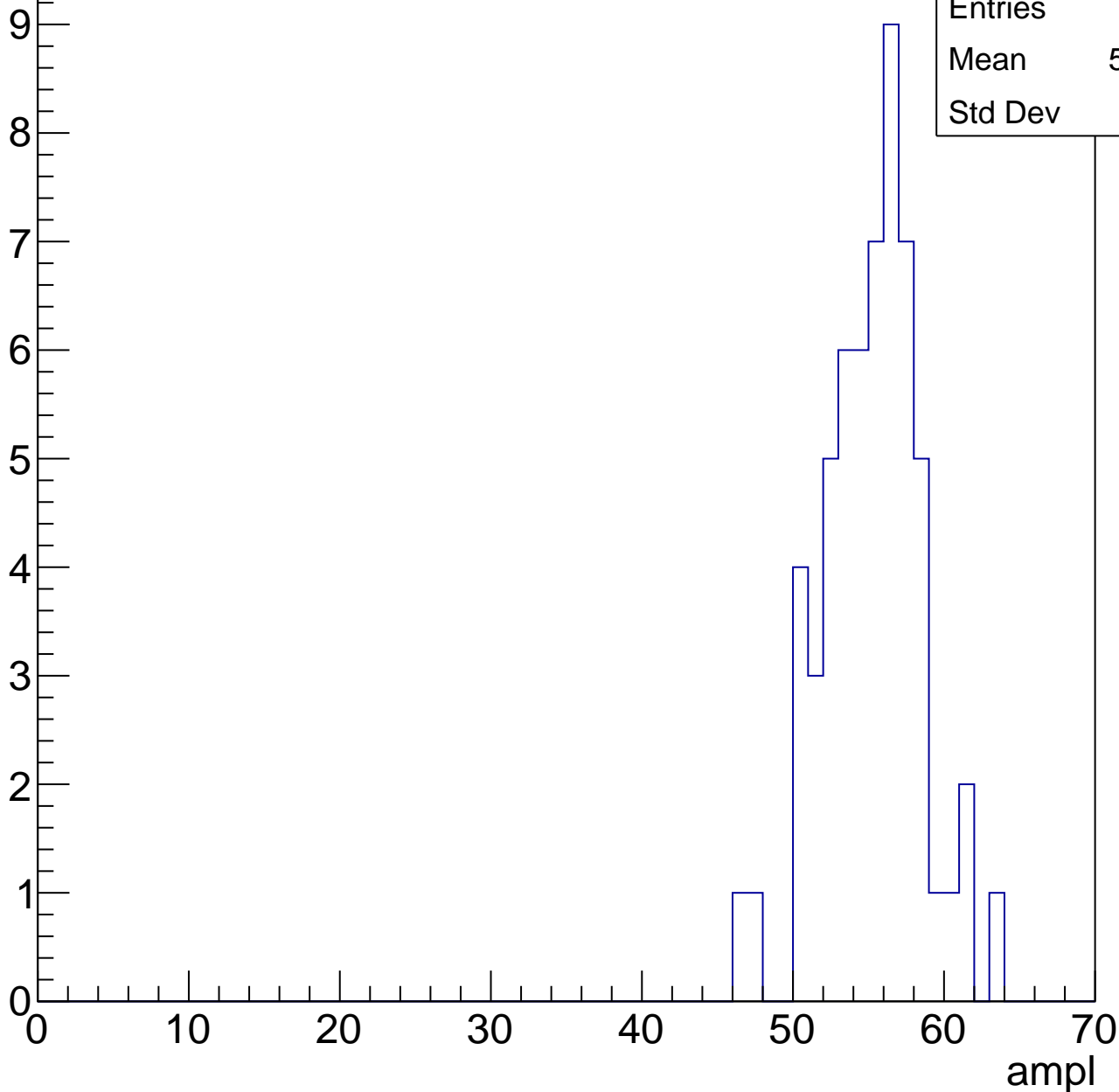


B1L003S, U20-ch23, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.75
Std Dev	3.25



B1L003S, U20-ch23, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.72
Std Dev	1.947

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

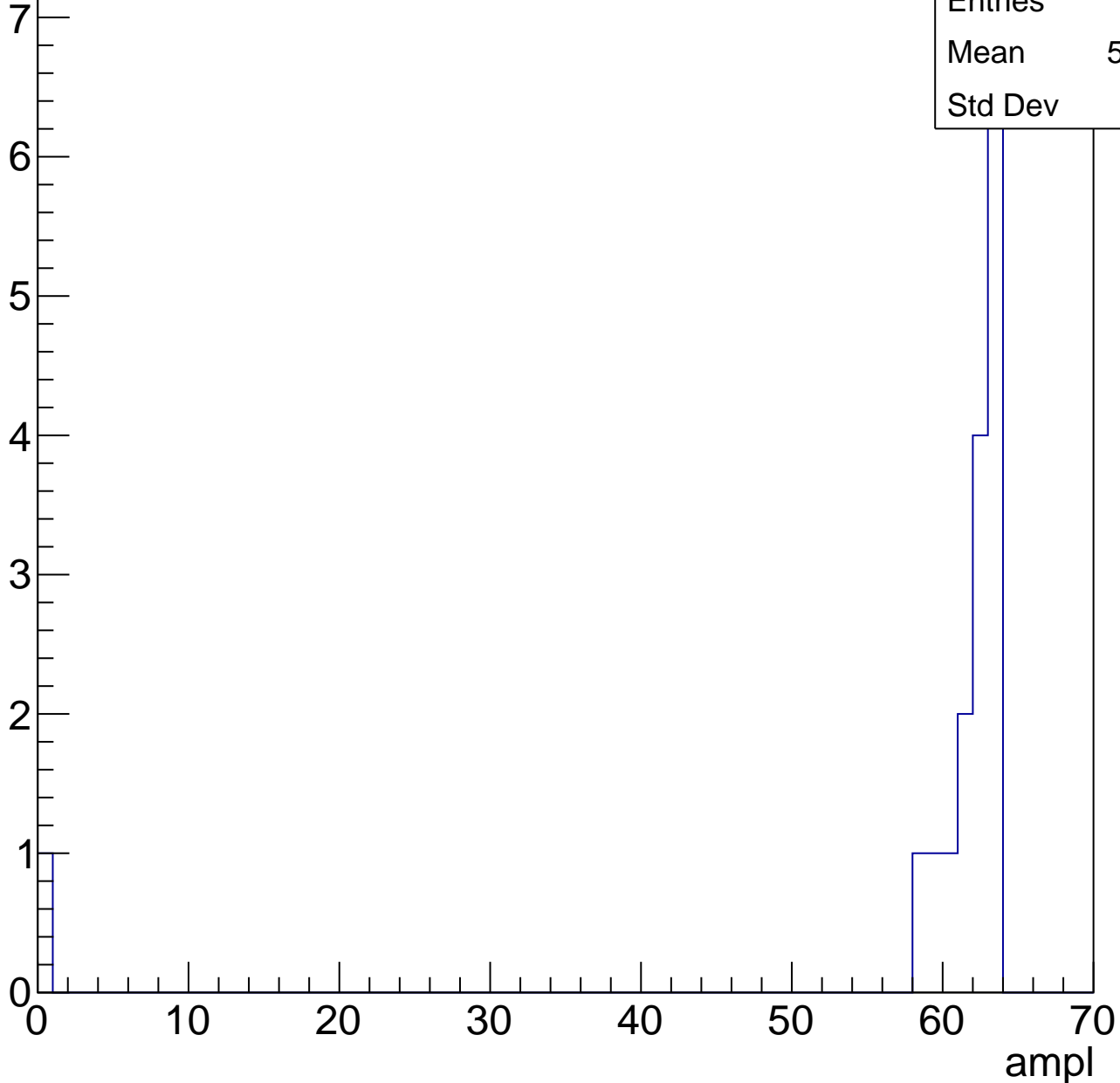
8

B1L003S, U20-ch23, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.12
Std Dev	14.6



B1L003S, U20-ch23, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch24, adc0

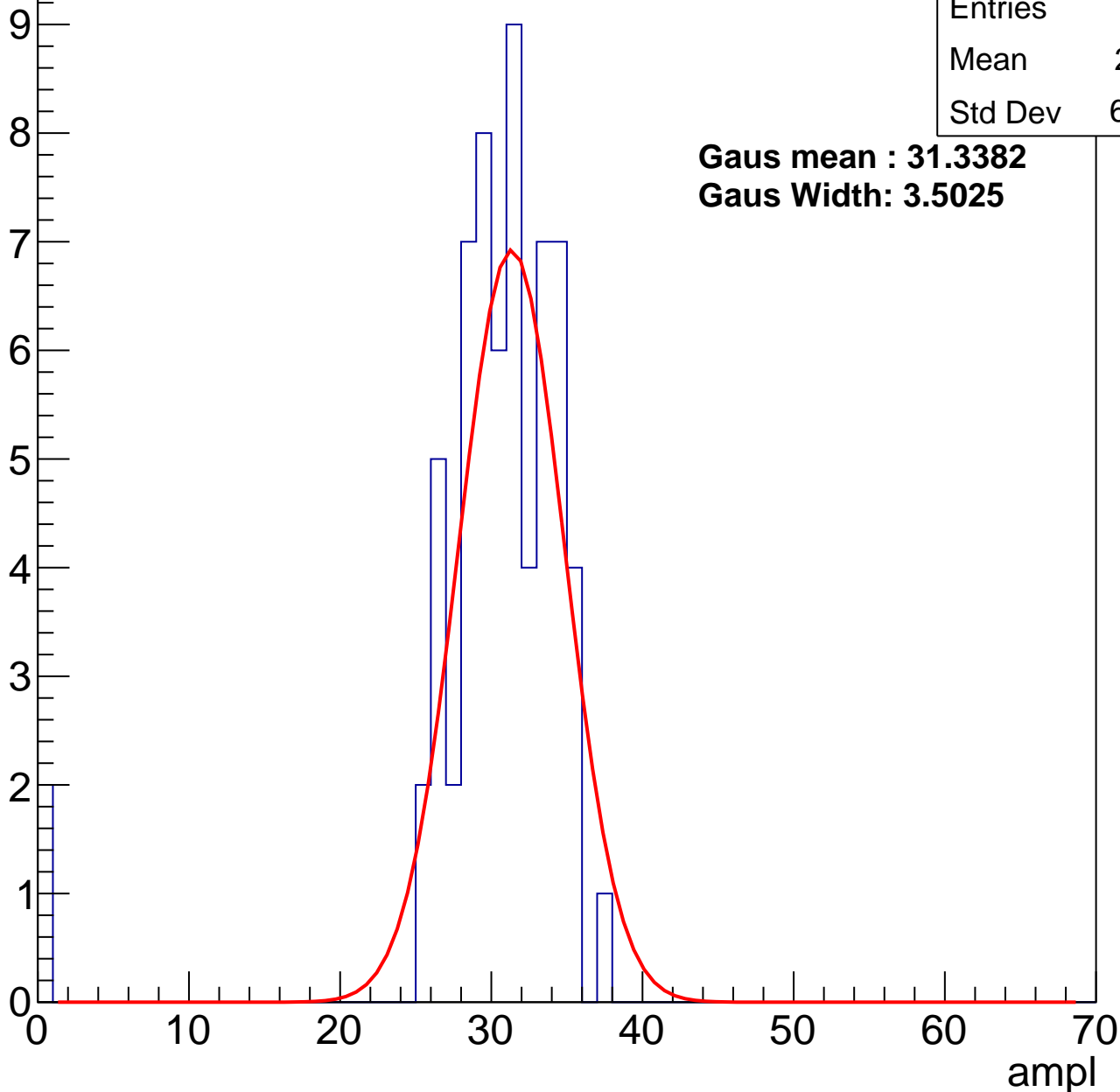
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.61
Std Dev	6.025

Gaus mean : 31.3382

Gaus Width: 3.5025



B1L003S, U20-ch24, adc1

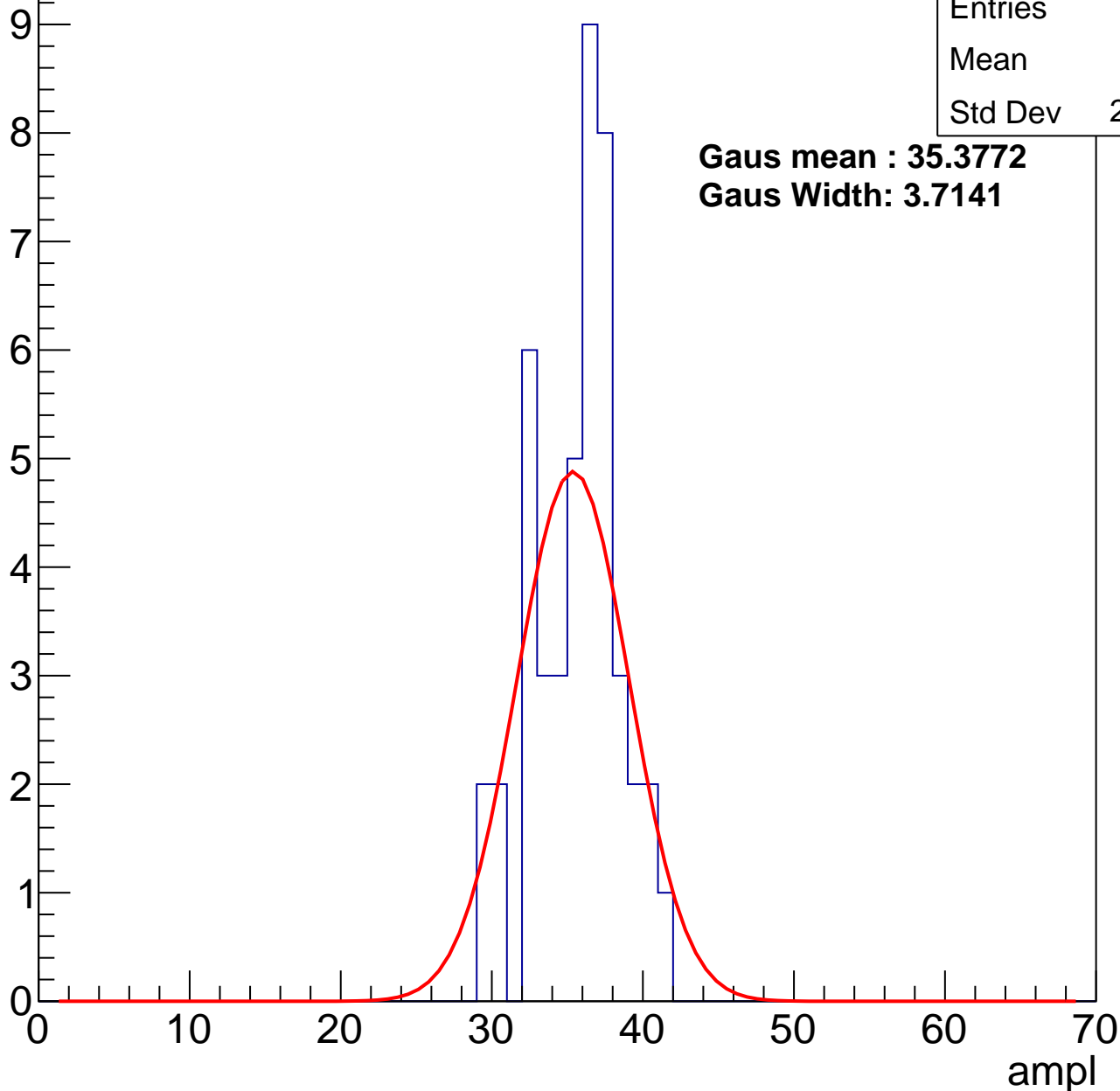
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	35.2
Std Dev	2.856

Gaus mean : 35.3772

Gaus Width: 3.7141



B1L003S, U20-ch24, adc2

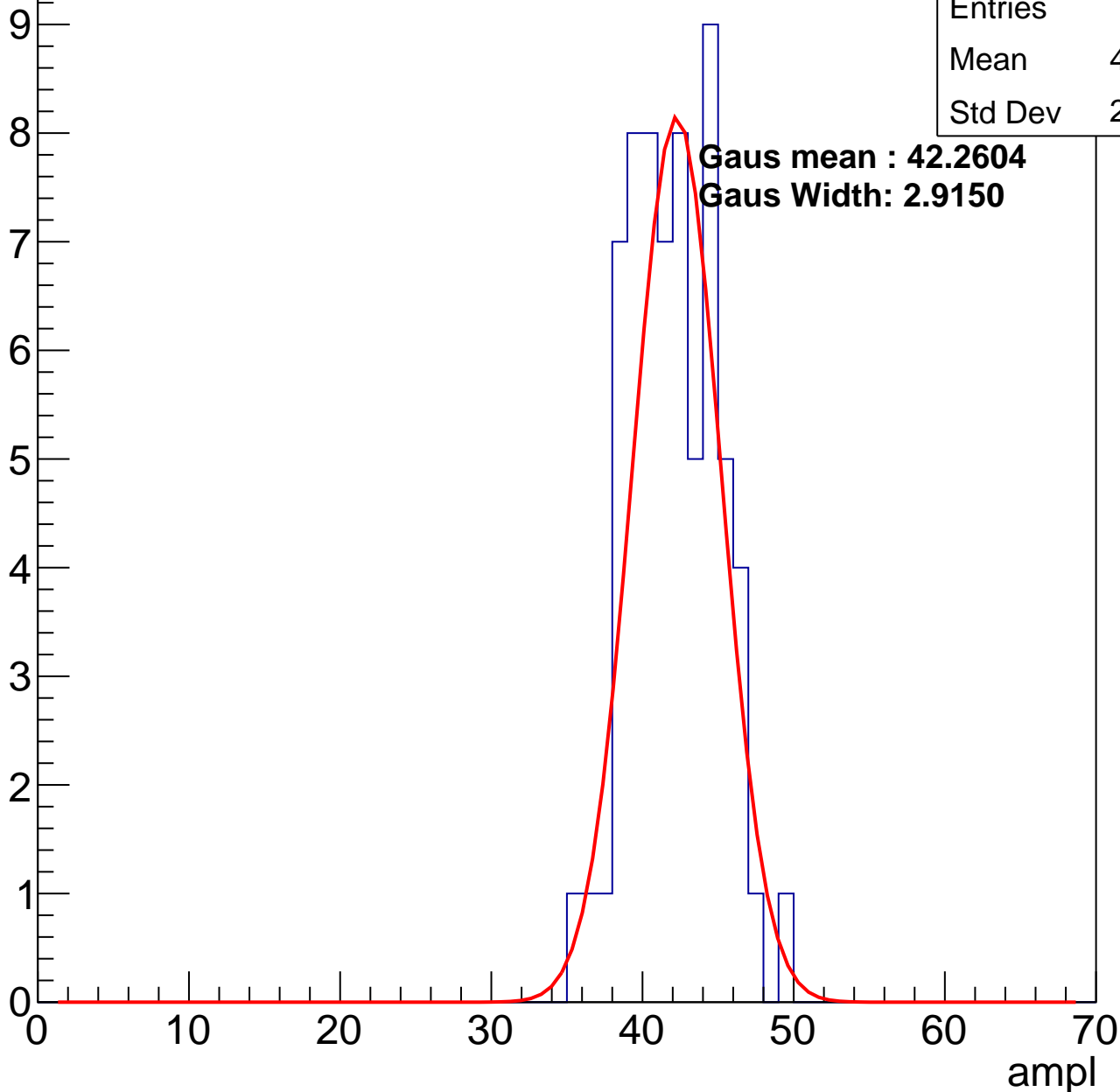
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.59
Std Dev	2.876

Gaus mean : 42.2604

Gaus Width: 2.9150



B1L003S, U20-ch24, adc3

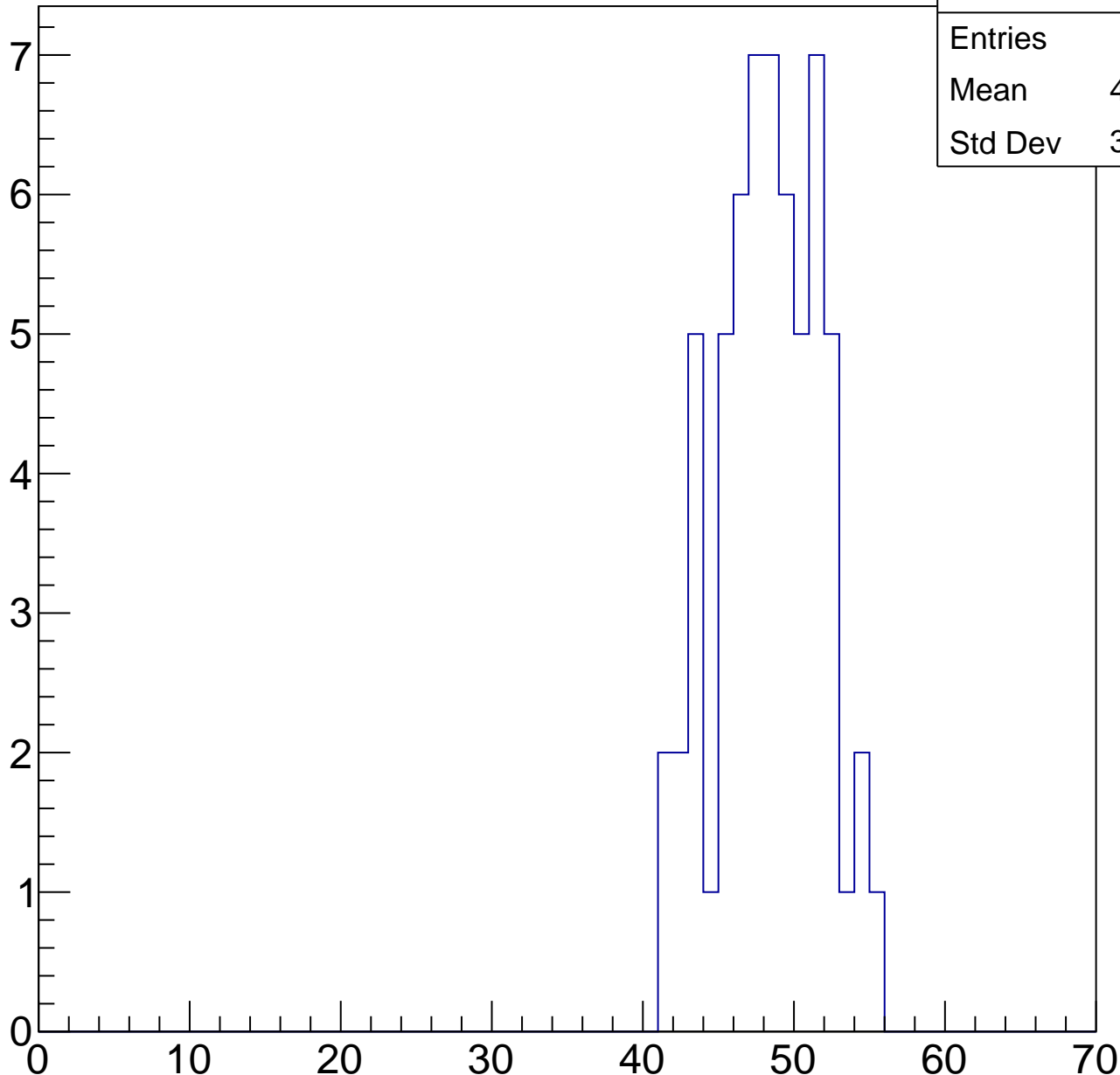
calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	62
Mean	47.87
Std Dev	3.362

ampl

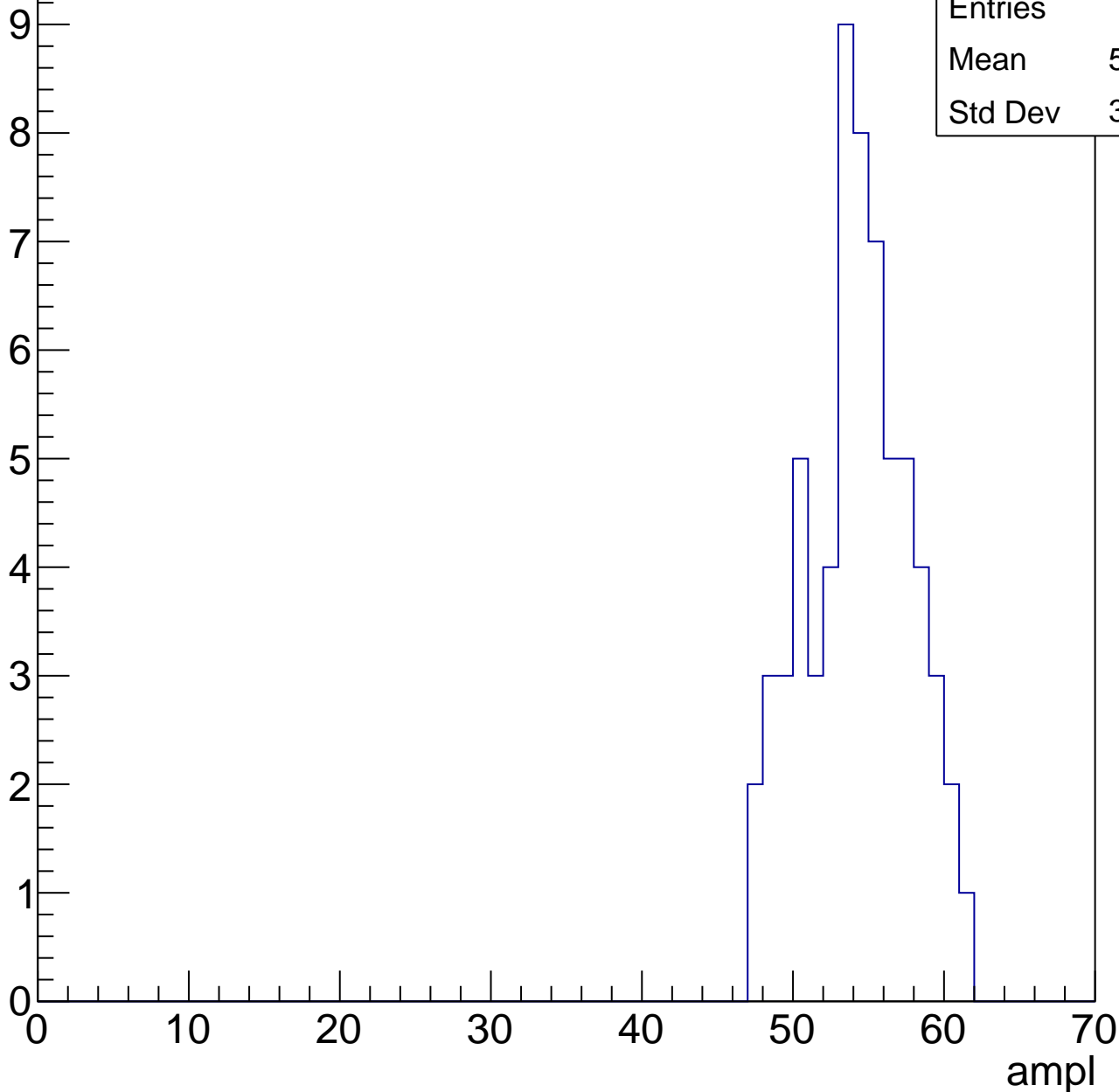


B1L003S, U20-ch24, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	53.83
Std Dev	3.408

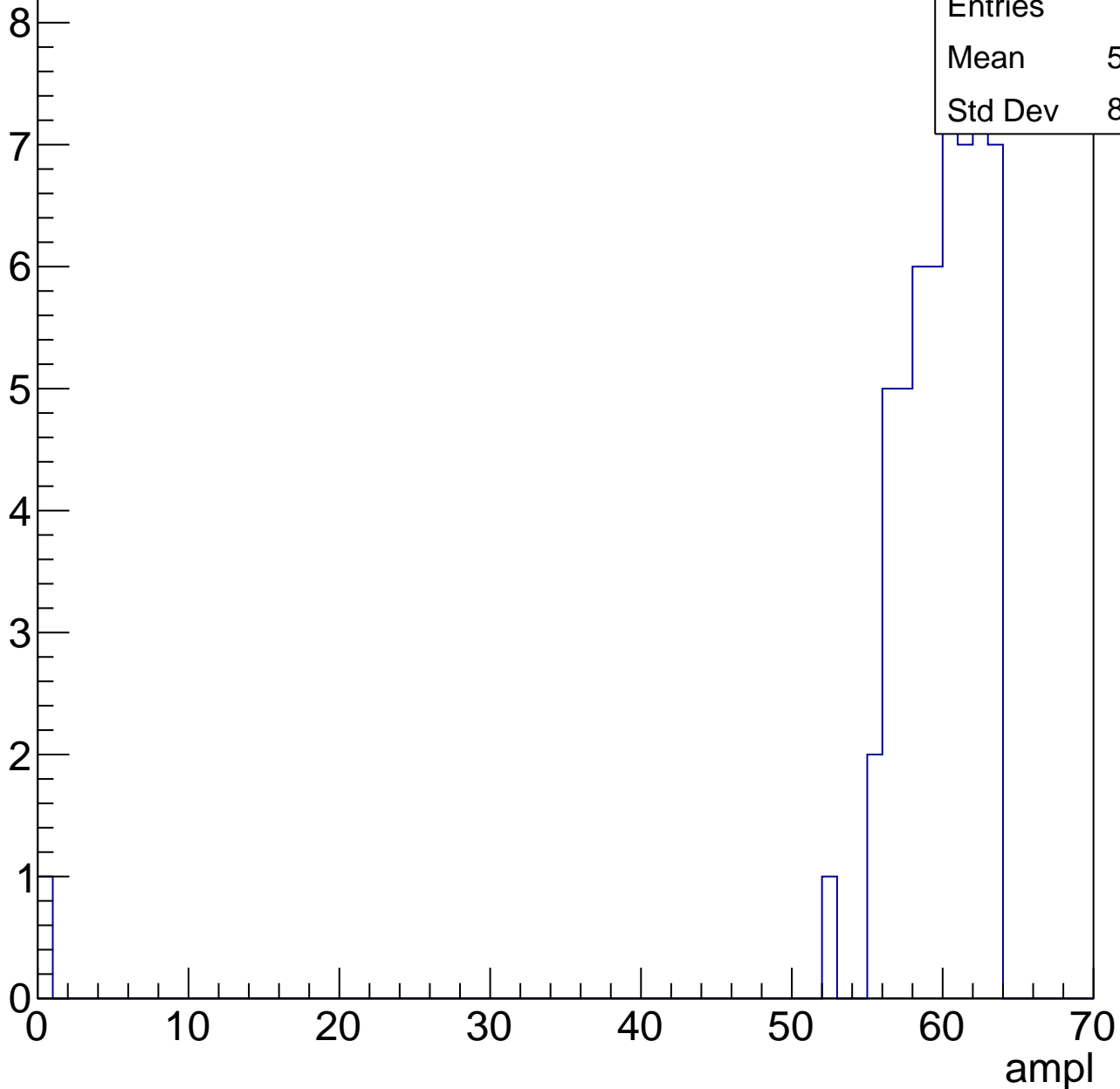


B1L003S, U20-ch24, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.45
Std Dev	8.276



B1L003S, U20-ch24, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

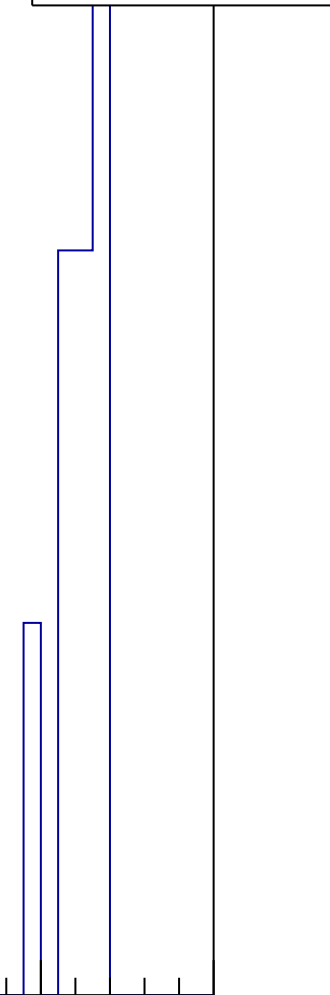
8

Mean

61.75

Std Dev

1.299



B1L003S, U20-ch24, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch25, adc0

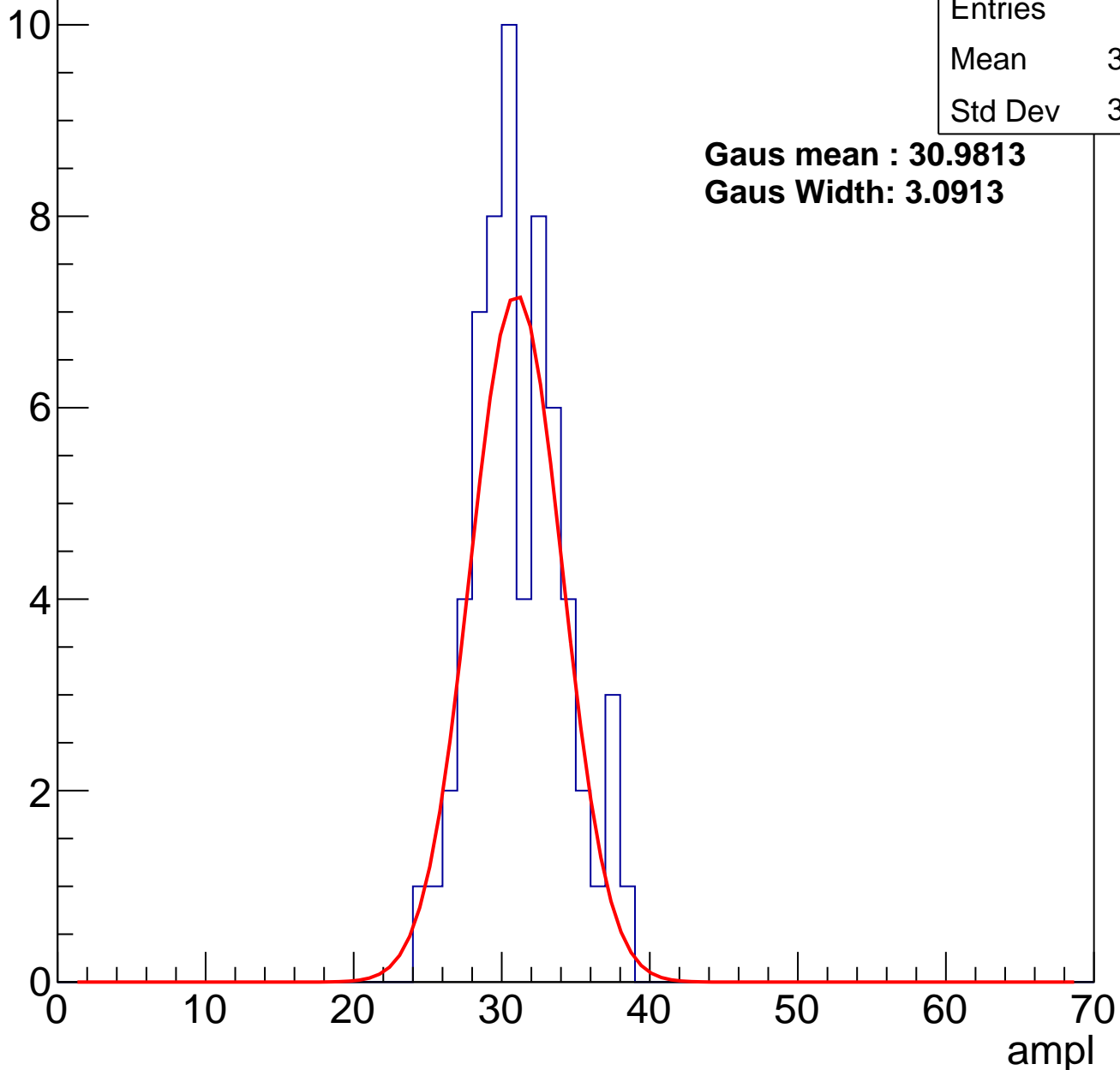
calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	30.74
Std Dev	3.063

Gaus mean : 30.9813

Gaus Width: 3.0913

Entry



B1L003S, U20-ch25, adc1

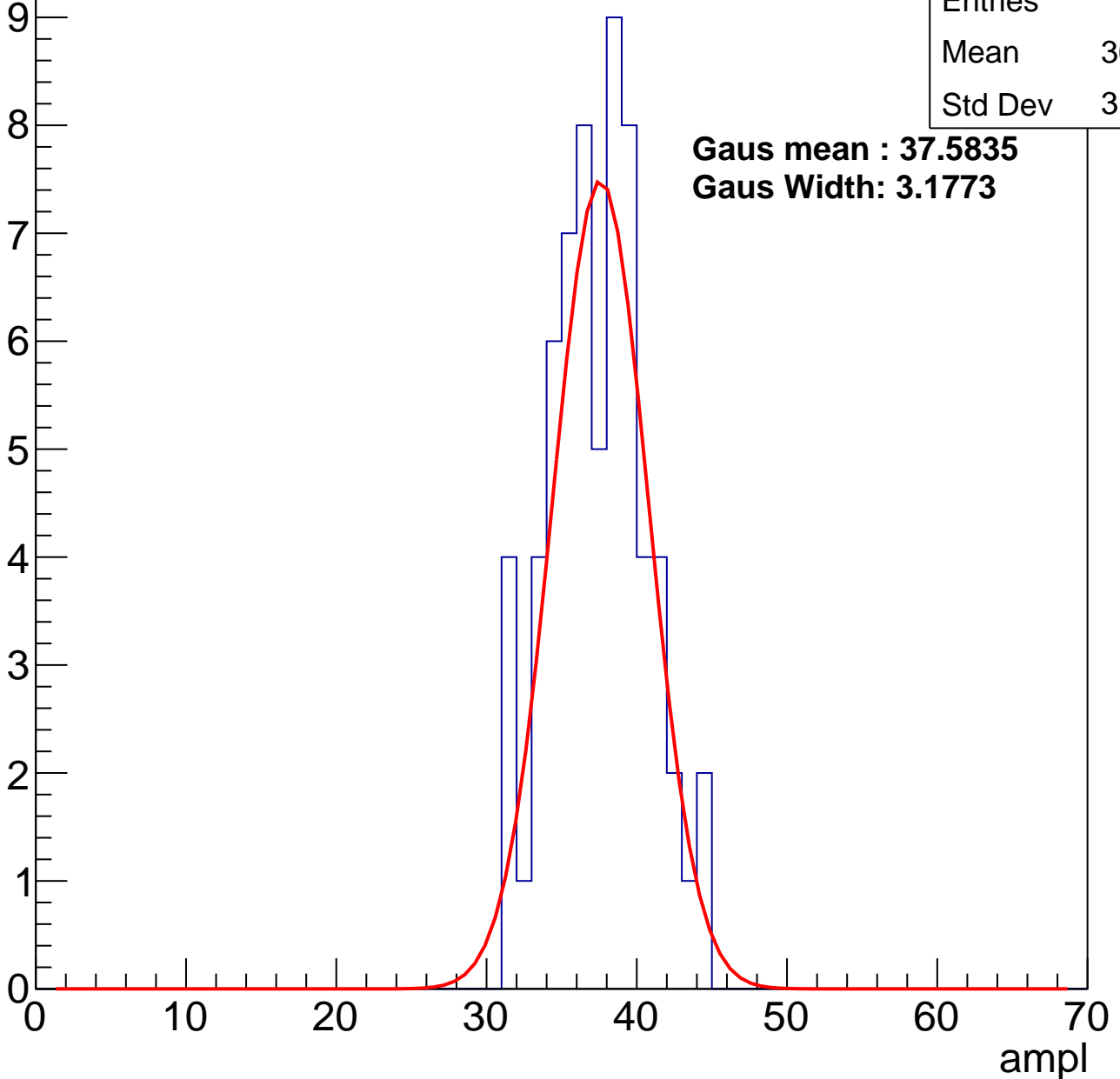
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.97
Std Dev	3.157

Gaus mean : 37.5835

Gaus Width: 3.1773



B1L003S, U20-ch25, adc2

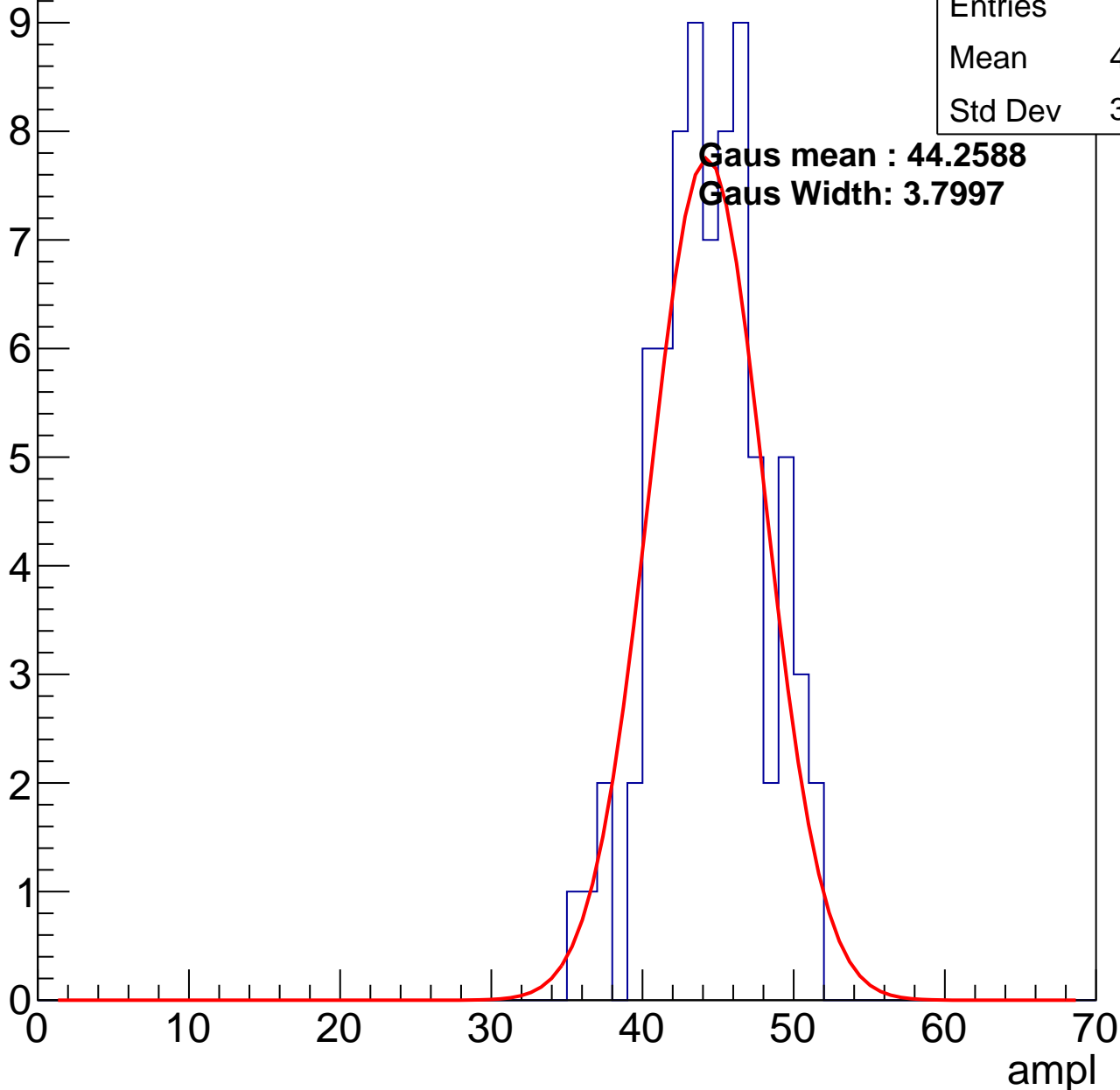
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	43.97
Std Dev	3.509

Gaus mean : 44.2588

Gaus Width: 3.7997

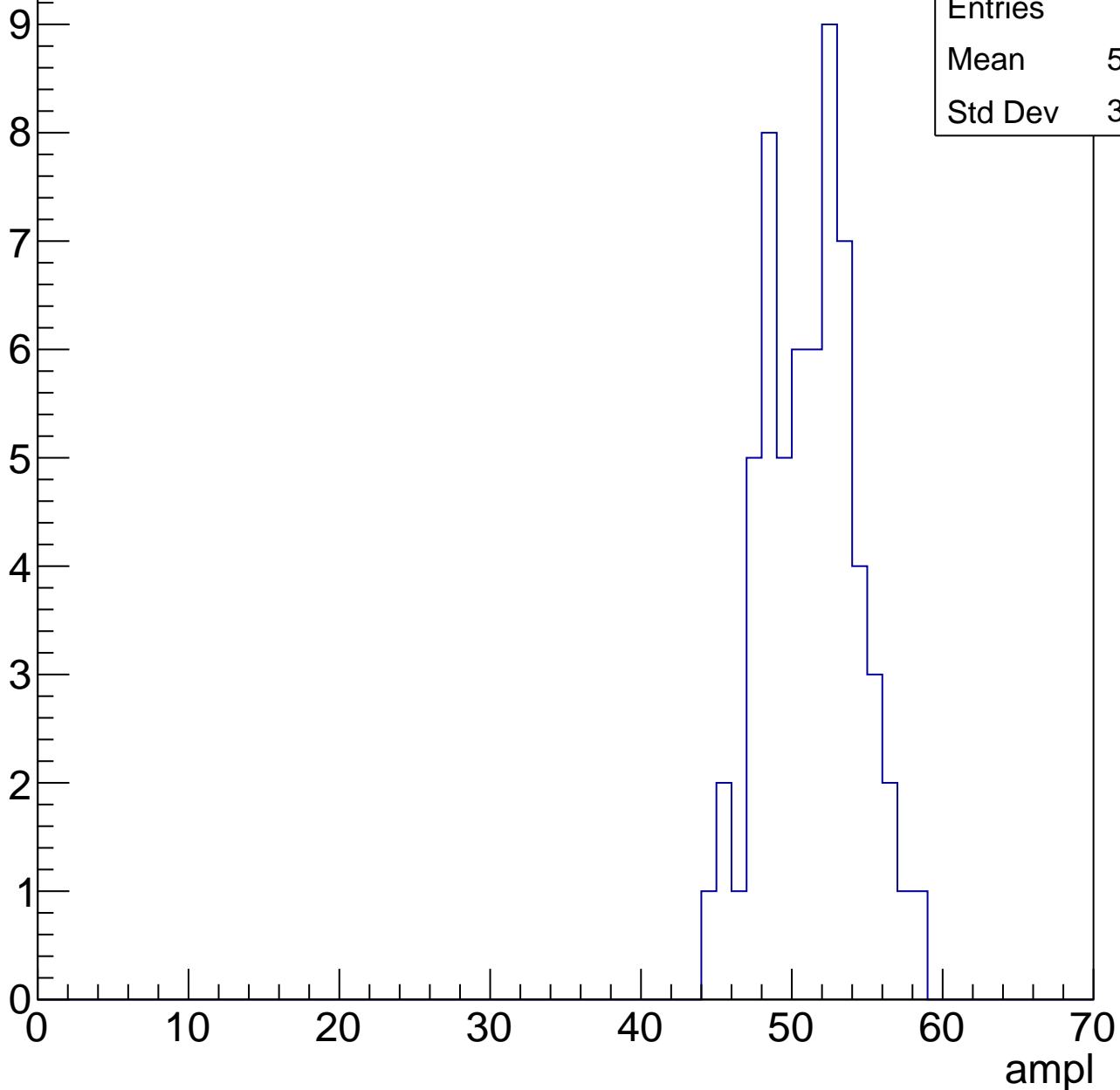


B1L003S, U20-ch25, adc3

calib_packv5_042523_0143.root, FC#13, port D2

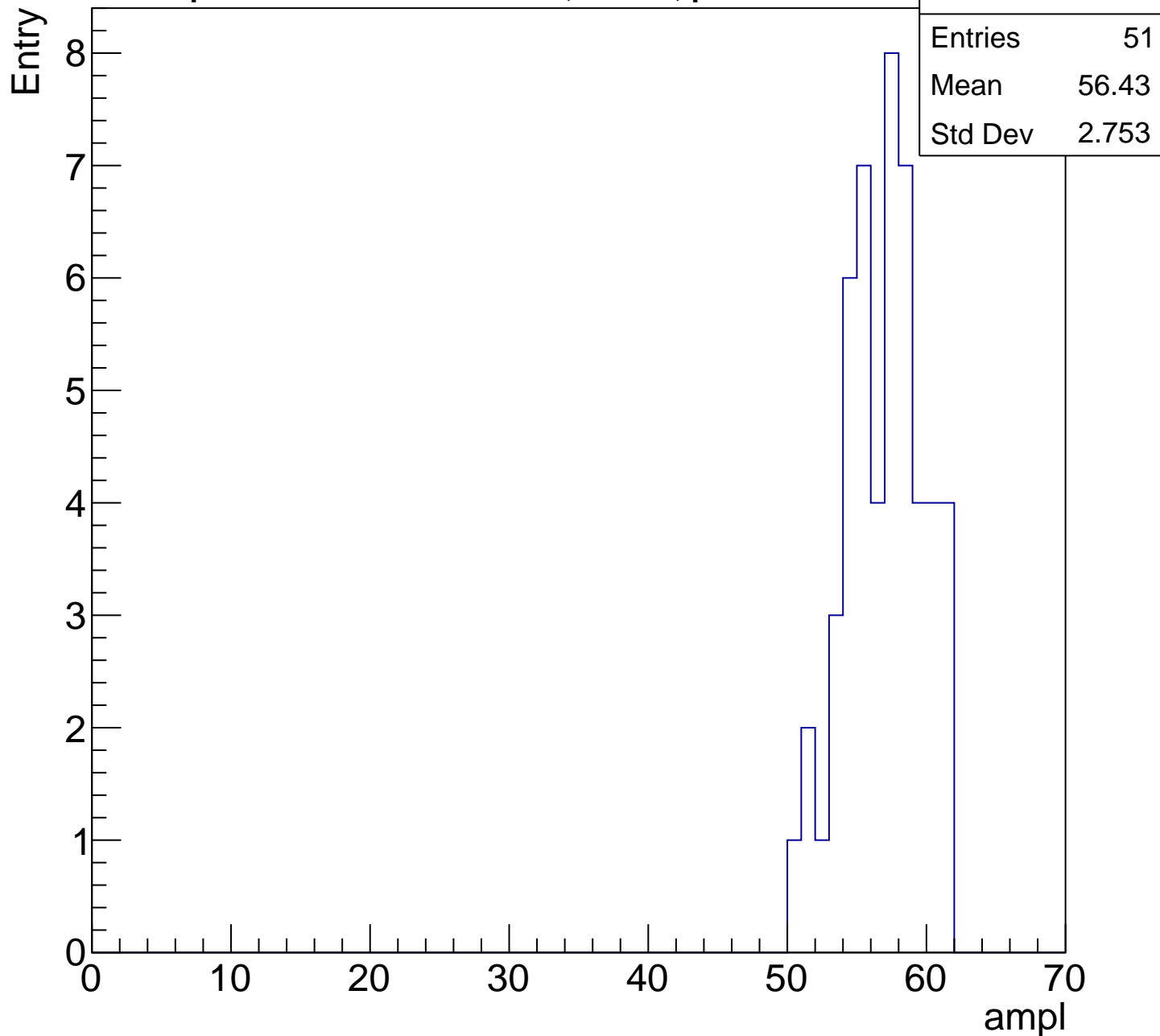
Entry

Entries	61
Mean	50.77
Std Dev	3.075



B1L003S, U20-ch25, adc4

calib_packv5_042523_0143.root, FC#13, port D2

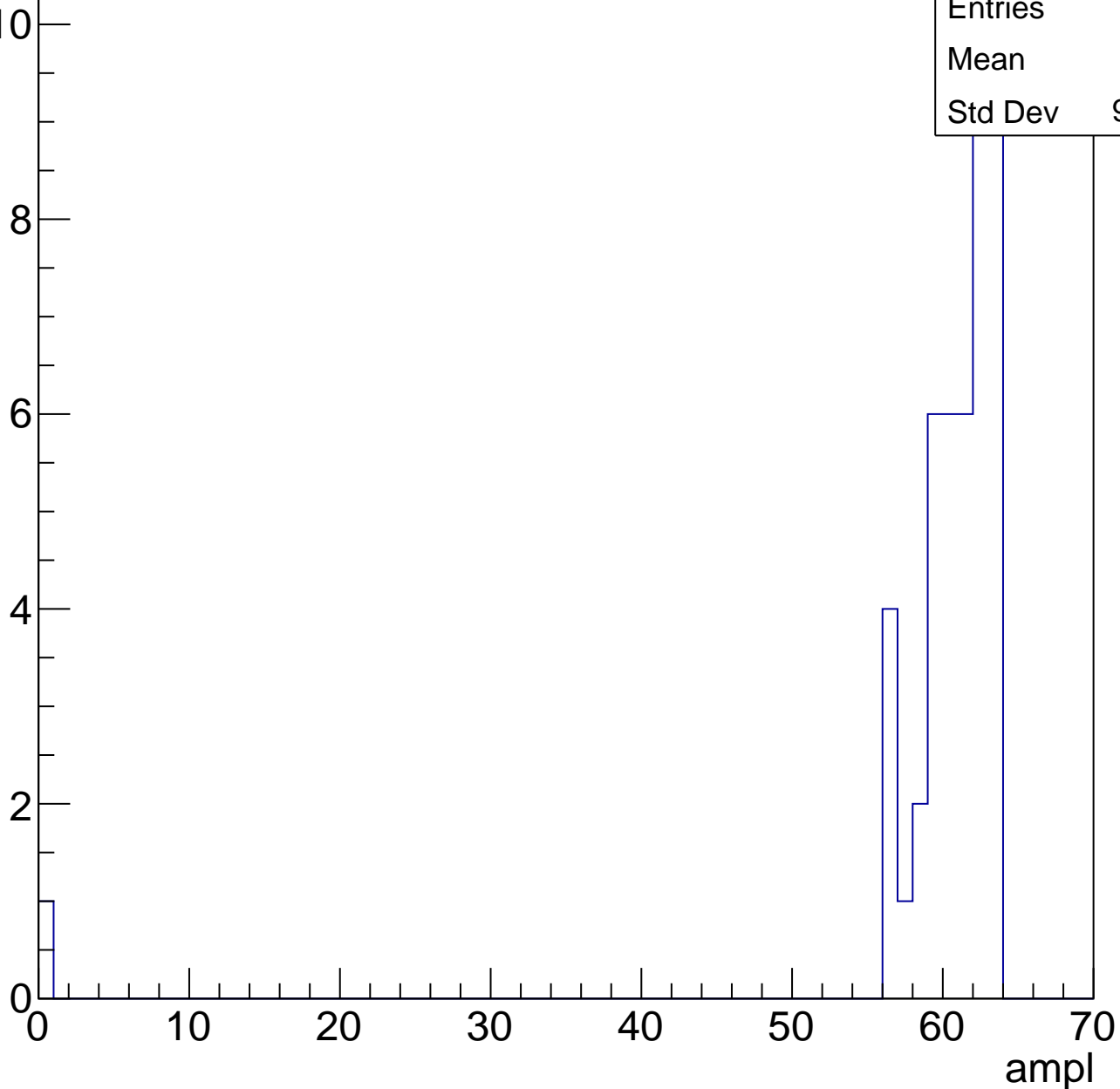


B1L003S, U20-ch25, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.2
Std Dev	9.171



B1L003S, U20-ch25, adc6

calib_packv5_042523_0143.root, FC#13, port D2

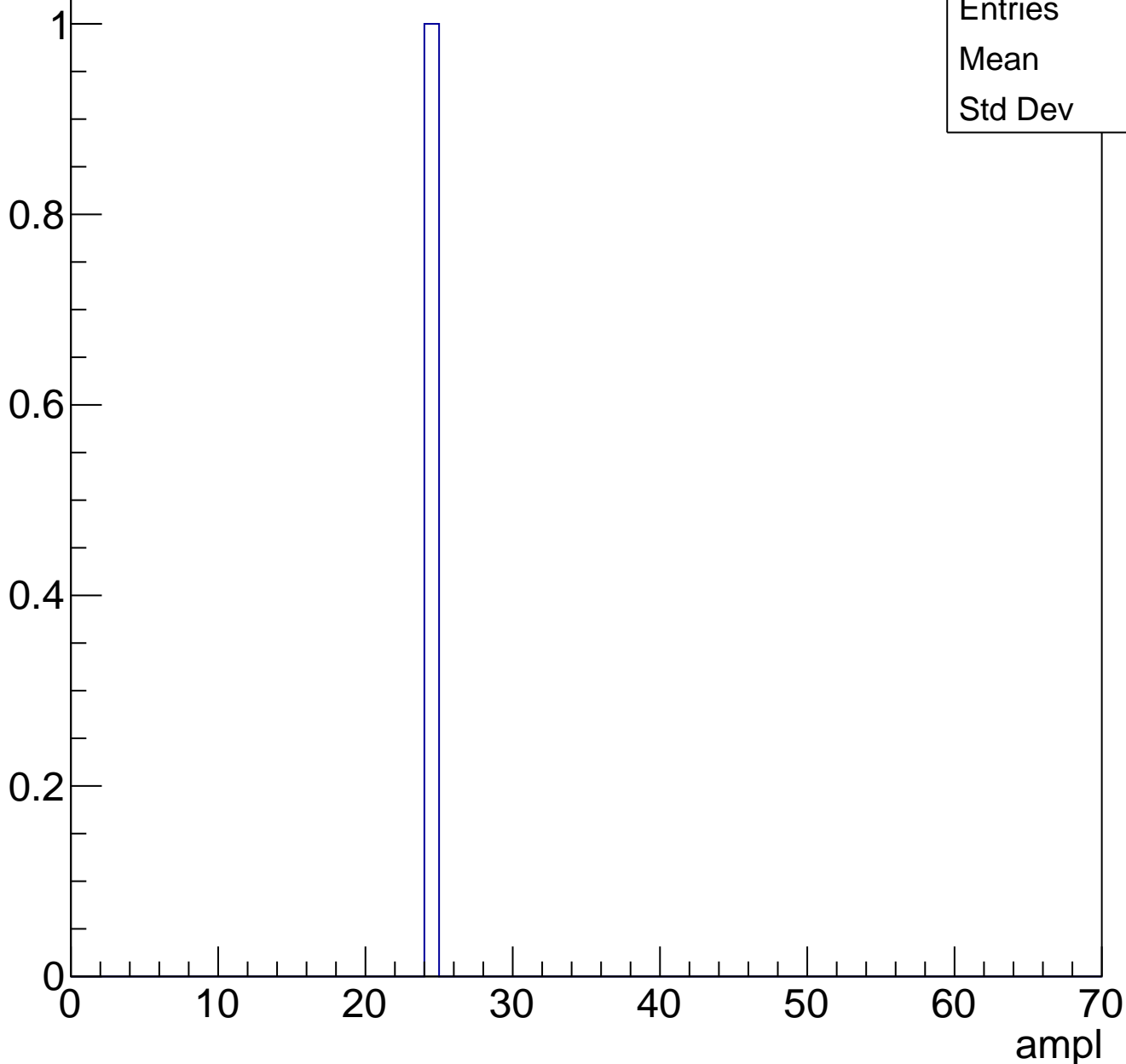
Entry



B1L003S, U20-ch25, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch26, adc0

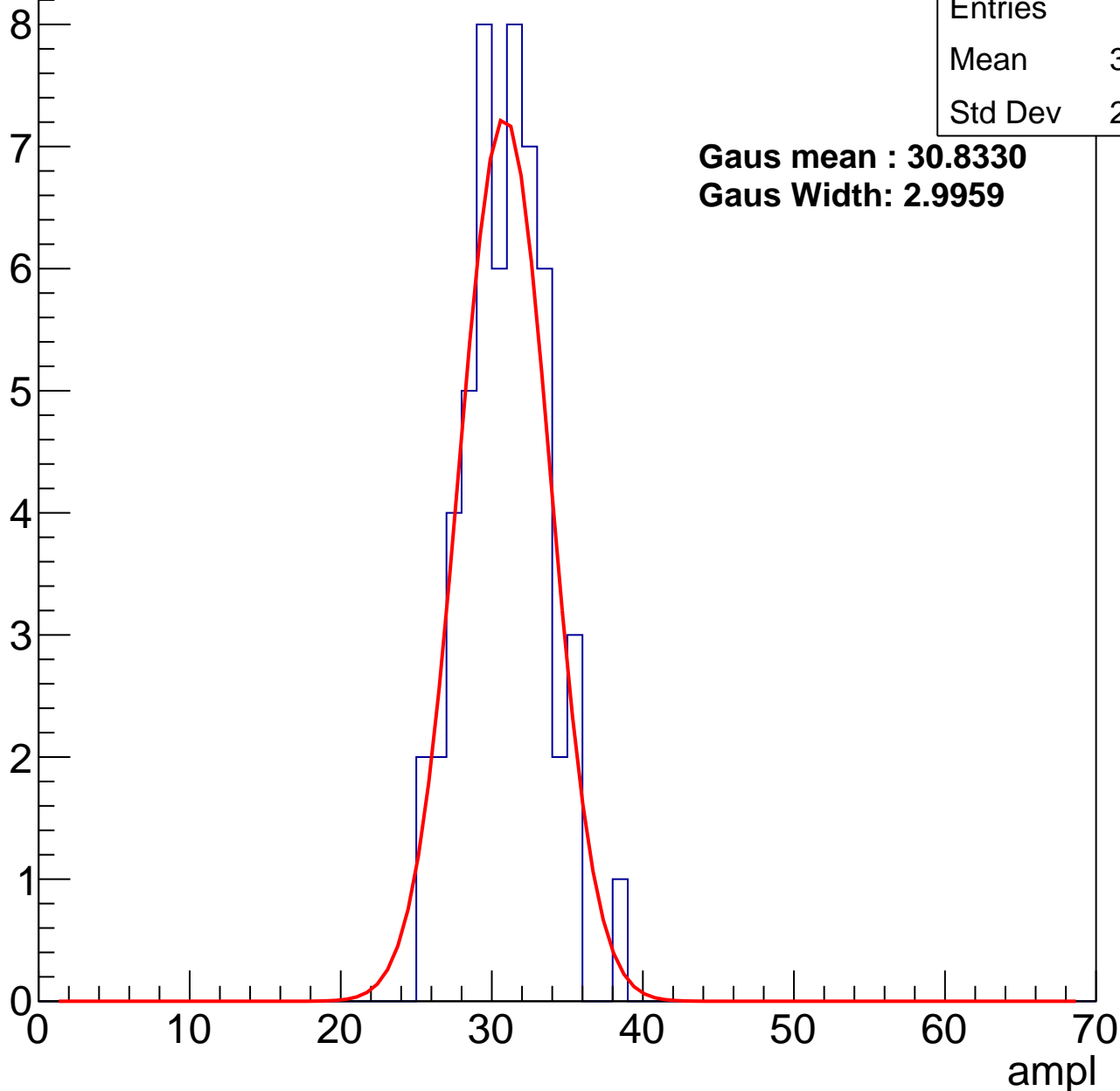
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	30.43
Std Dev	2.712

Gaus mean : 30.8330

Gaus Width: 2.9959

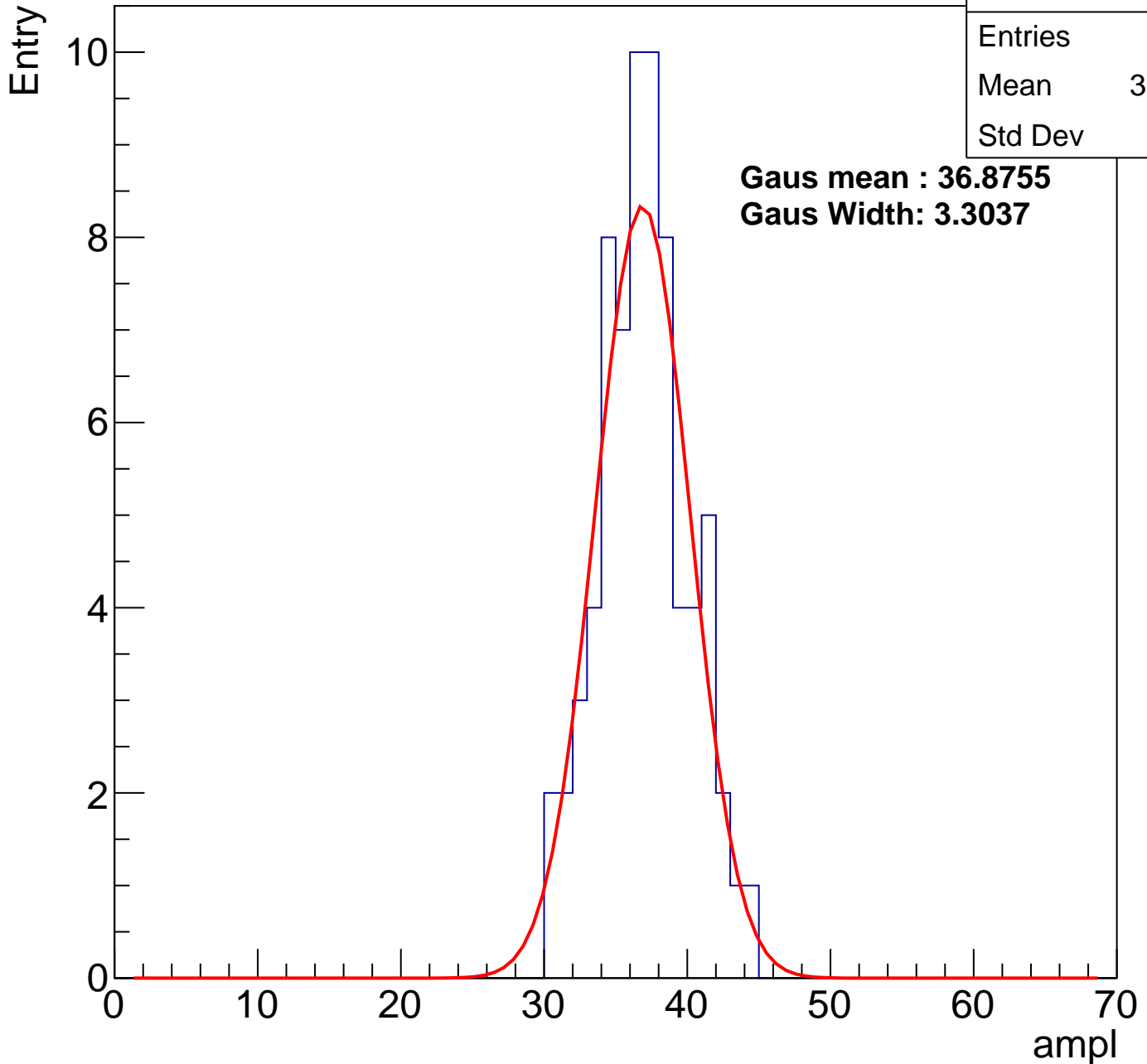


B1L003S, U20-ch26, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	36.52
Std Dev	3.09

Gaus mean : 36.8755
Gaus Width: 3.3037



B1L003S, U20-ch26, adc2

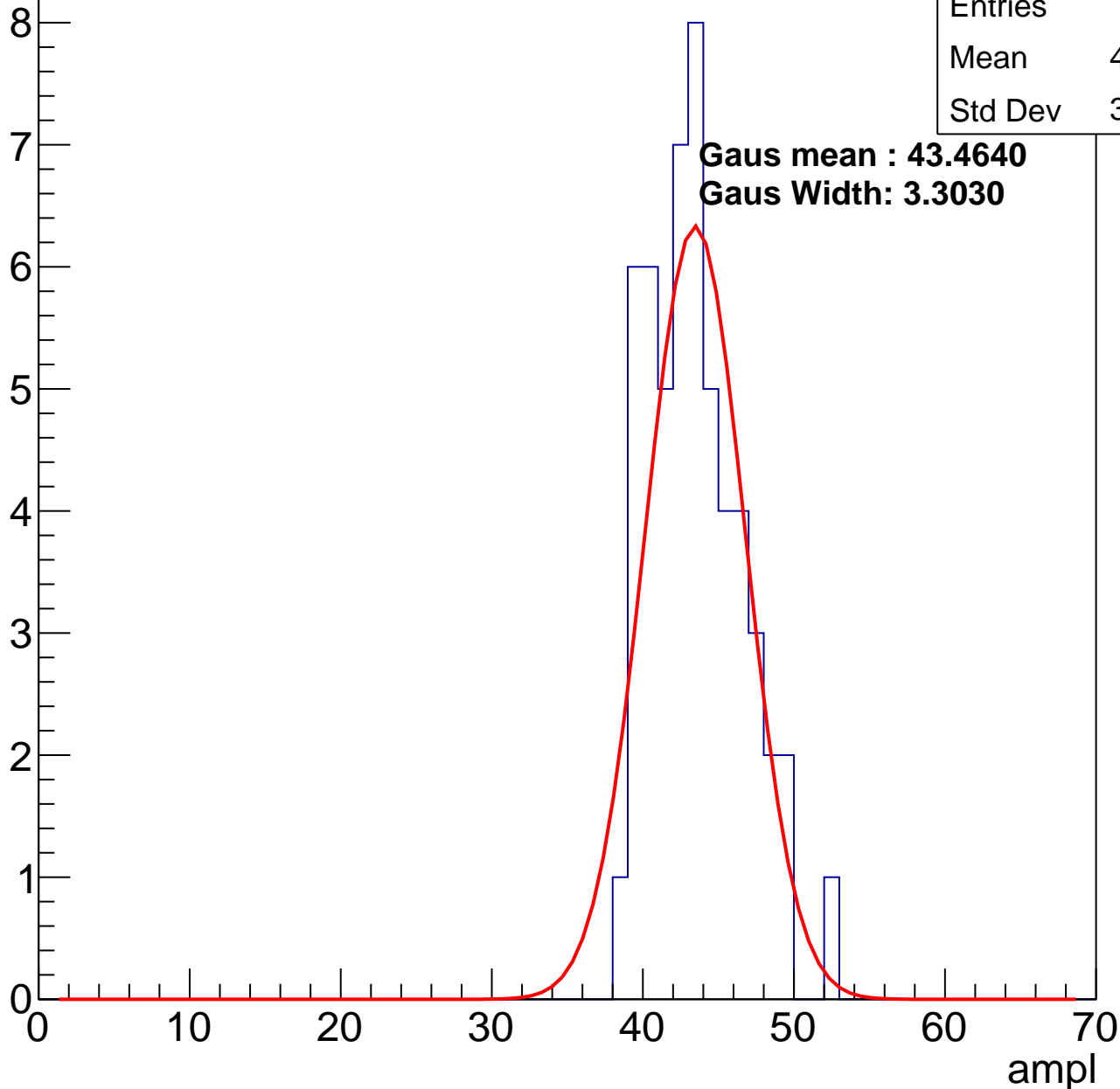
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.07
Std Dev	3.072

Gaus mean : 43.4640

Gaus Width: 3.3030

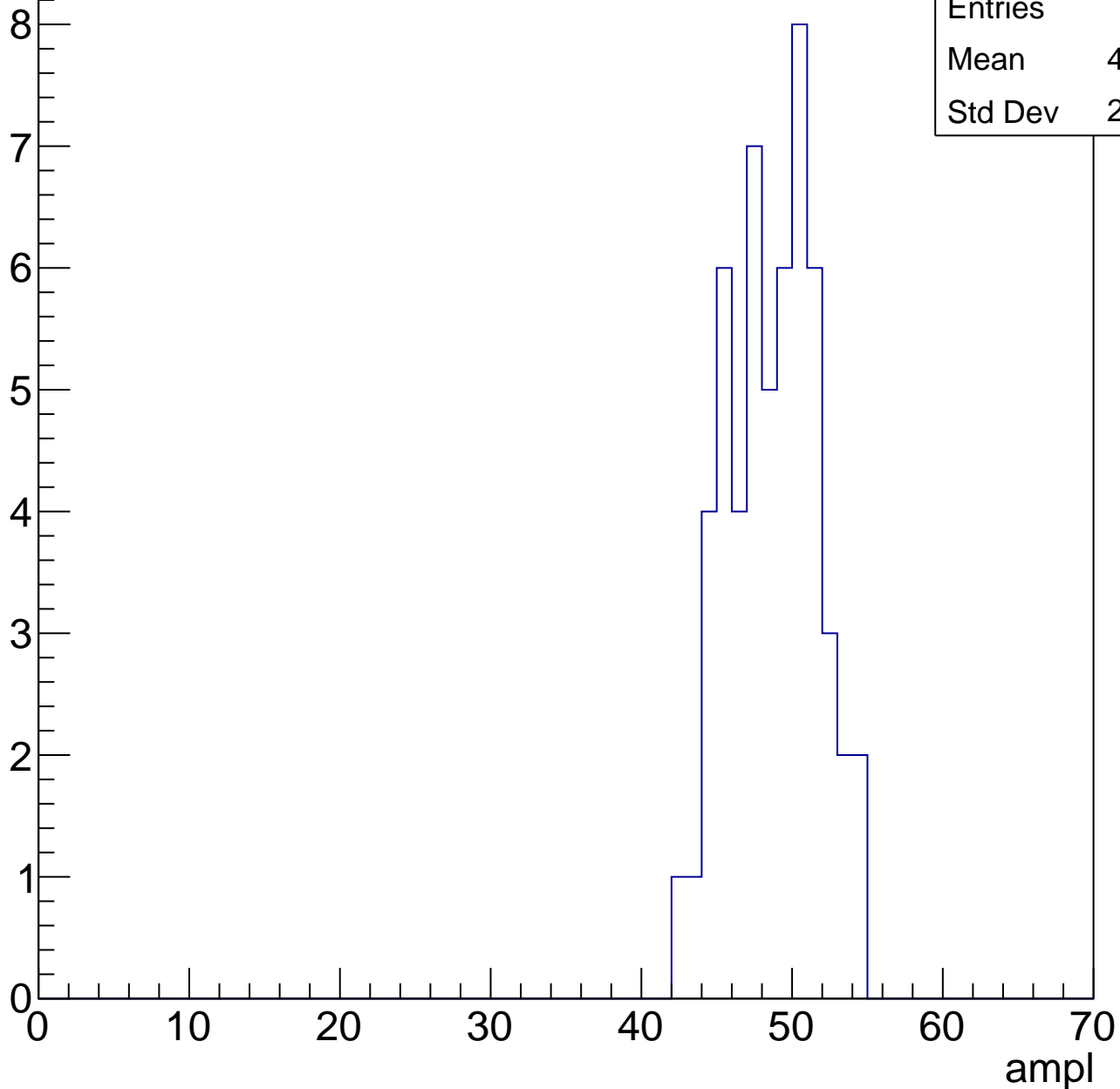


B1L003S, U20-ch26, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	48.25
Std Dev	2.893

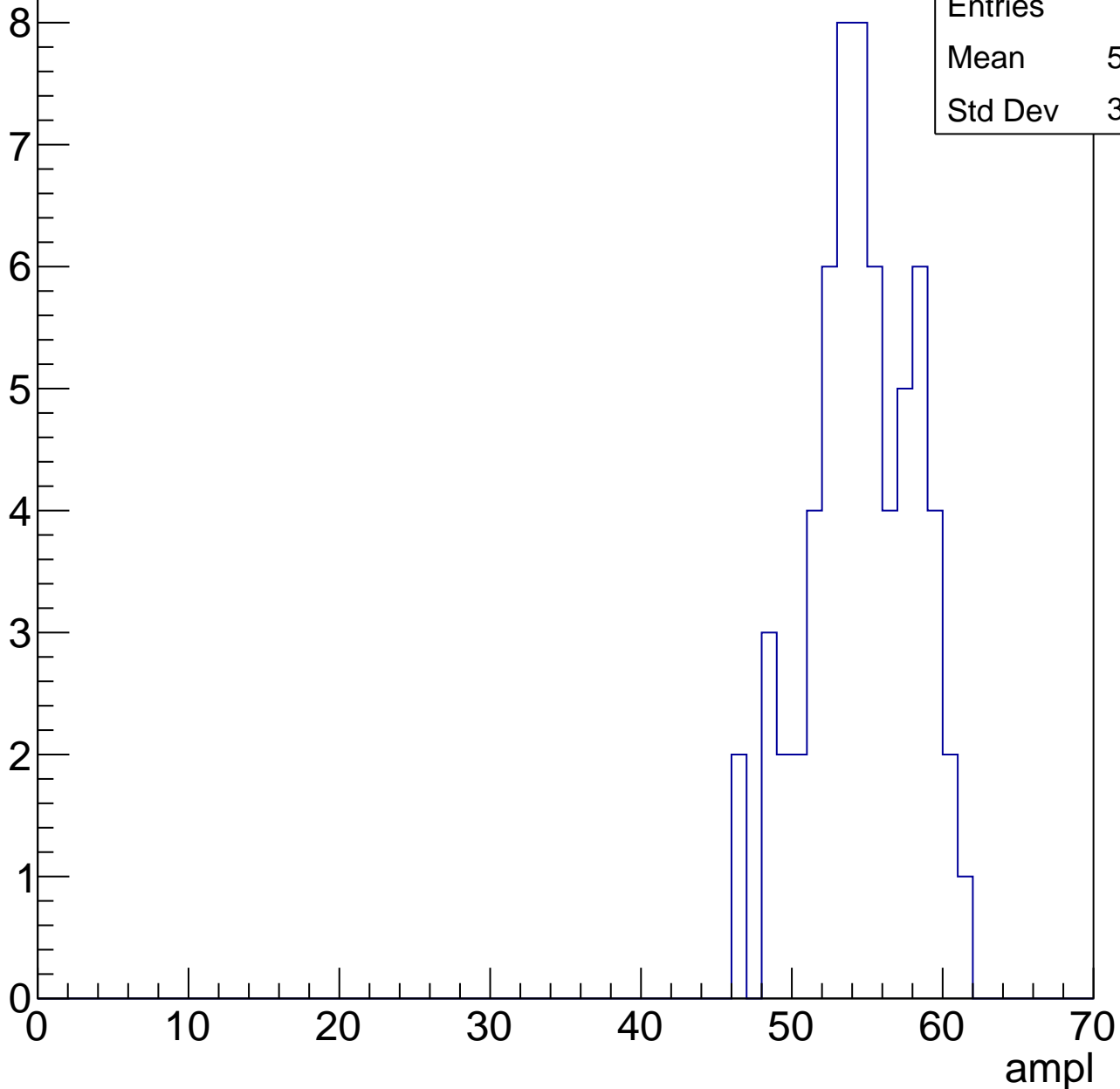


B1L003S, U20-ch26, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	54.13
Std Dev	3.494

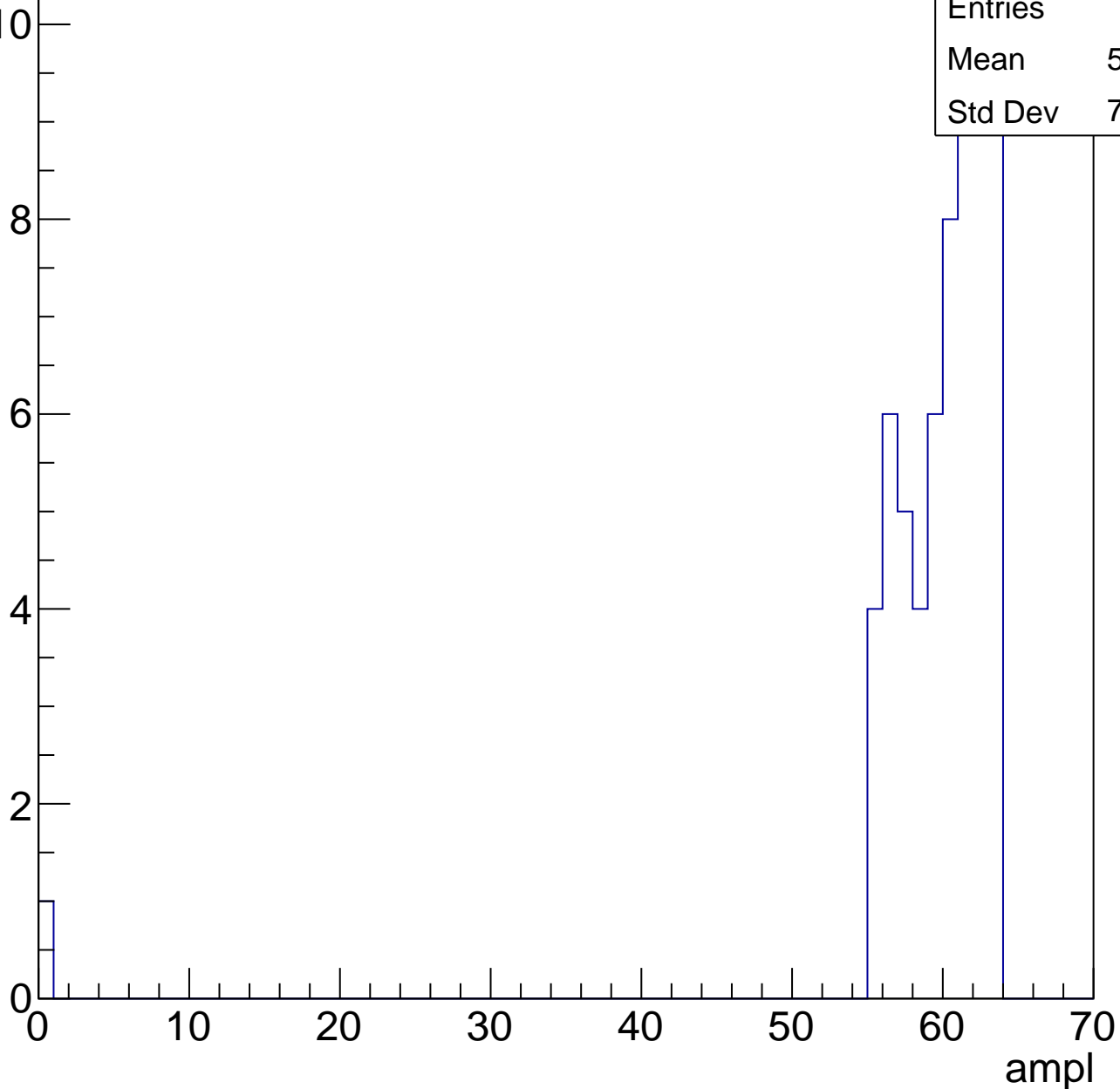


B1L003S, U20-ch26, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	58.76
Std Dev	7.928



B1L003S, U20-ch26, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch26, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch27, adc0

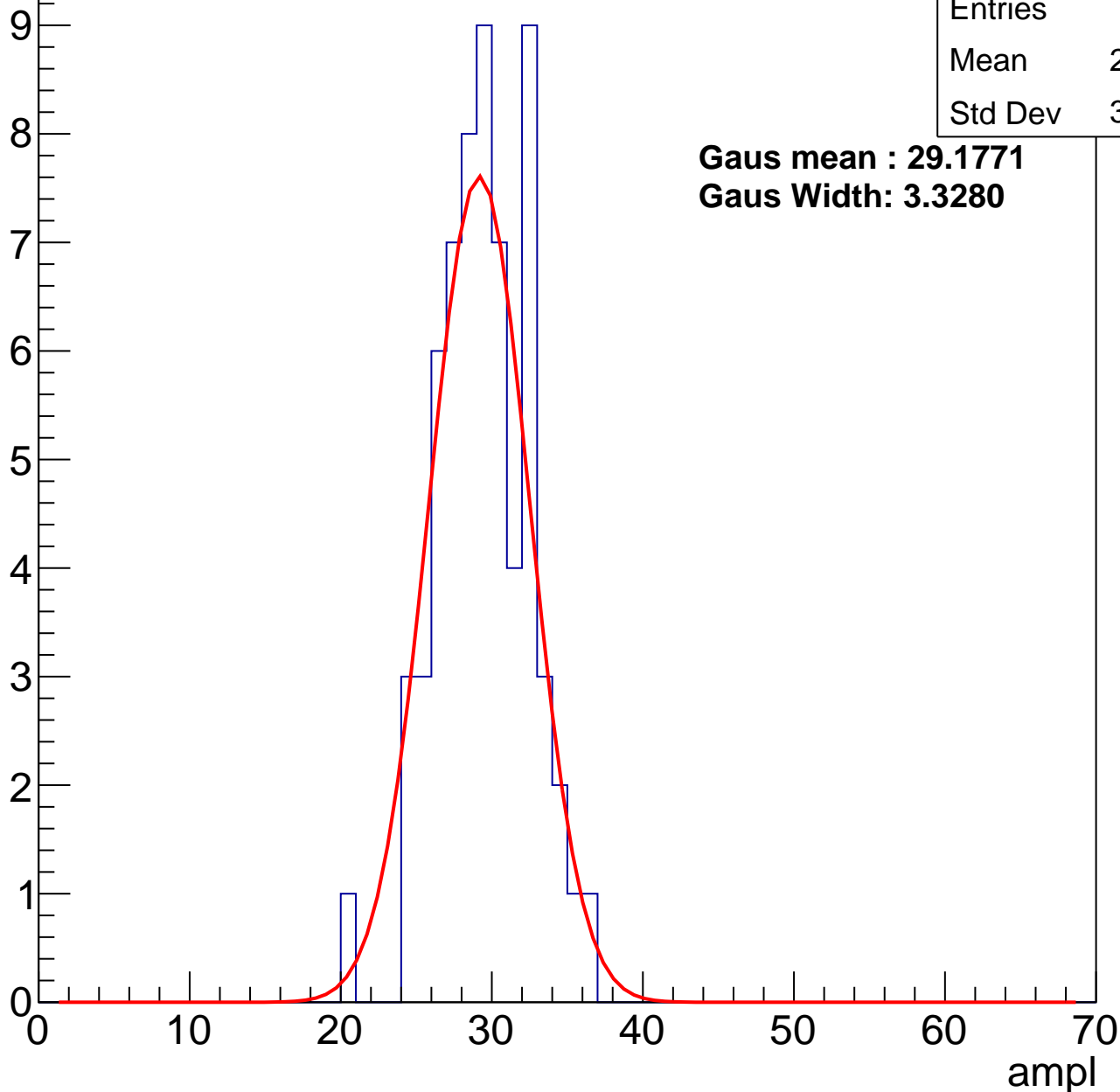
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.02
Std Dev	3.013

Gaus mean : 29.1771

Gaus Width: 3.3280



B1L003S, U20-ch27, adc1

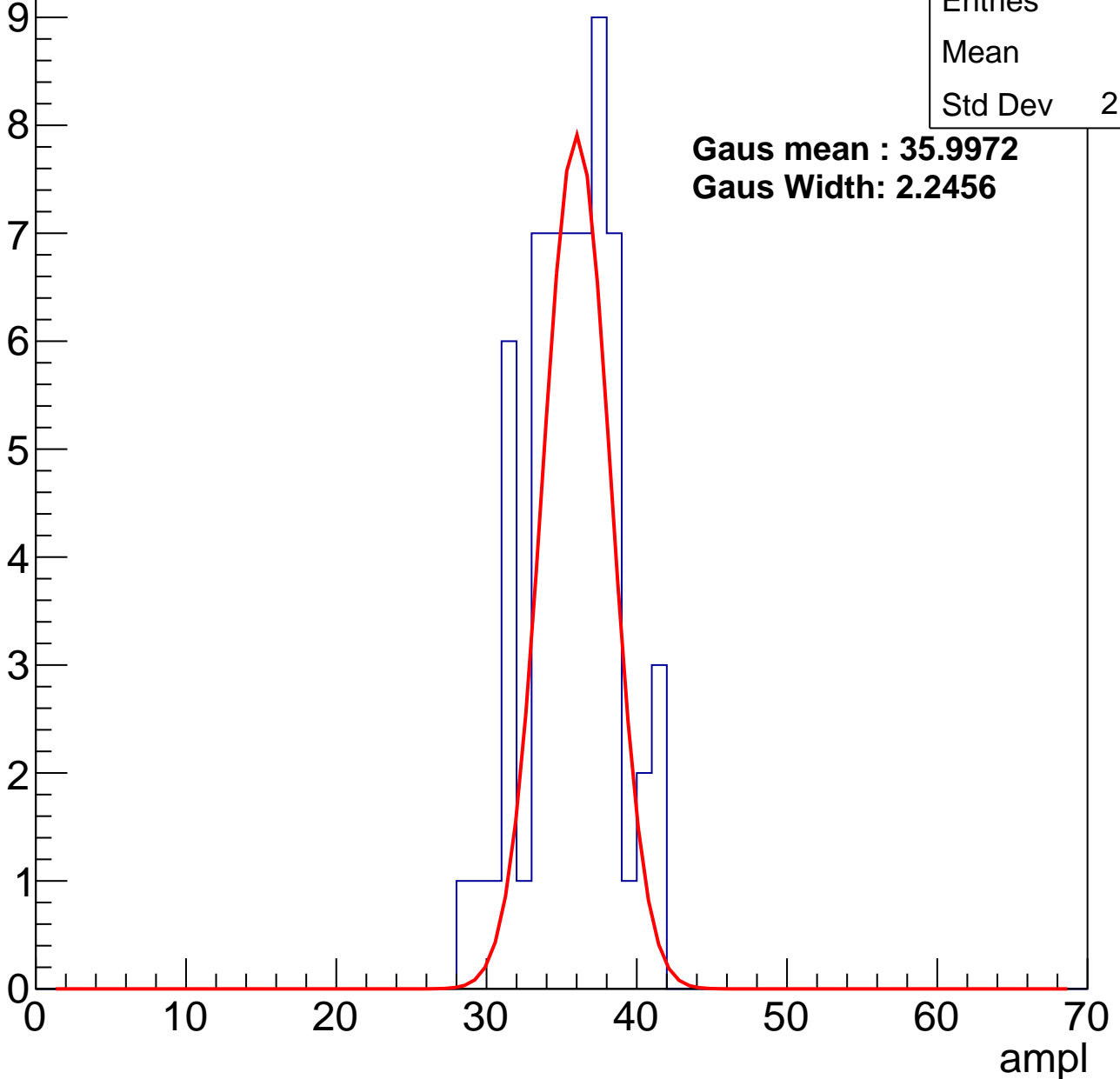
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.2
Std Dev	2.965

Gaus mean : 35.9972

Gaus Width: 2.2456



B1L003S, U20-ch27, adc2

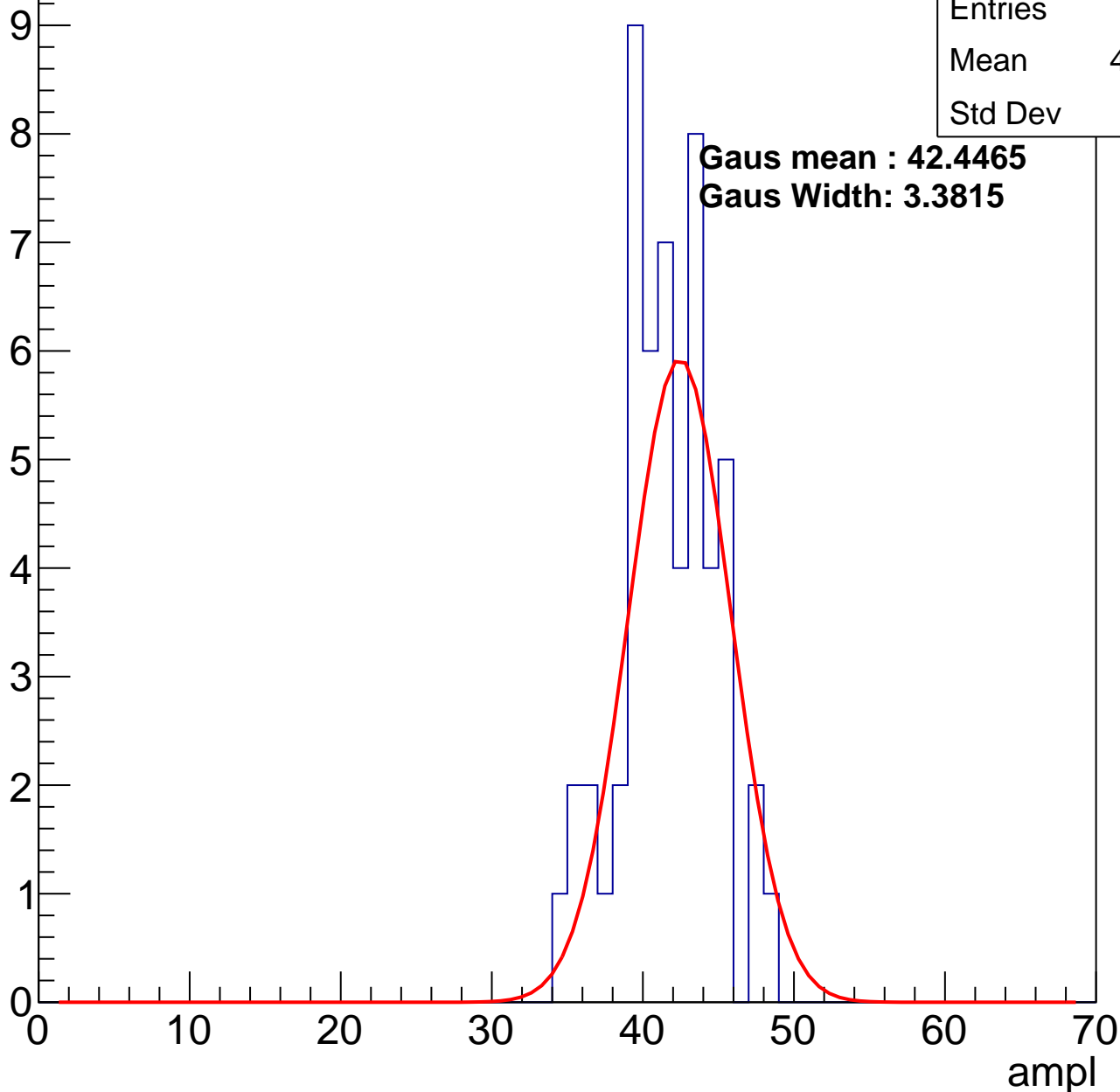
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.15
Std Dev	3.1

Gaus mean : 42.4465

Gaus Width: 3.3815

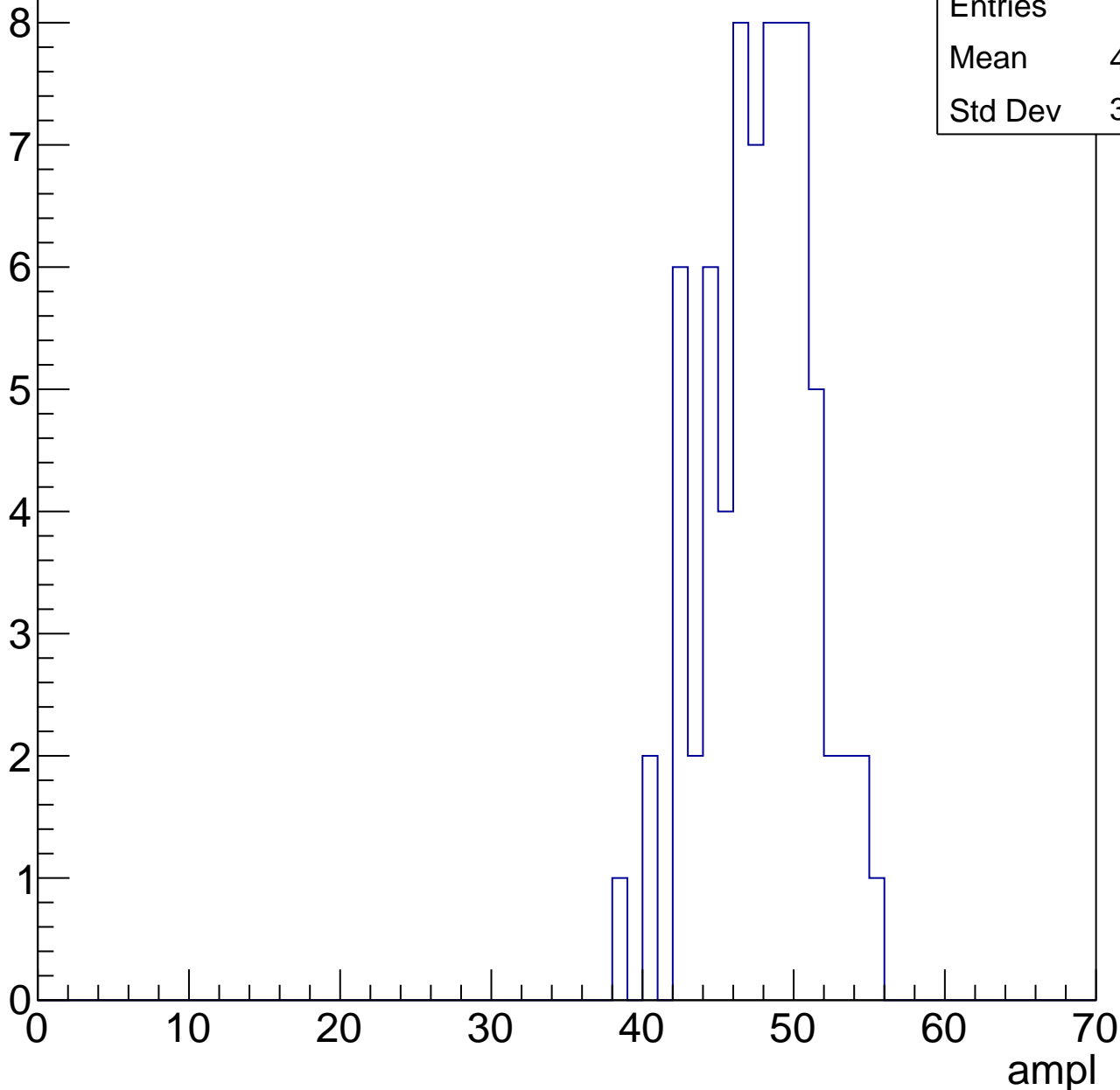


B1L003S, U20-ch27, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	47.24
Std Dev	3.557

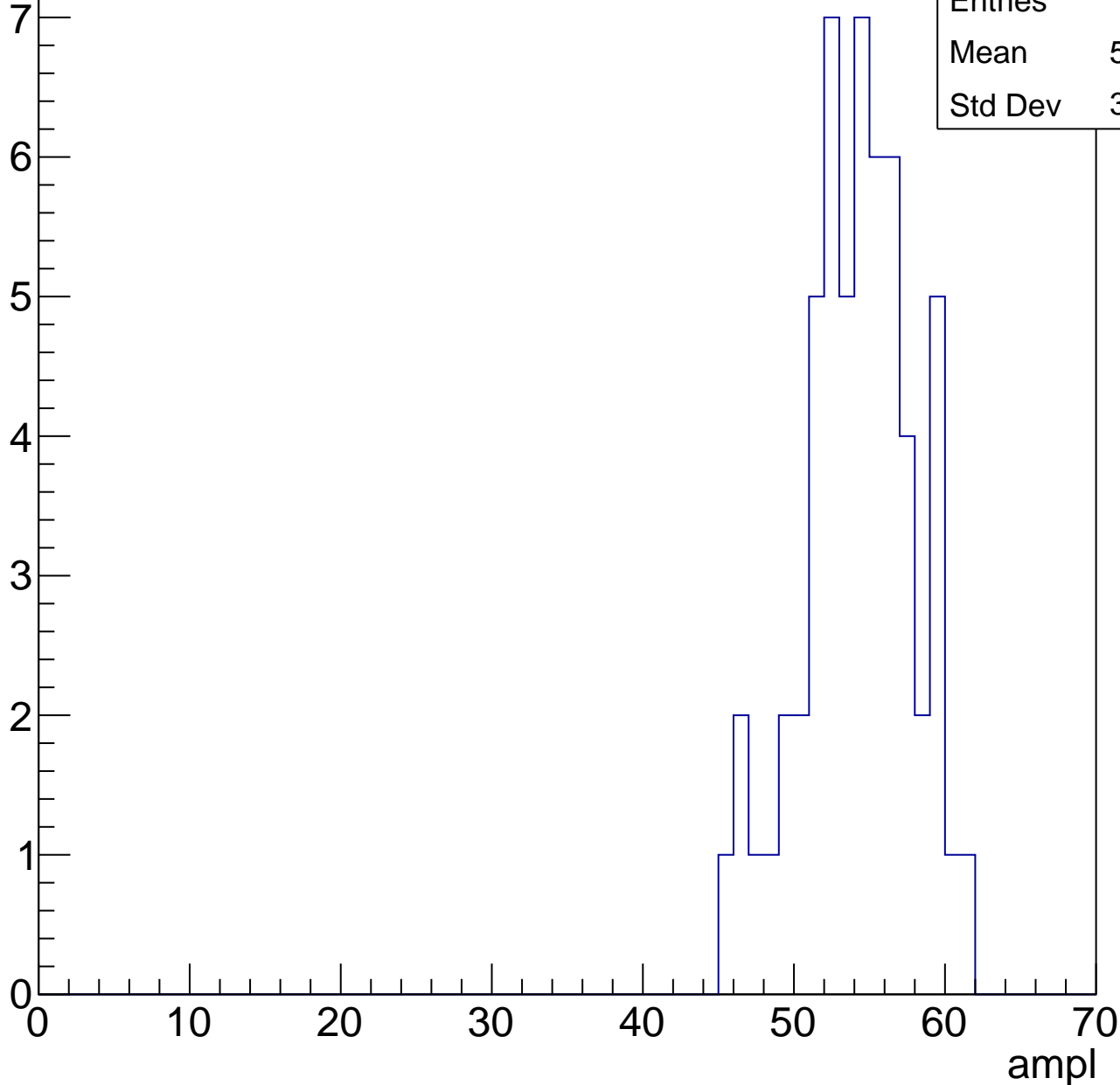


B1L003S, U20-ch27, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.76
Std Dev	3.617

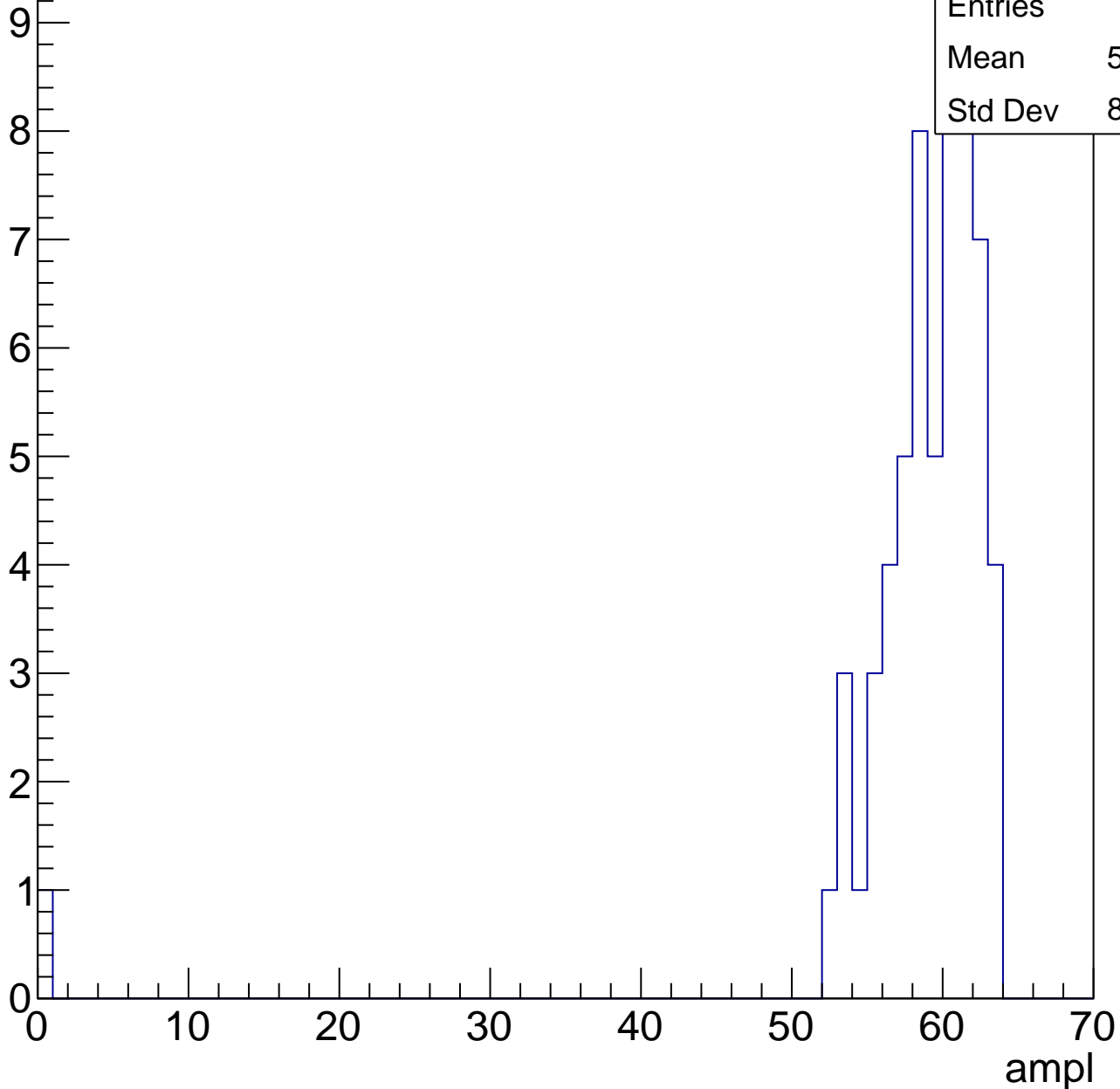


B1L003S, U20-ch27, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.85
Std Dev	8.096

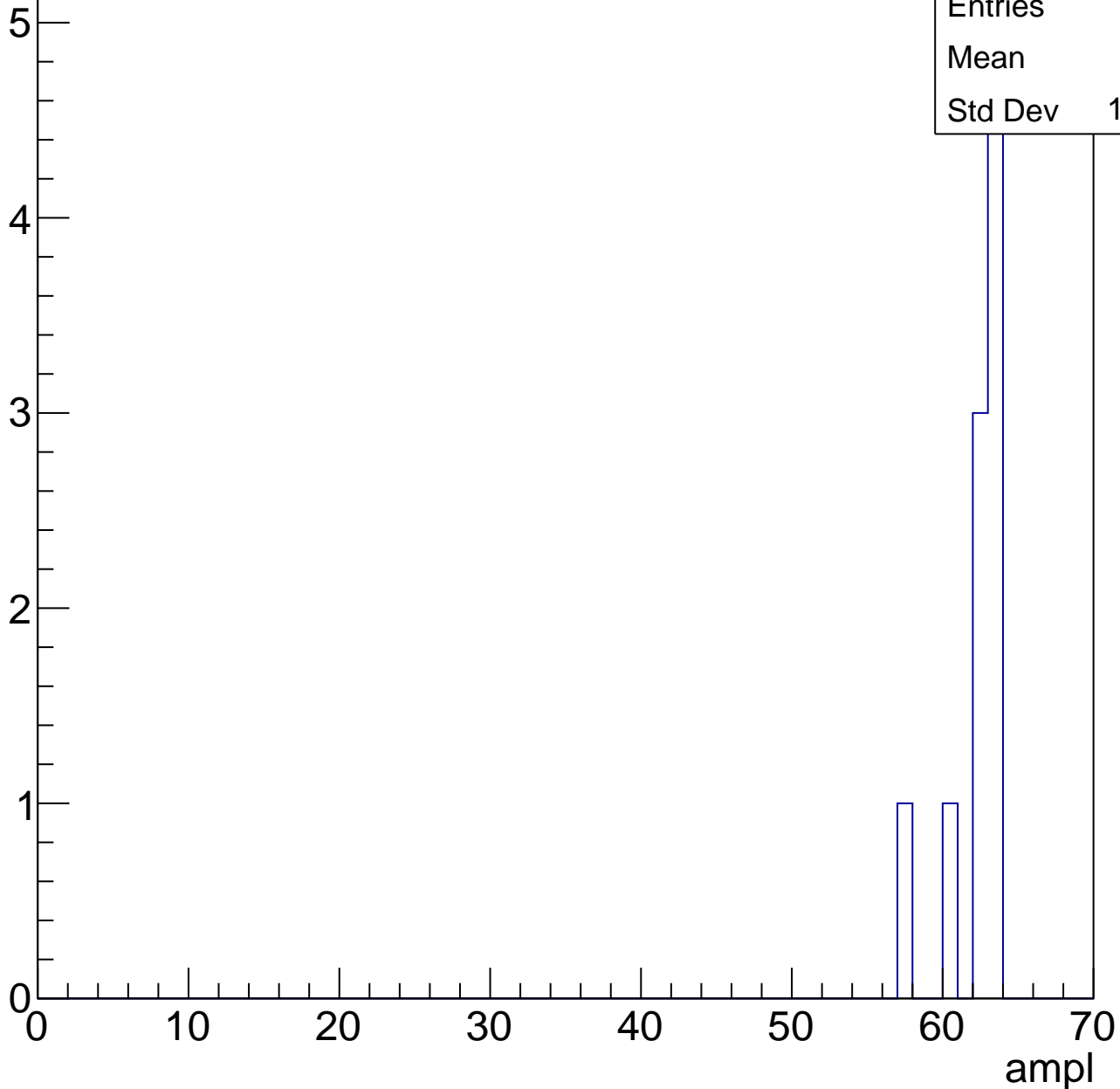


B1L003S, U20-ch27, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.8
Std Dev	1.833



B1L003S, U20-ch27, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch28, adc0

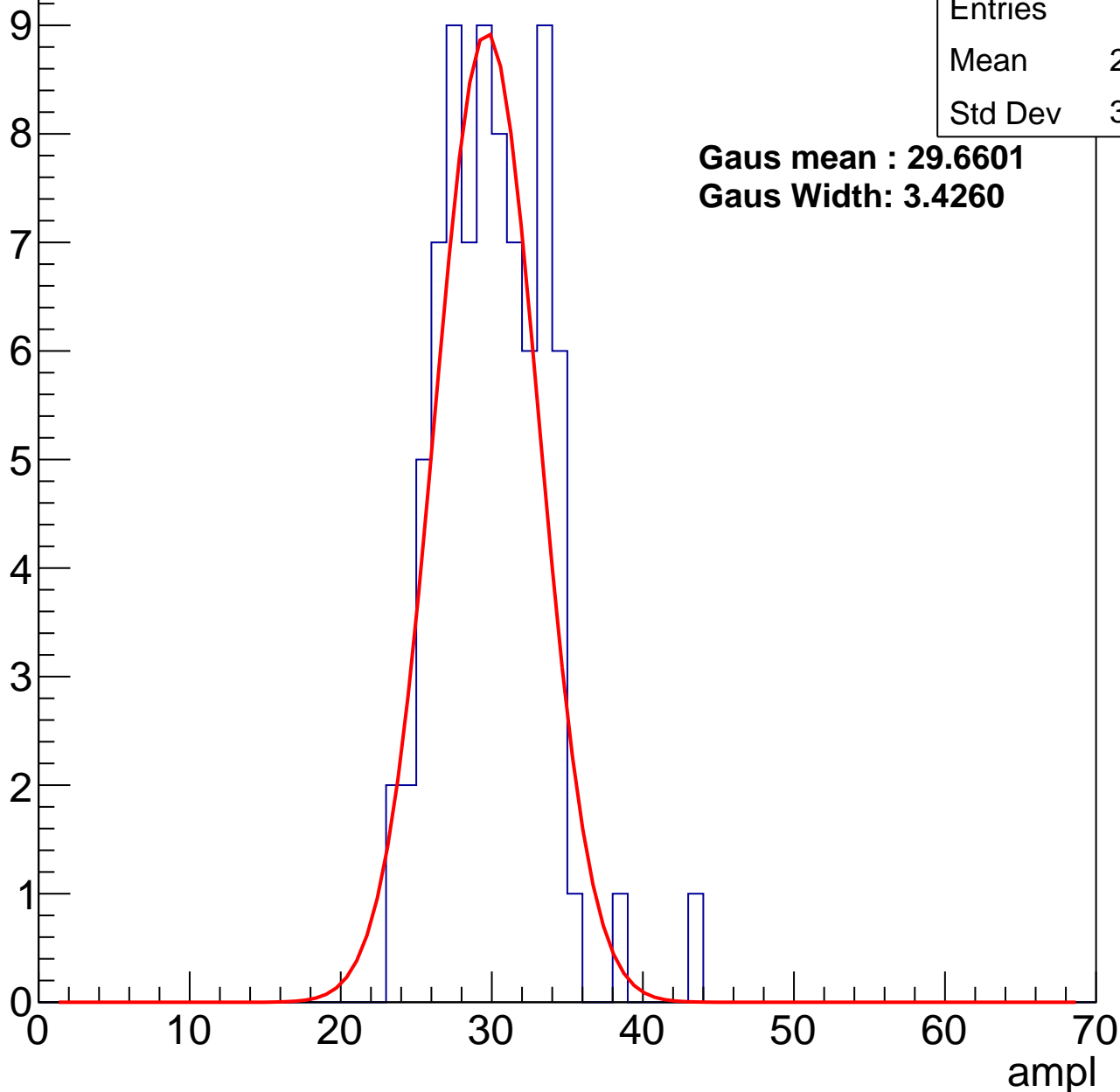
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.59
Std Dev	3.495

Gaus mean : 29.6601

Gaus Width: 3.4260



B1L003S, U20-ch28, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	36.54
Std Dev	3.017

Gaus mean : 37.5981

Gaus Width: 2.8842

Entry

10

8

6

4

2

0

0

10

20

30

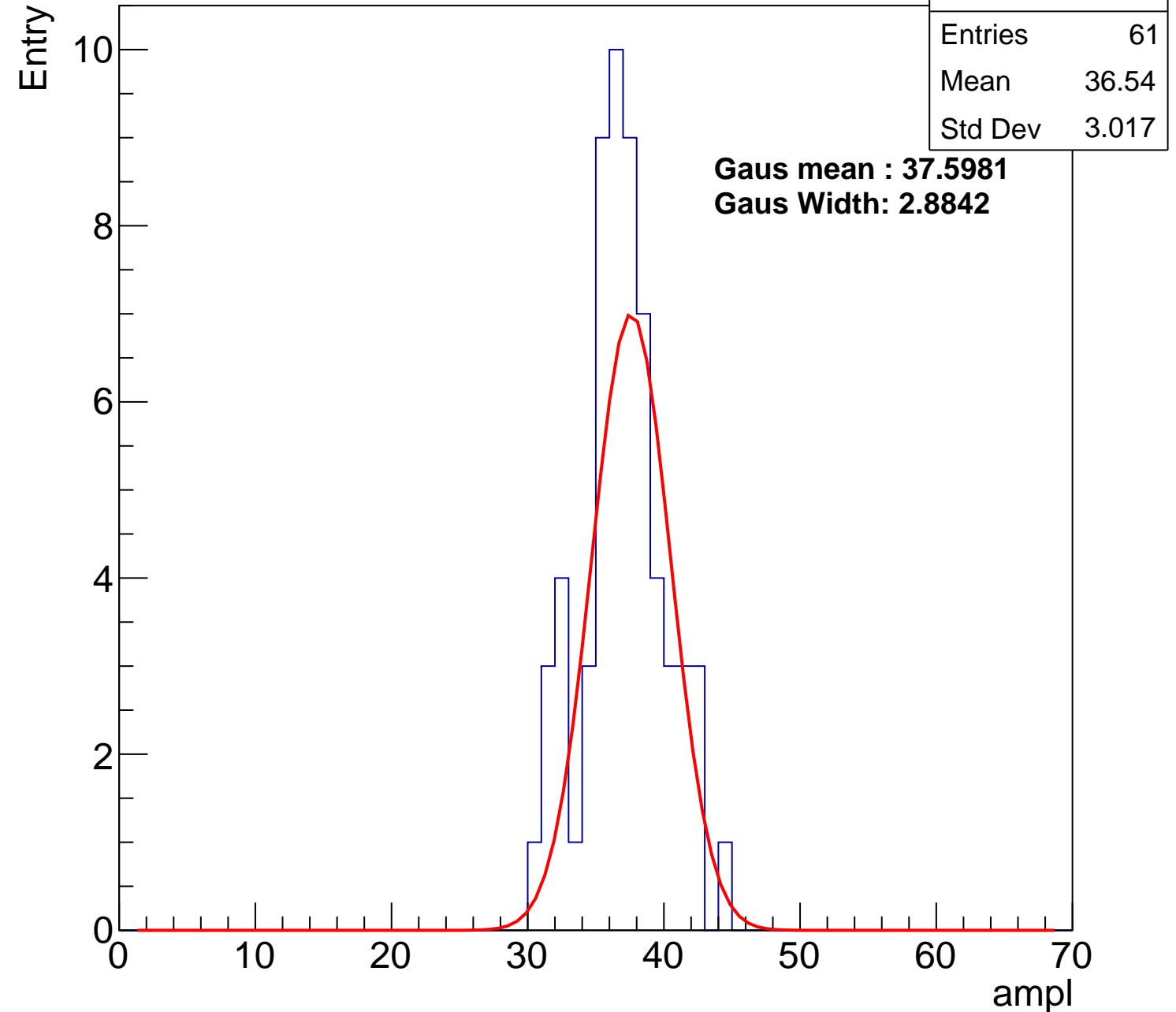
40

50

60

70

ampl



B1L003S, U20-ch28, adc2

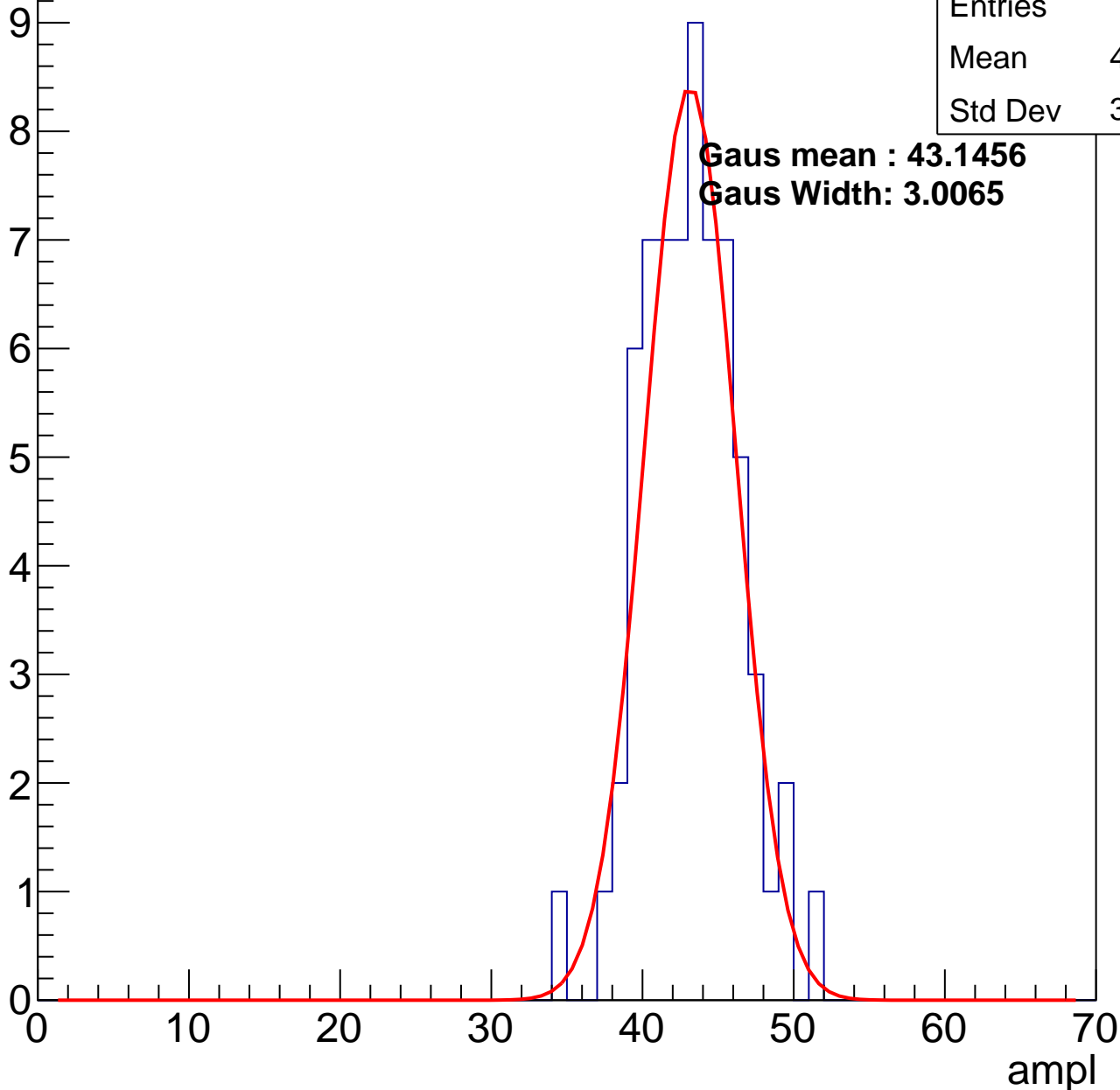
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.73
Std Dev	3.126

Gaus mean : 43.1456

Gaus Width: 3.0065

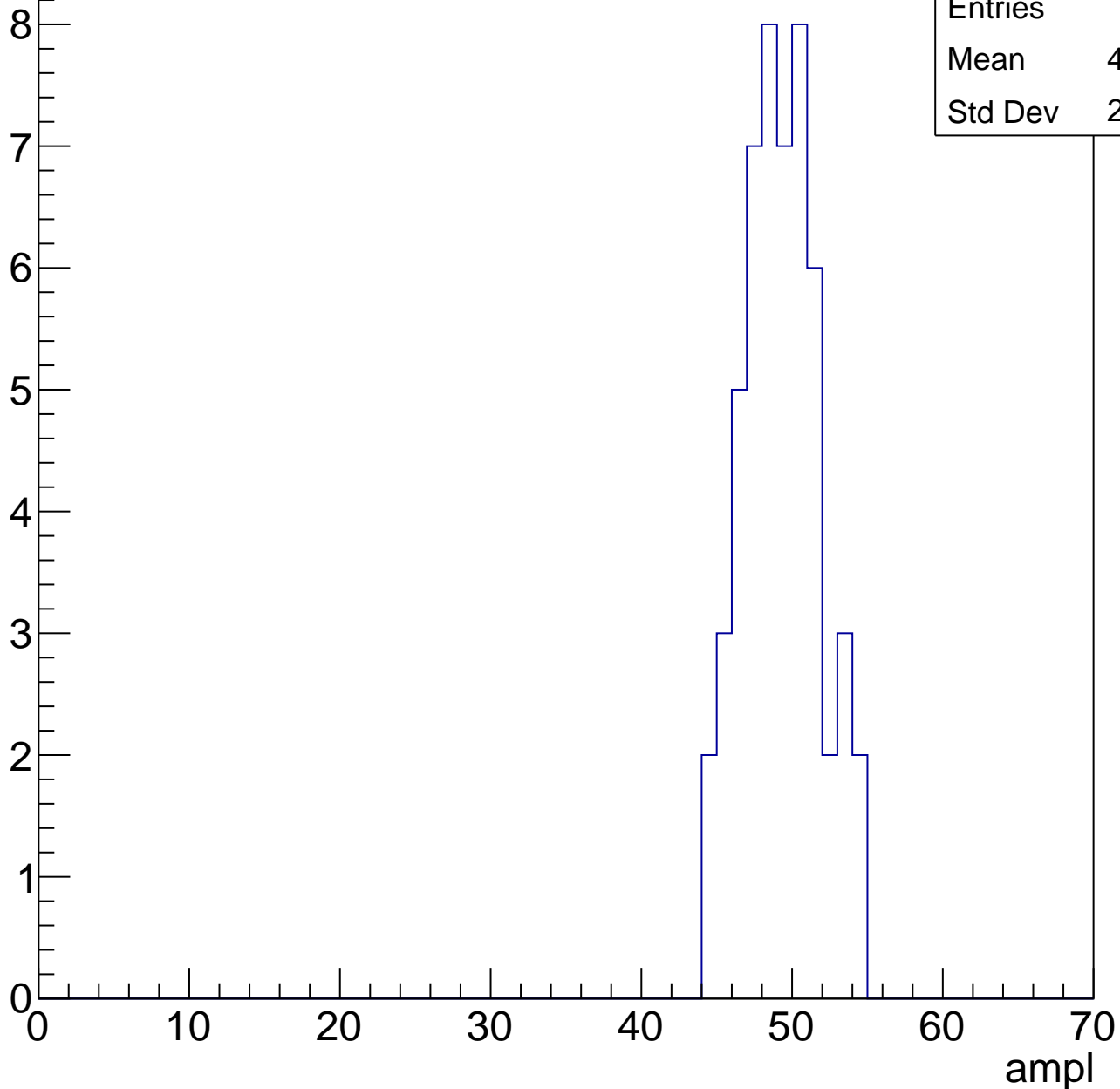


B1L003S, U20-ch28, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	48.79
Std Dev	2.475



B1L003S, U20-ch28, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	54.77
Std Dev	2.871

Entry

10

8

6

4

2

0

0

10

20

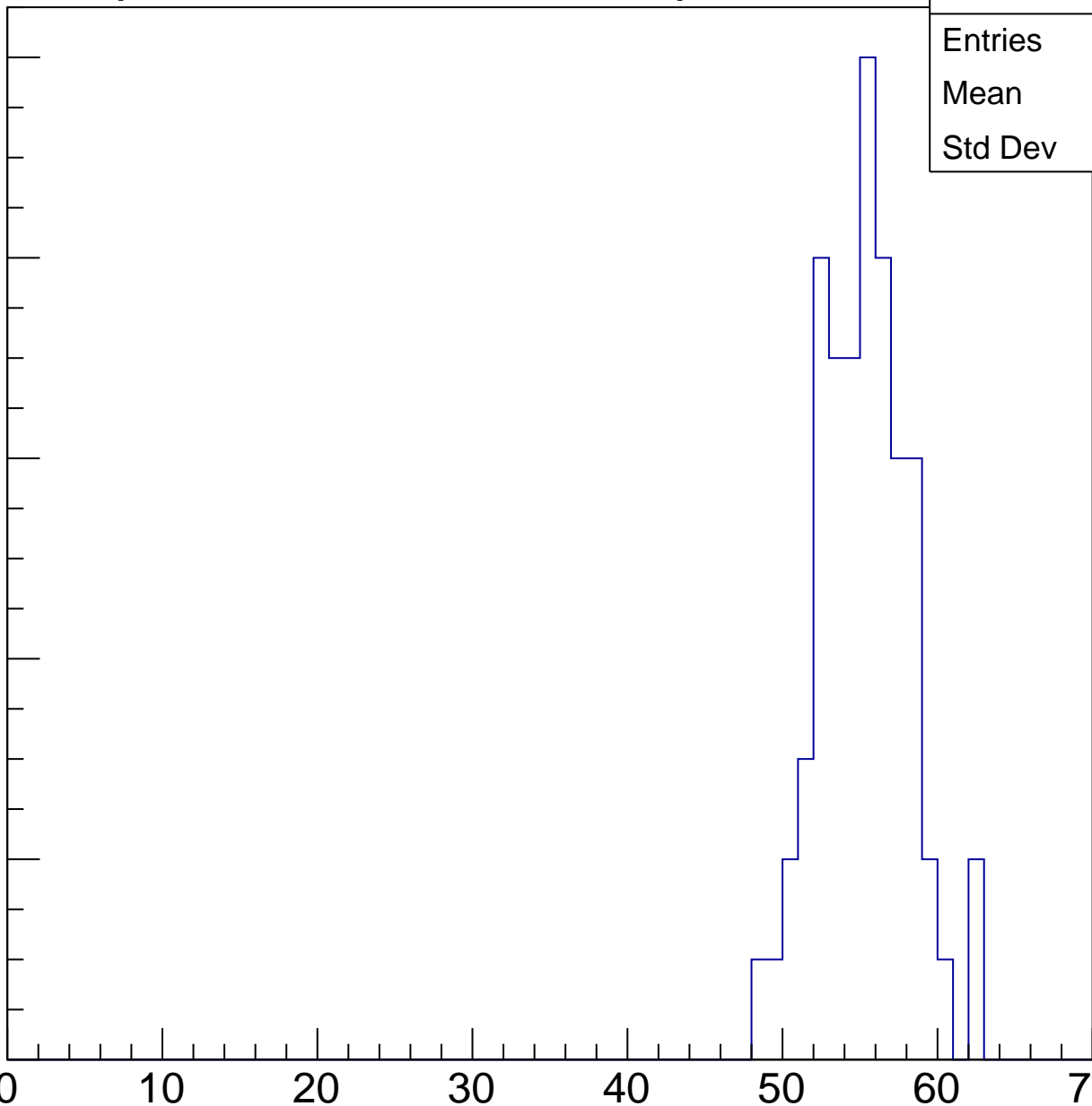
30

40

50

60

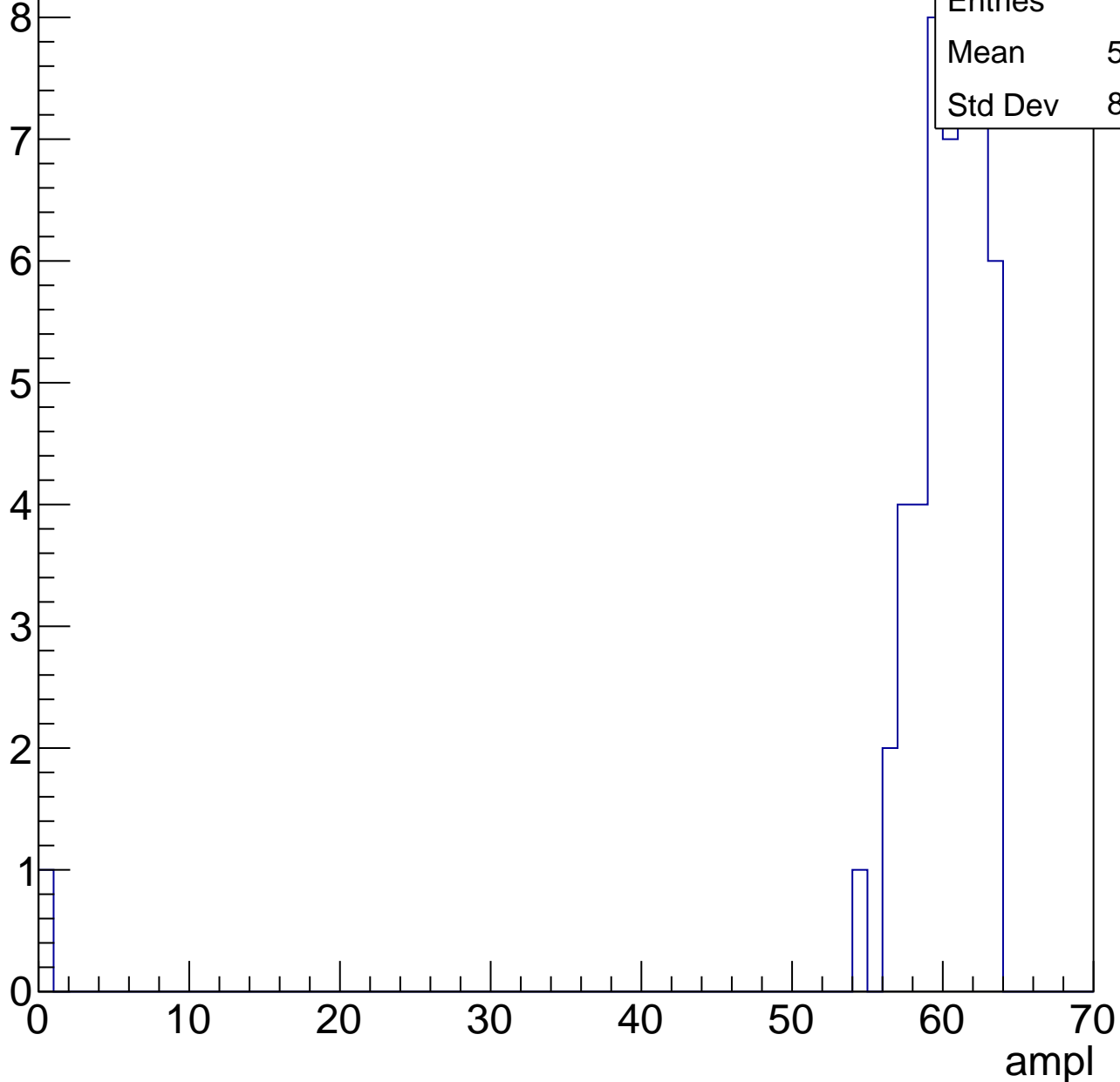
ampl



B1L003S, U20-ch28, adc5

calib_packv5_042523_0143.root, FC#13, port D2

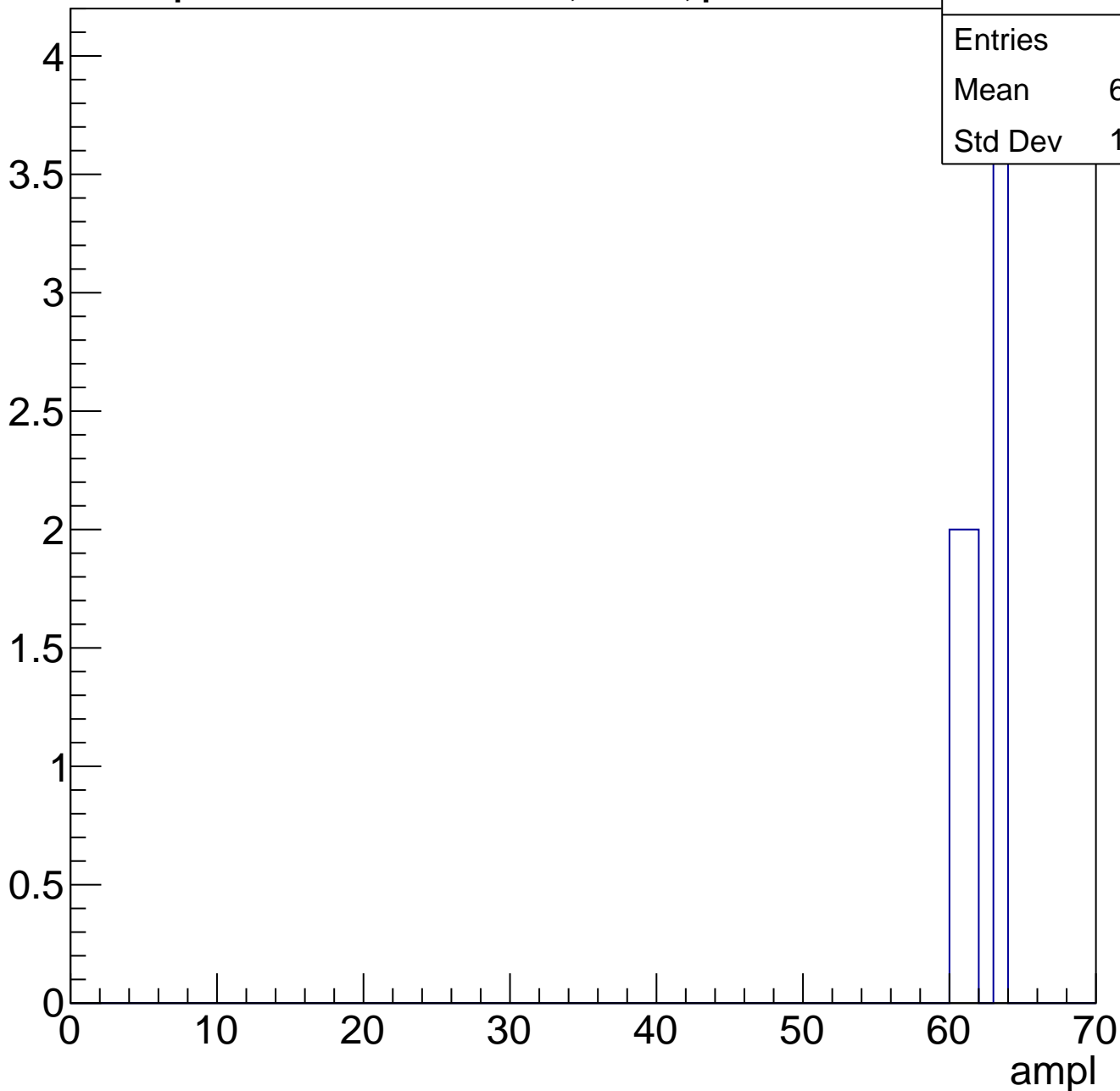
Entry



B1L003S, U20-ch28, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch28, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch29, adc0

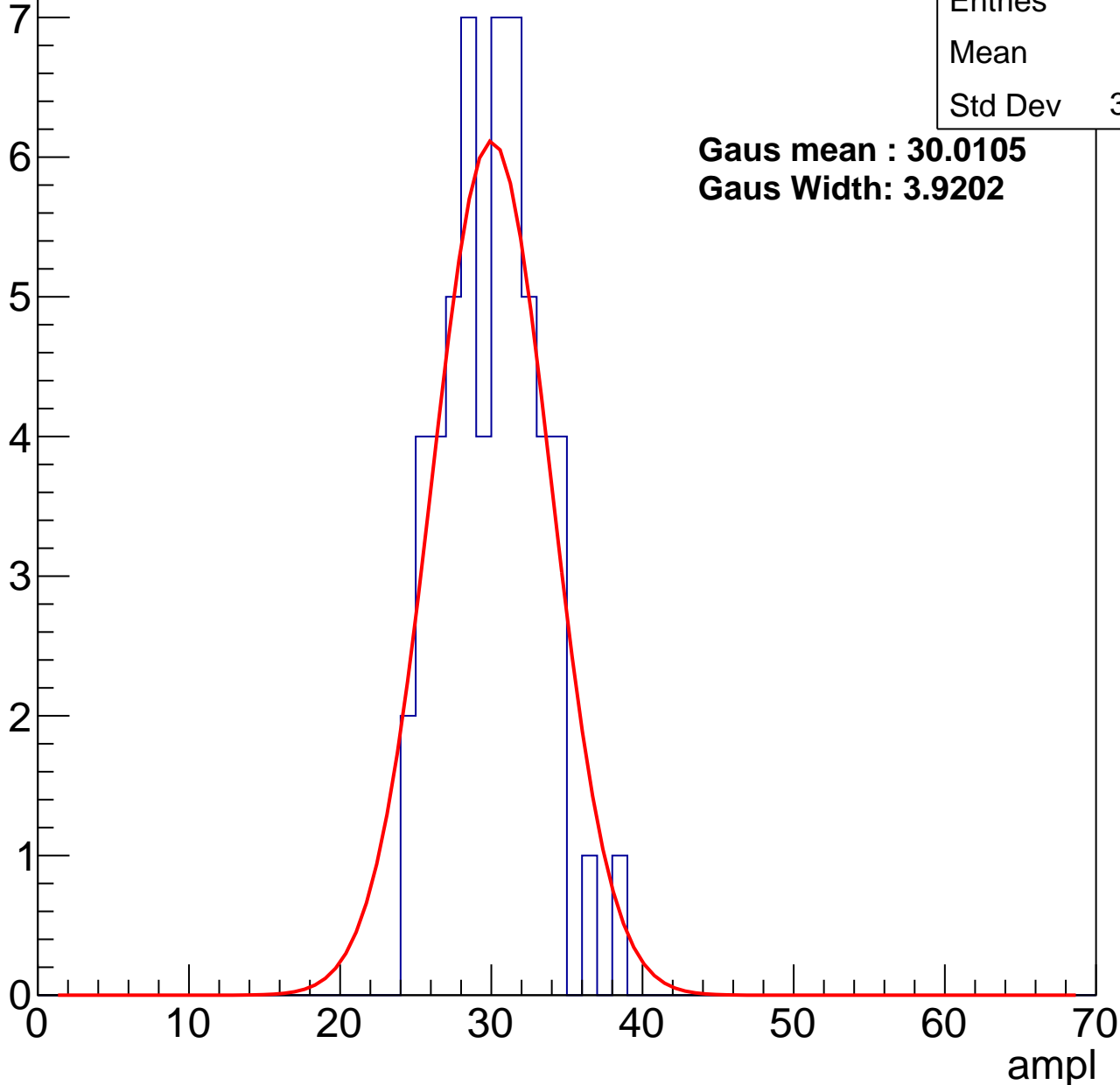
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.6
Std Dev	3.108

Gaus mean : 30.0105

Gaus Width: 3.9202



B1L003S, U20-ch29, adc1

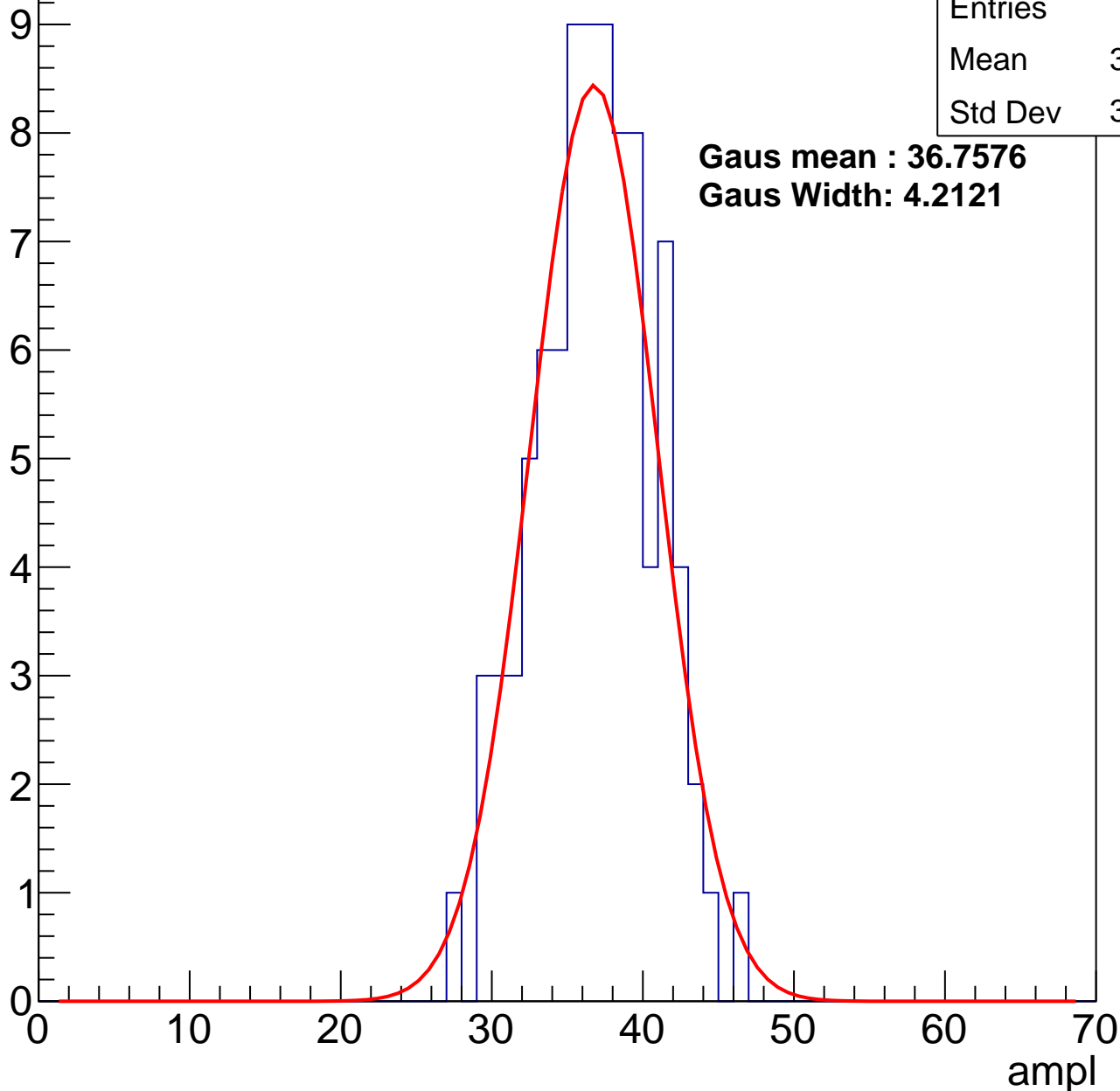
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	89
Mean	36.38
Std Dev	3.856

Gaus mean : 36.7576

Gaus Width: 4.2121



B1L003S, U20-ch29, adc2

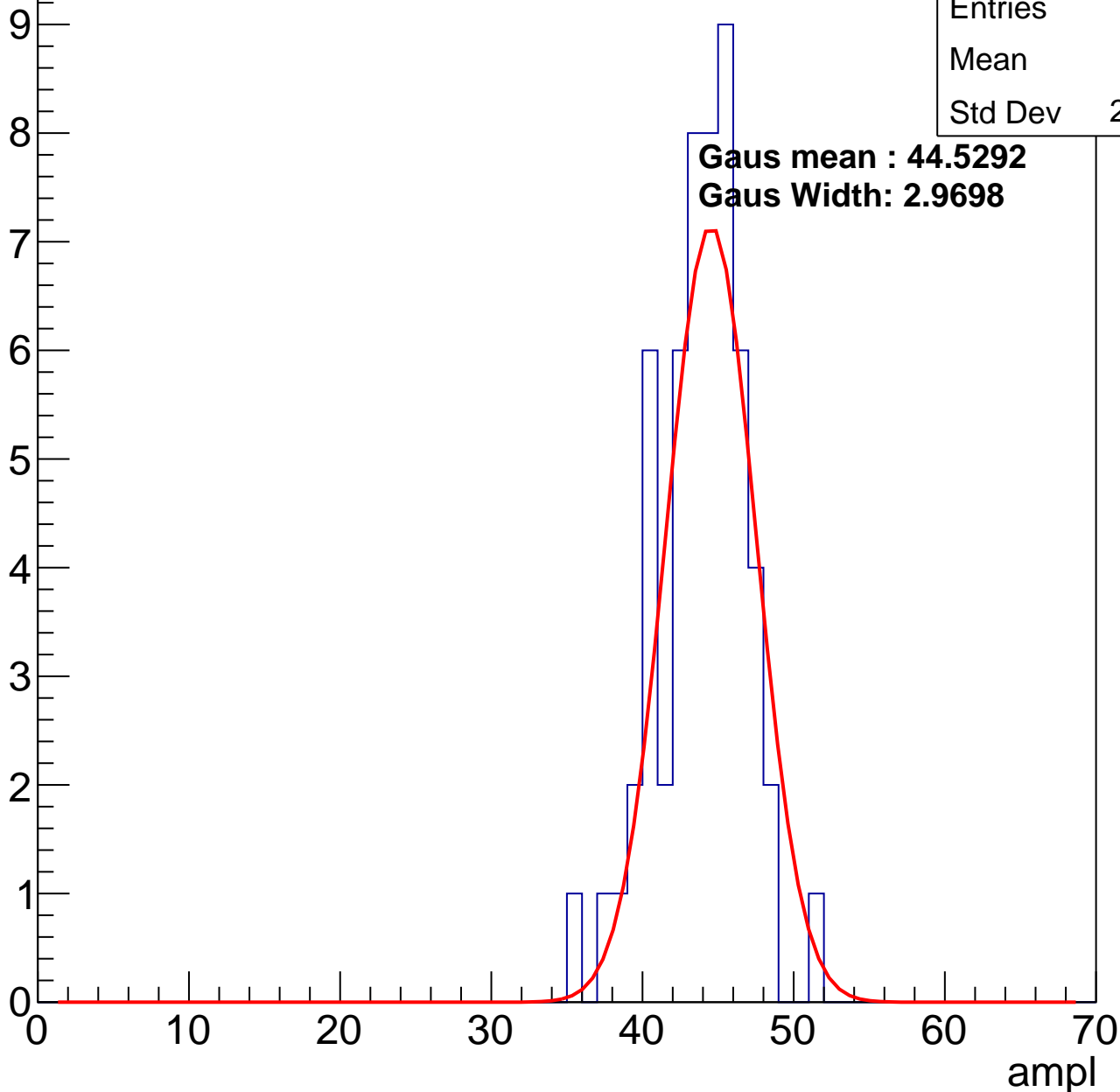
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.4
Std Dev	2.937

Gaus mean : 44.5292

Gaus Width: 2.9698



B1L003S, U20-ch29, adc3

calib_packv5_042523_0143.root, FC#13, port D2

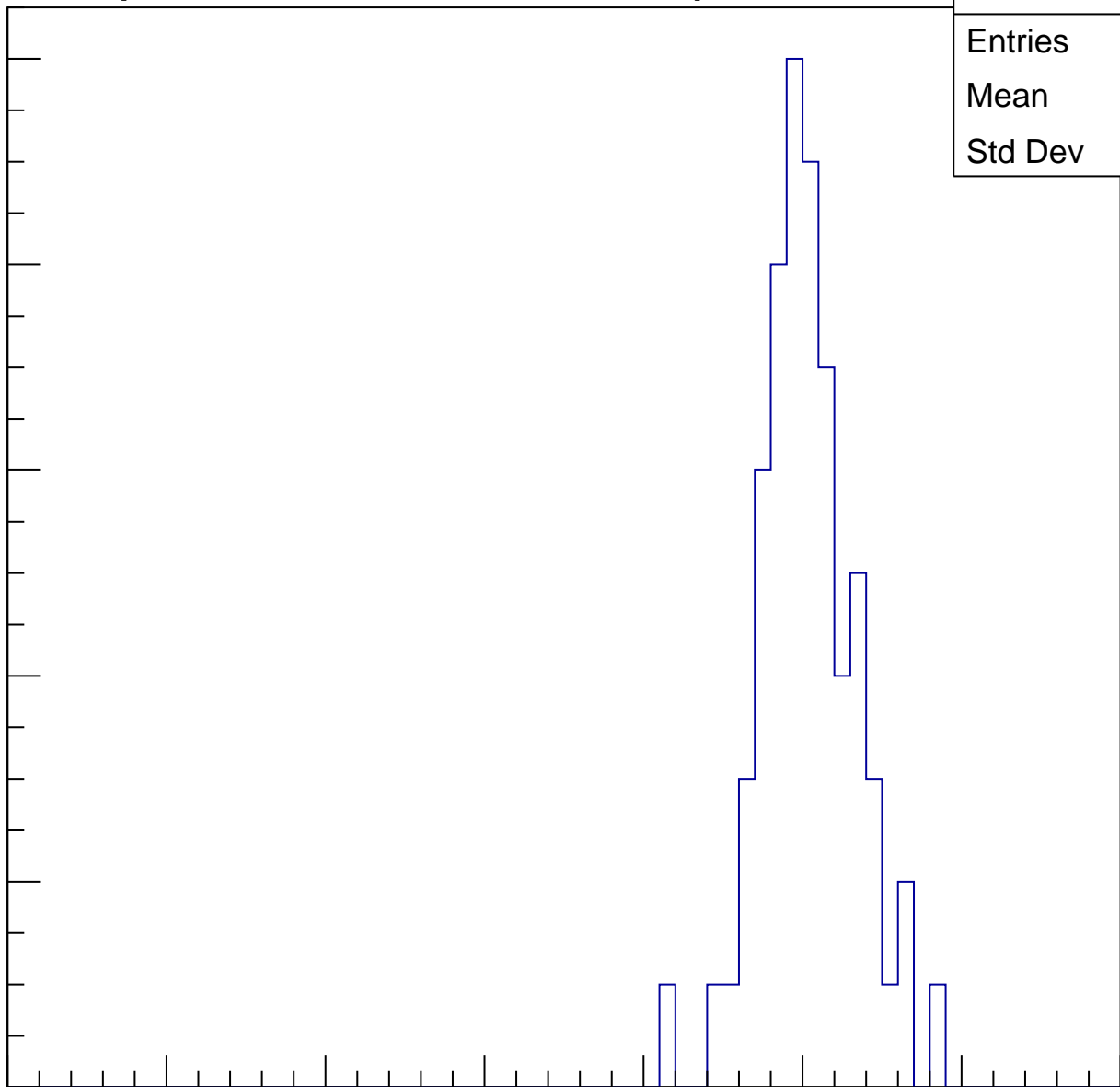
Entries	62
Mean	49.85
Std Dev	3.01

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

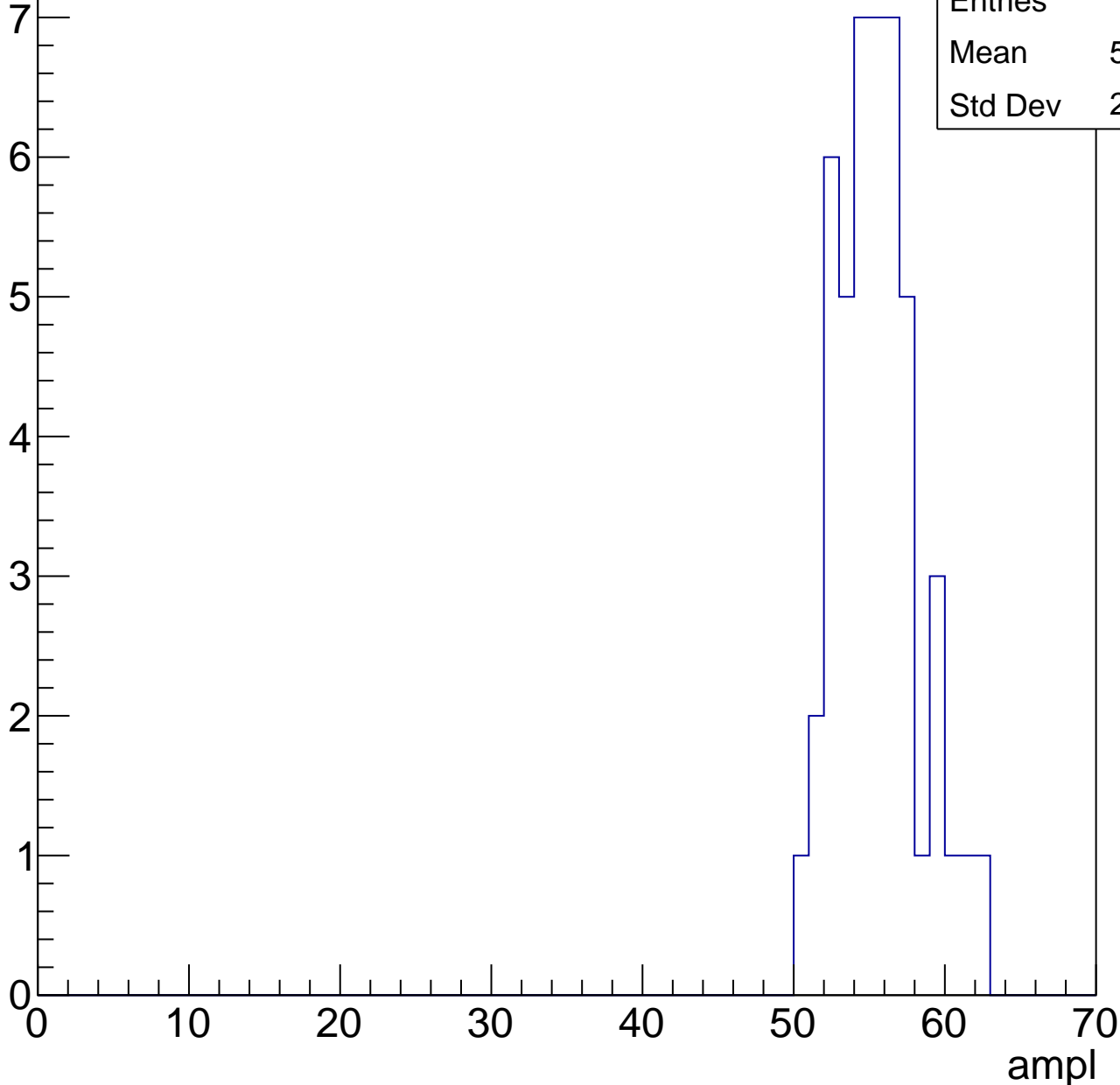


B1L003S, U20-ch29, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

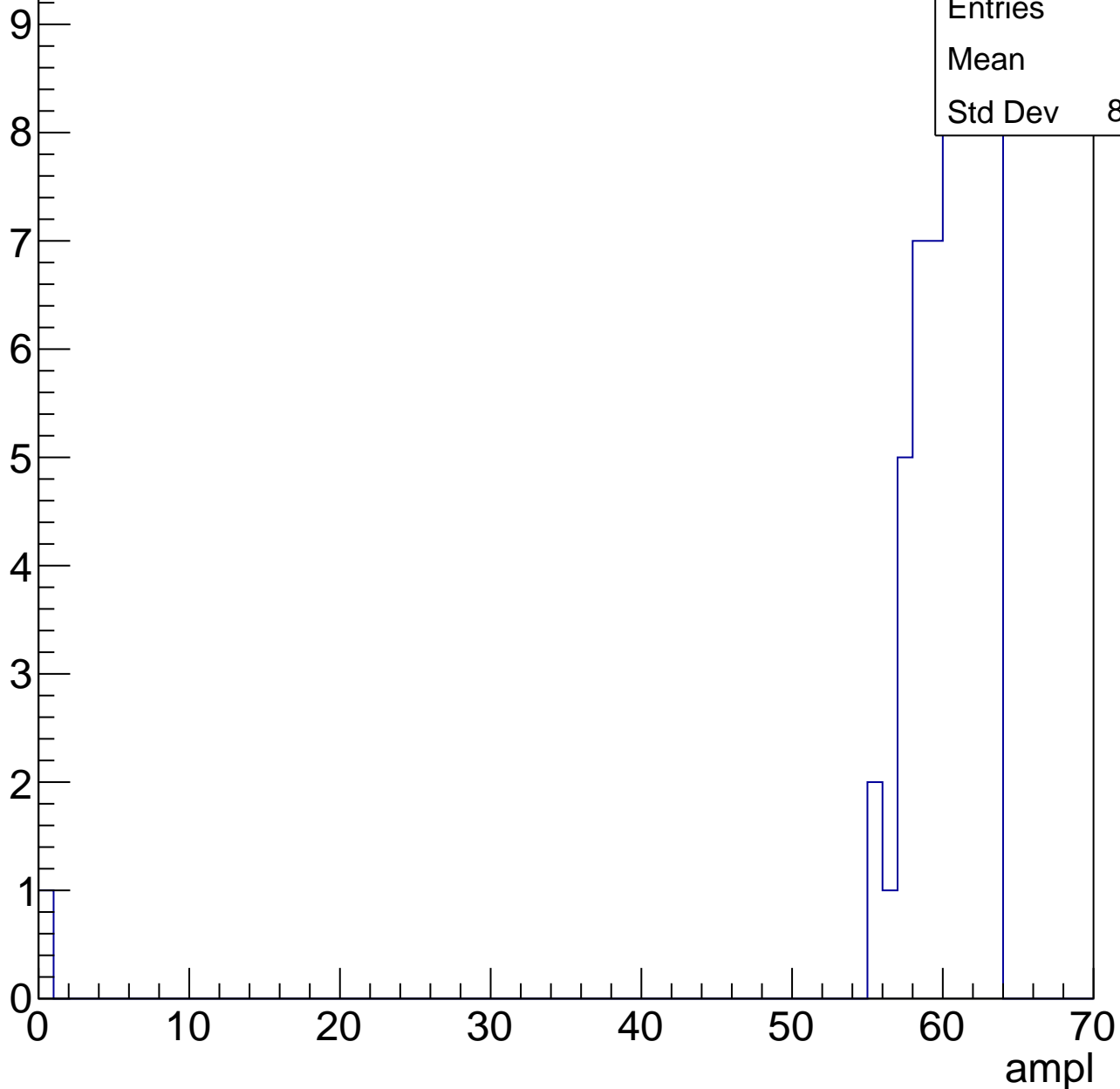
Entries	47
Mean	55.04
Std Dev	2.657



B1L003S, U20-ch29, adc5

calib_packv5_042523_0143.root, FC#13, port D2

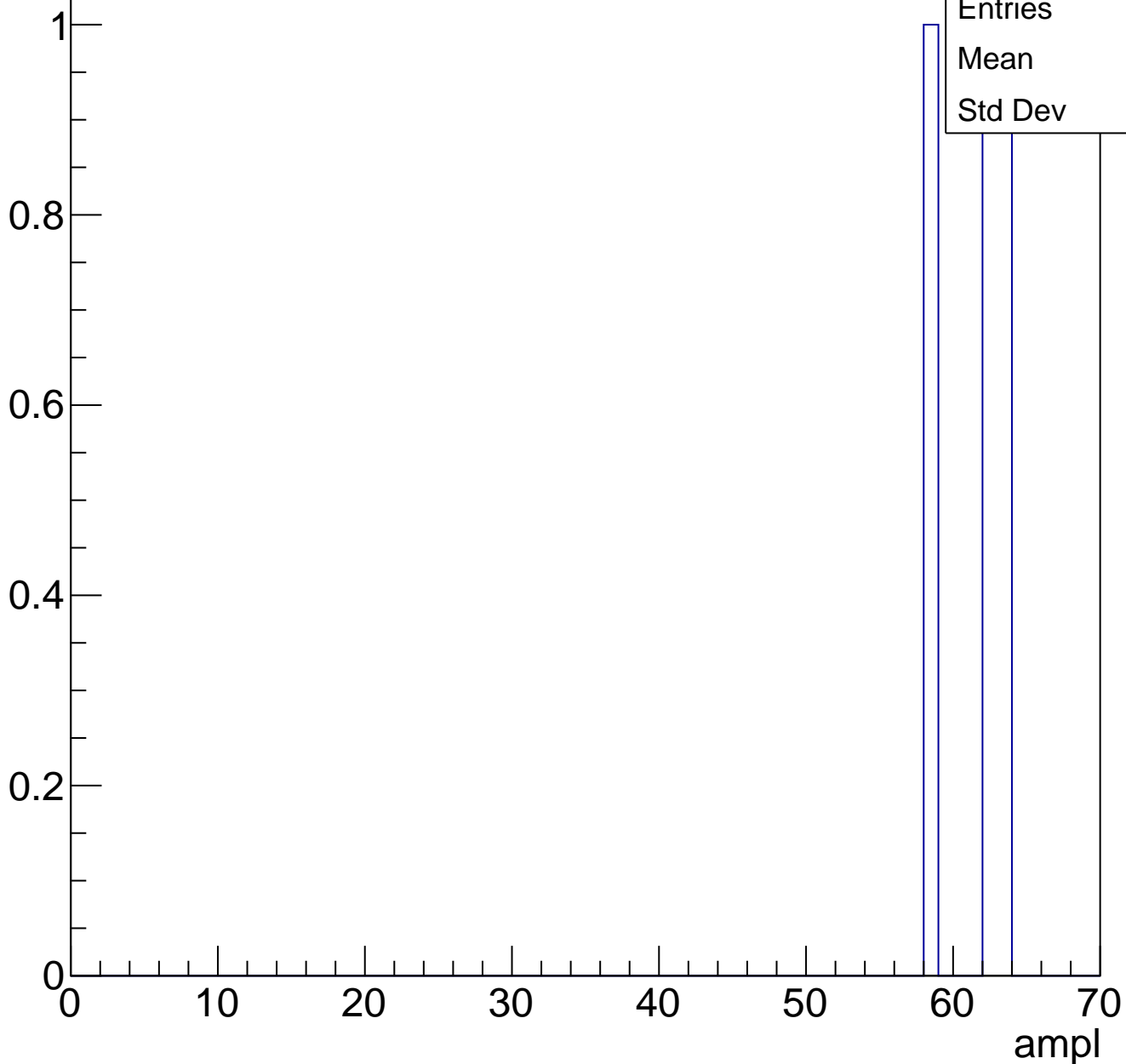
Entry



B1L003S, U20-ch29, adc6

calib_packv5_042523_0143.root, FC#13, port D2

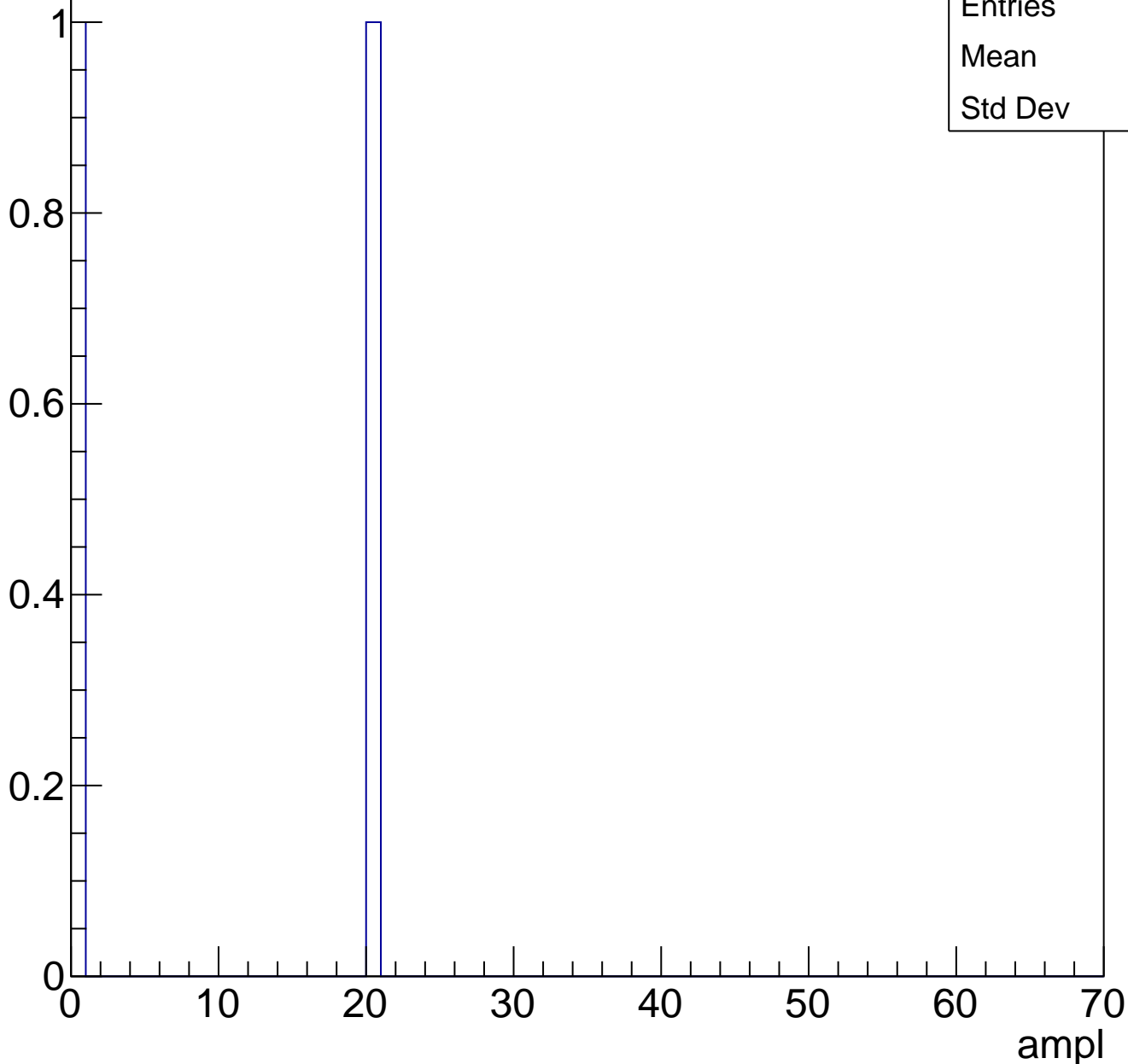
Entry



B1L003S, U20-ch29, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch30, adc0

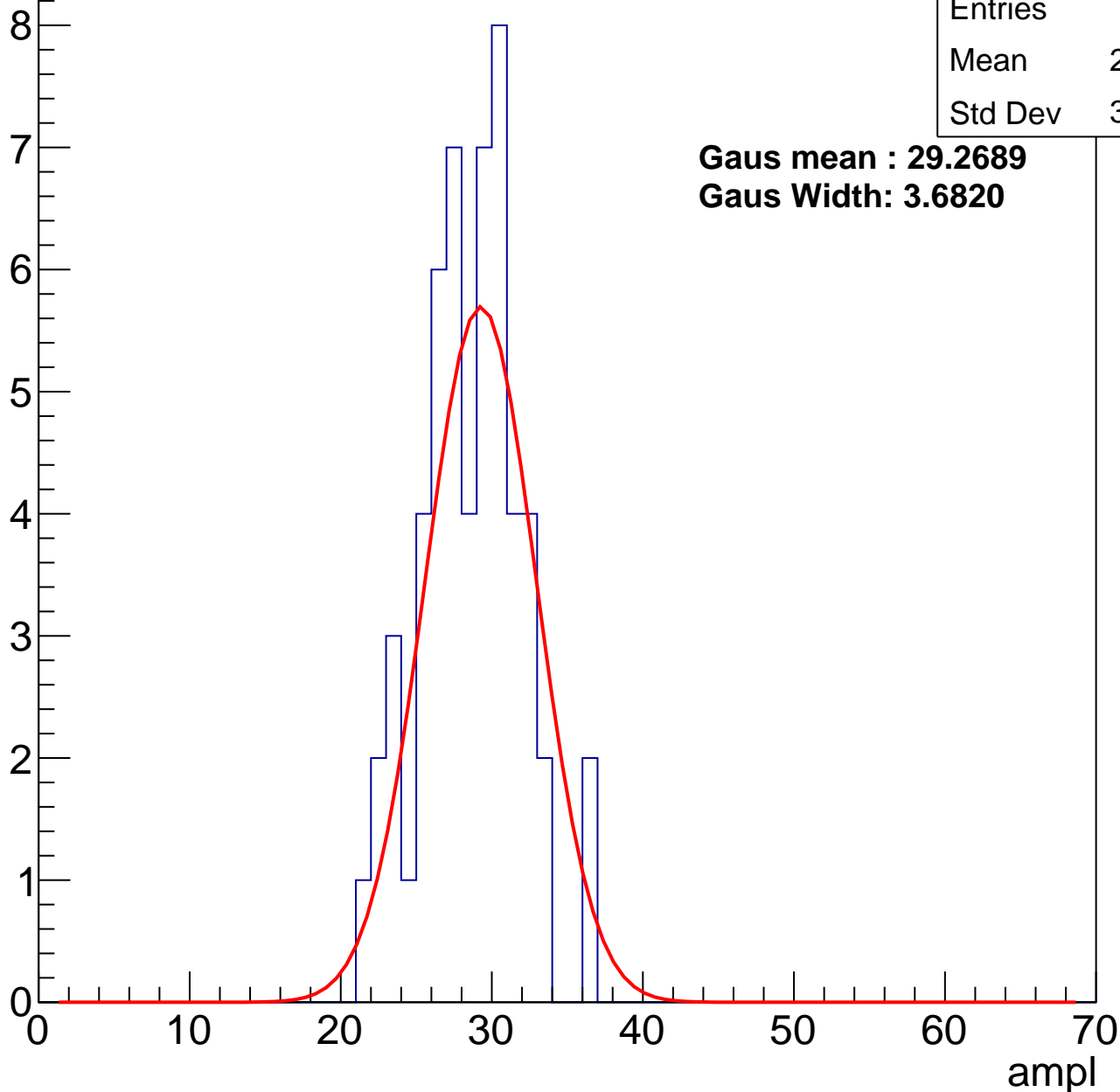
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.15
Std Dev	3.289

Gaus mean : 29.2689

Gaus Width: 3.6820



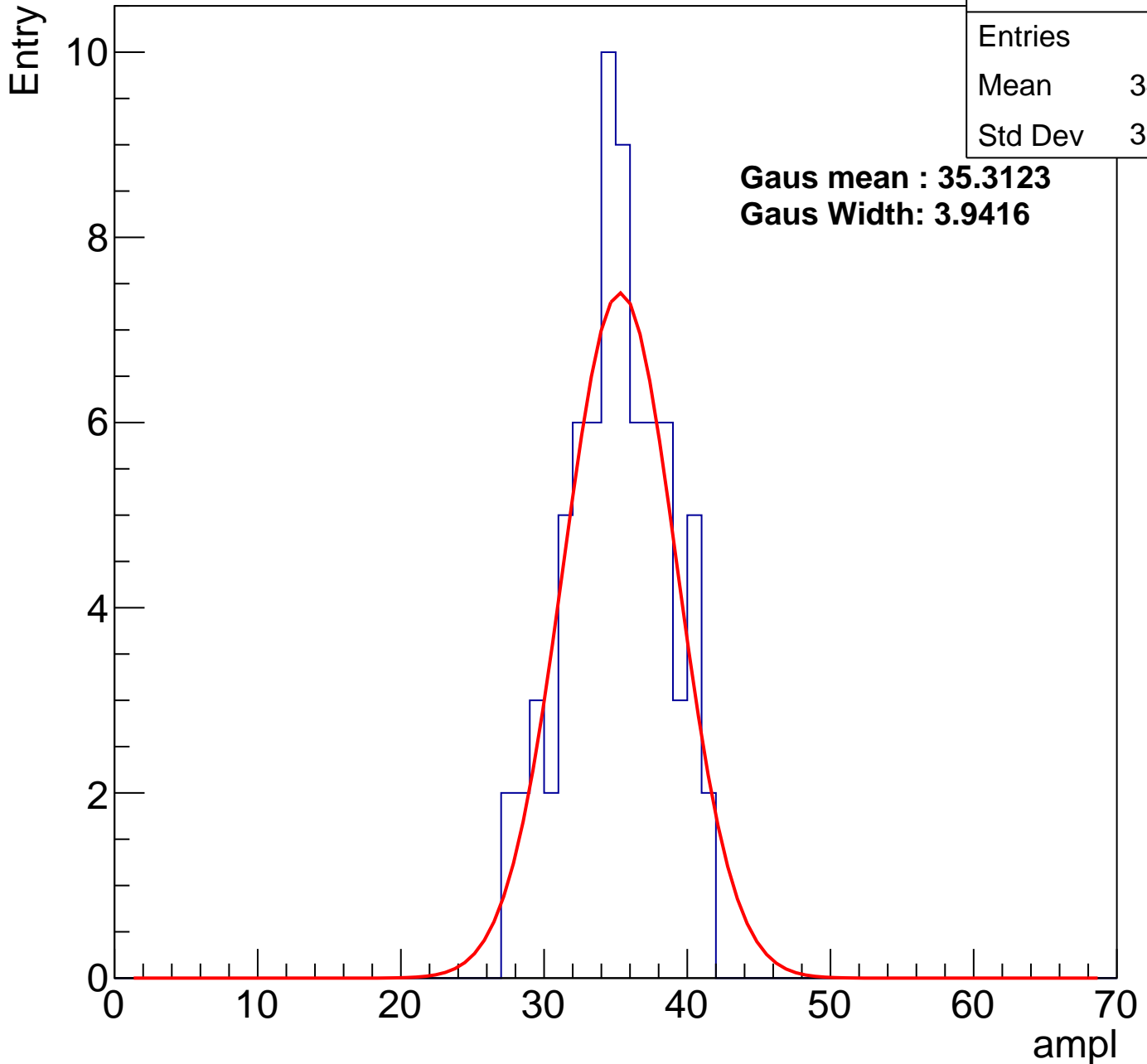
B1L003S, U20-ch30, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	34.55
Std Dev	3.444

Gaus mean : 35.3123

Gaus Width: 3.9416



B1L003S, U20-ch30, adc2

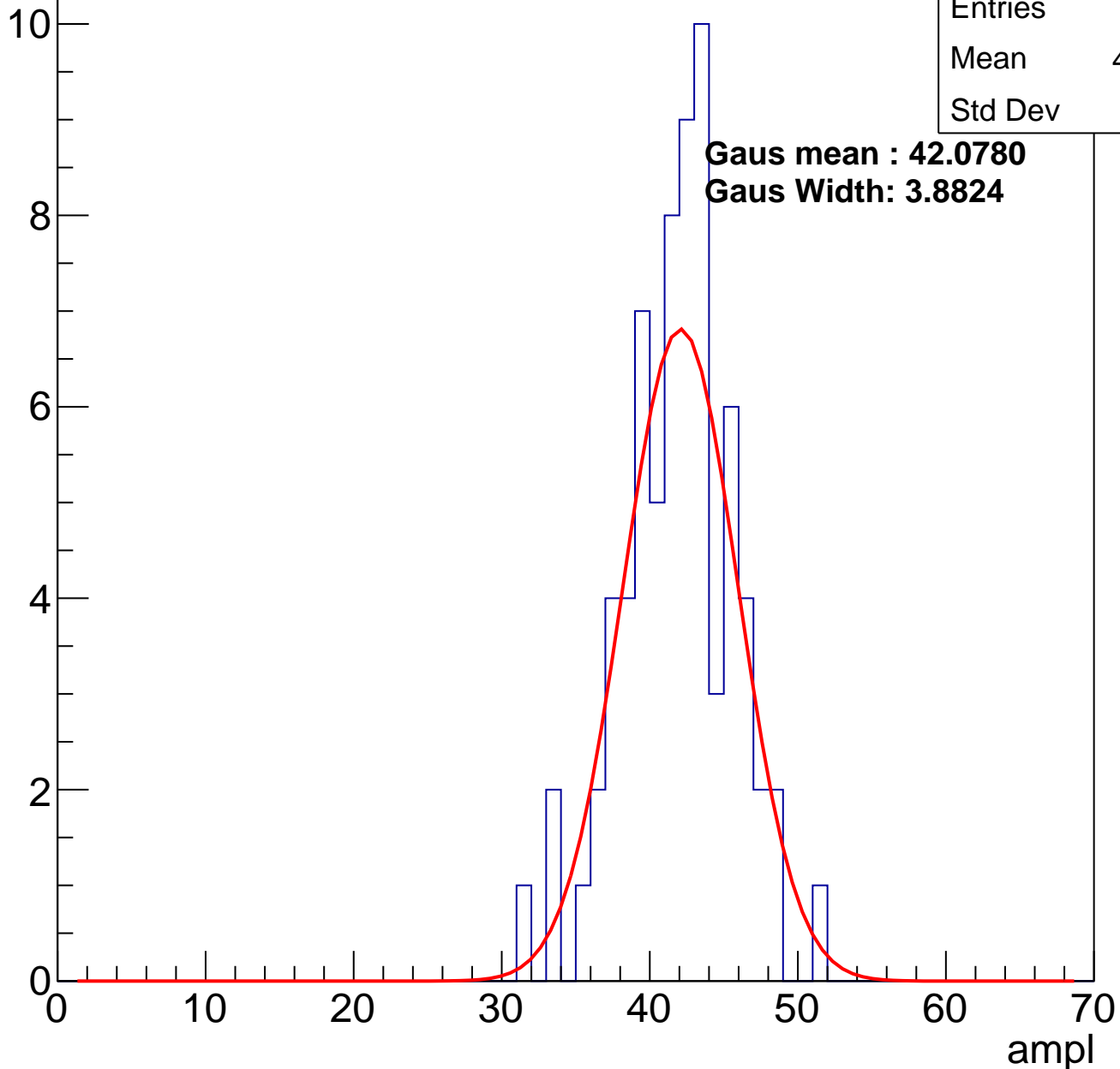
calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	41.41
Std Dev	3.71

Gaus mean : 42.0780

Gaus Width: 3.8824

Entry

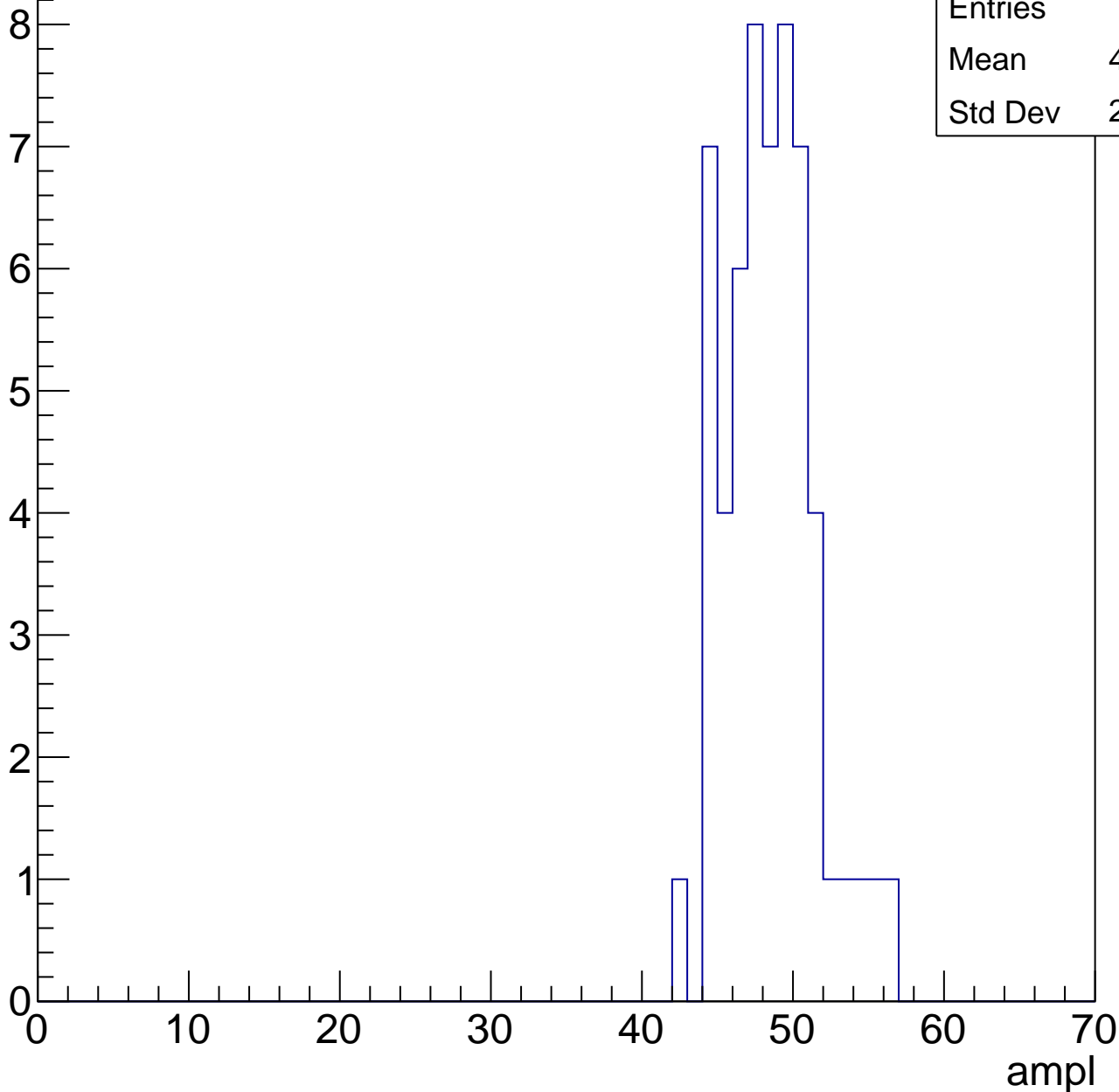


B1L003S, U20-ch30, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.96
Std Dev	2.896

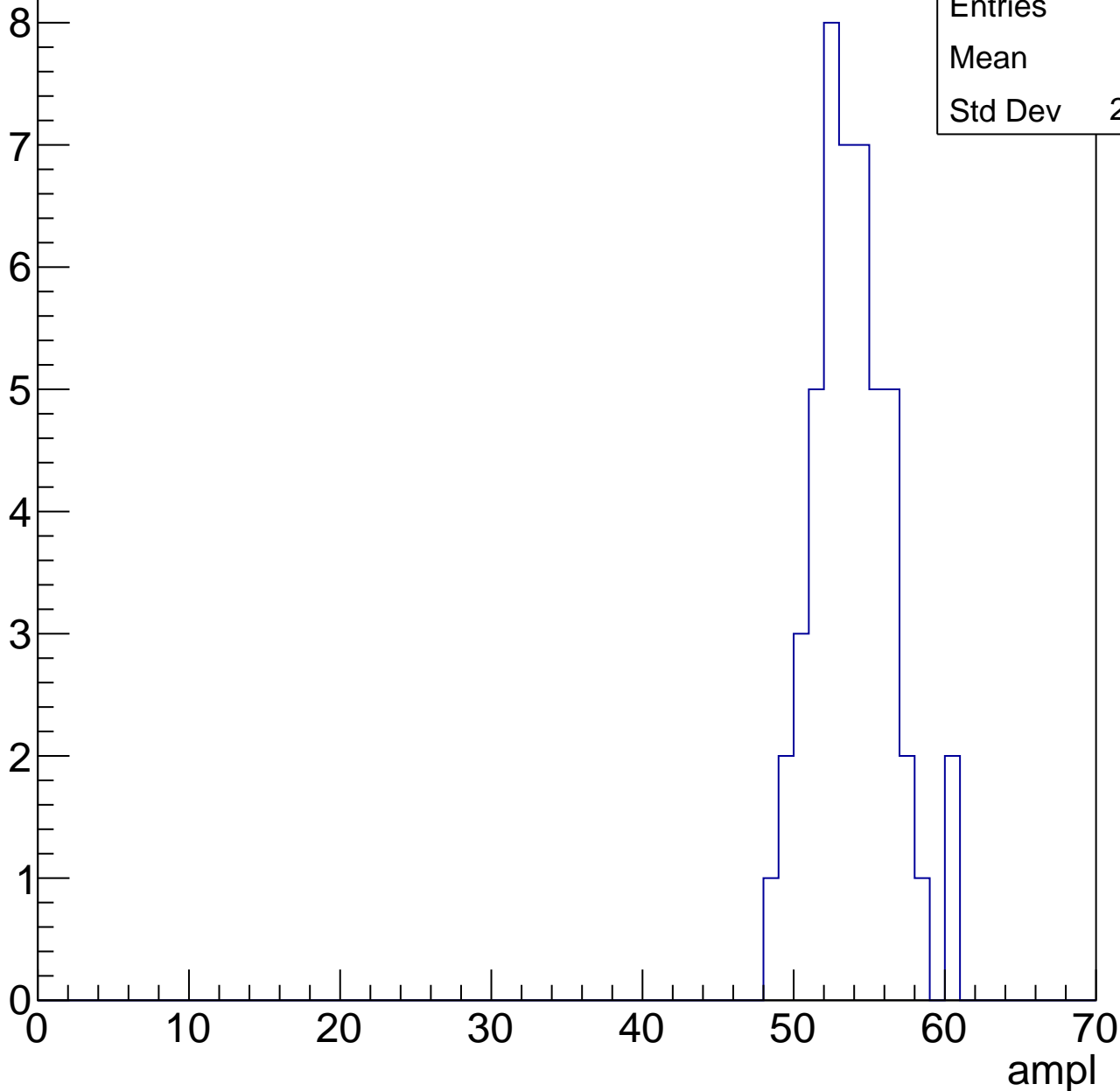


B1L003S, U20-ch30, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

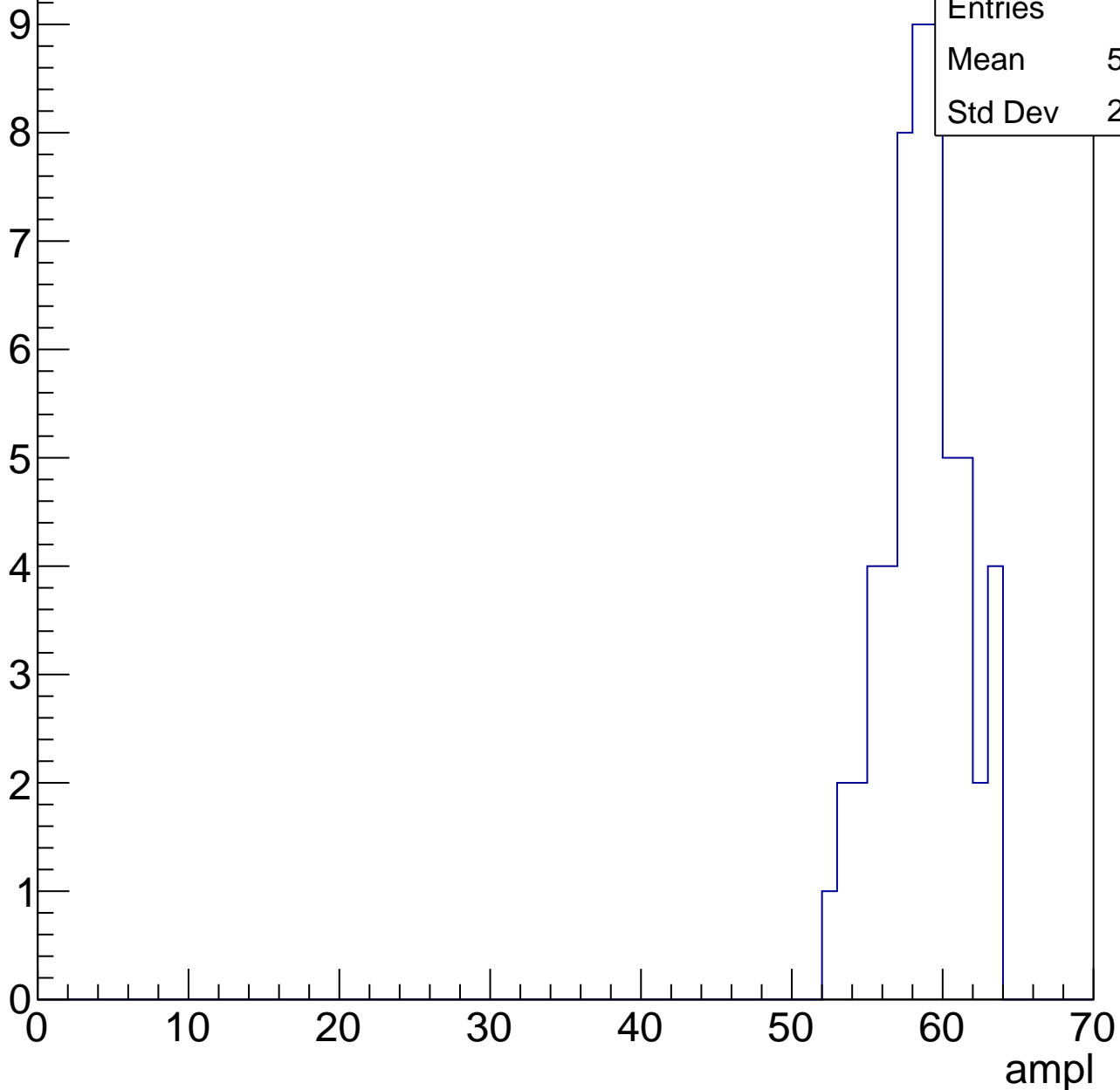
Entries	48
Mean	53.4
Std Dev	2.628



B1L003S, U20-ch30, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

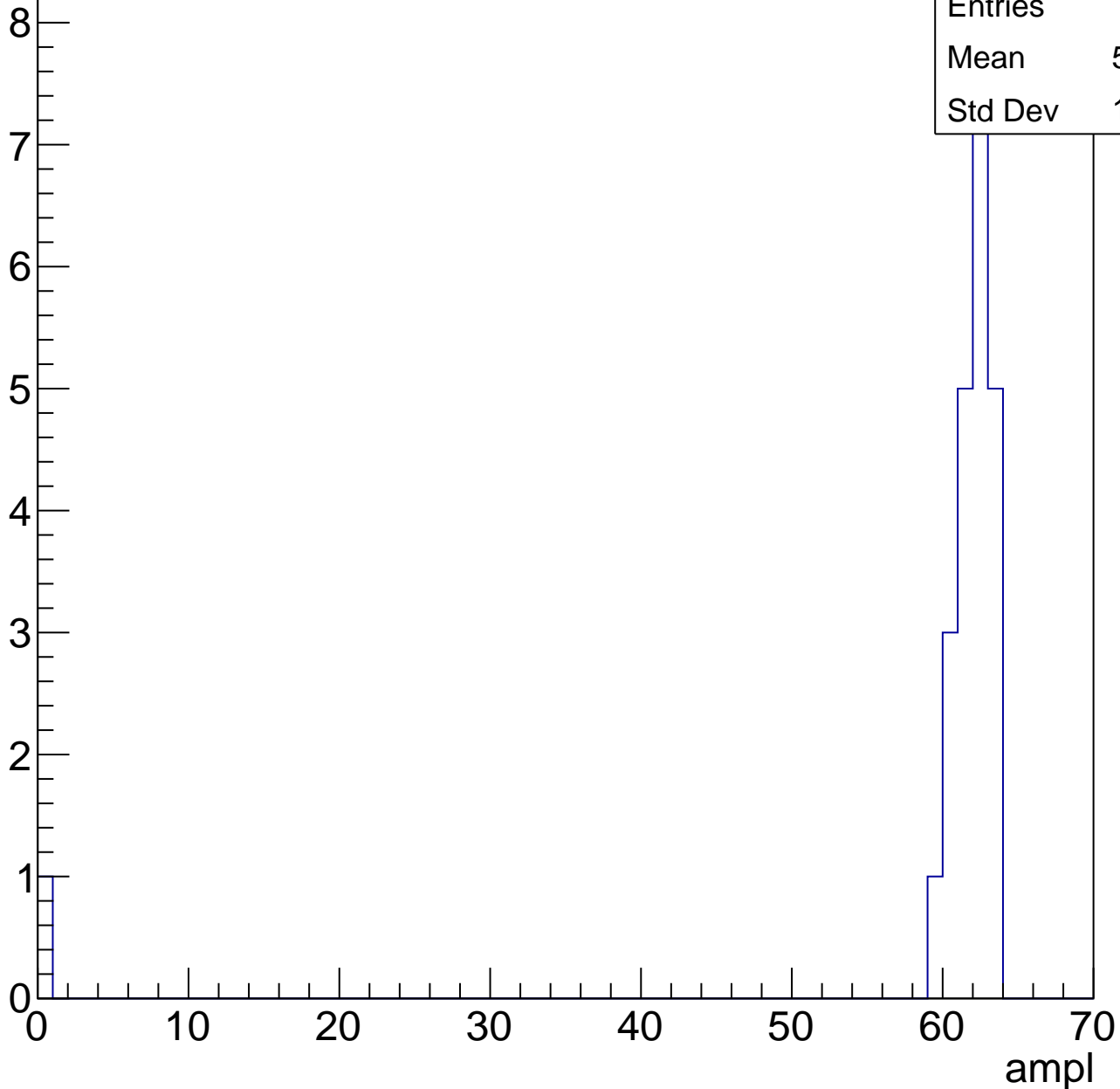


B1L003S, U20-ch30, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.91
Std Dev	12.61



B1L003S, U20-ch30, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U20-ch31, adc0

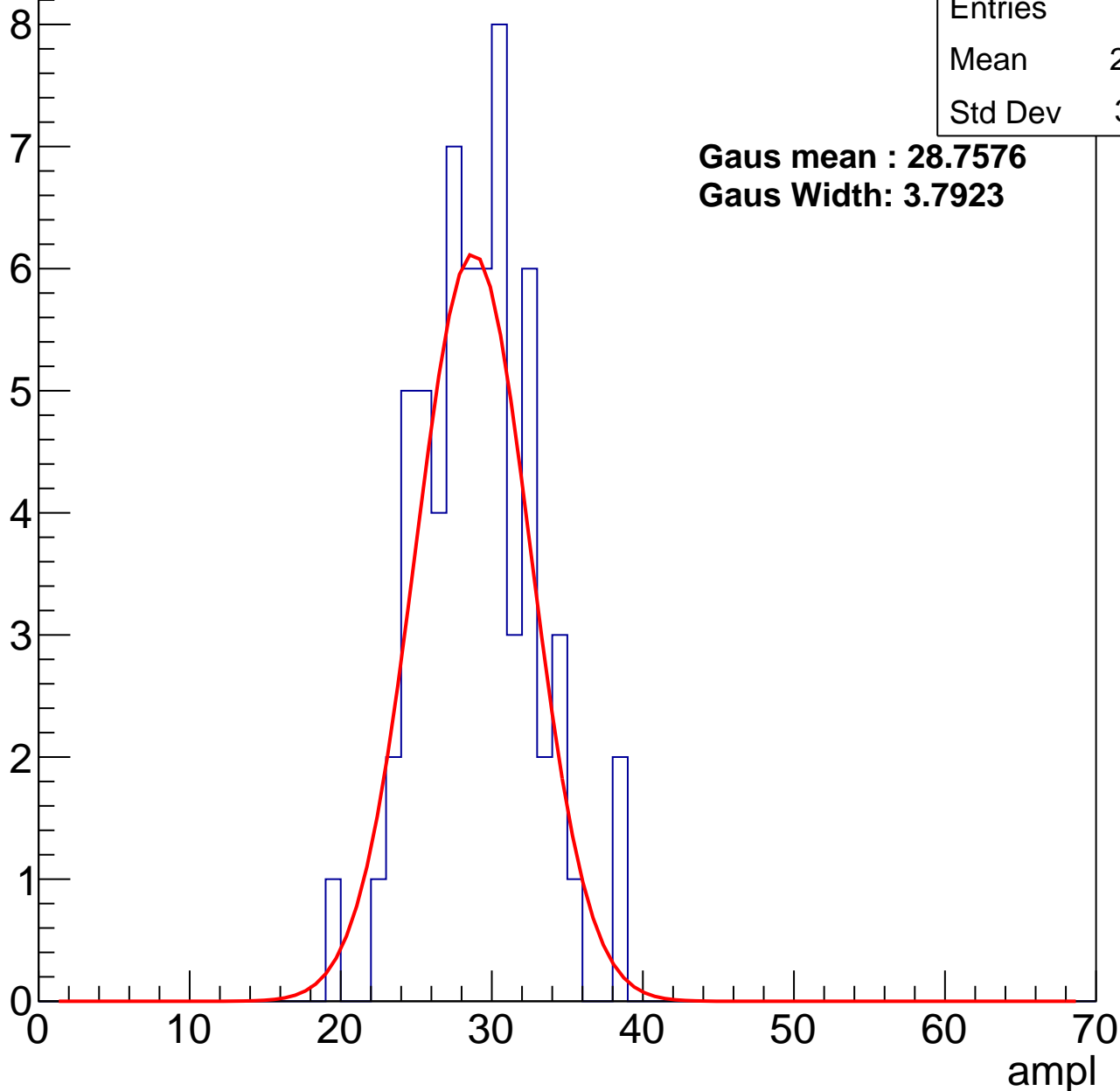
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	28.56
Std Dev	3.731

Gaus mean : 28.7576

Gaus Width: 3.7923

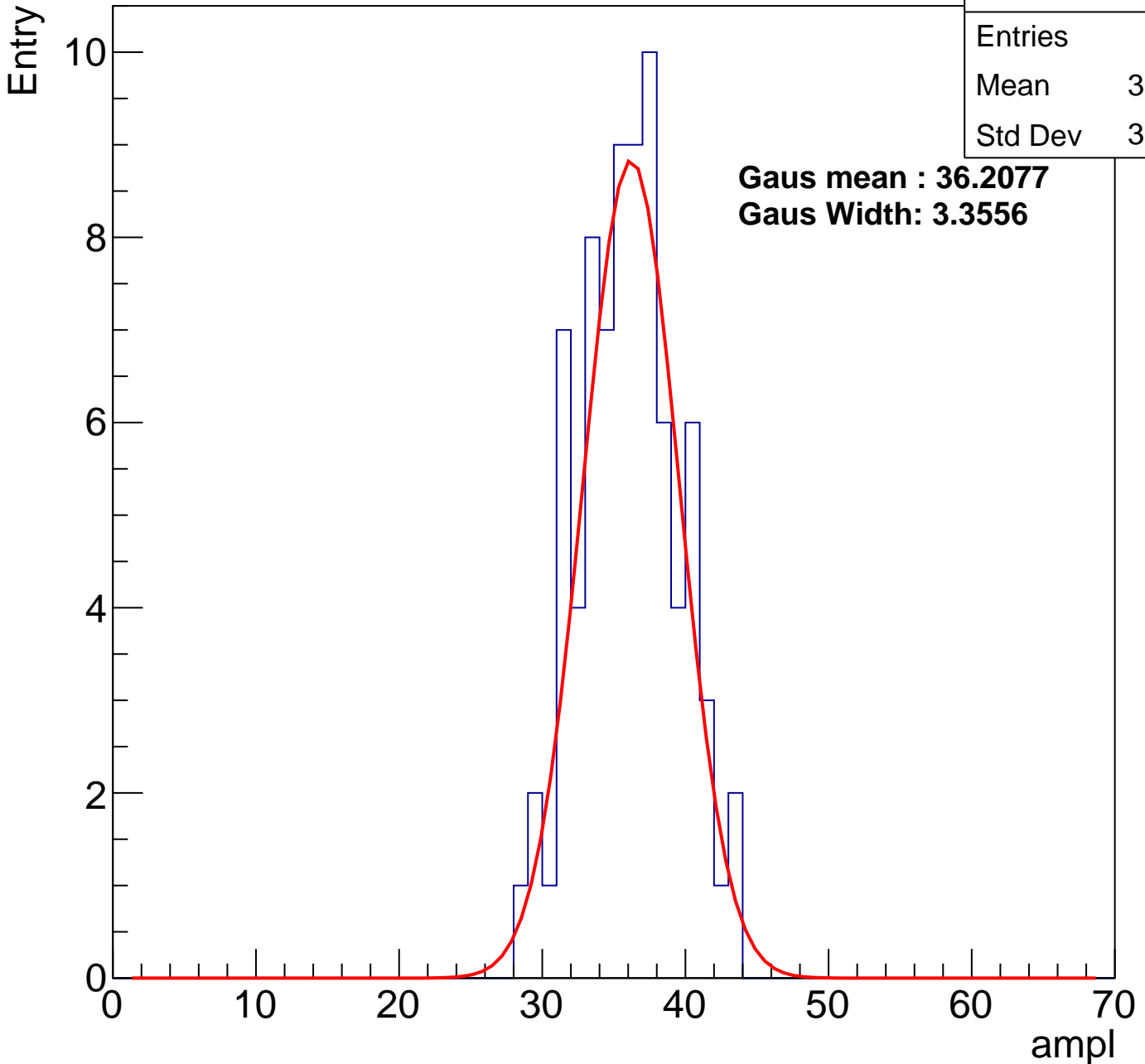


B1L003S, U20-ch31, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	35.59
Std Dev	3.368

Gaus mean : 36.2077
Gaus Width: 3.3556

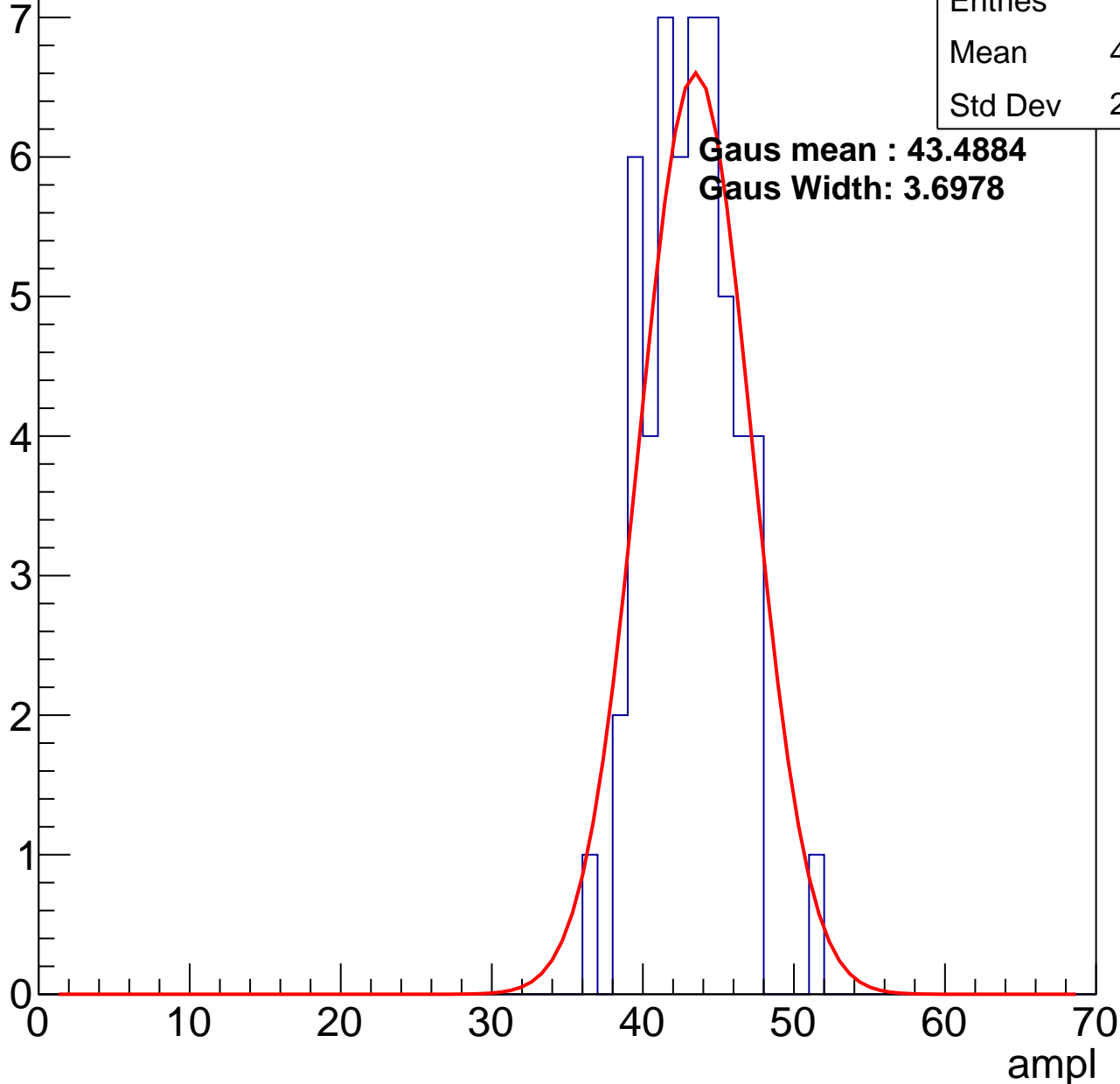


B1L003S, U20-ch31, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.63
Std Dev	2.882

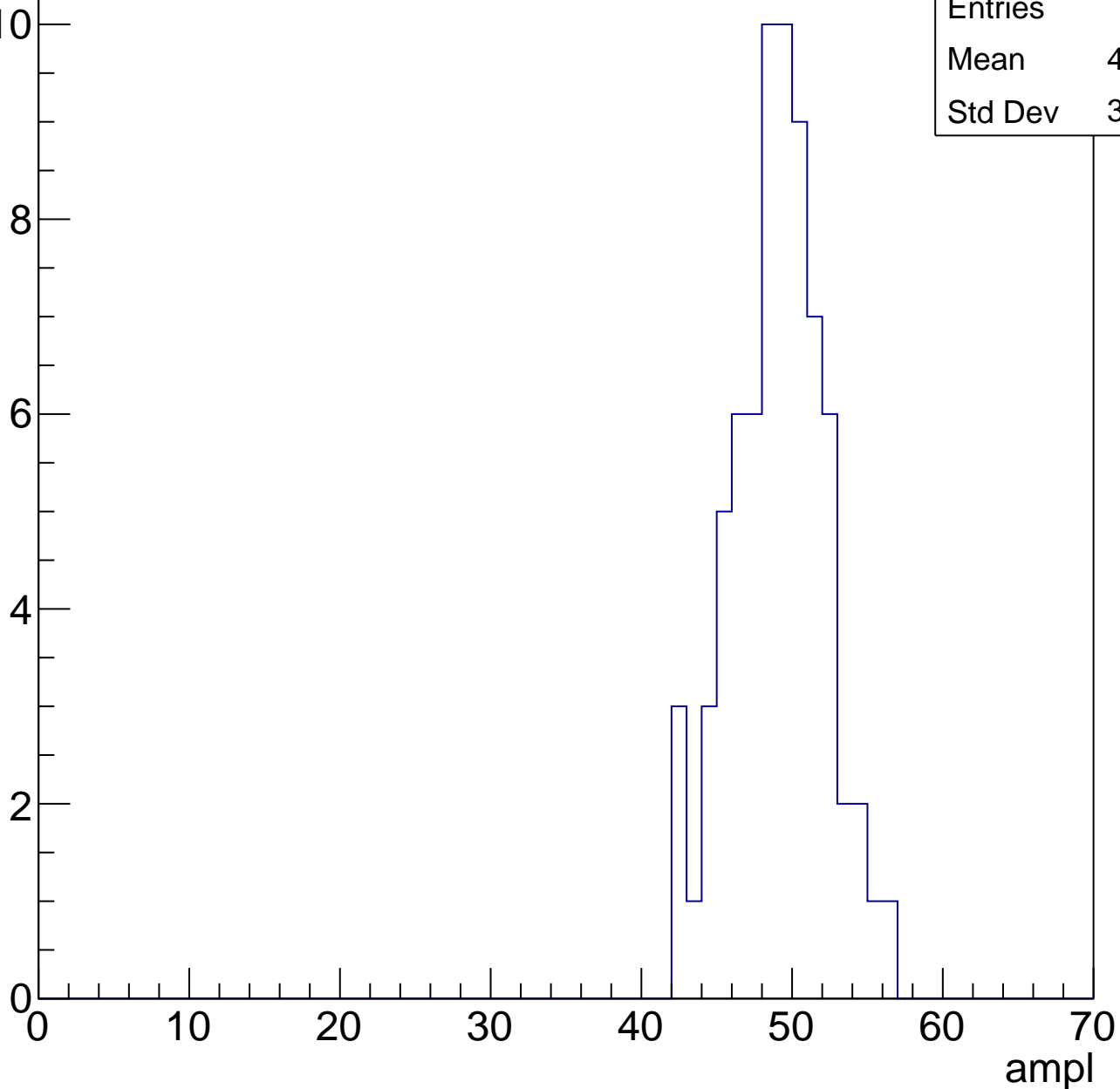


B1L003S, U20-ch31, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.58
Std Dev	3.054

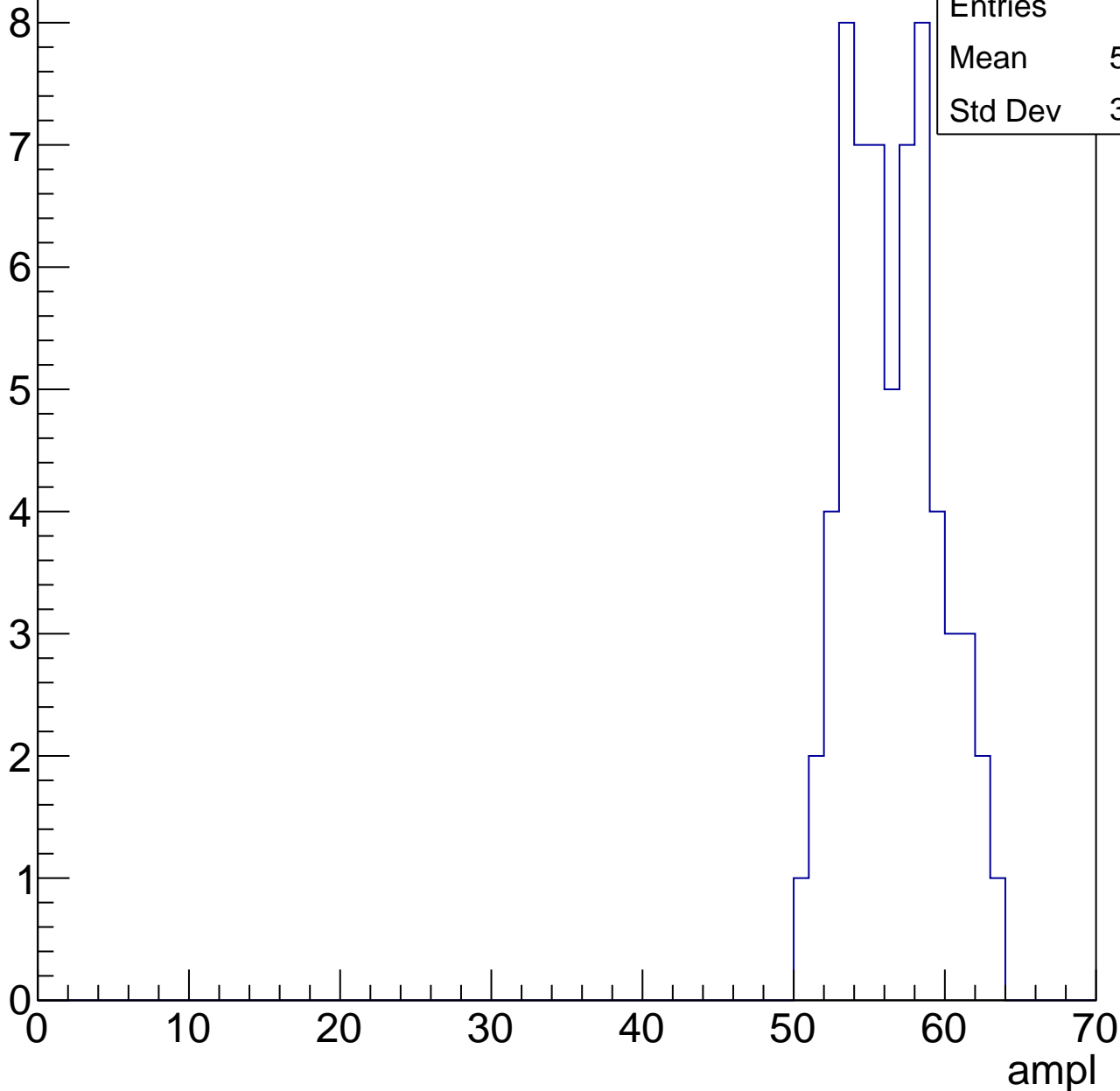


B1L003S, U20-ch31, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	56.06
Std Dev	3.047

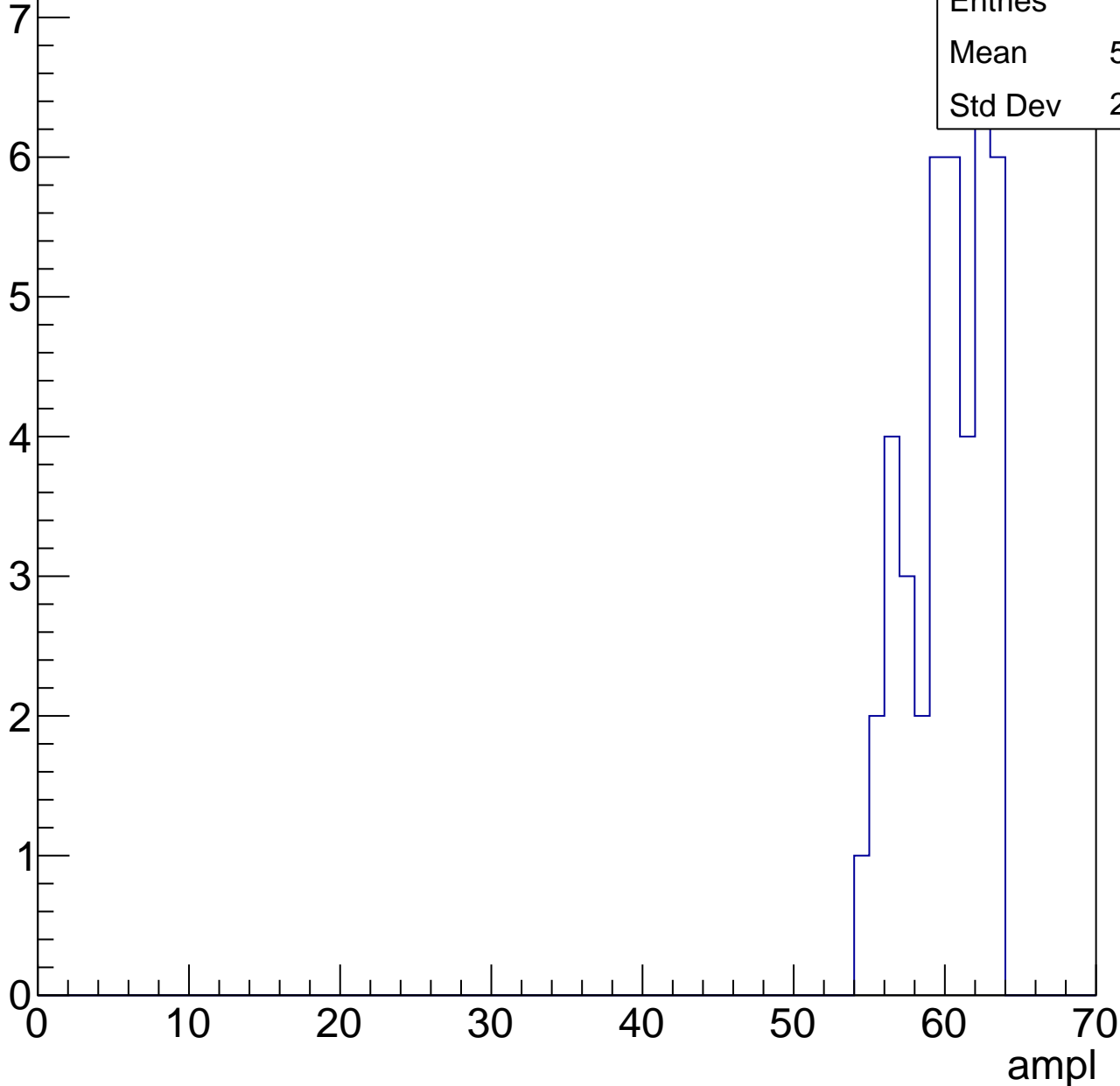


B1L003S, U20-ch31, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	59.63
Std Dev	2.573



B1L003S, U20-ch31, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

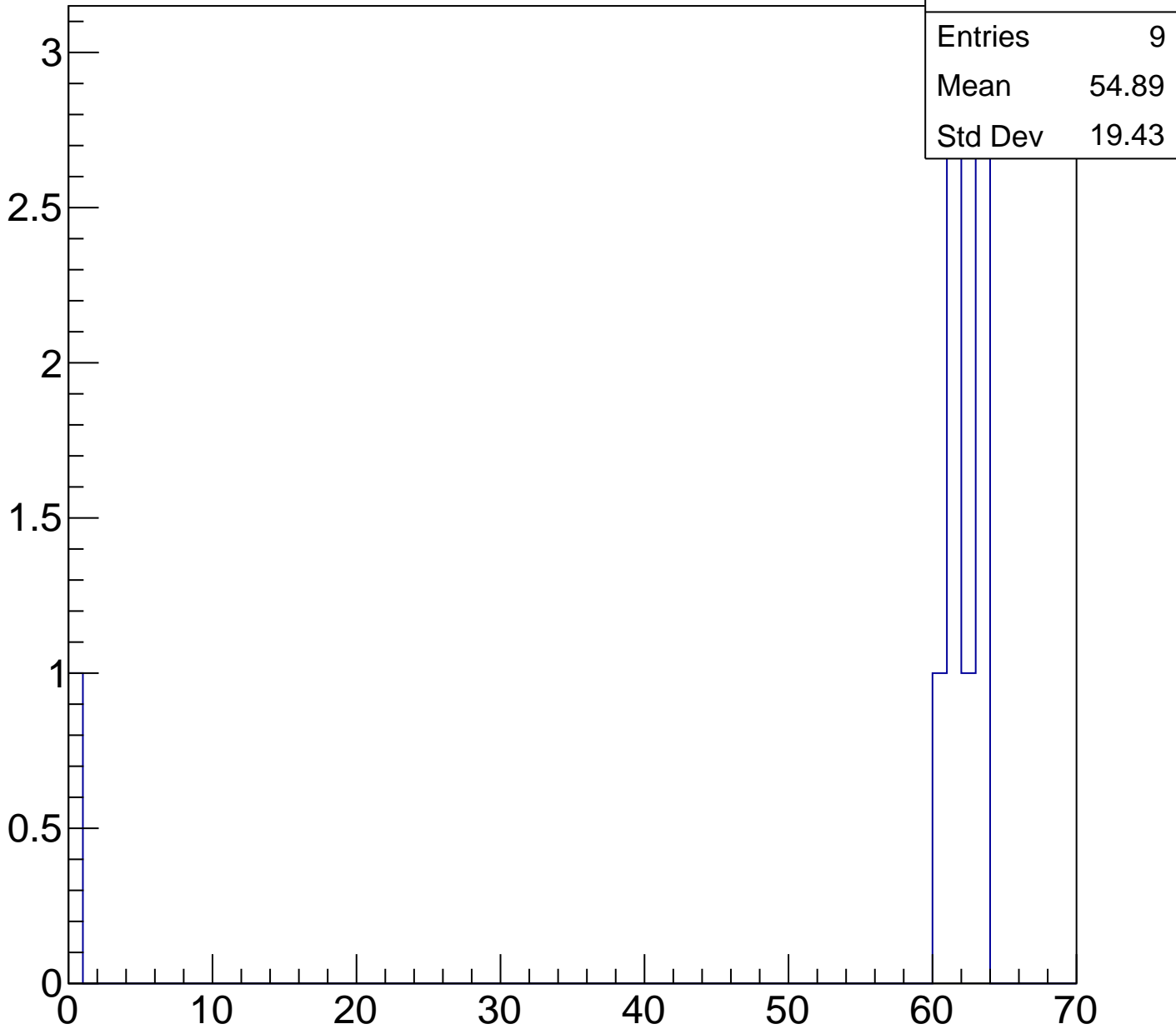
1

0.5

0

ampl

Entries	9
Mean	54.89
Std Dev	19.43



B1L003S, U20-ch31, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch32, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	29.57
Std Dev	3.619

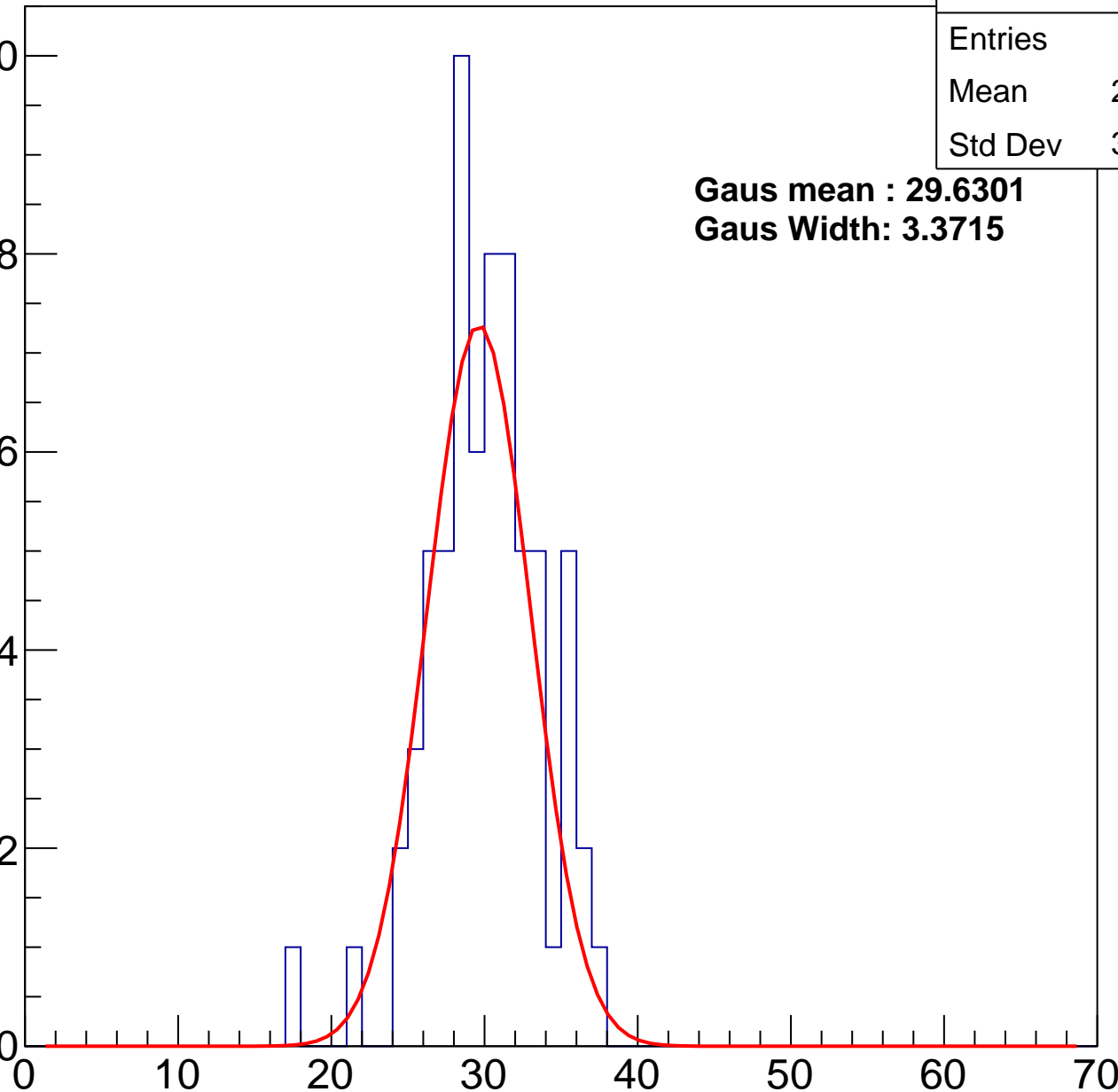
Gaus mean : 29.6301

Gaus Width: 3.3715

Entry

10
8
6
4
2
0

ampl



B1L003S, U20-ch32, adc1

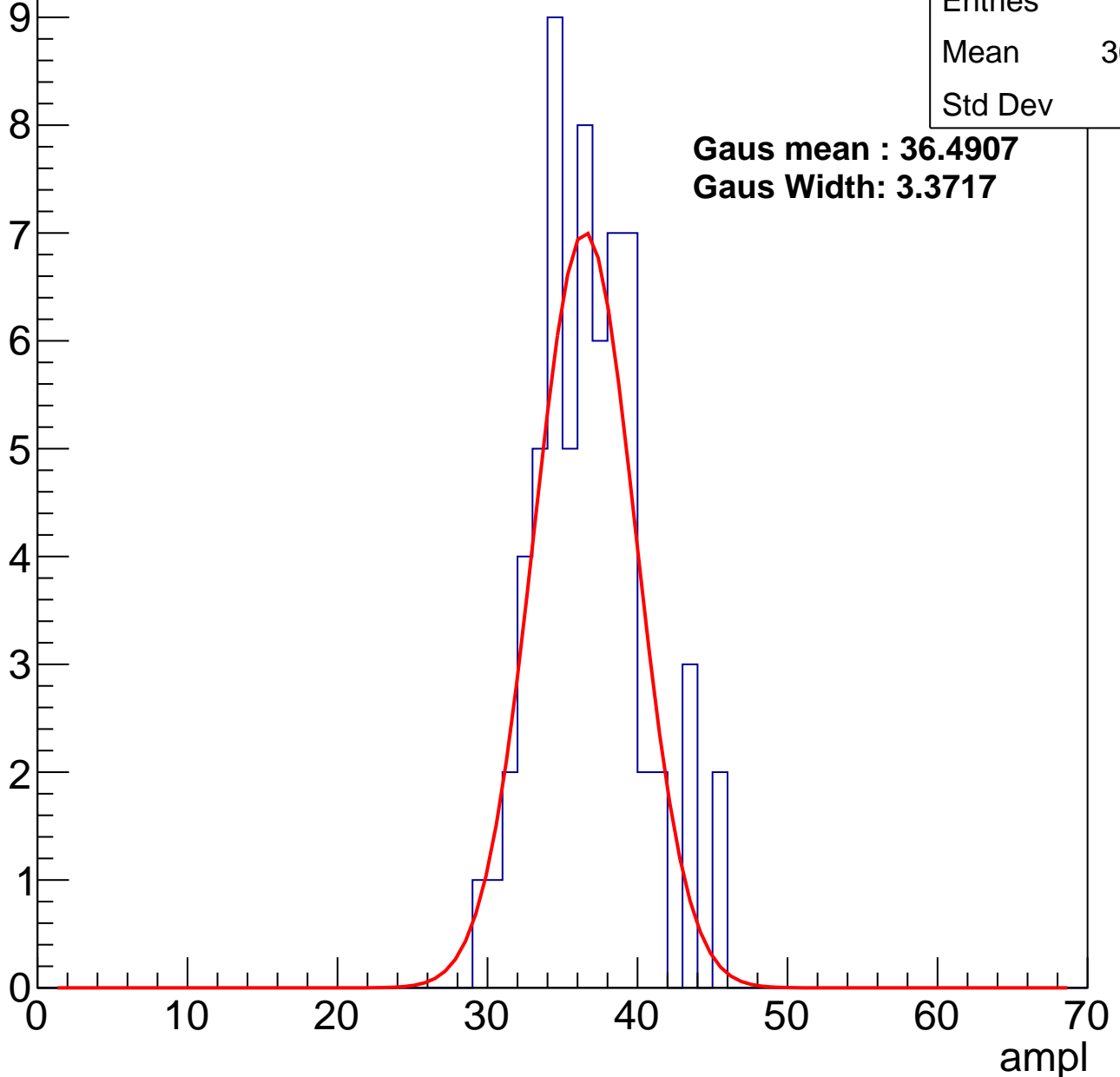
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.33
Std Dev	3.46

Gaus mean : 36.4907

Gaus Width: 3.3717



B1L003S, U20-ch32, adc2

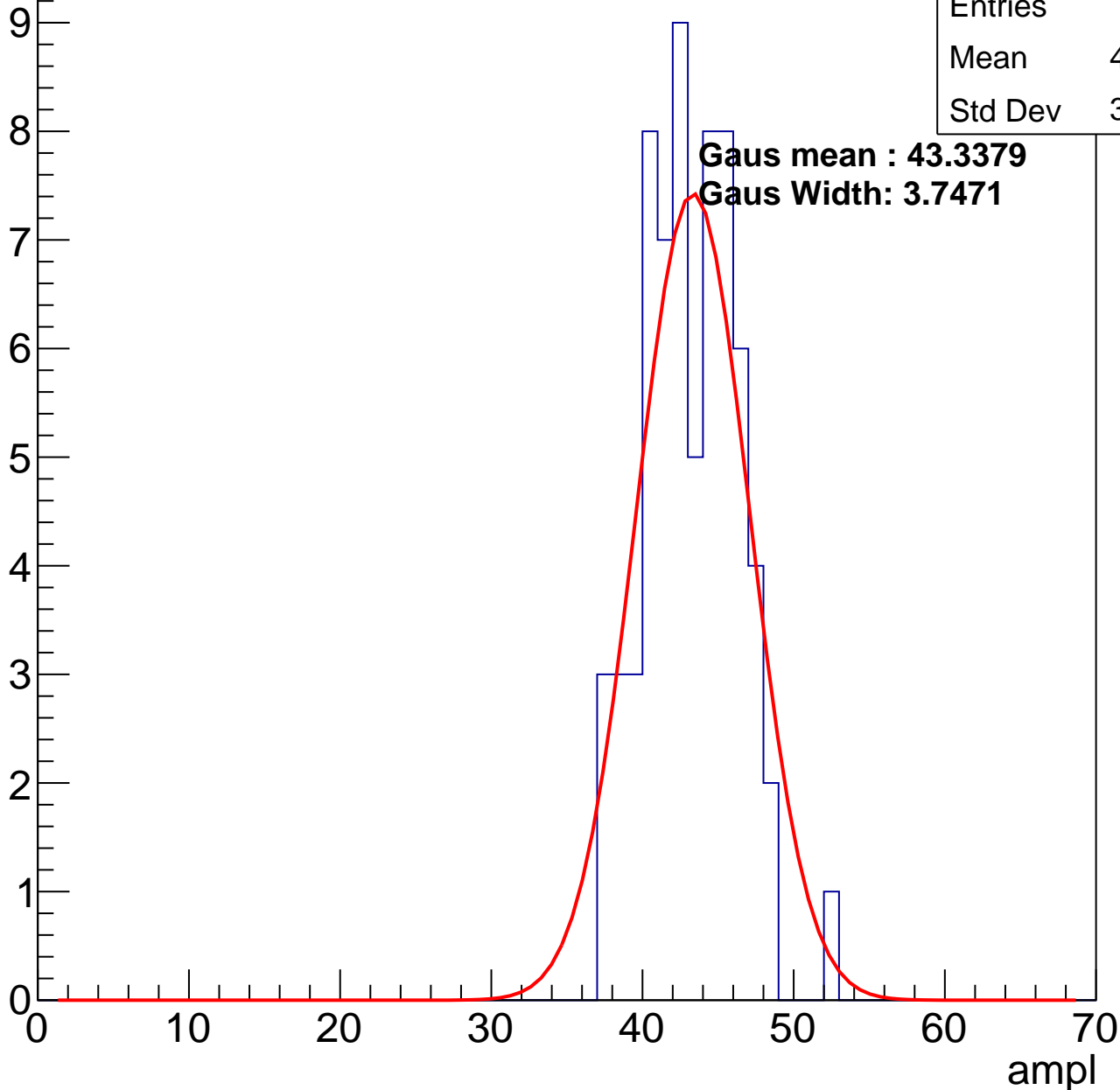
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.78
Std Dev	3.056

Gaus mean : 43.3379

Gaus Width: 3.7471

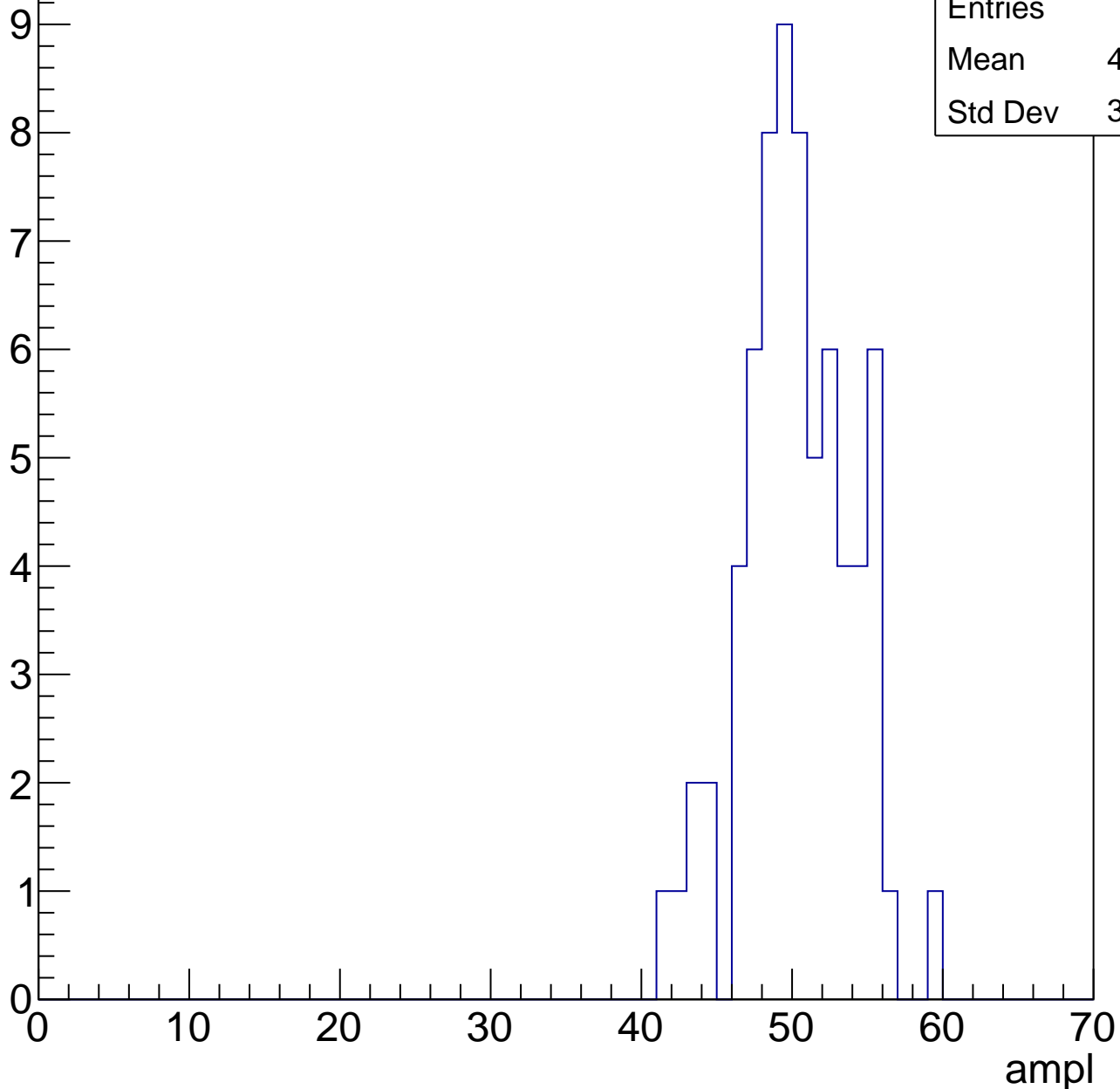


B1L003S, U20-ch32, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

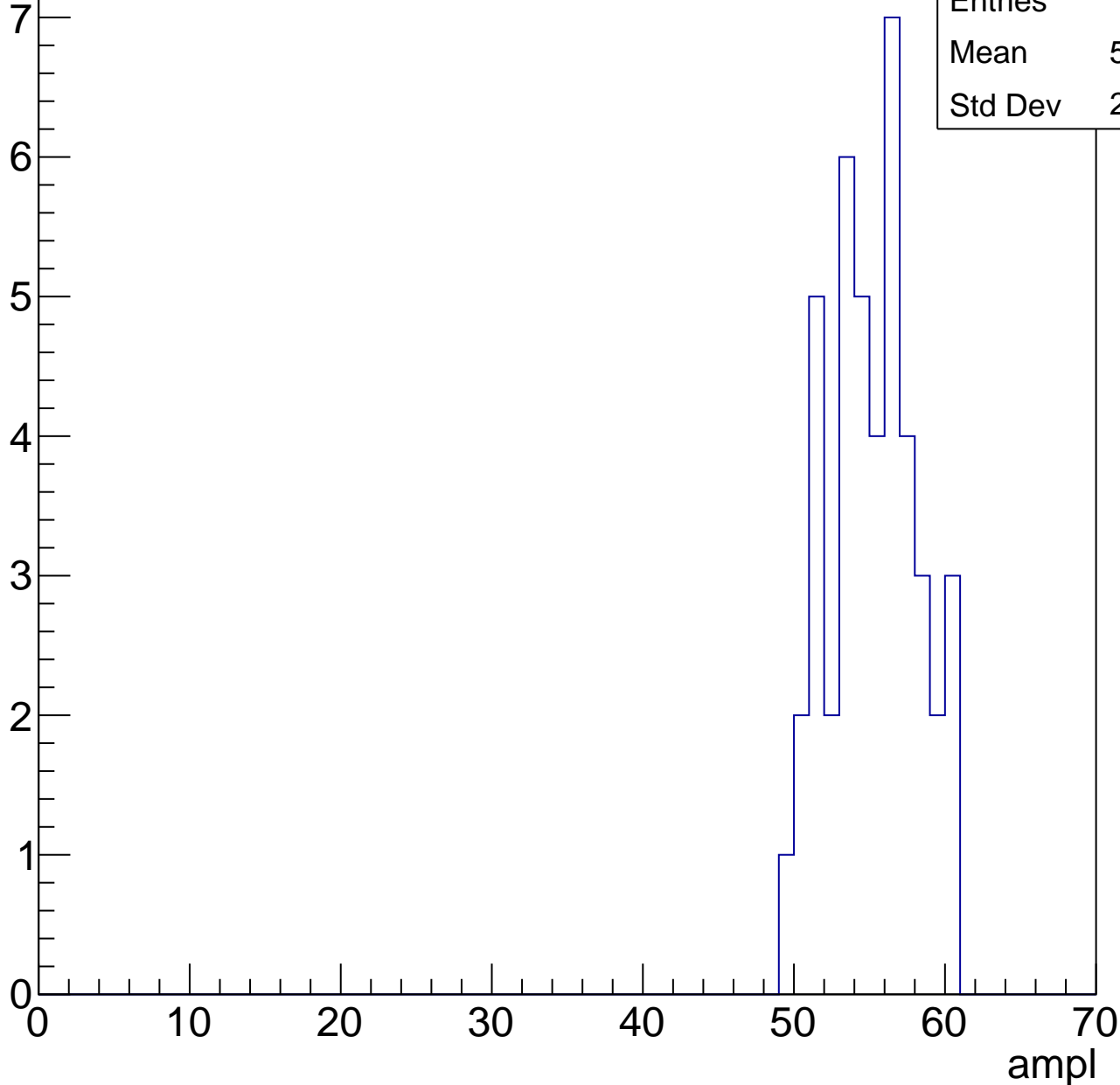
Entries	68
Mean	49.82
Std Dev	3.573



B1L003S, U20-ch32, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

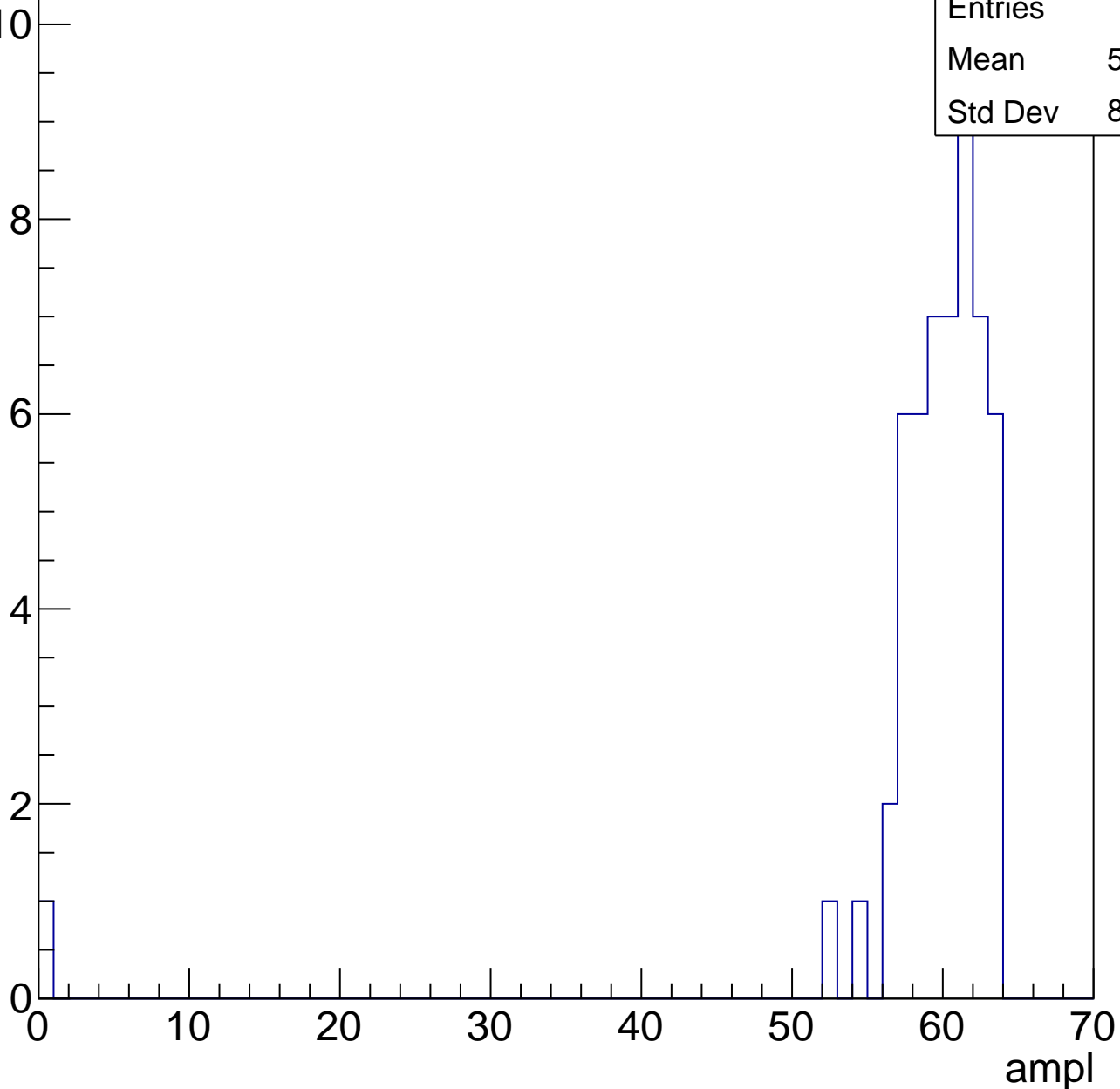


B1L003S, U20-ch32, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

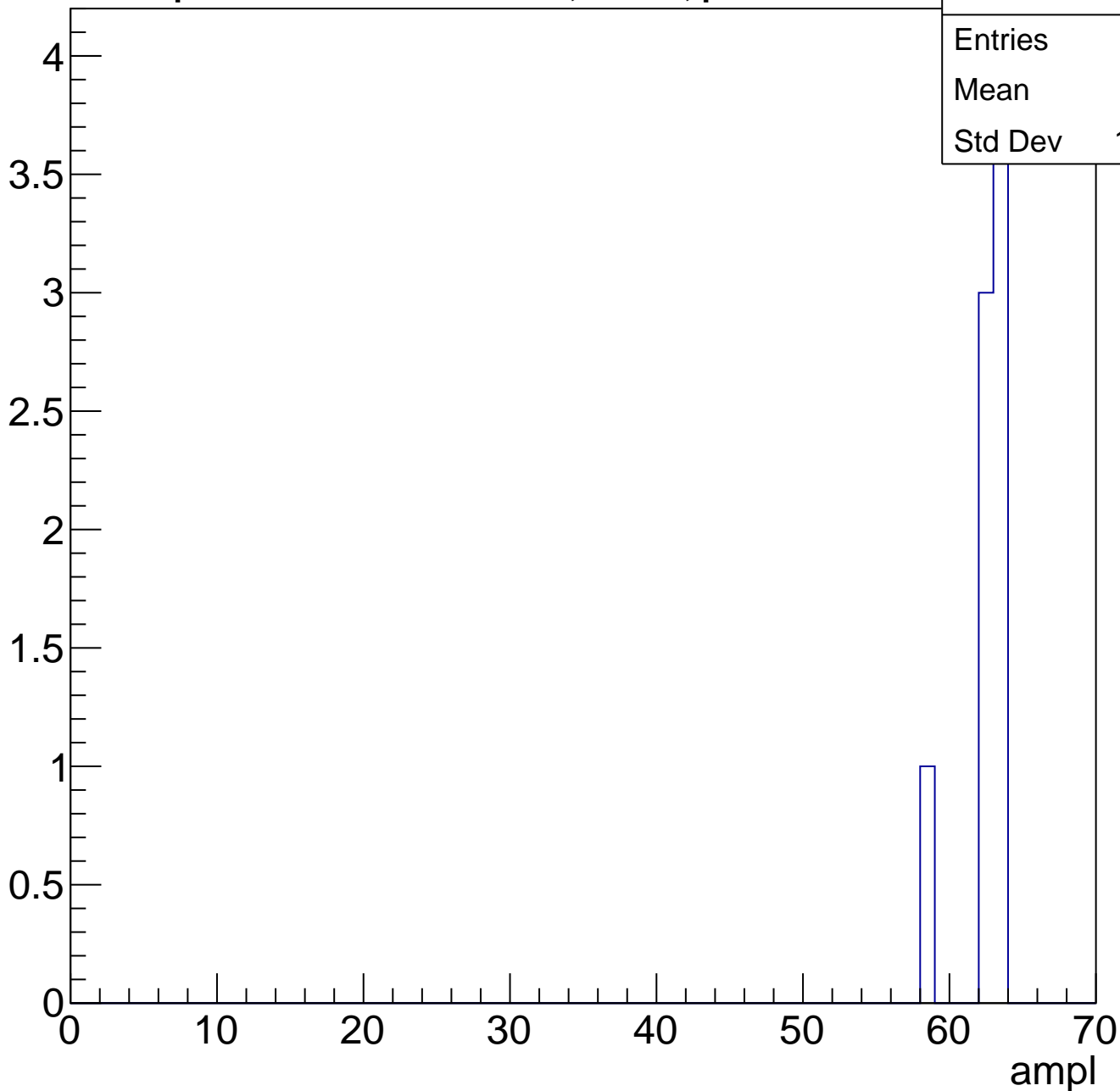
Entries	54
Mean	58.57
Std Dev	8.388



B1L003S, U20-ch32, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch32, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch33, adc0

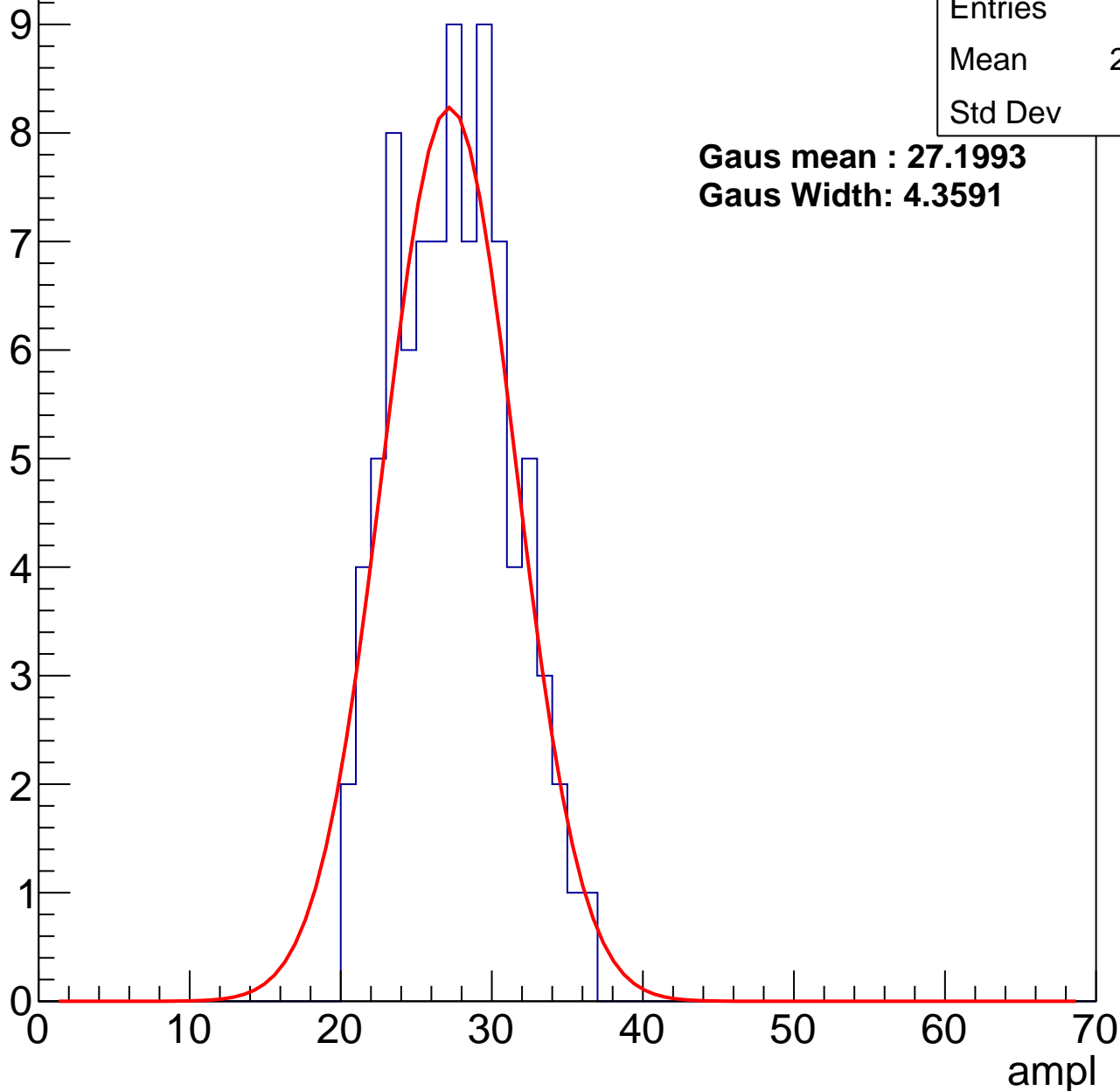
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	87
Mean	27.02
Std Dev	3.76

Gaus mean : 27.1993

Gaus Width: 4.3591



B1L003S, U20-ch33, adc1

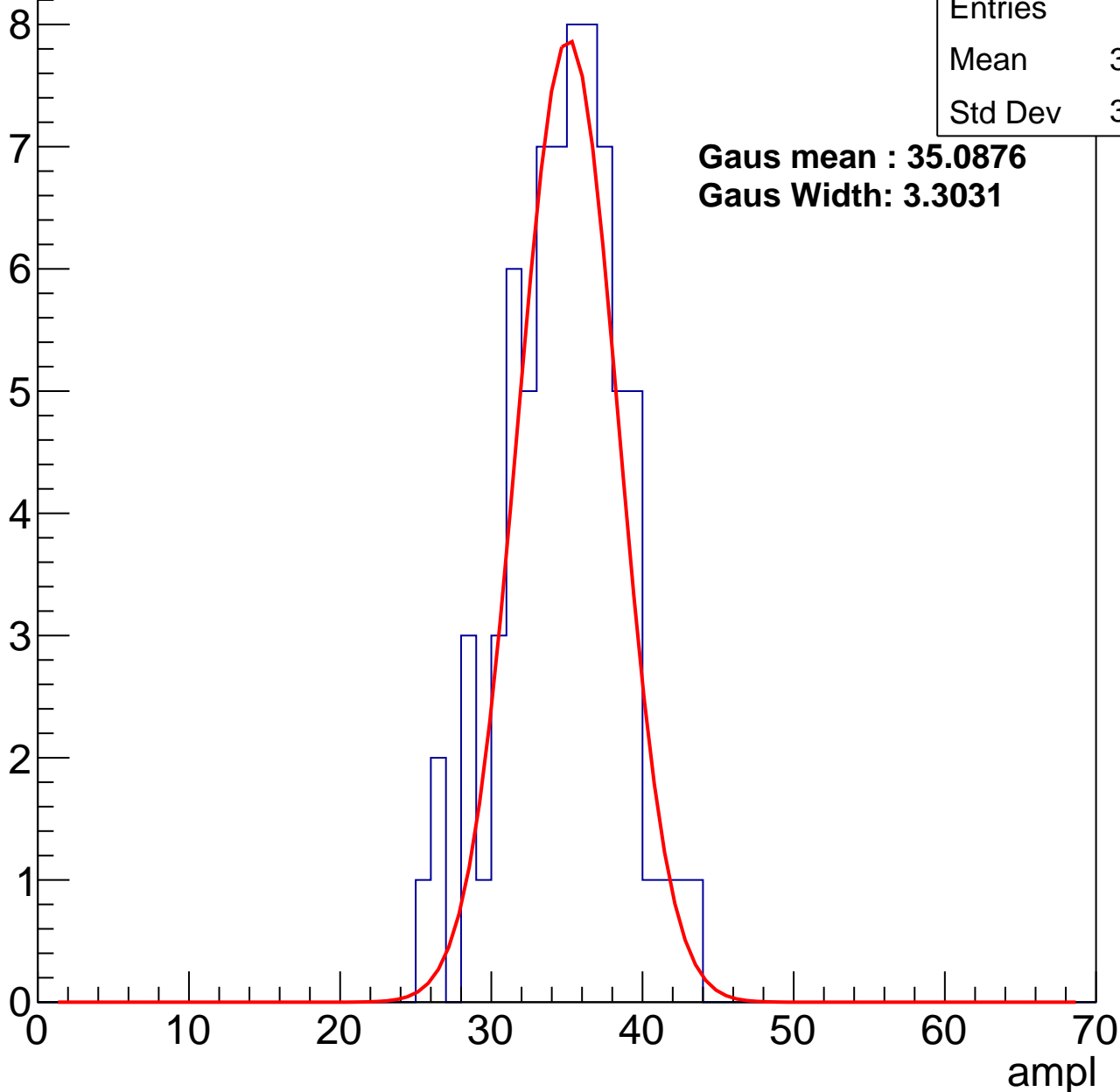
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	34.35
Std Dev	3.727

Gaus mean : 35.0876

Gaus Width: 3.3031



B1L003S, U20-ch33, adc2

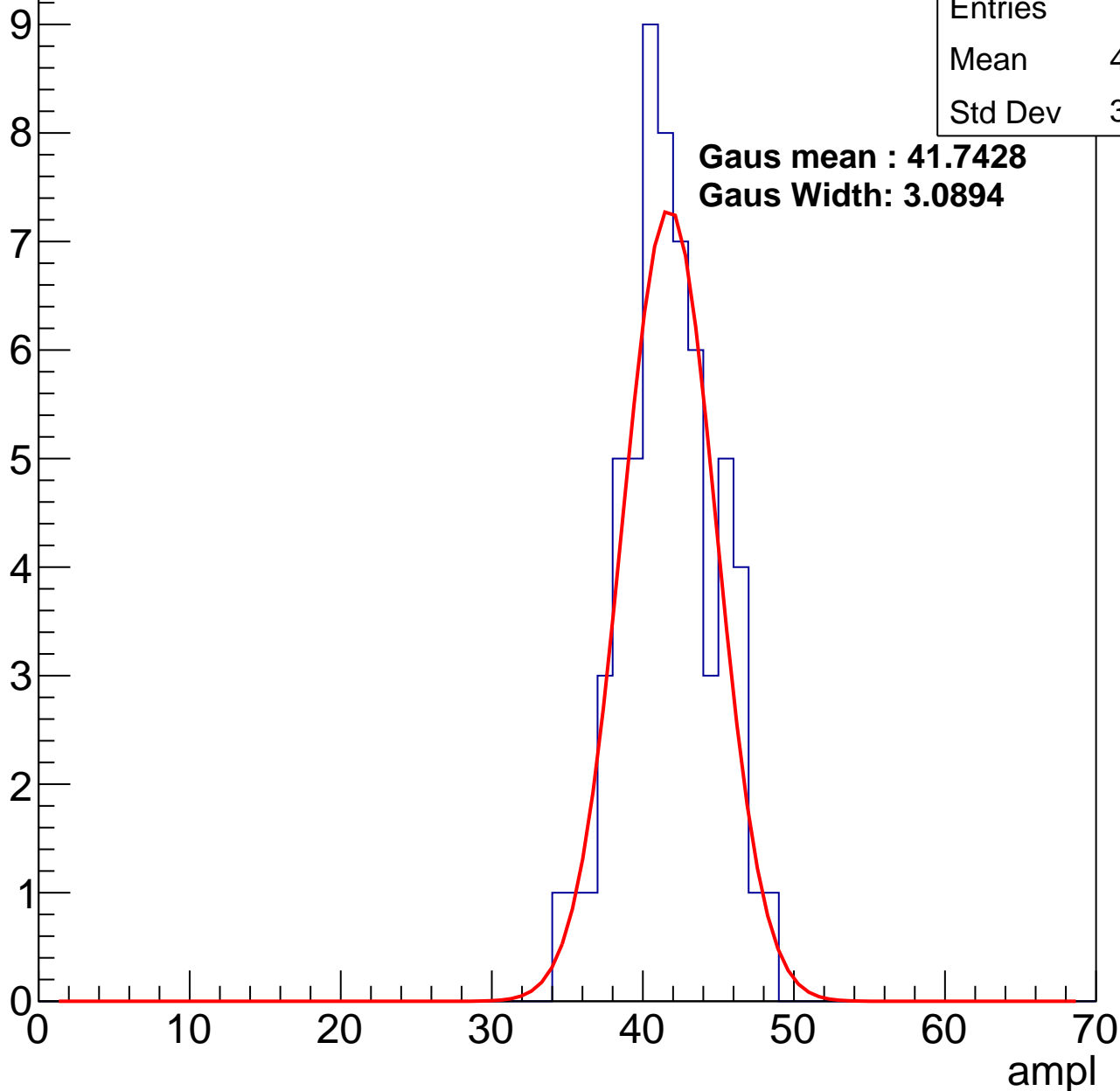
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	41.28
Std Dev	3.028

Gaus mean : 41.7428

Gaus Width: 3.0894

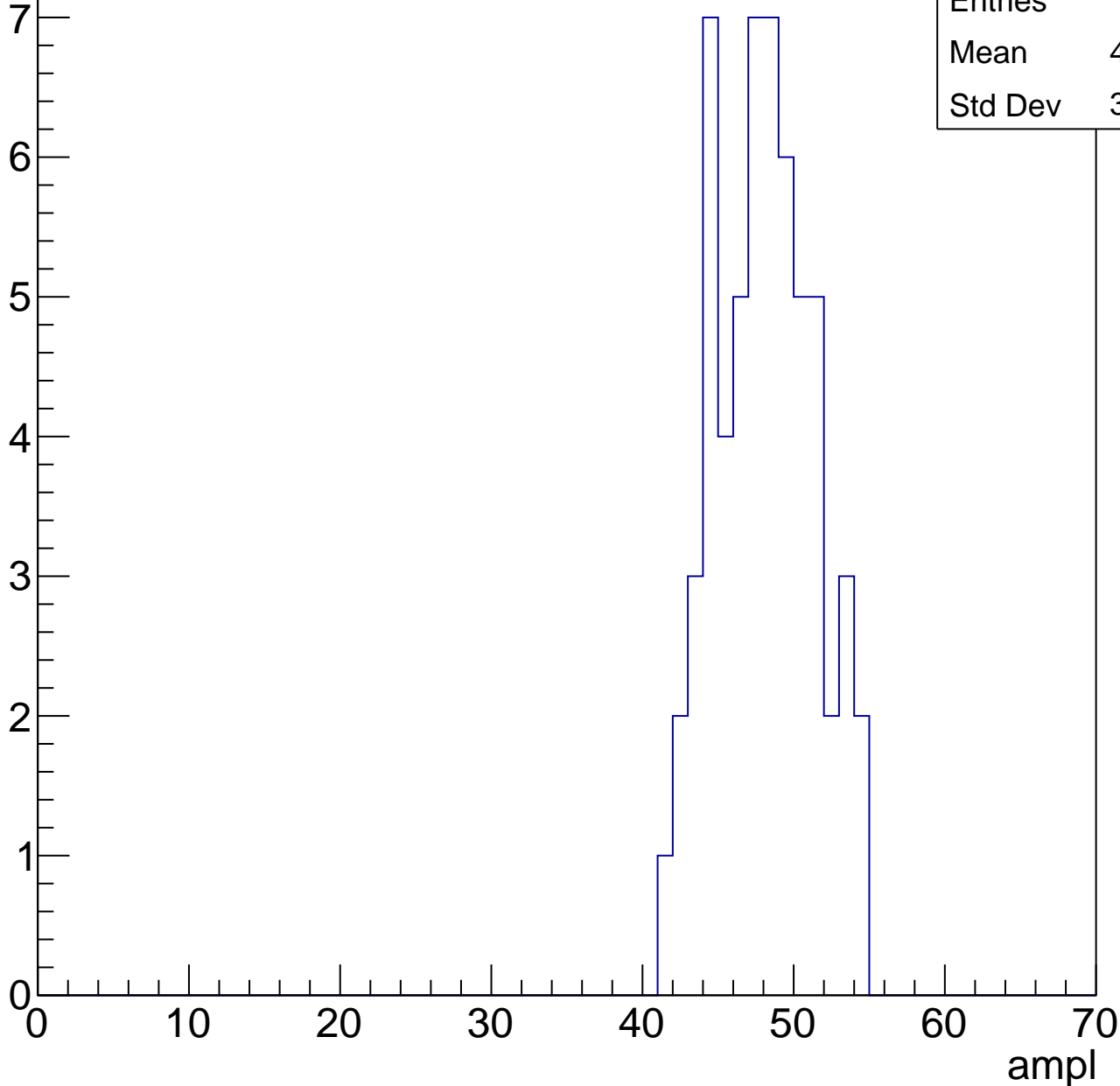


B1L003S, U20-ch33, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	47.58
Std Dev	3.217

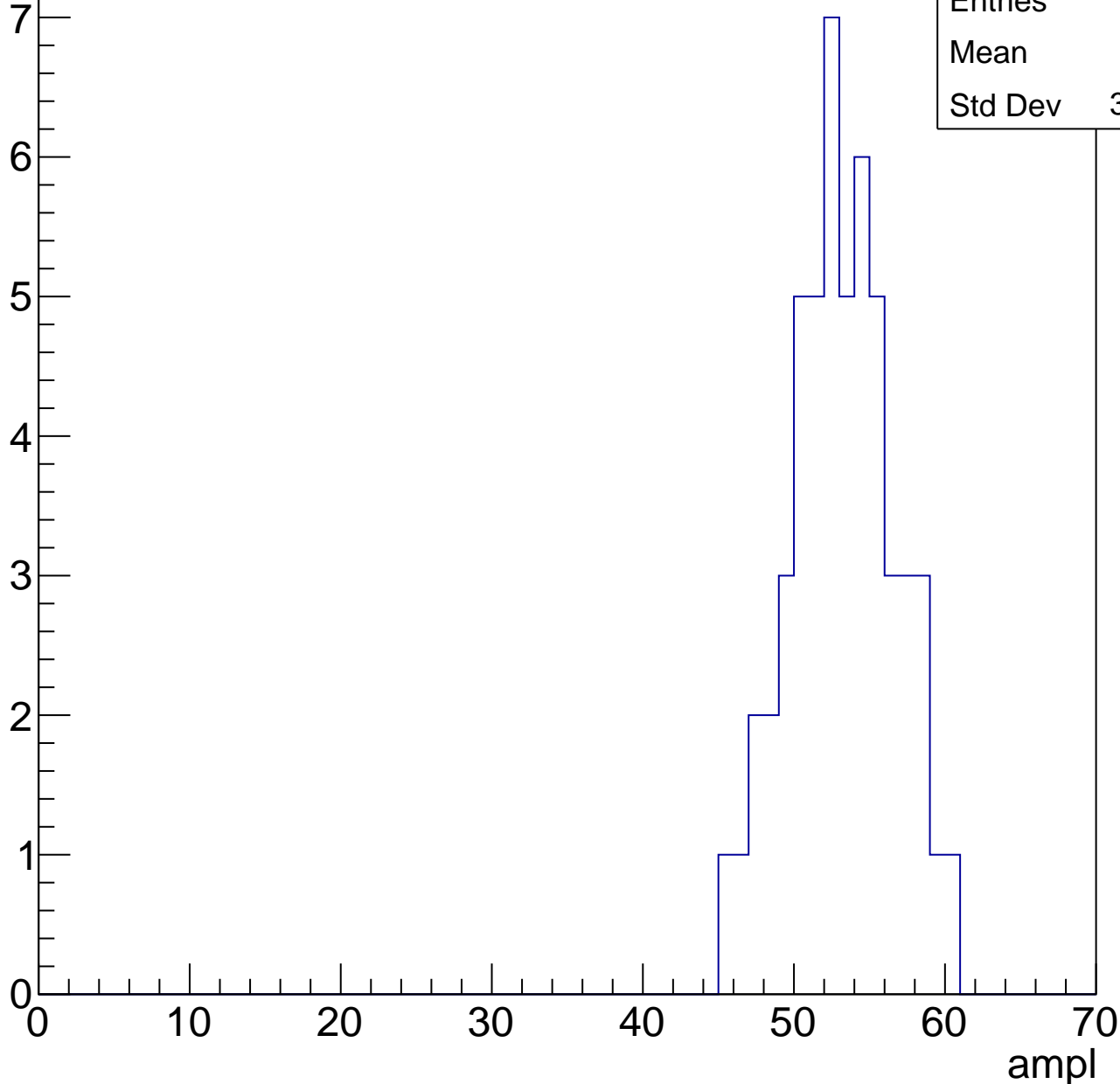


B1L003S, U20-ch33, adc4

calib_packv5_042523_0143.root, FC#13, port D2

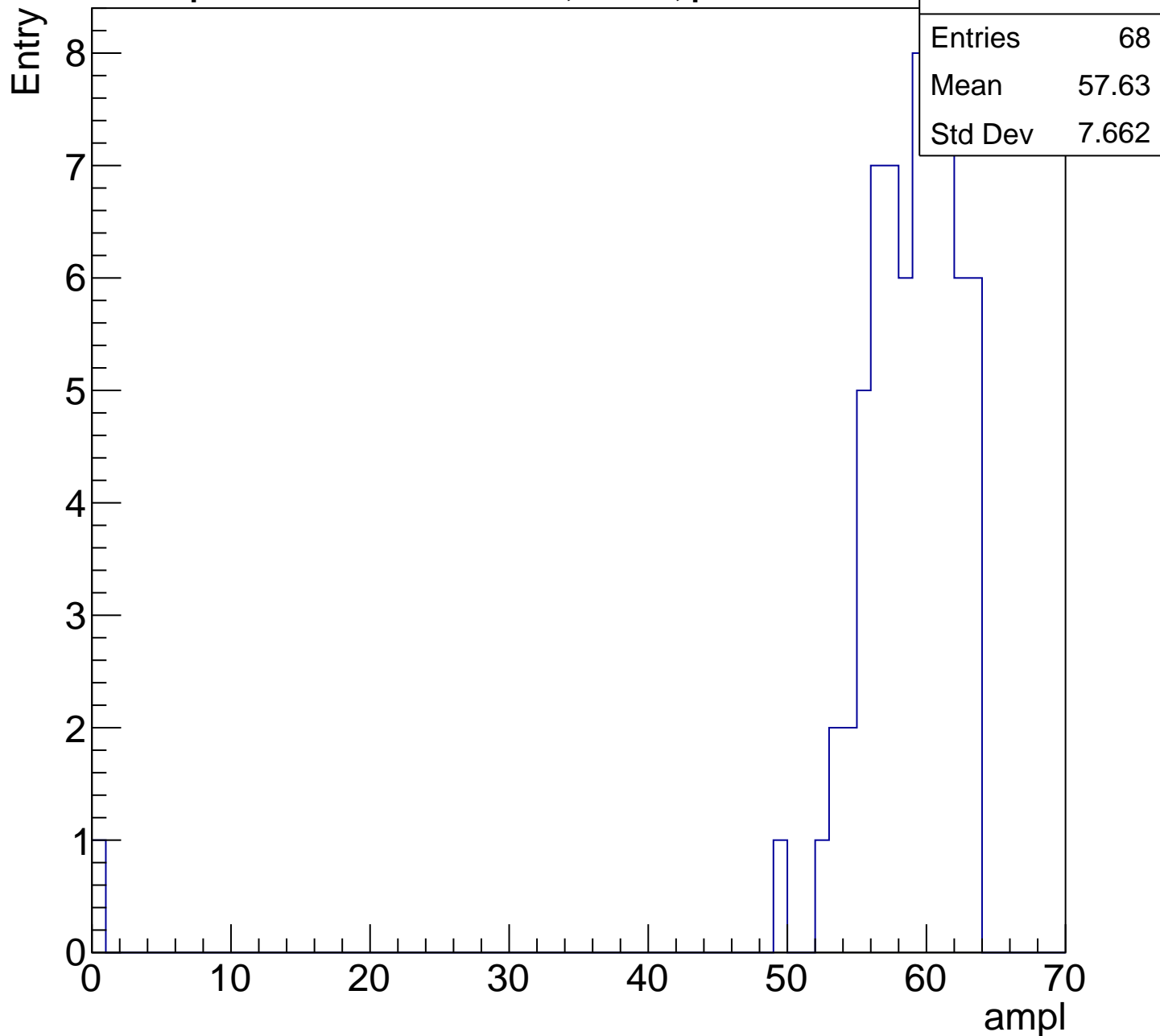
Entry

Entries	53
Mean	52.7
Std Dev	3.396



B1L003S, U20-ch33, adc5

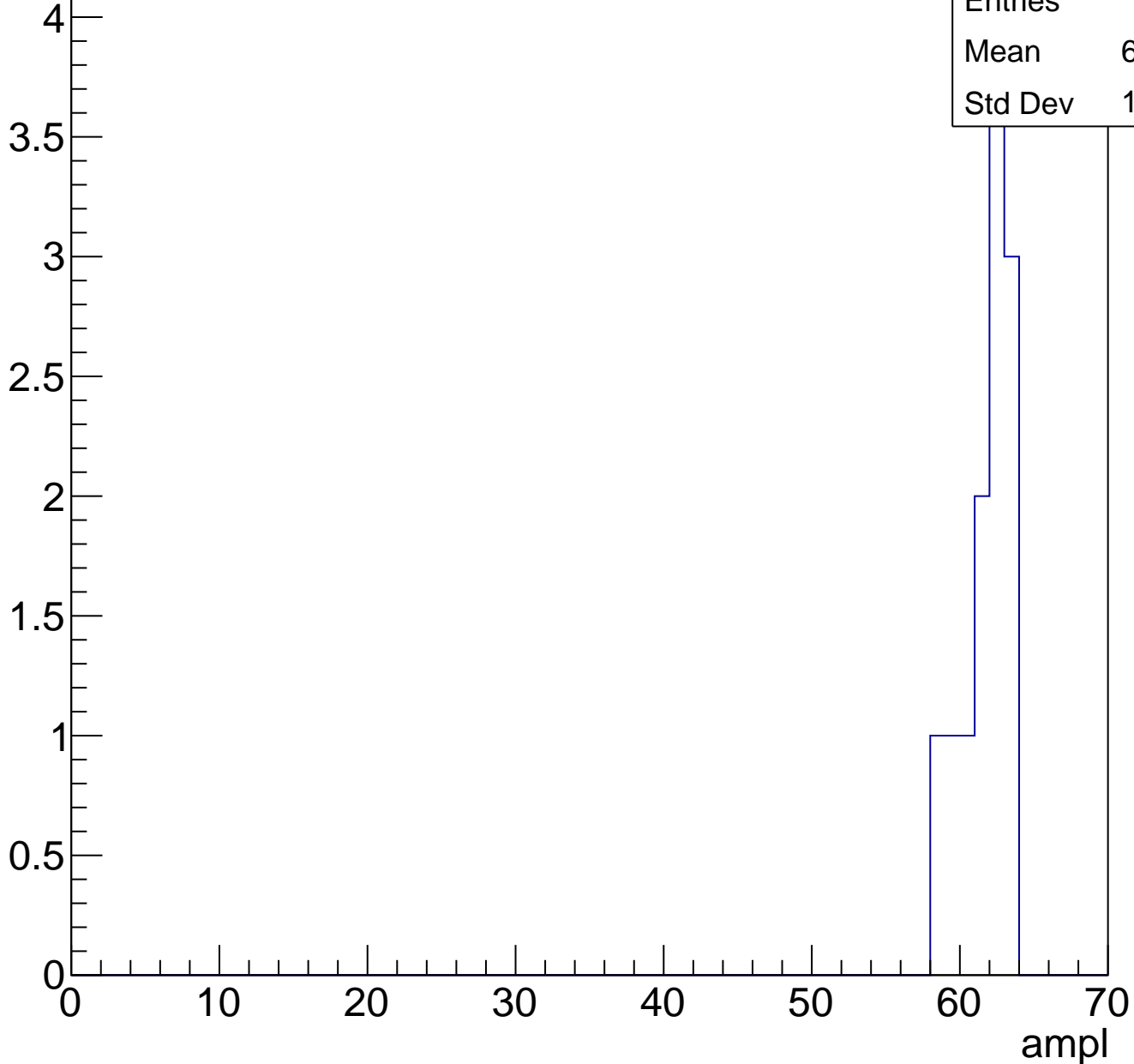
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch33, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch33, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U20-ch34, adc0

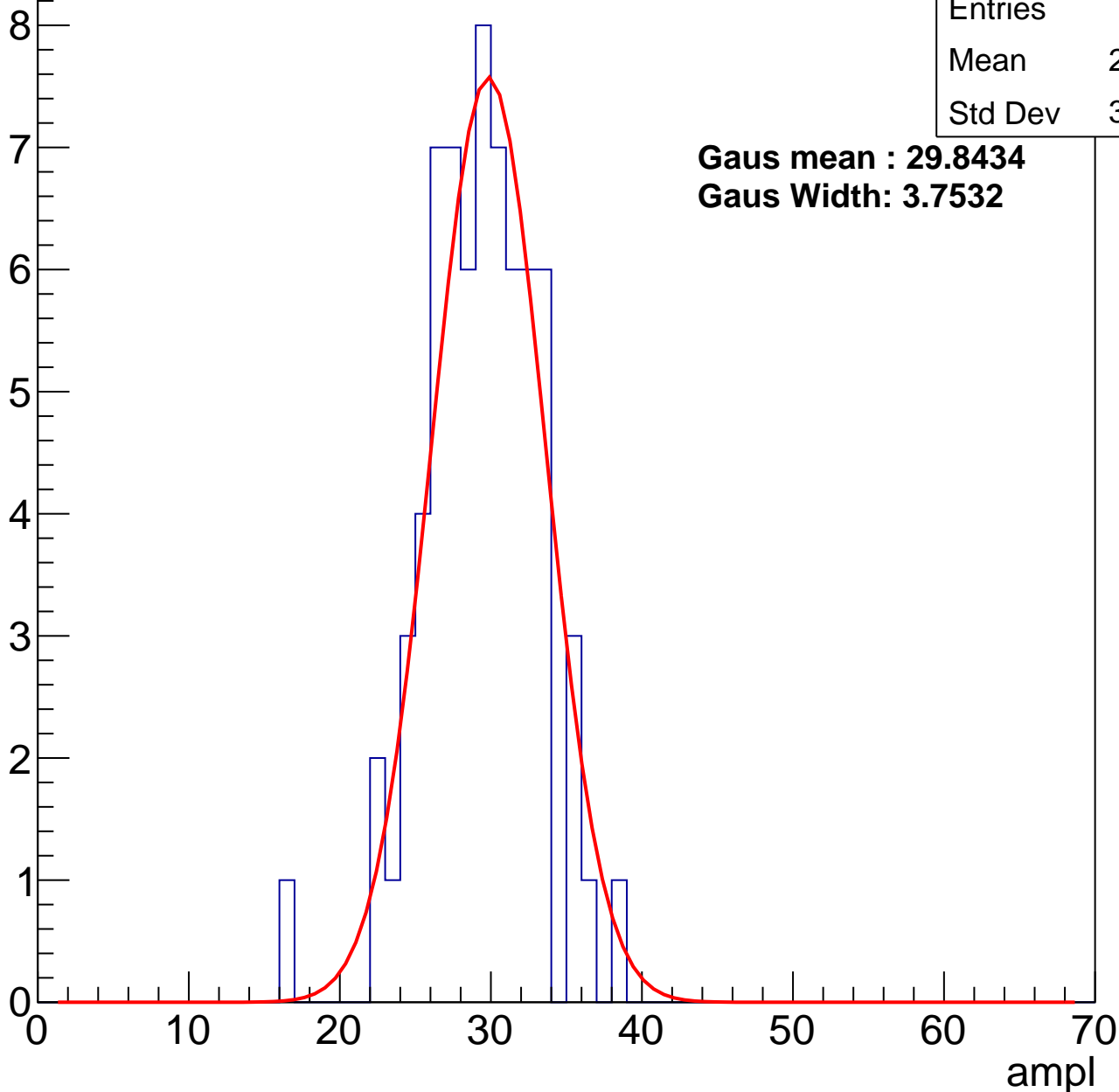
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	28.86
Std Dev	3.723

Gaus mean : 29.8434

Gaus Width: 3.7532



B1L003S, U20-ch34, adc1

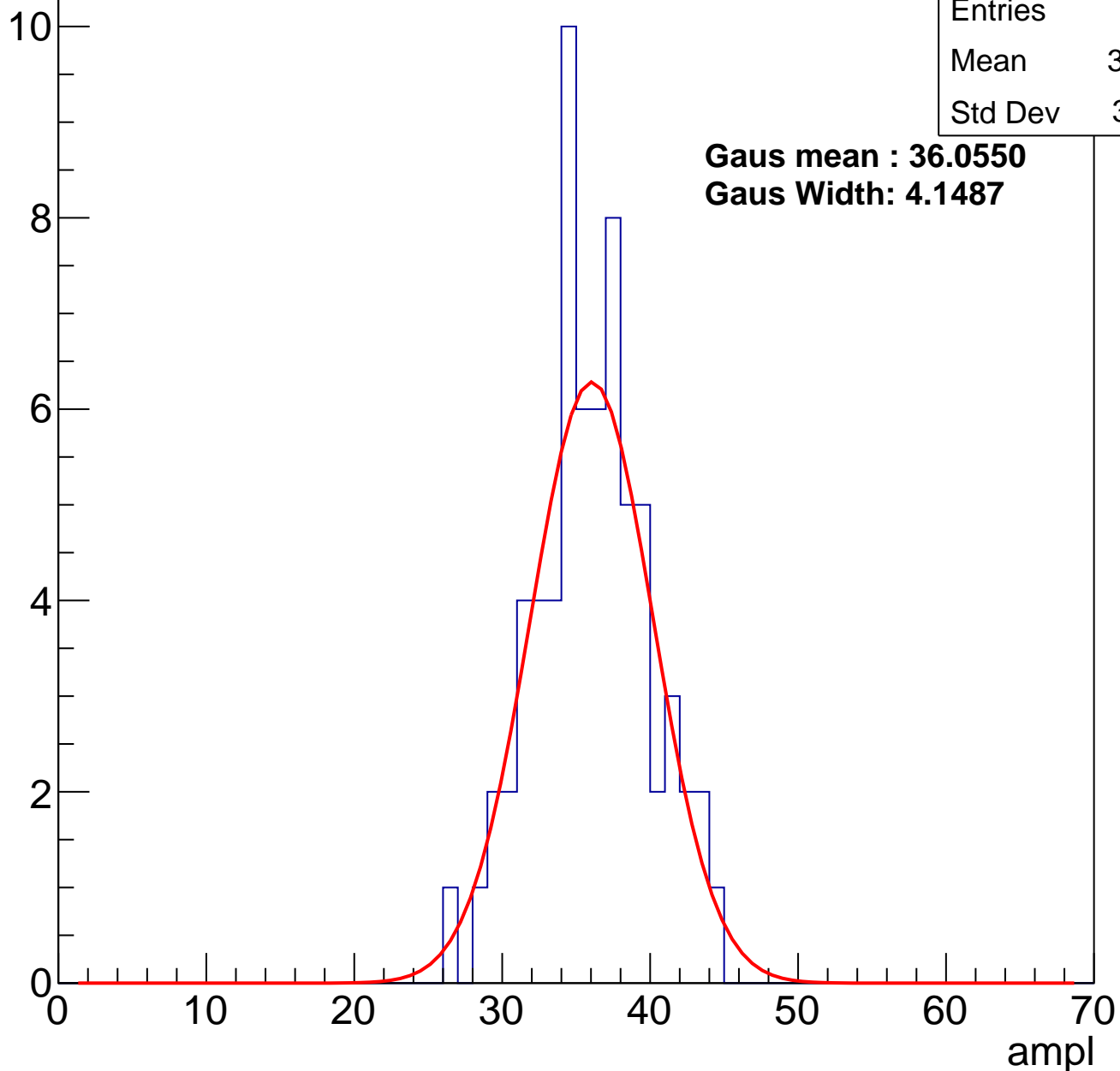
calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	35.59
Std Dev	3.801

Gaus mean : 36.0550

Gaus Width: 4.1487

Entry



B1L003S, U20-ch34, adc2

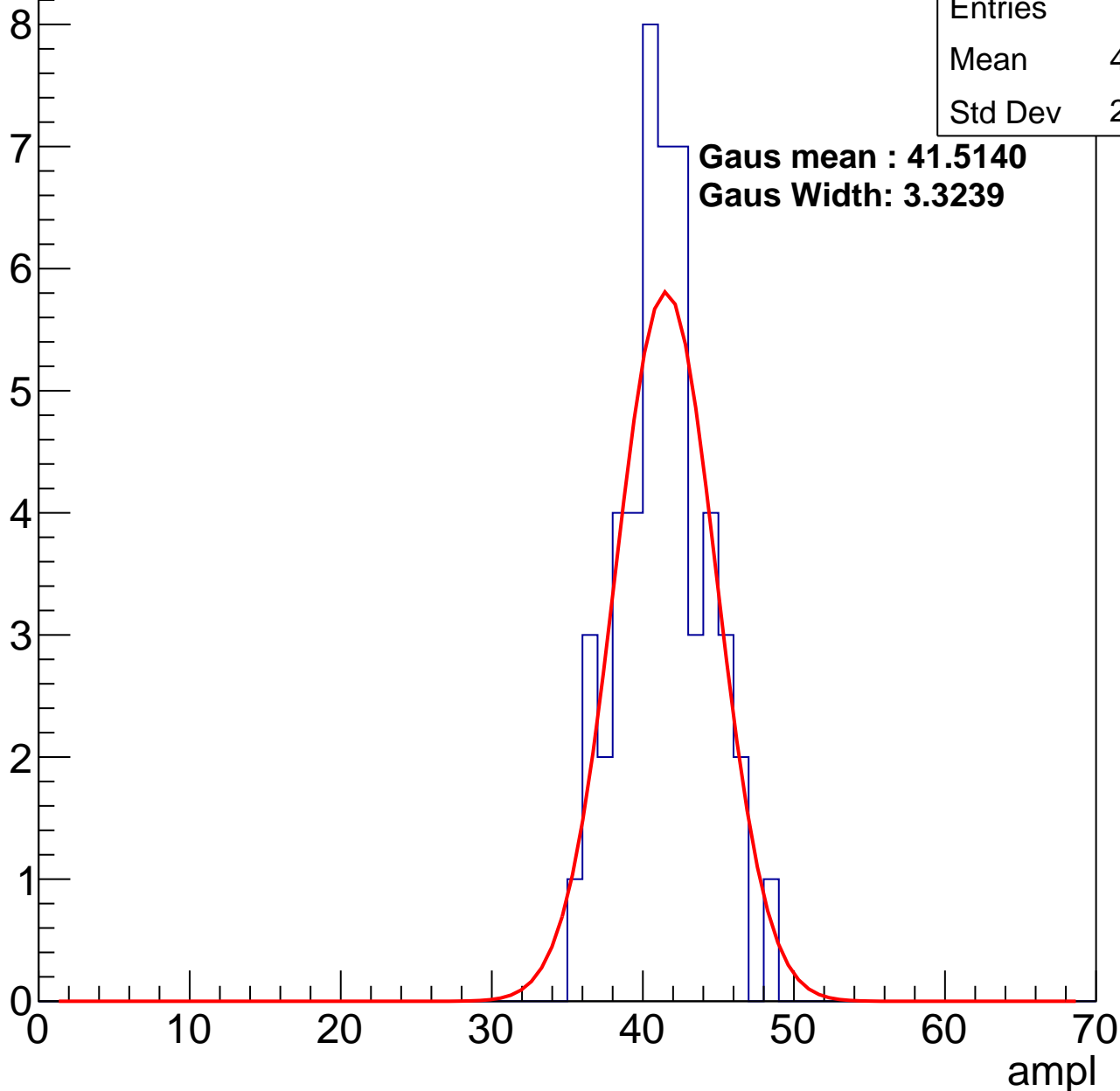
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	40.94
Std Dev	2.874

Gaus mean : 41.5140

Gaus Width: 3.3239



B1L003S, U20-ch34, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	93
Mean	48.46
Std Dev	3.307

Entry

10

8

6

4

2

0

0

10

20

30

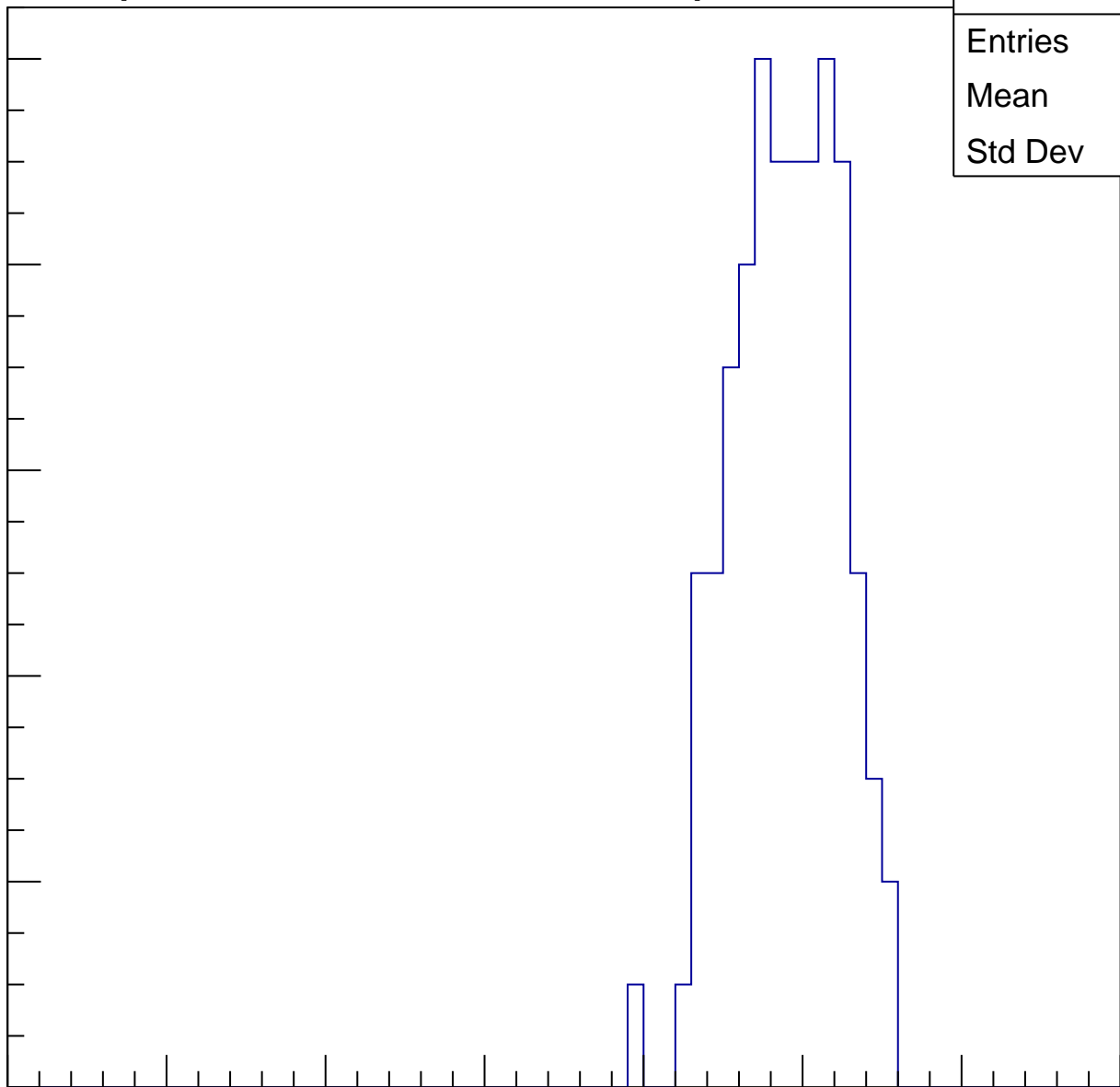
40

50

60

70

ampl

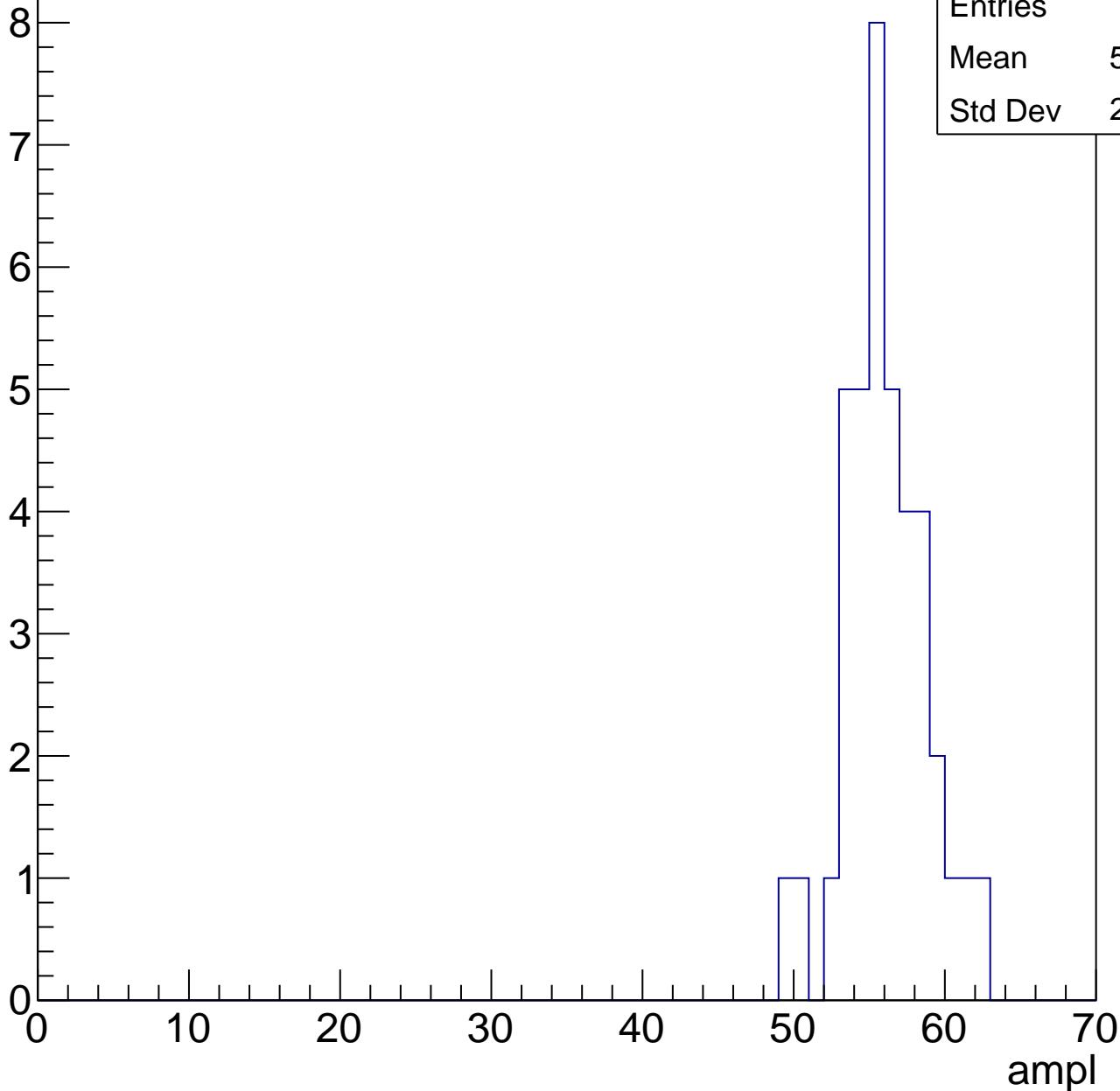


B1L003S, U20-ch34, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	55.56
Std Dev	2.687

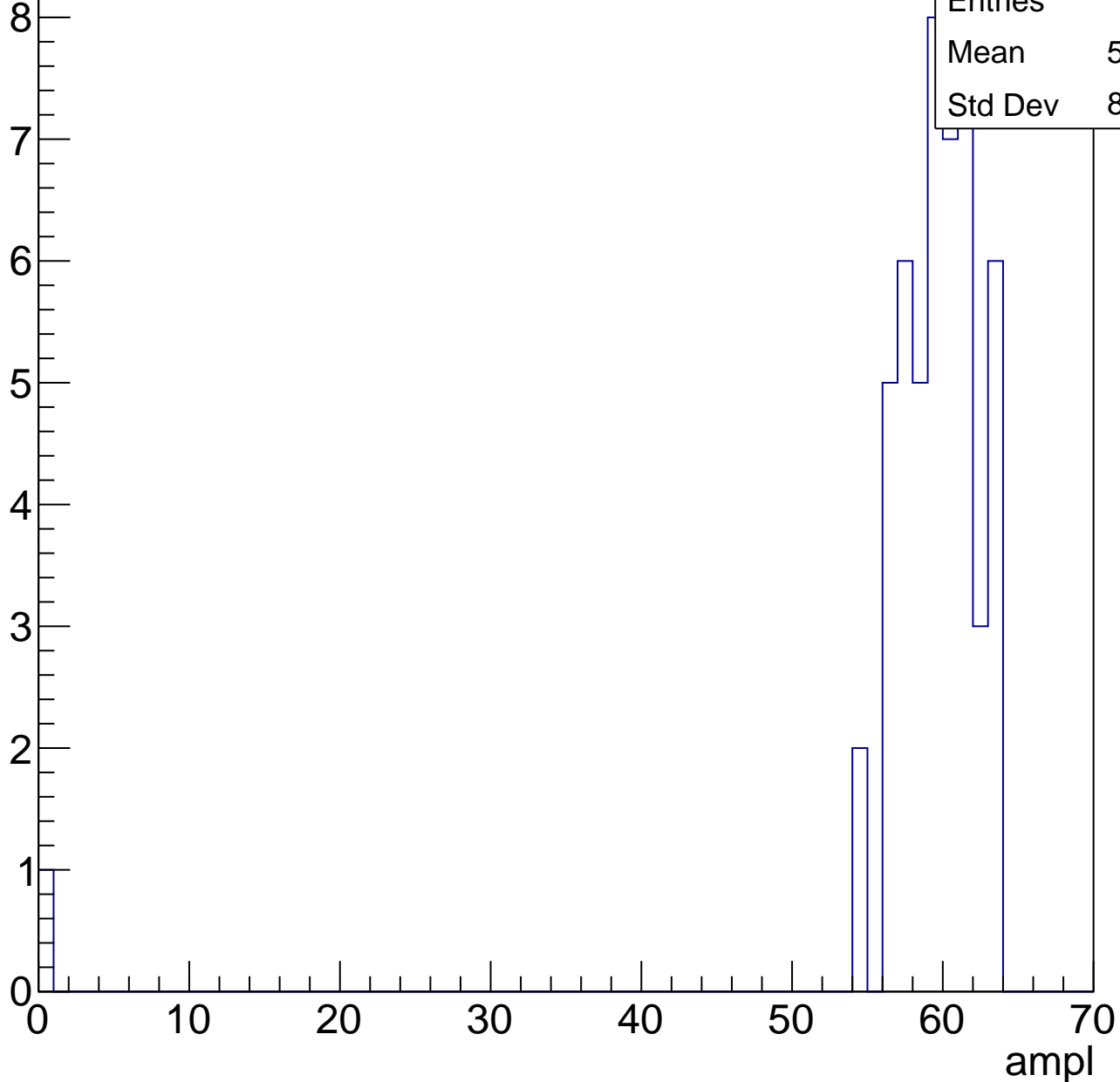


B1L003S, U20-ch34, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

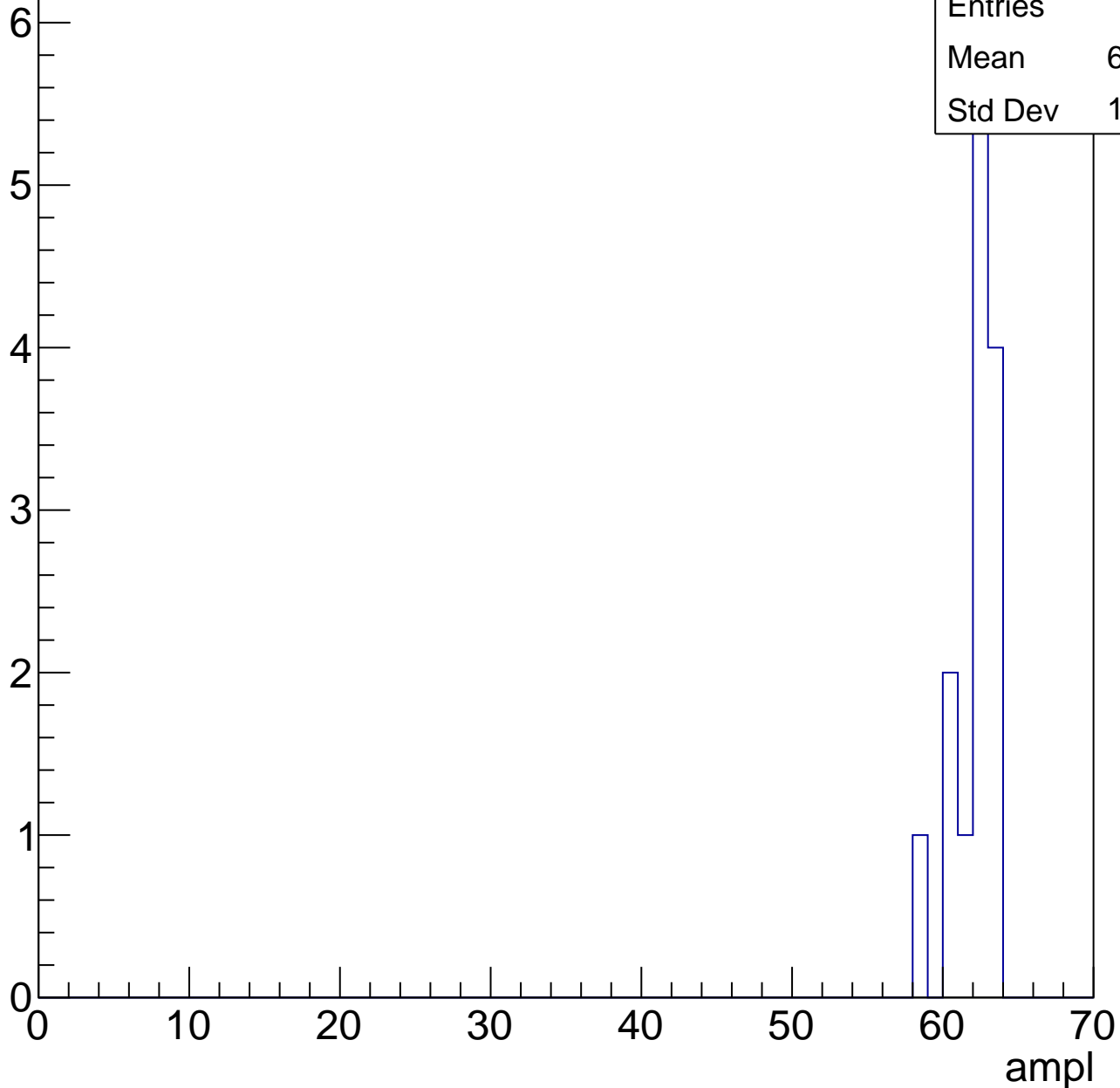
Entries	51
Mean	58.12
Std Dev	8.549



B1L003S, U20-ch34, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch34, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U20-ch35, adc0

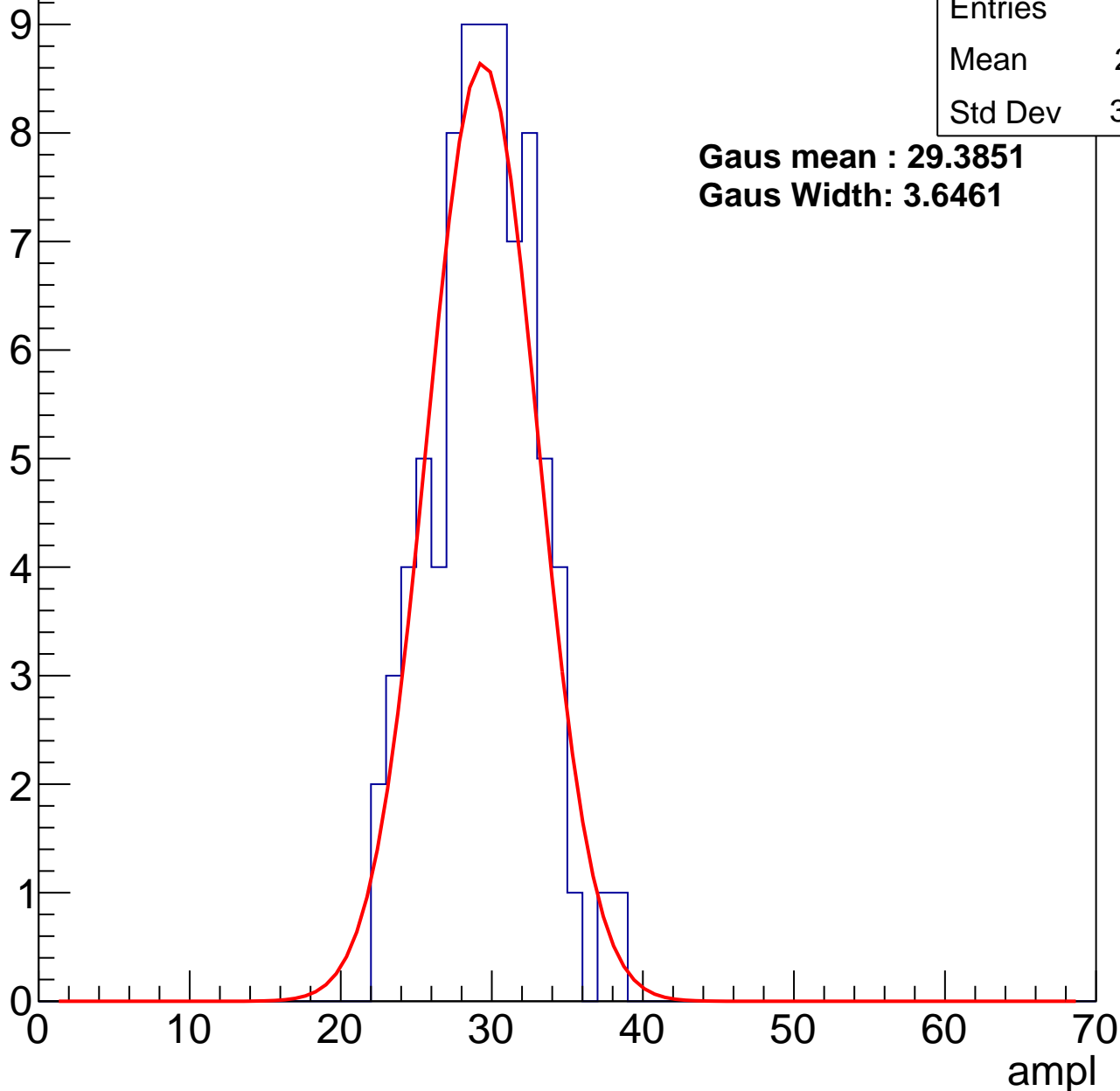
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.01
Std Dev	3.415

Gaus mean : 29.3851

Gaus Width: 3.6461



B1L003S, U20-ch35, adc1

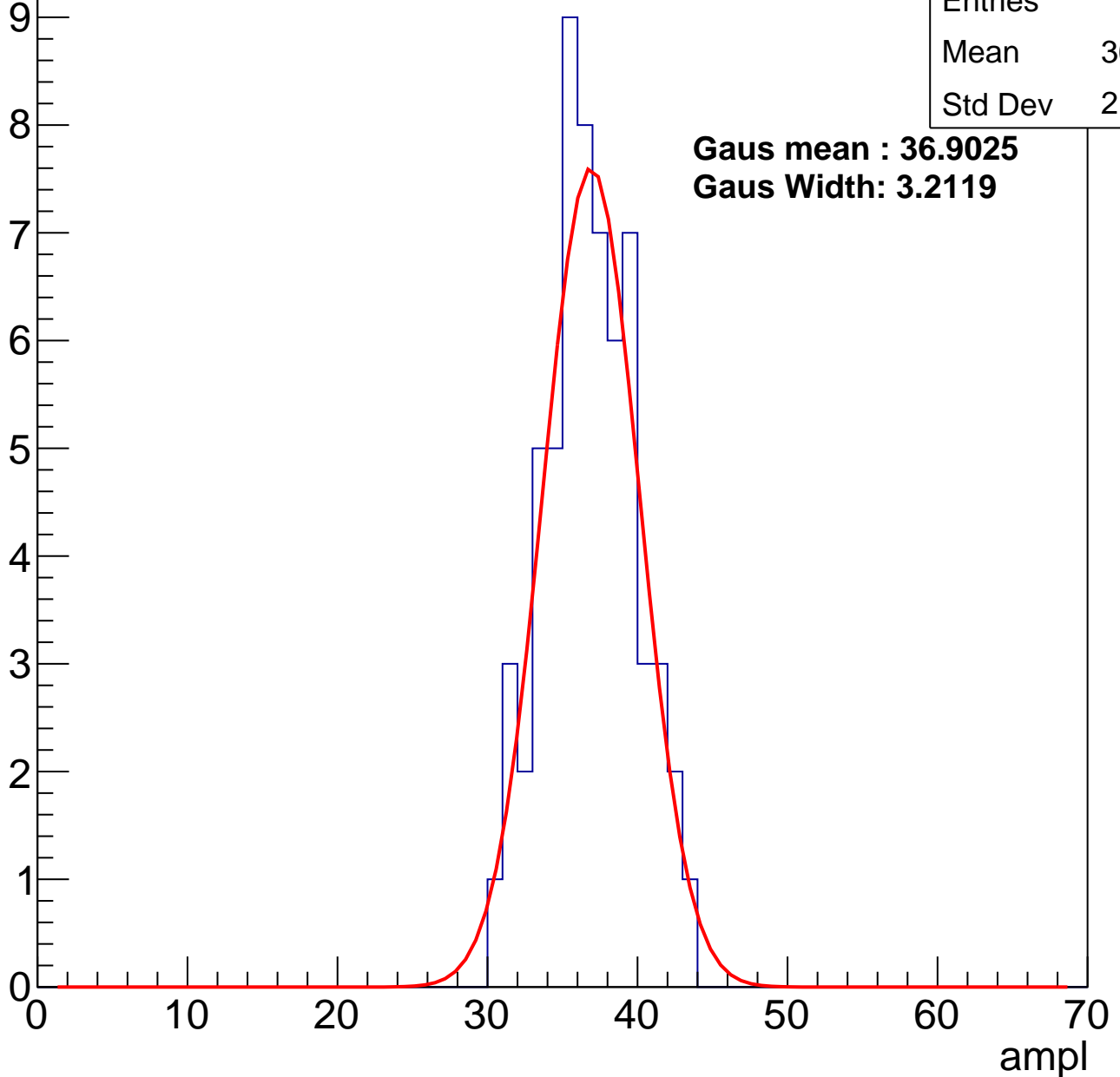
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.37
Std Dev	2.969

Gaus mean : 36.9025

Gaus Width: 3.2119



B1L003S, U20-ch35, adc2

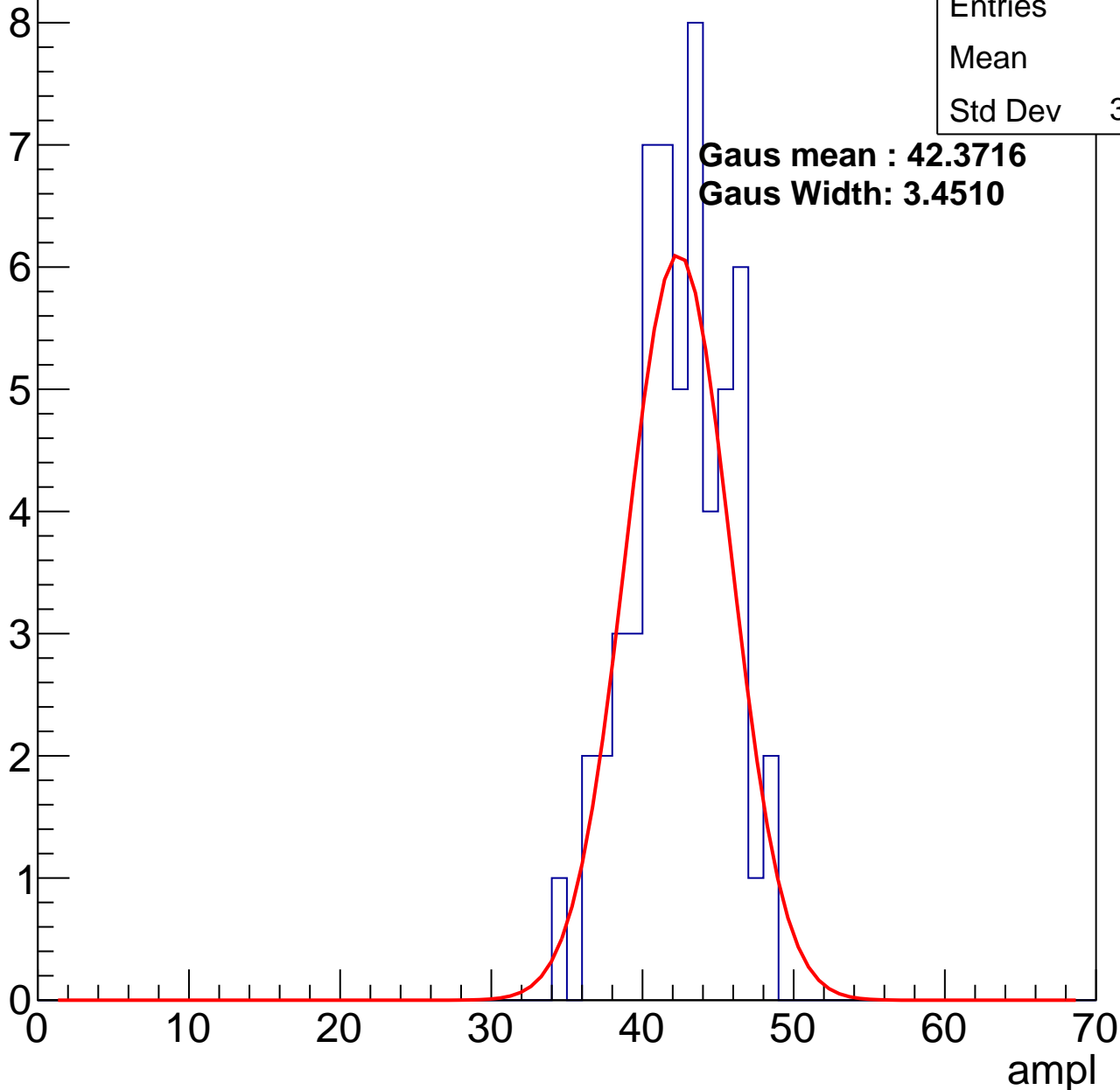
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42
Std Dev	3.157

Gaus mean : 42.3716

Gaus Width: 3.4510

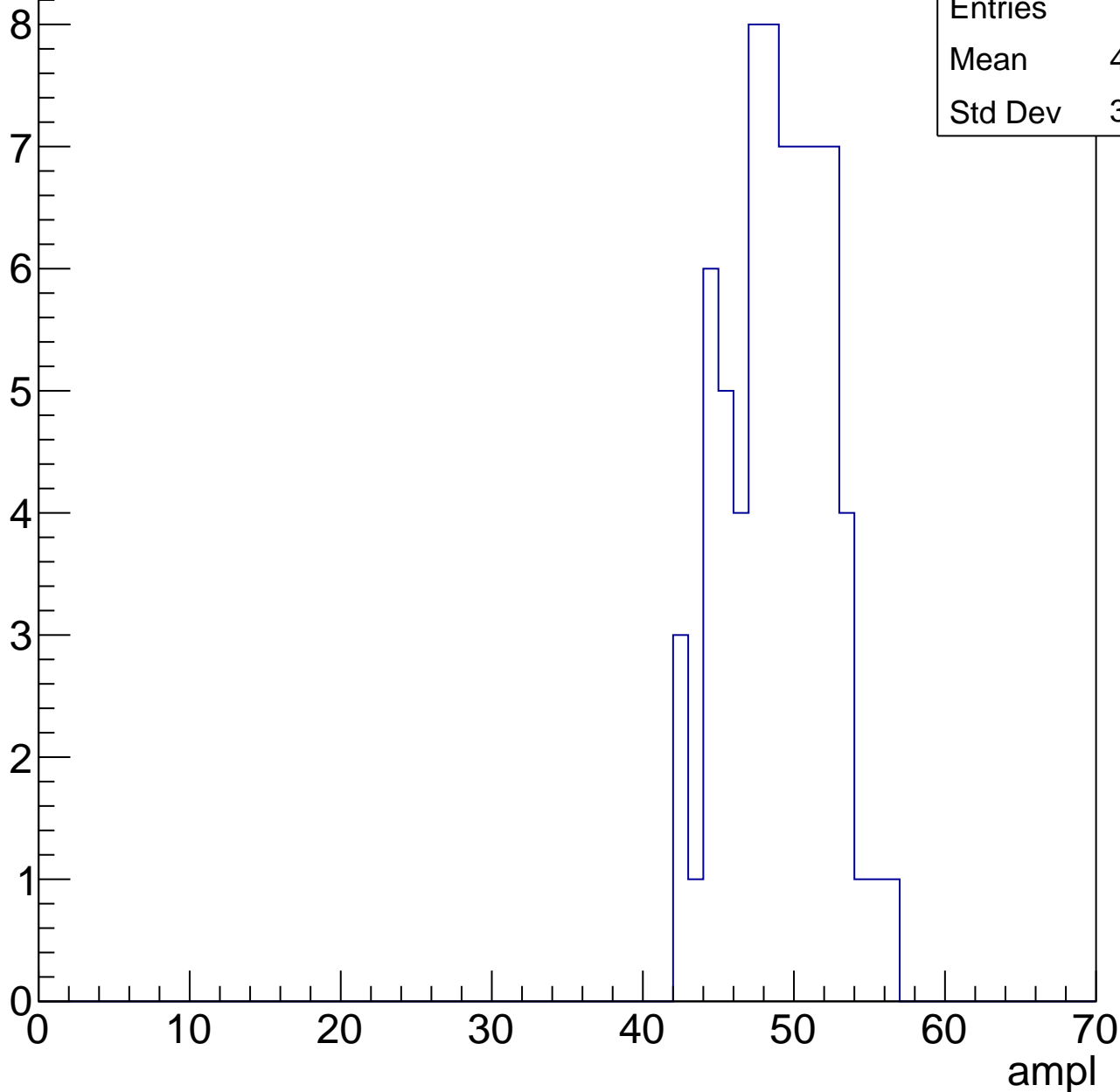


B1L003S, U20-ch35, adc3

calib_packv5_042523_0143.root, FC#13, port D2

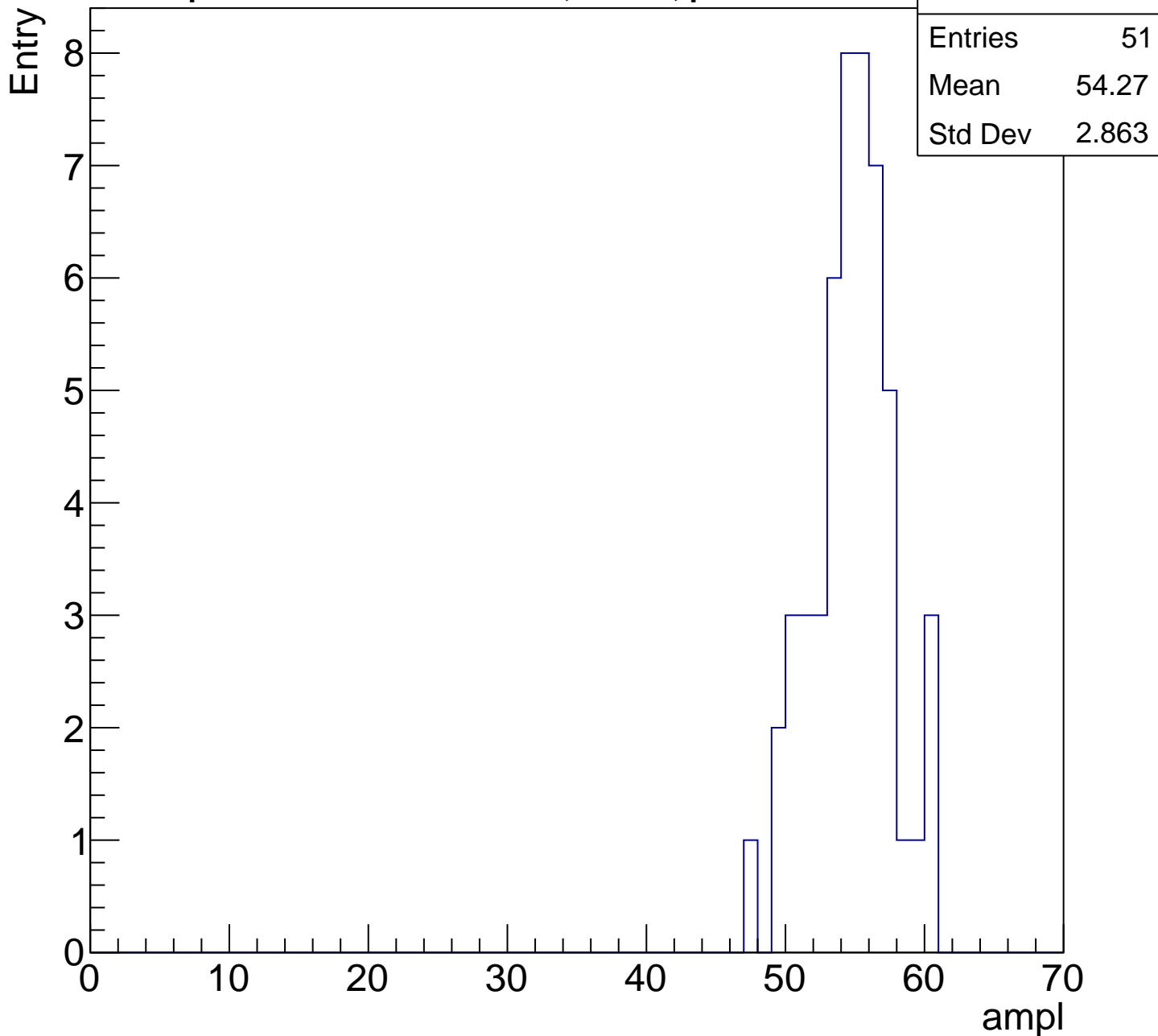
Entry

Entries	70
Mean	48.47
Std Dev	3.255



B1L003S, U20-ch35, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch35, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	57
Mean	59.4
Std Dev	2.519

Entry

10

8

6

4

2

0

0

10

20

30

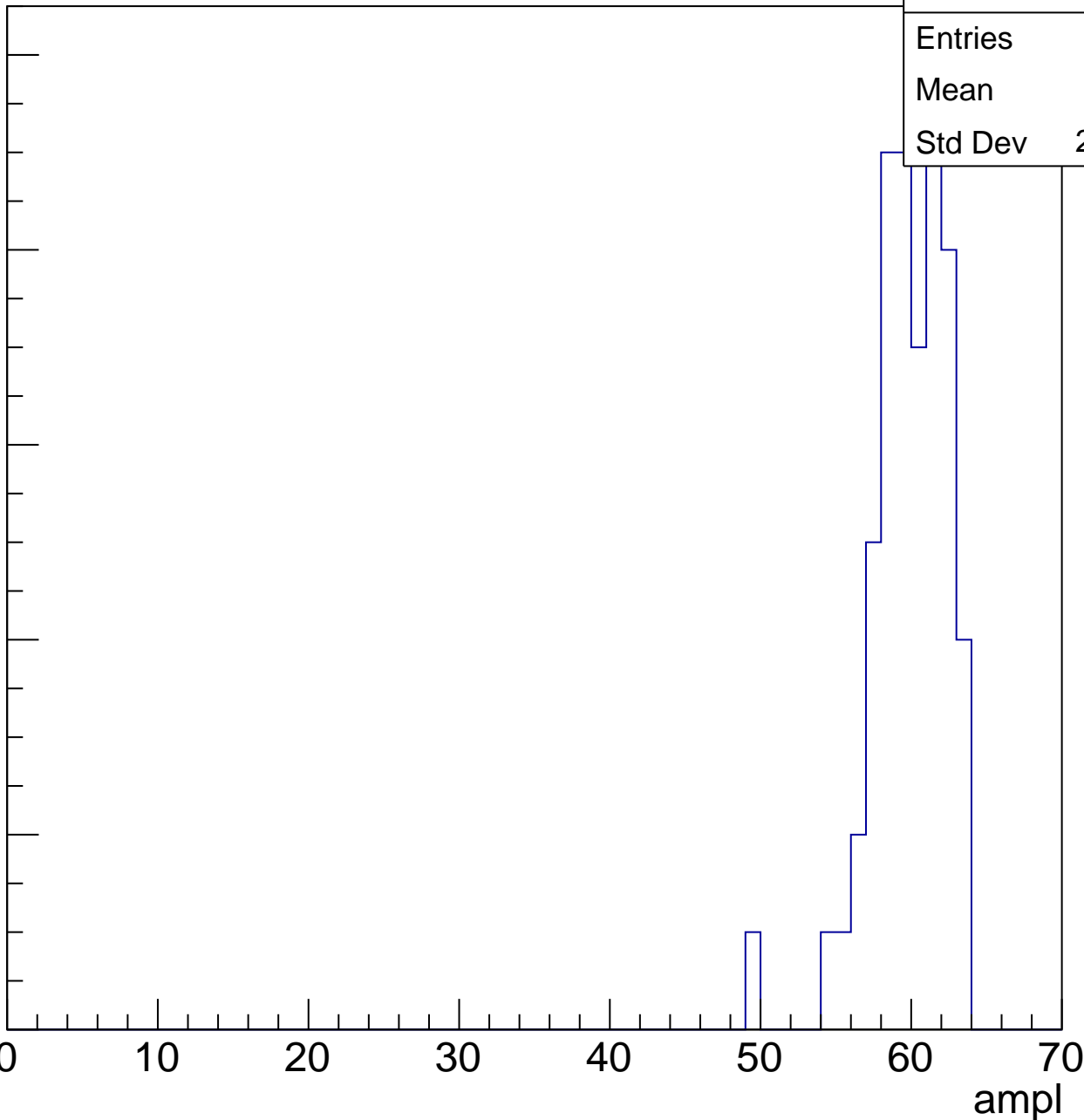
40

50

60

70

ampl

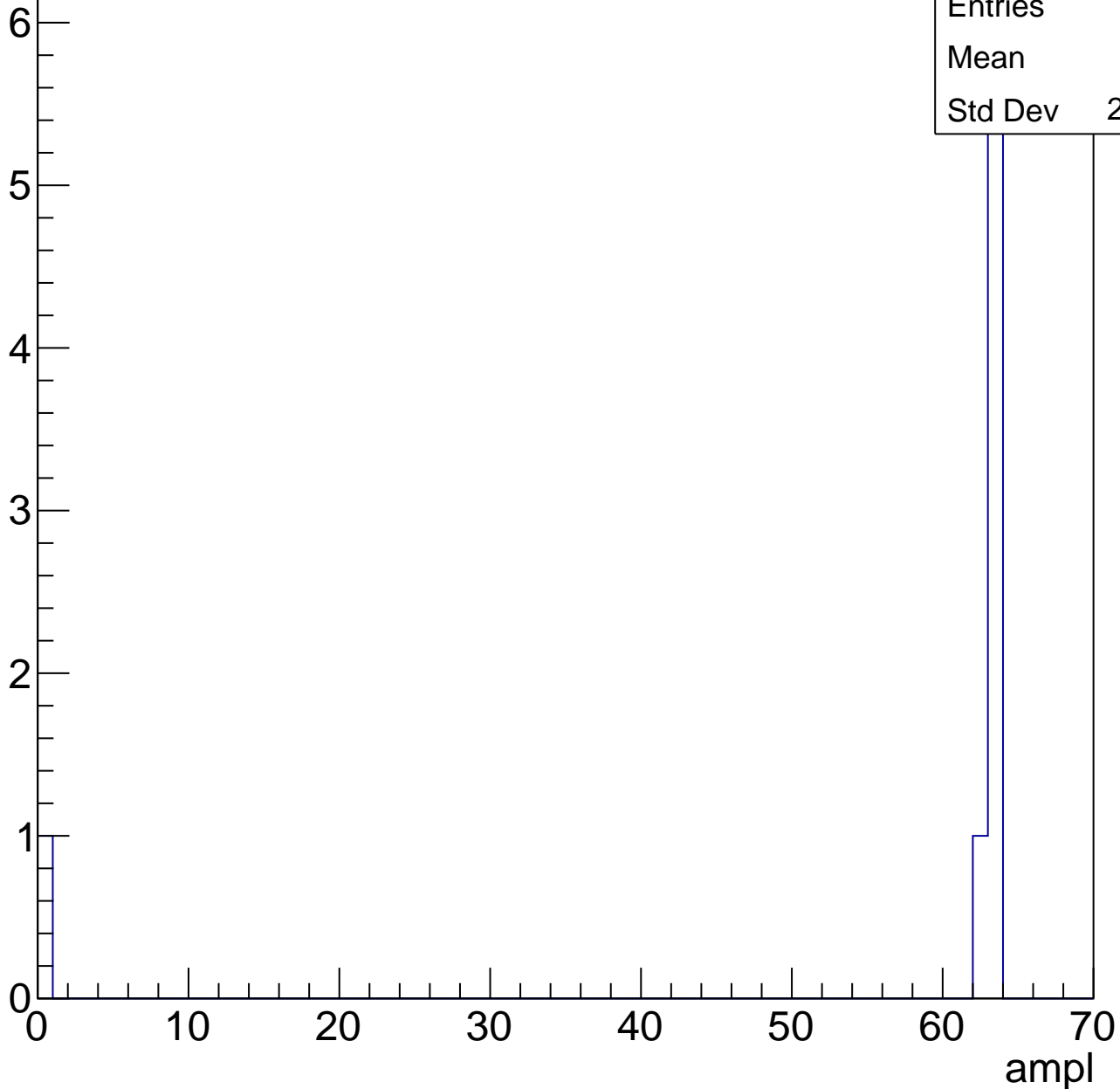


B1L003S, U20-ch35, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

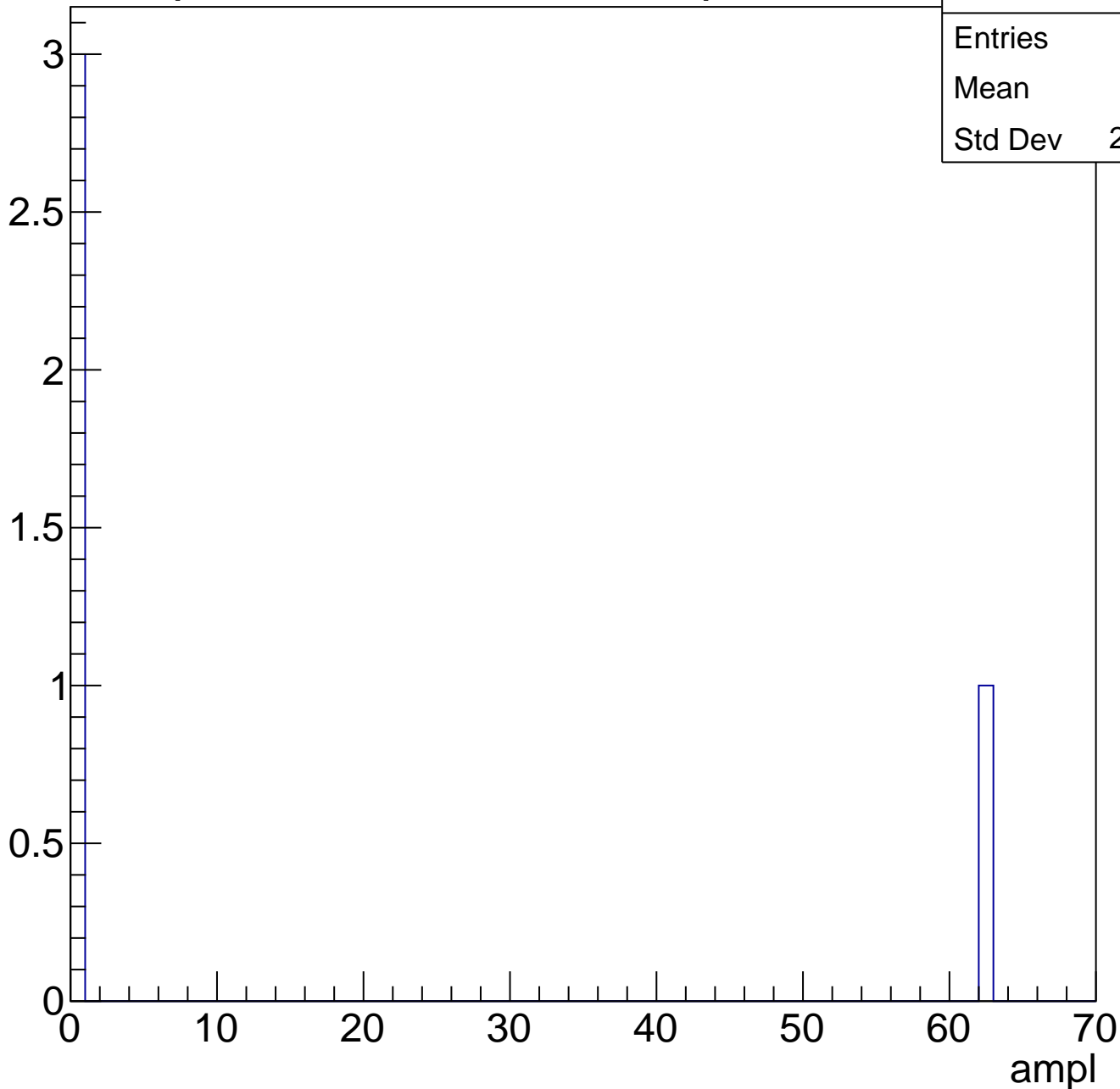
Entries	8
Mean	55
Std Dev	20.79



B1L003S, U20-ch35, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch36, adc0

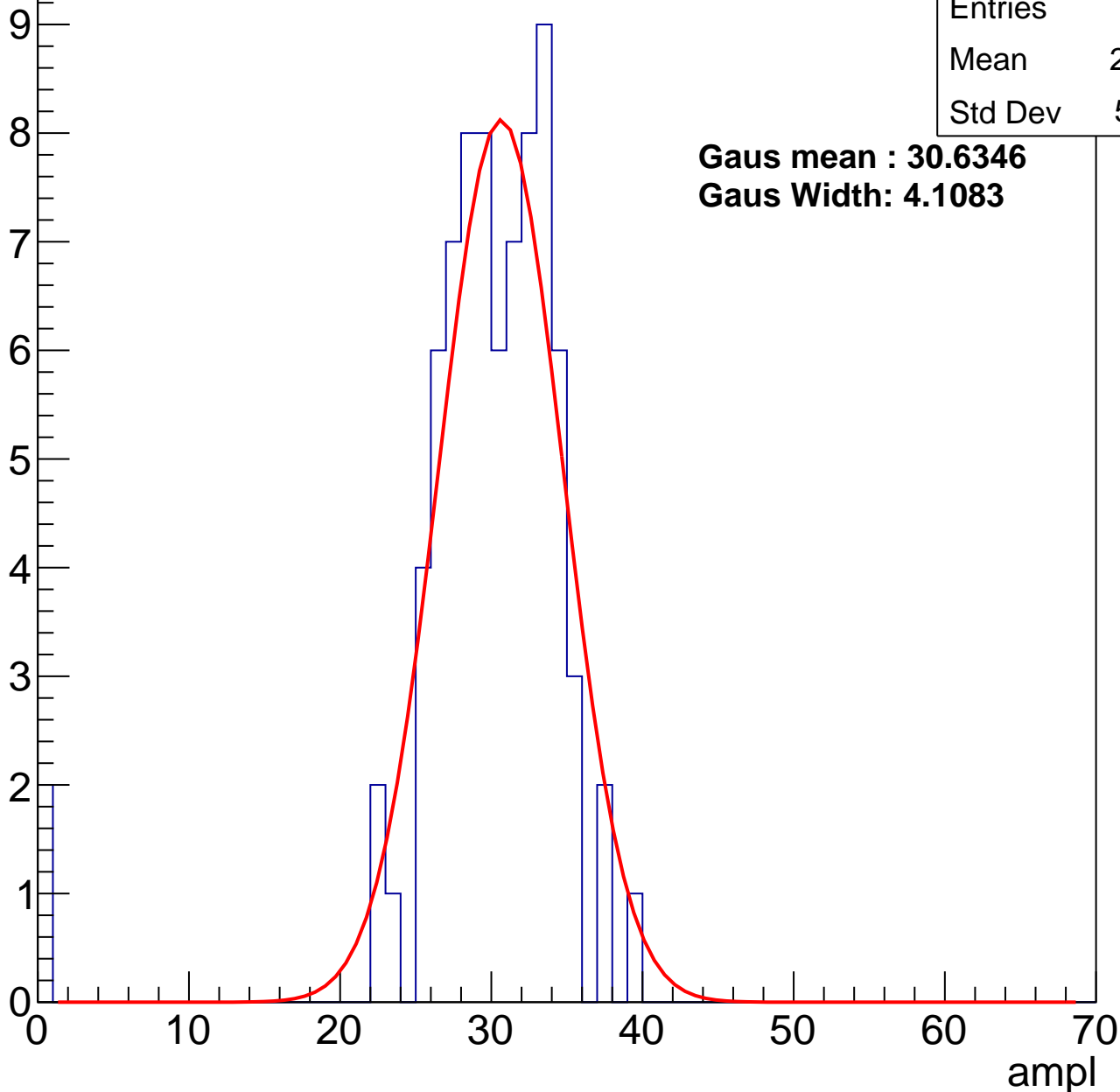
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.25
Std Dev	5.811

Gaus mean : 30.6346

Gaus Width: 4.1083



B1L003S, U20-ch36, adc1

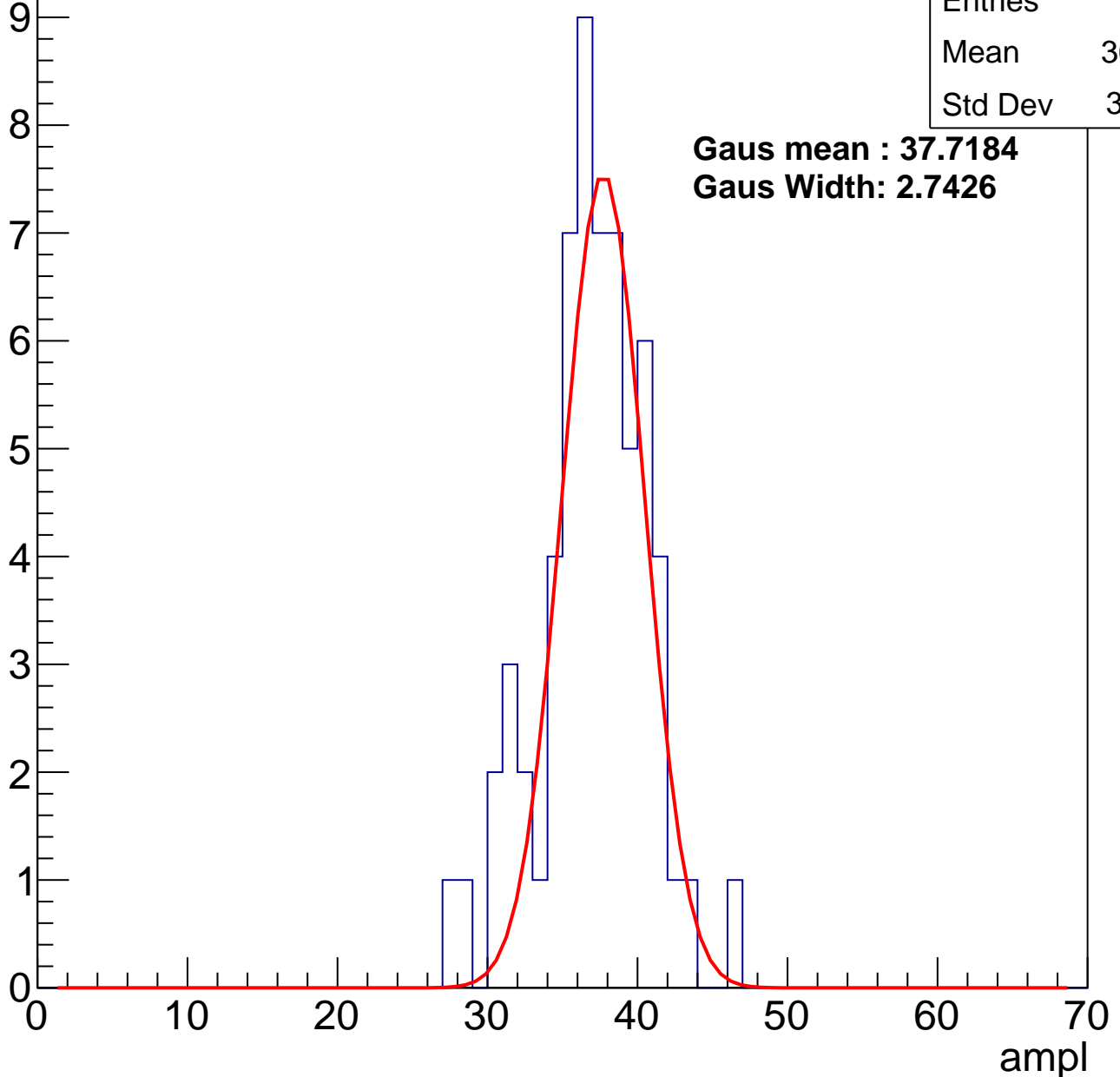
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.53
Std Dev	3.591

Gaus mean : 37.7184

Gaus Width: 2.7426



B1L003S, U20-ch36, adc2

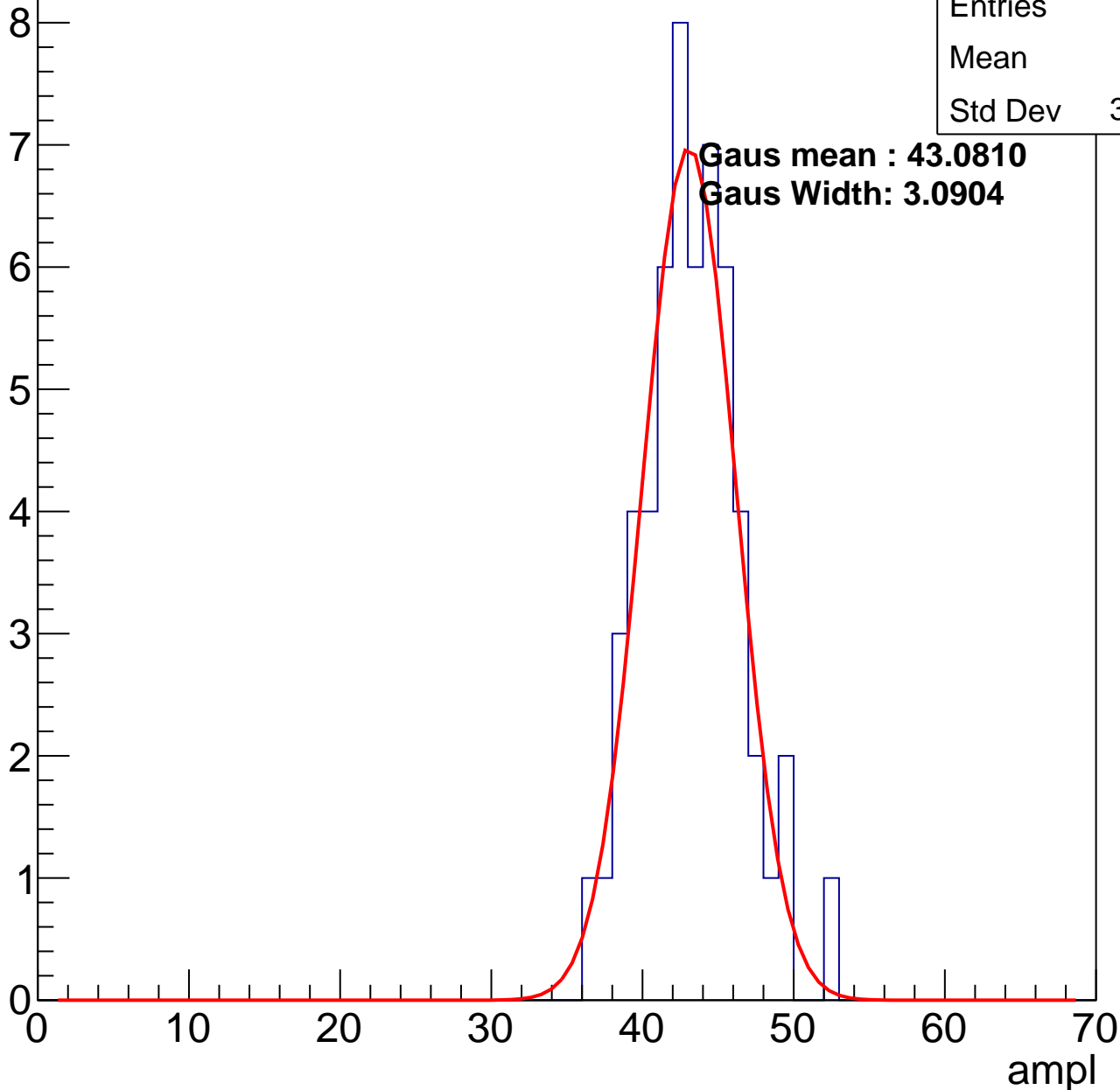
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.8
Std Dev	3.182

Gaus mean : 43.0810

Gaus Width: 3.0904

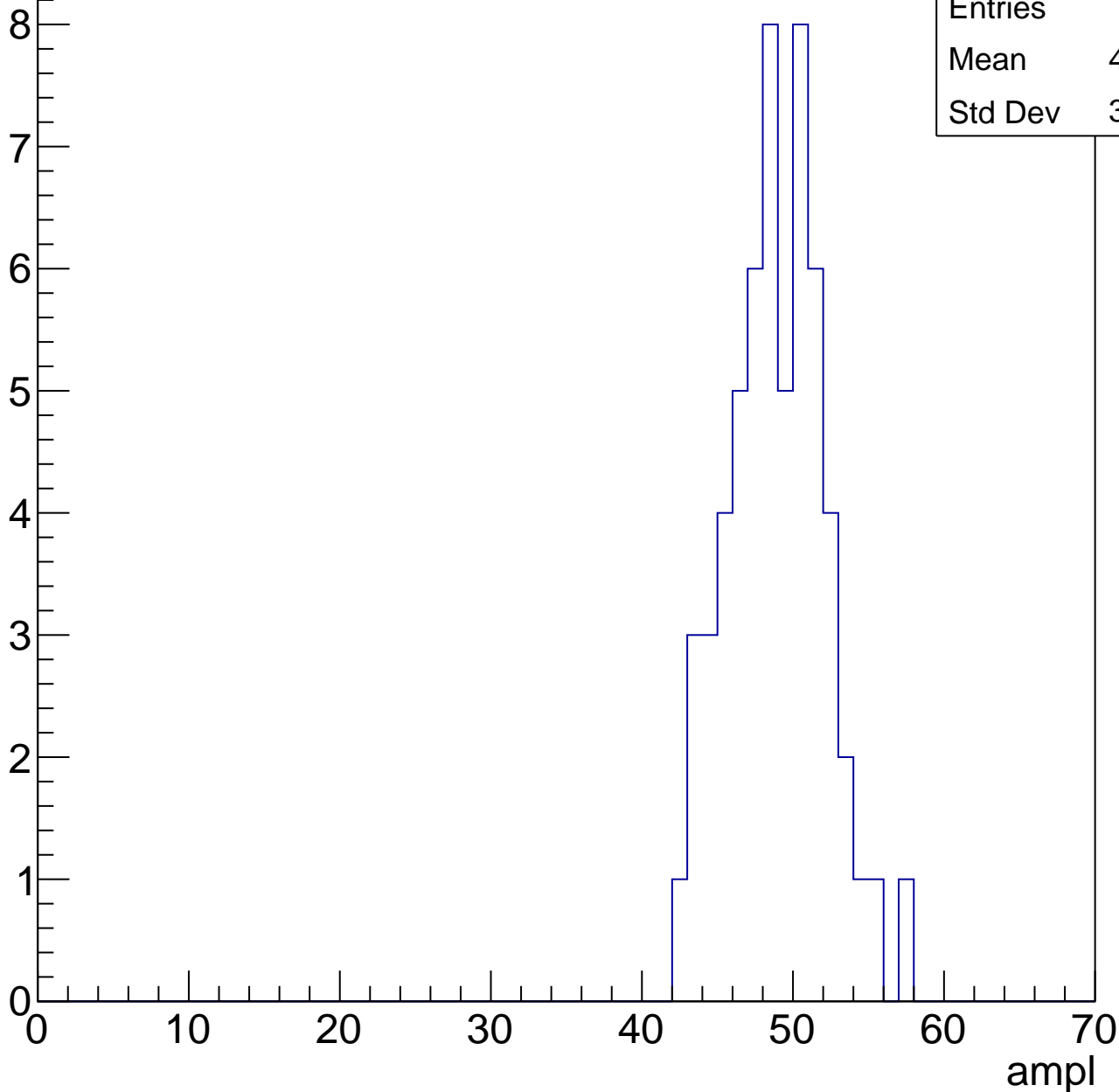


B1L003S, U20-ch36, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

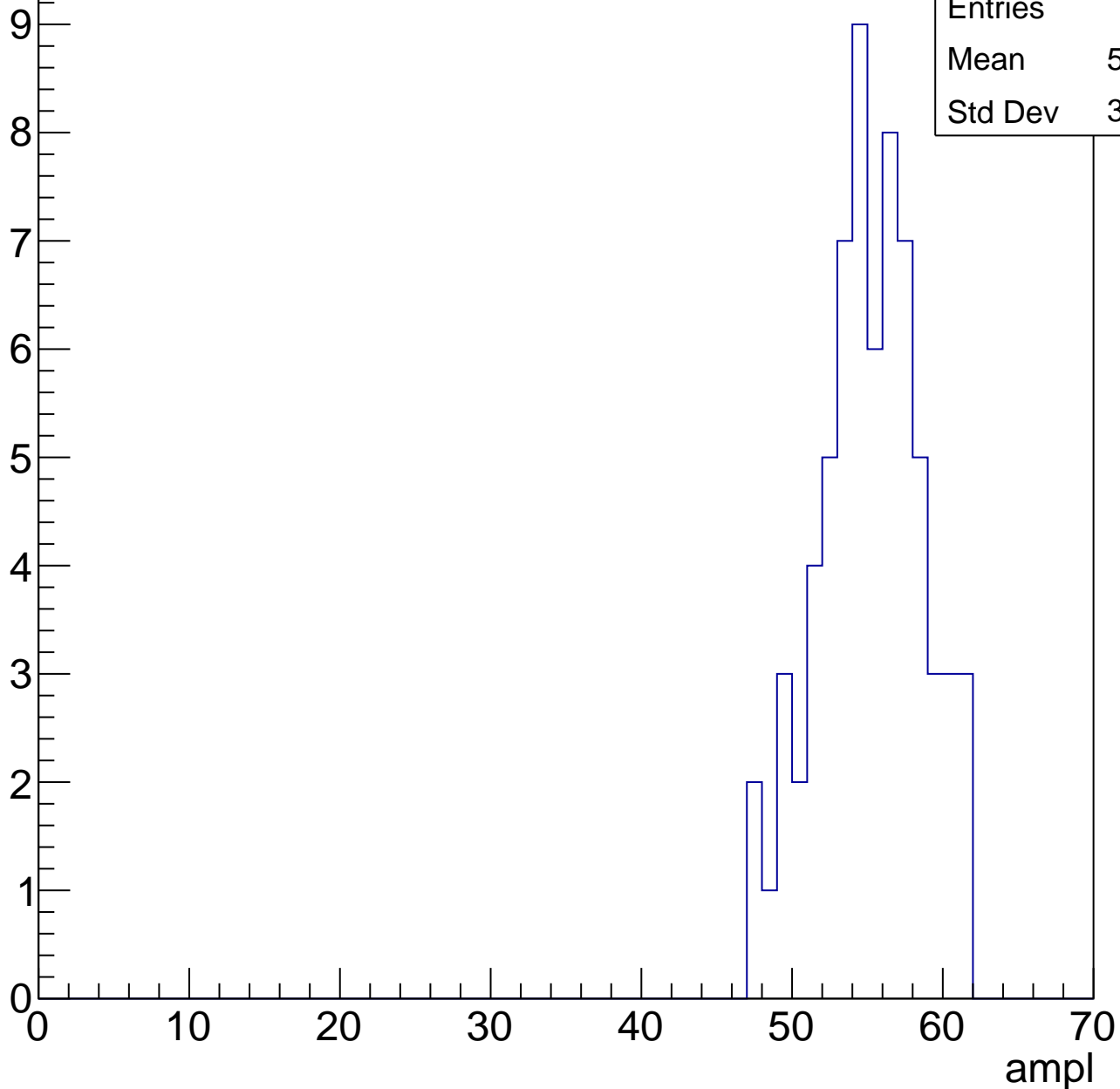
Entries	58
Mean	48.45
Std Dev	3.163



B1L003S, U20-ch36, adc4

calib_packv5_042523_0143.root, FC#13, port D2

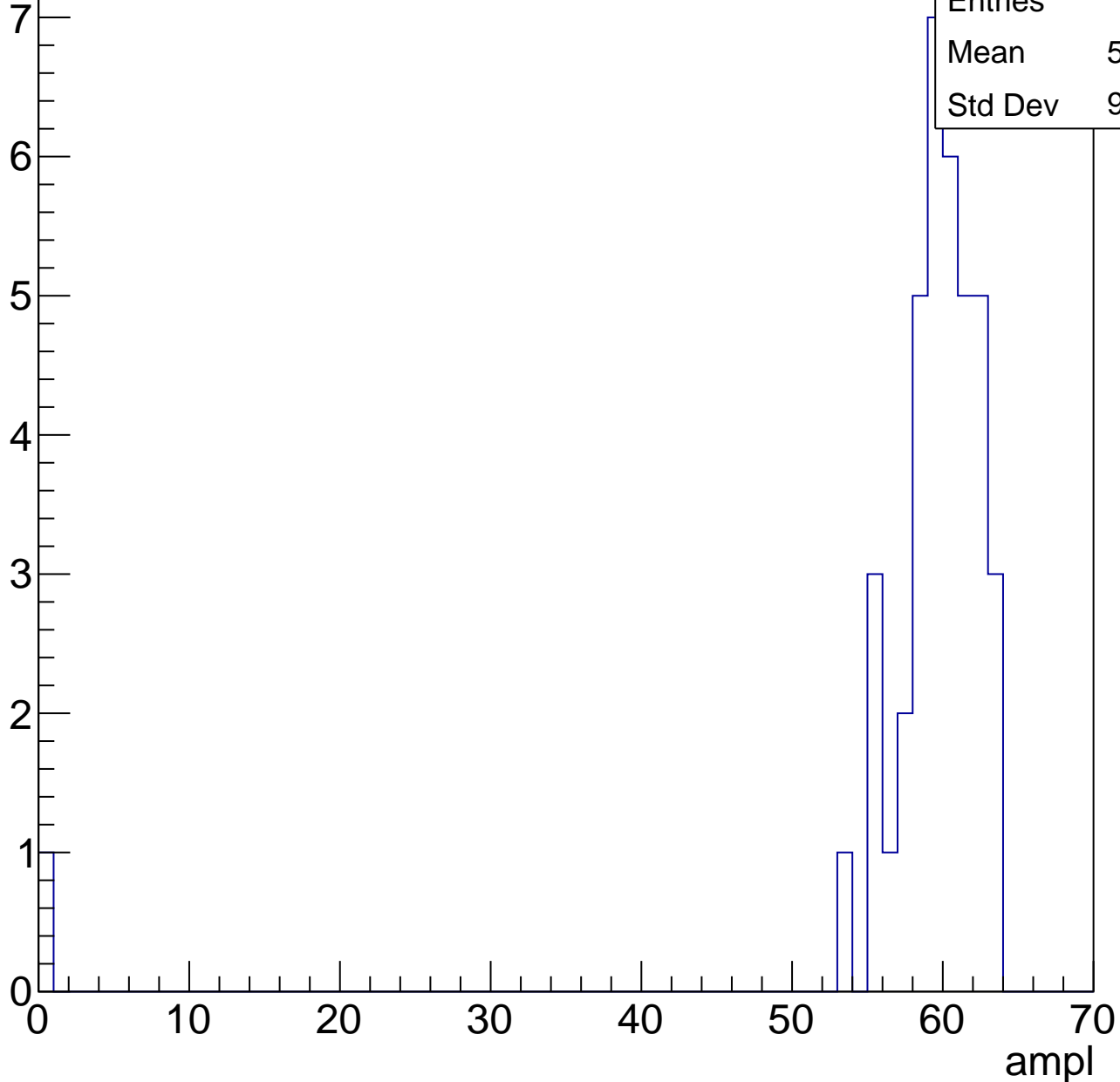
Entry



B1L003S, U20-ch36, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

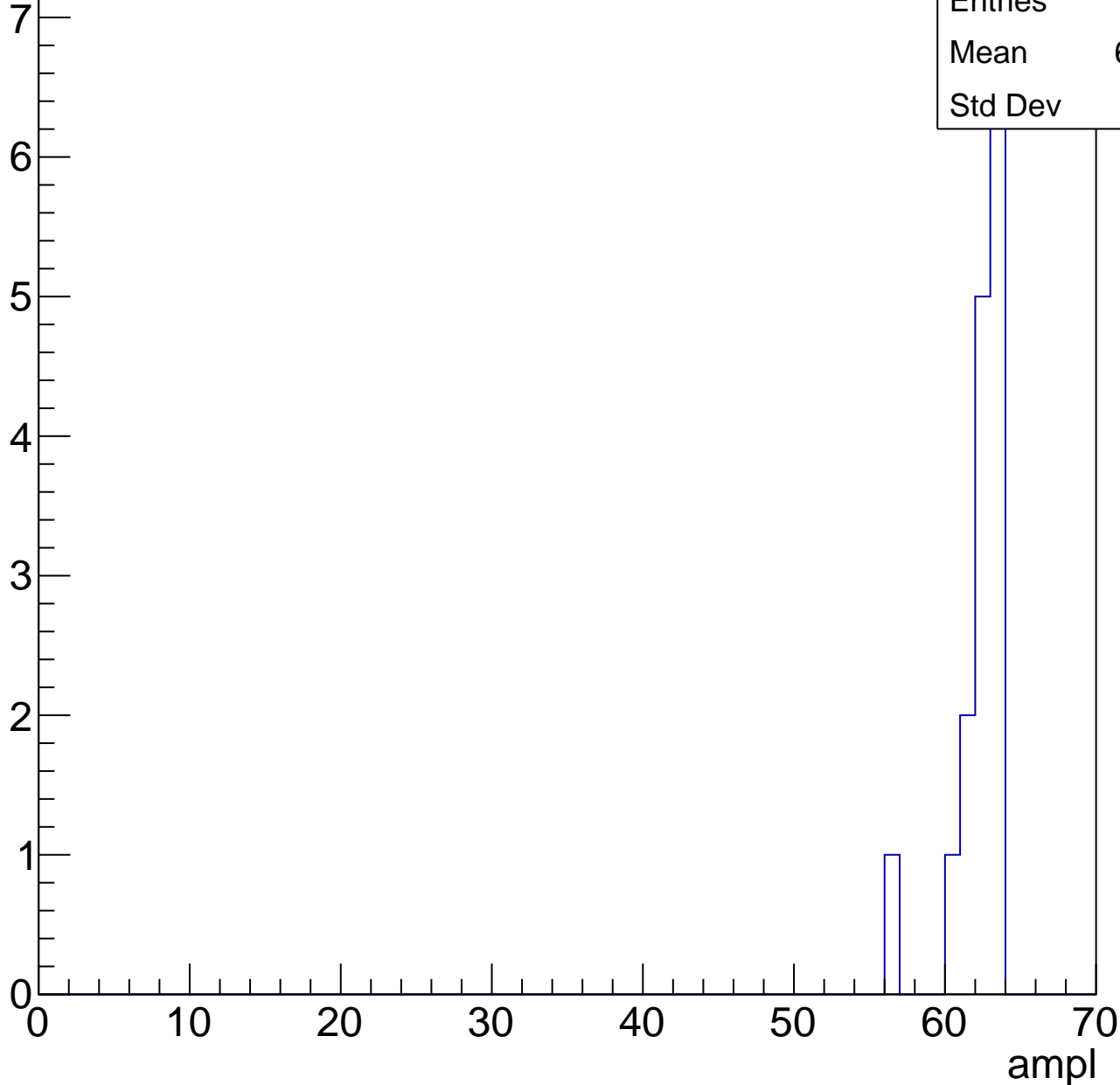


B1L003S, U20-ch36, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	61.81
Std Dev	1.74



B1L003S, U20-ch36, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch37, adc0

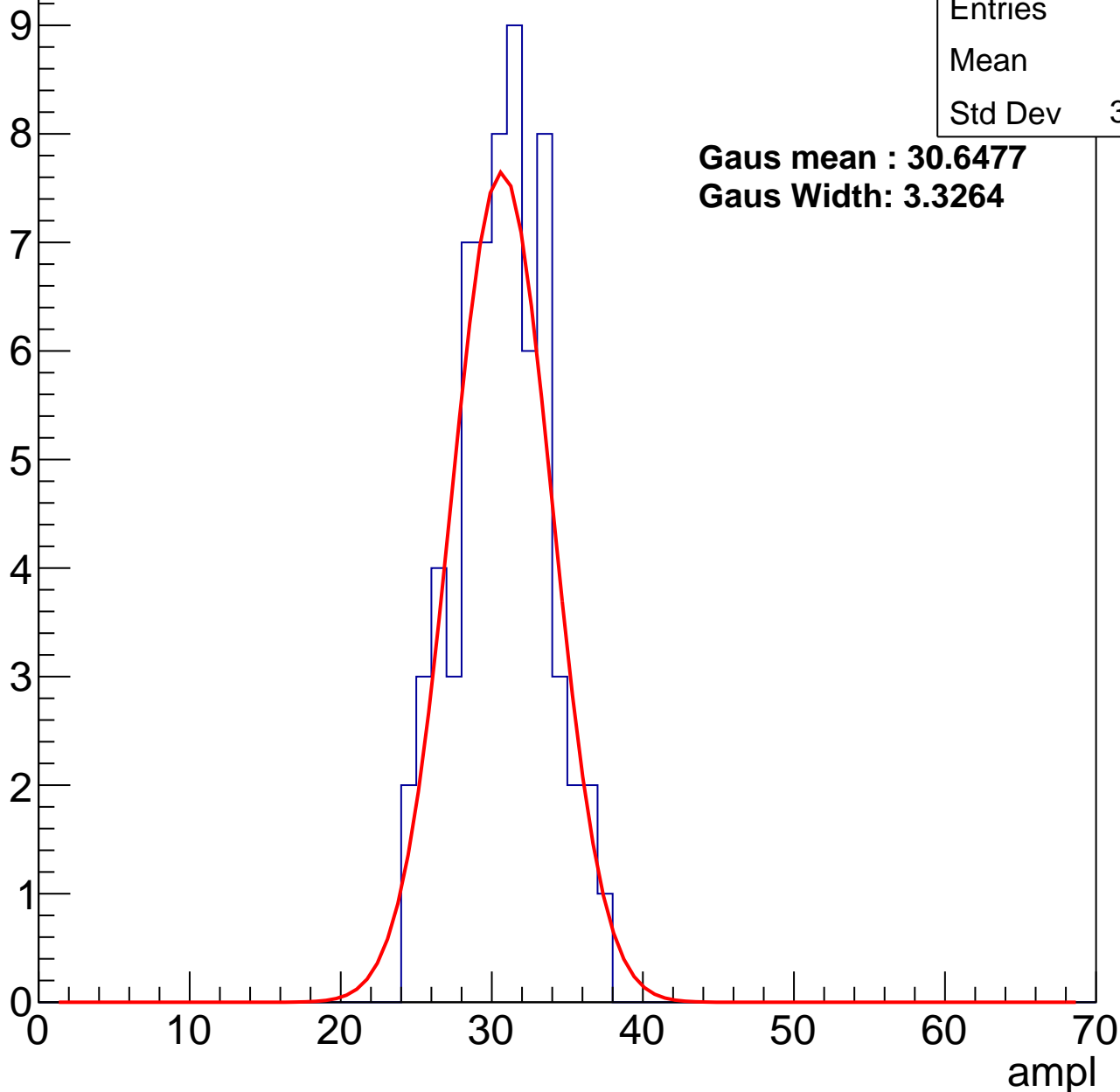
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.2
Std Dev	3.024

Gaus mean : 30.6477

Gaus Width: 3.3264



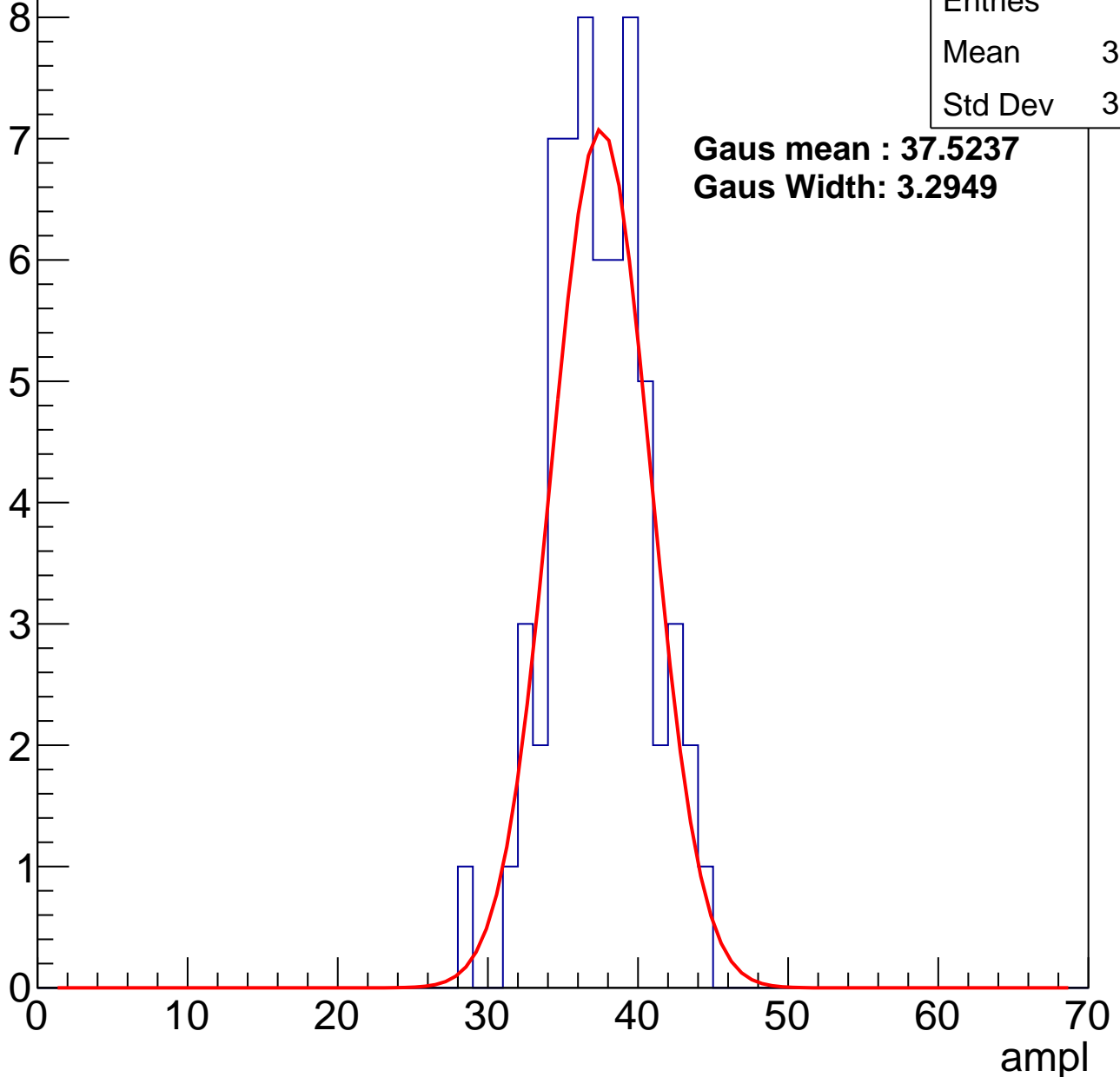
B1L003S, U20-ch37, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.97
Std Dev	3.198

Gaus mean : 37.5237
Gaus Width: 3.2949



B1L003S, U20-ch37, adc2

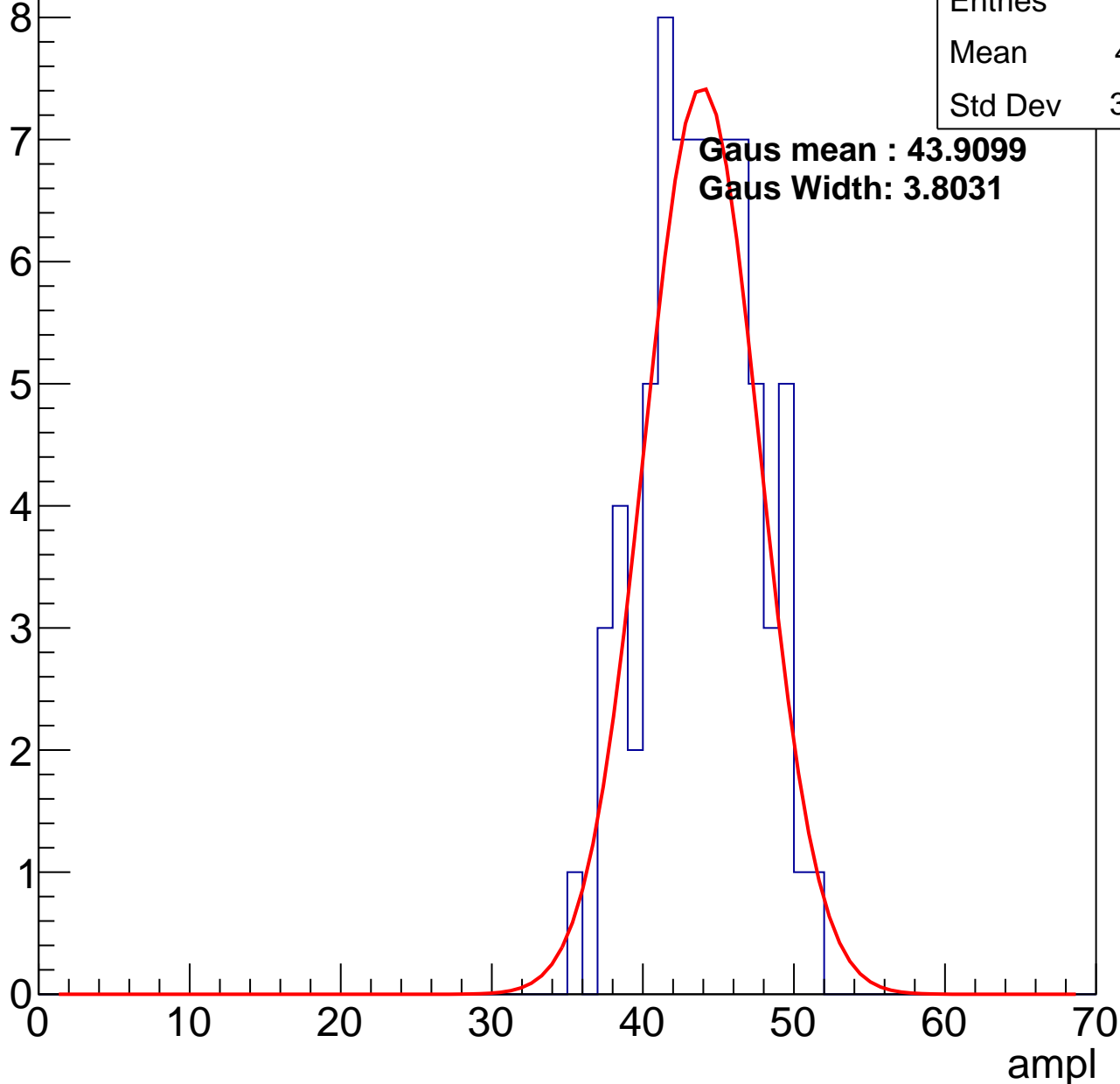
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.41
Std Dev	3.553

Gaus mean : 43.9099

Gaus Width: 3.8031

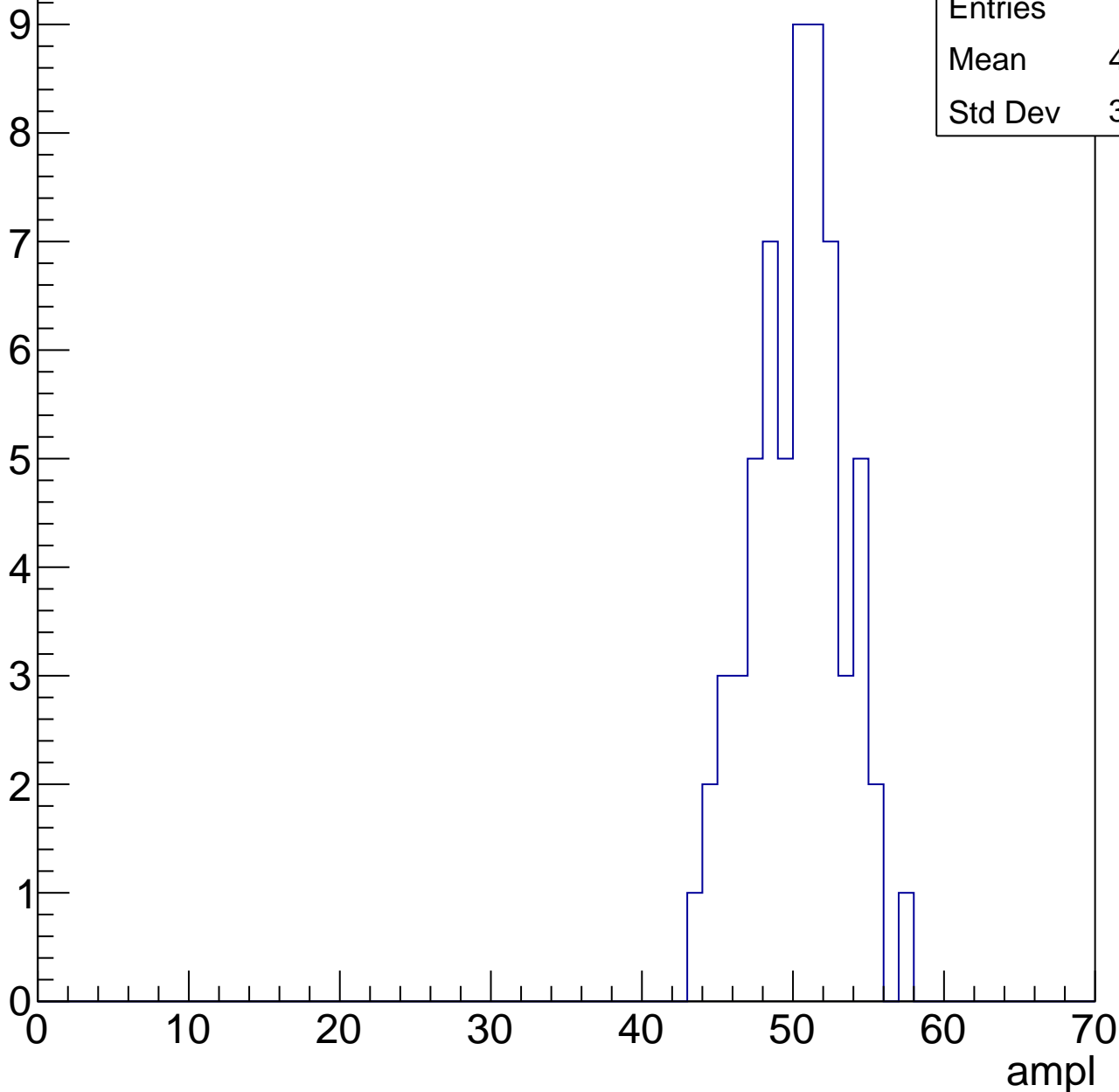


B1L003S, U20-ch37, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

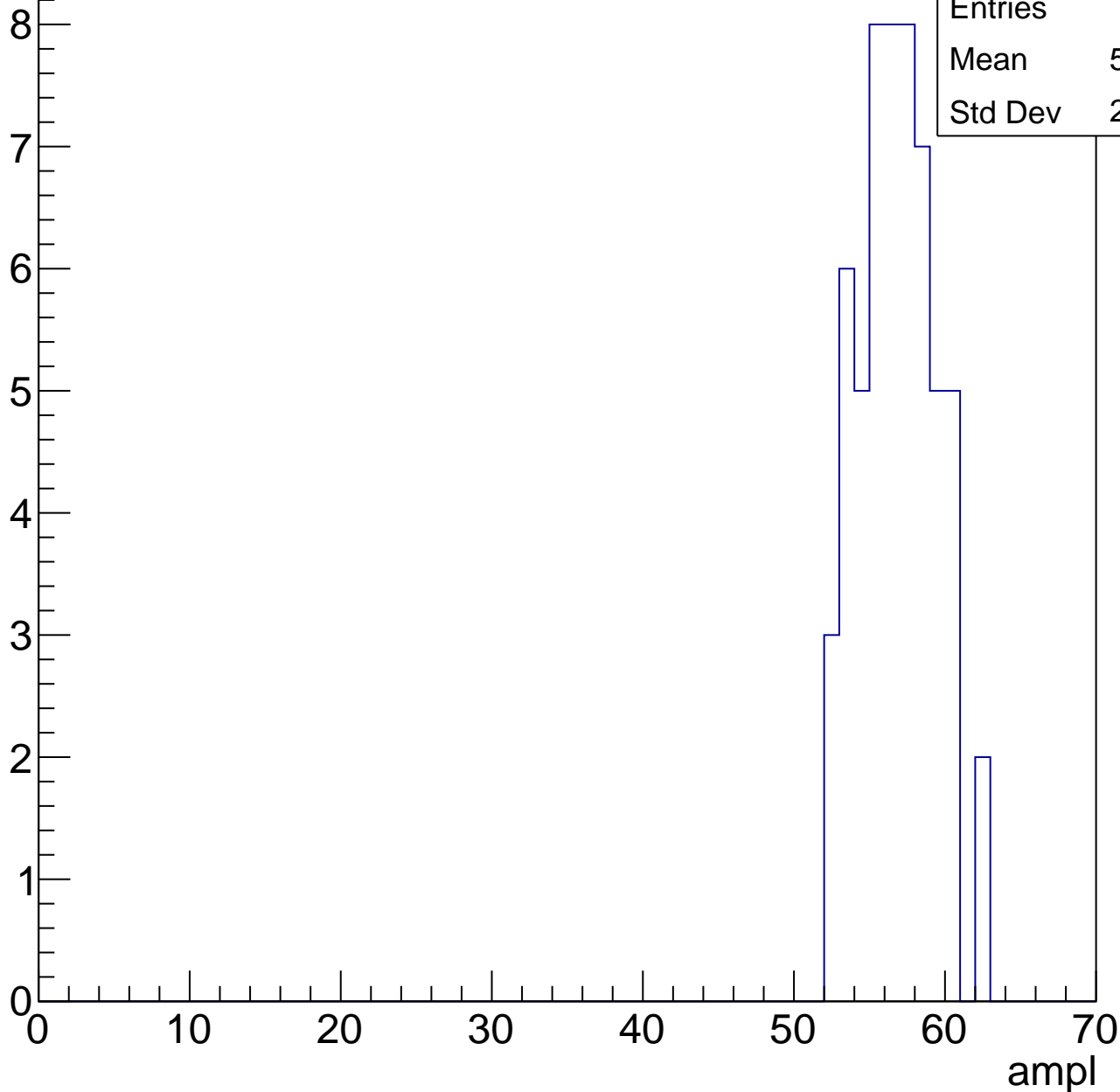
Entries	62
Mean	49.82
Std Dev	3.014



B1L003S, U20-ch37, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

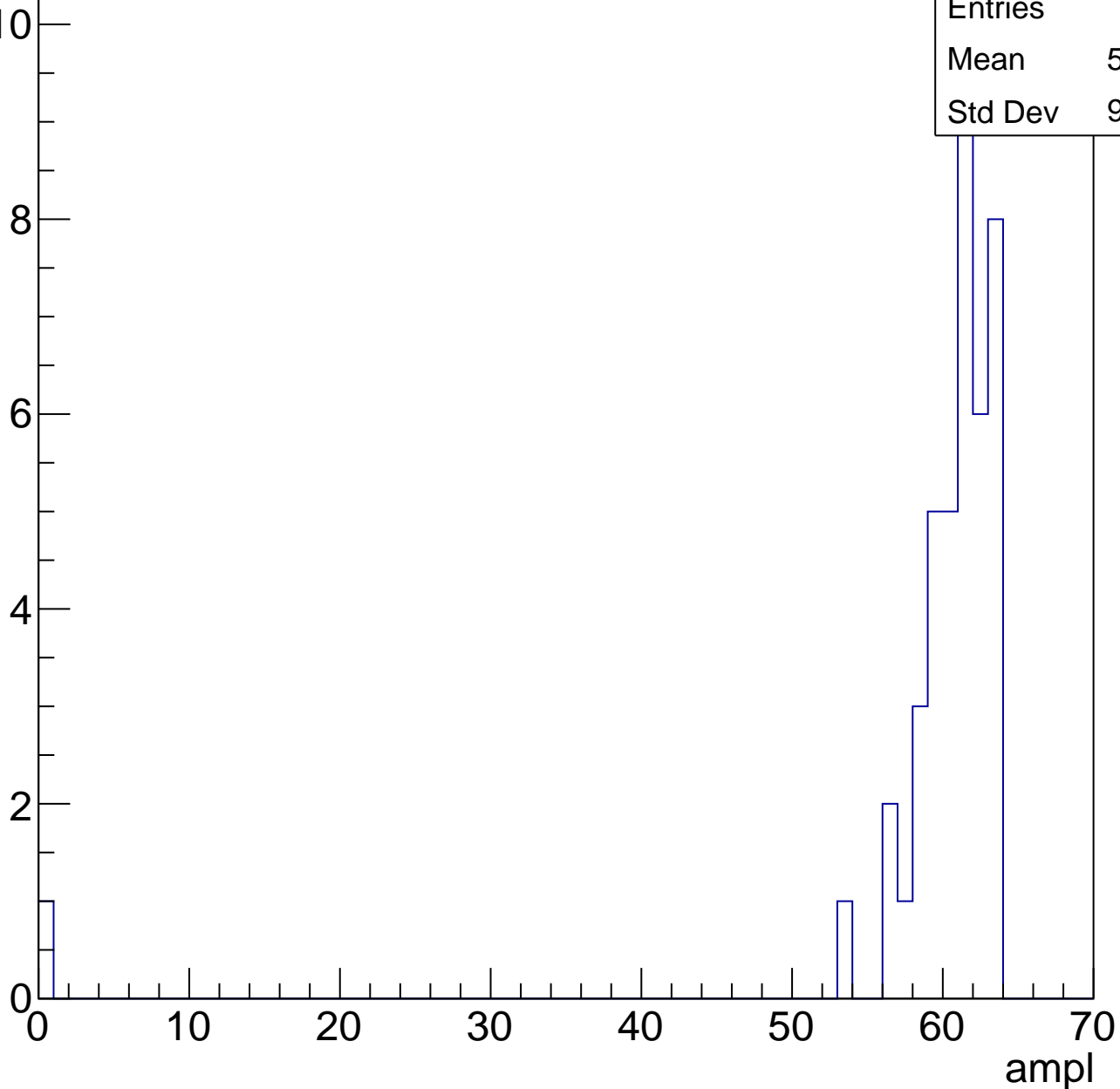


B1L003S, U20-ch37, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.98
Std Dev	9.473



B1L003S, U20-ch37, adc6

calib_packv5_042523_0143.root, FC#13, port D2

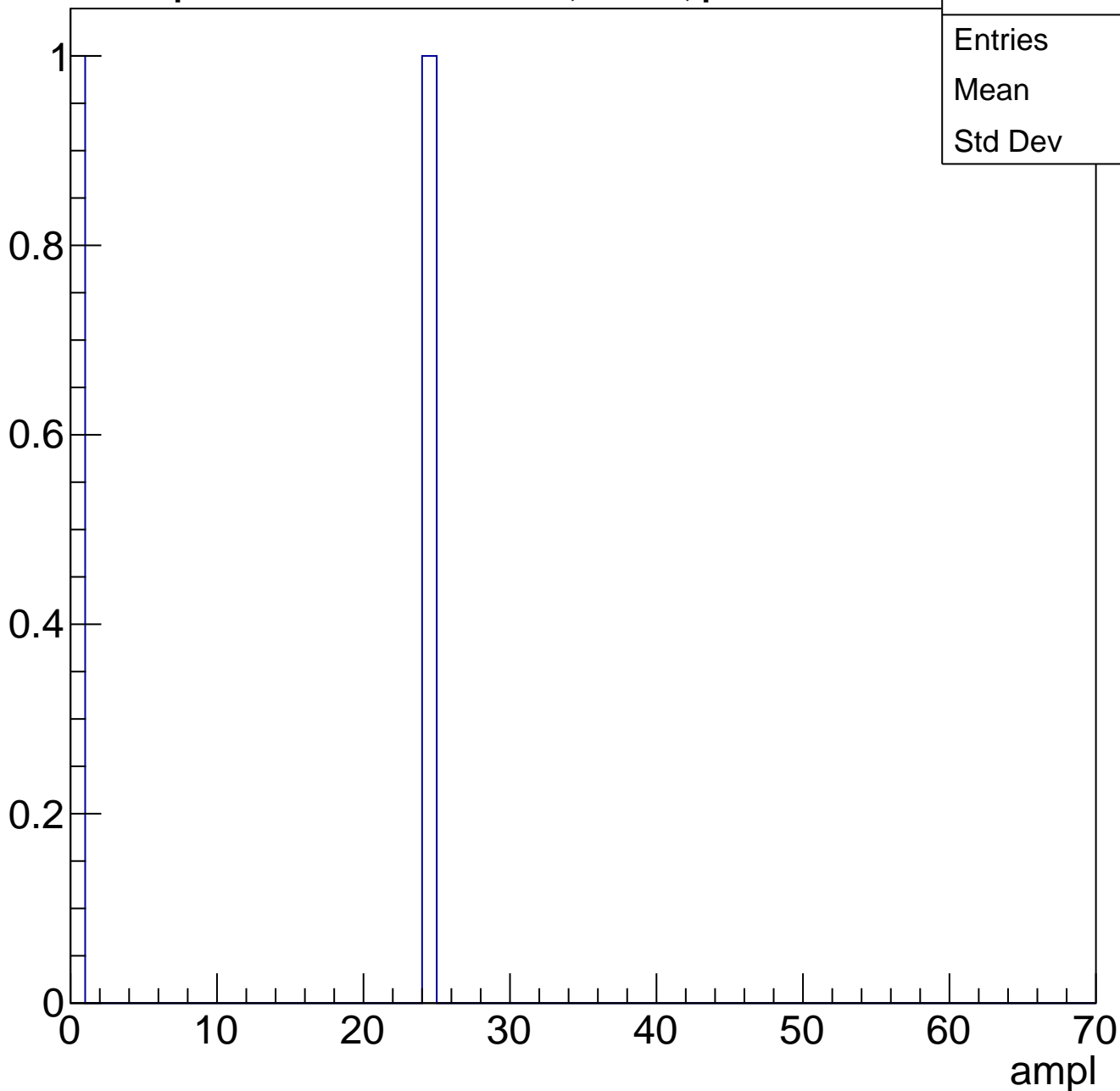
Entry



B1L003S, U20-ch37, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch38, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	28.57
Std Dev	3.377

Gaus mean : 29.2557

Gaus Width: 3.0112

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L003S, U20-ch38, adc1

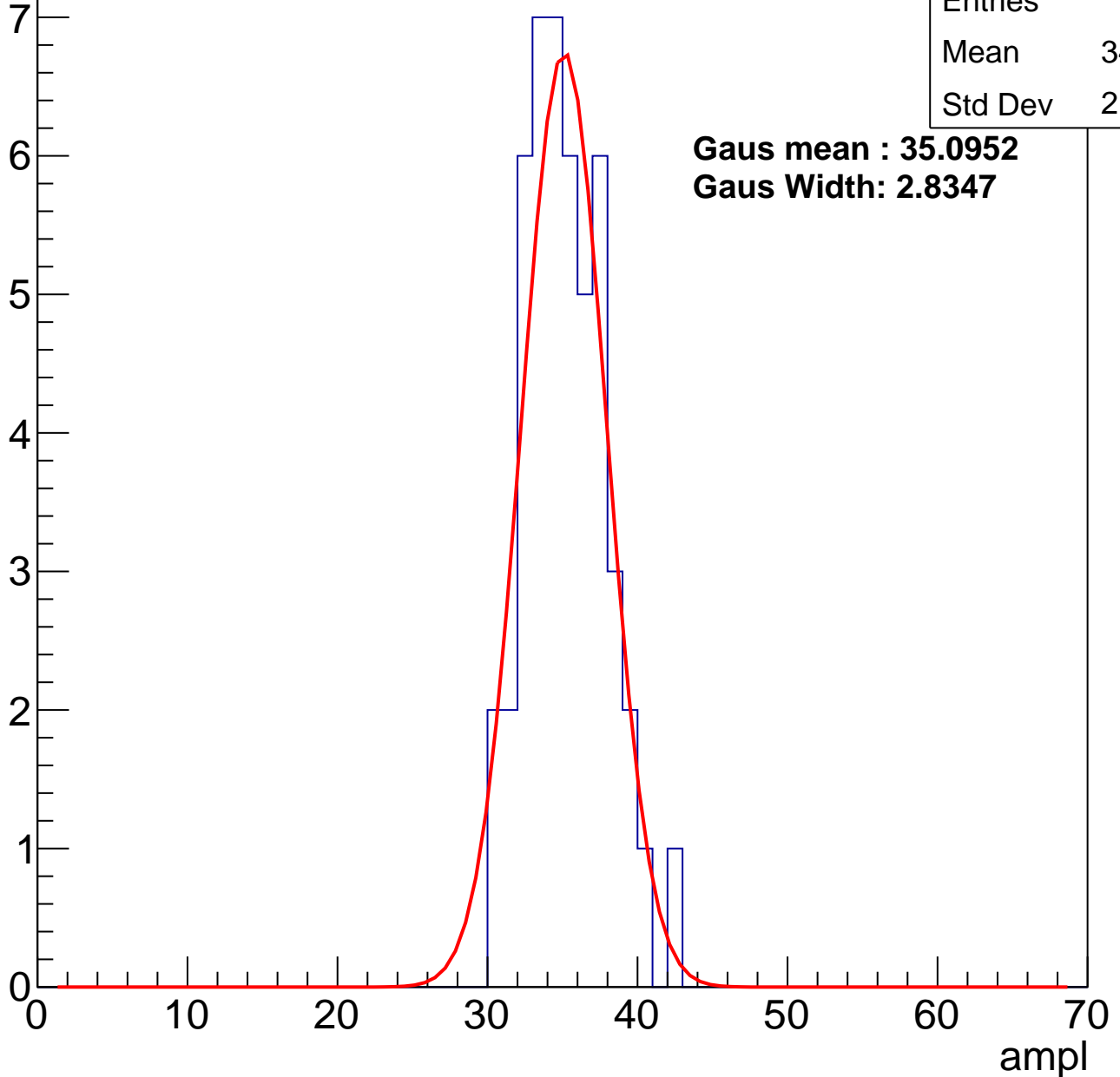
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	34.77
Std Dev	2.624

Gaus mean : 35.0952

Gaus Width: 2.8347



B1L003S, U20-ch38, adc2

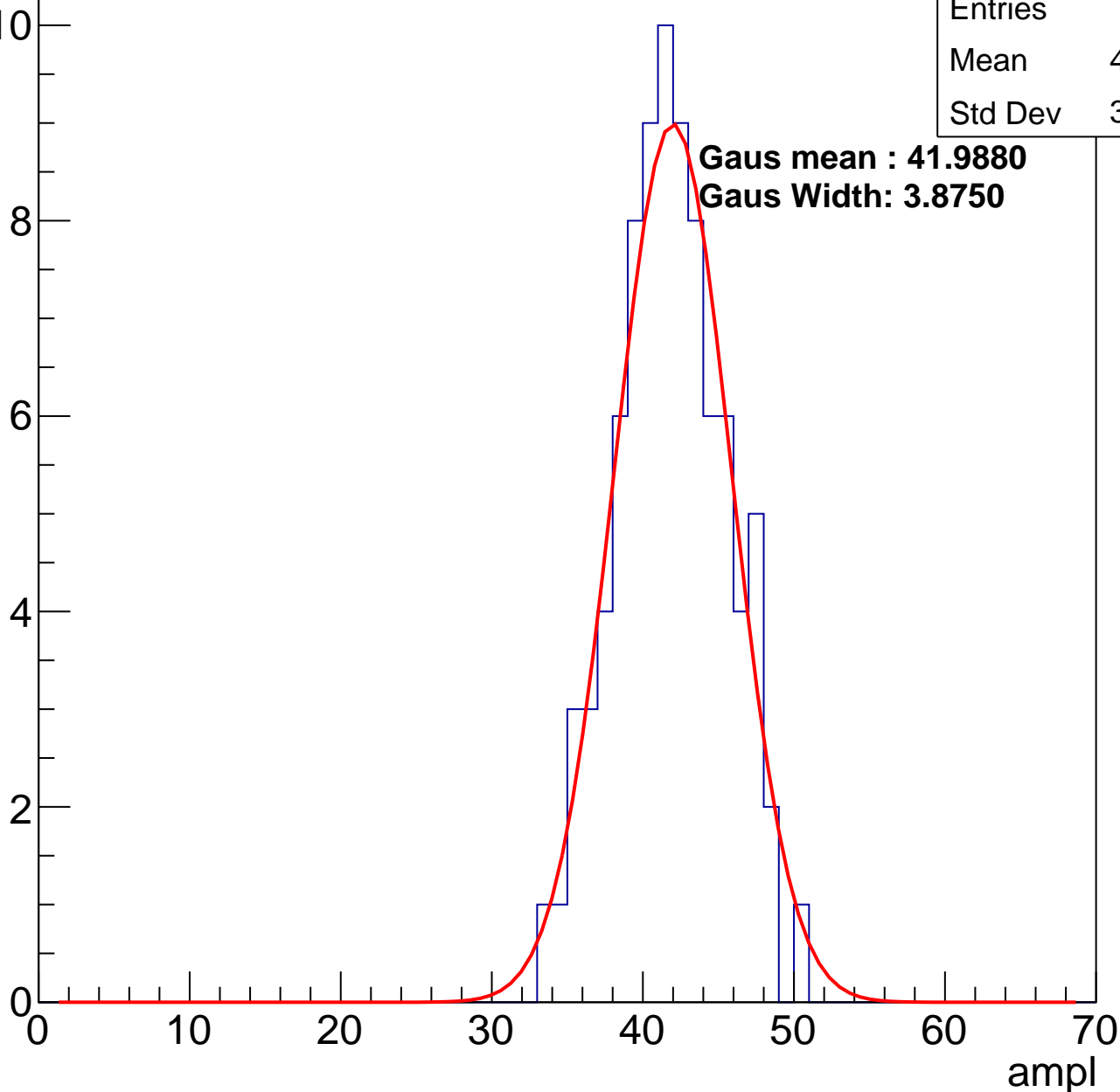
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	41.38
Std Dev	3.564

Gaus mean : 41.9880

Gaus Width: 3.8750

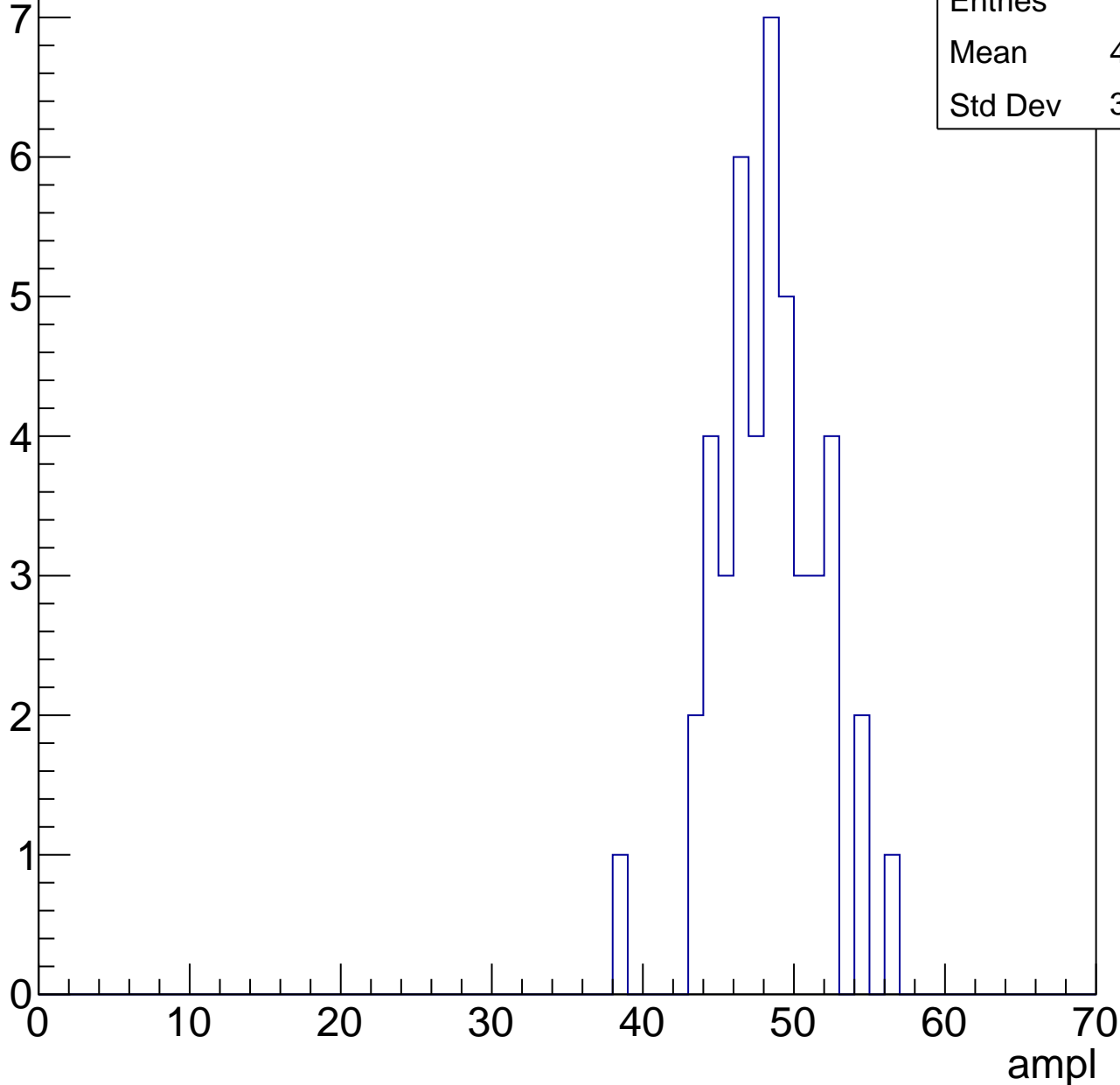


B1L003S, U20-ch38, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	47.89
Std Dev	3.375

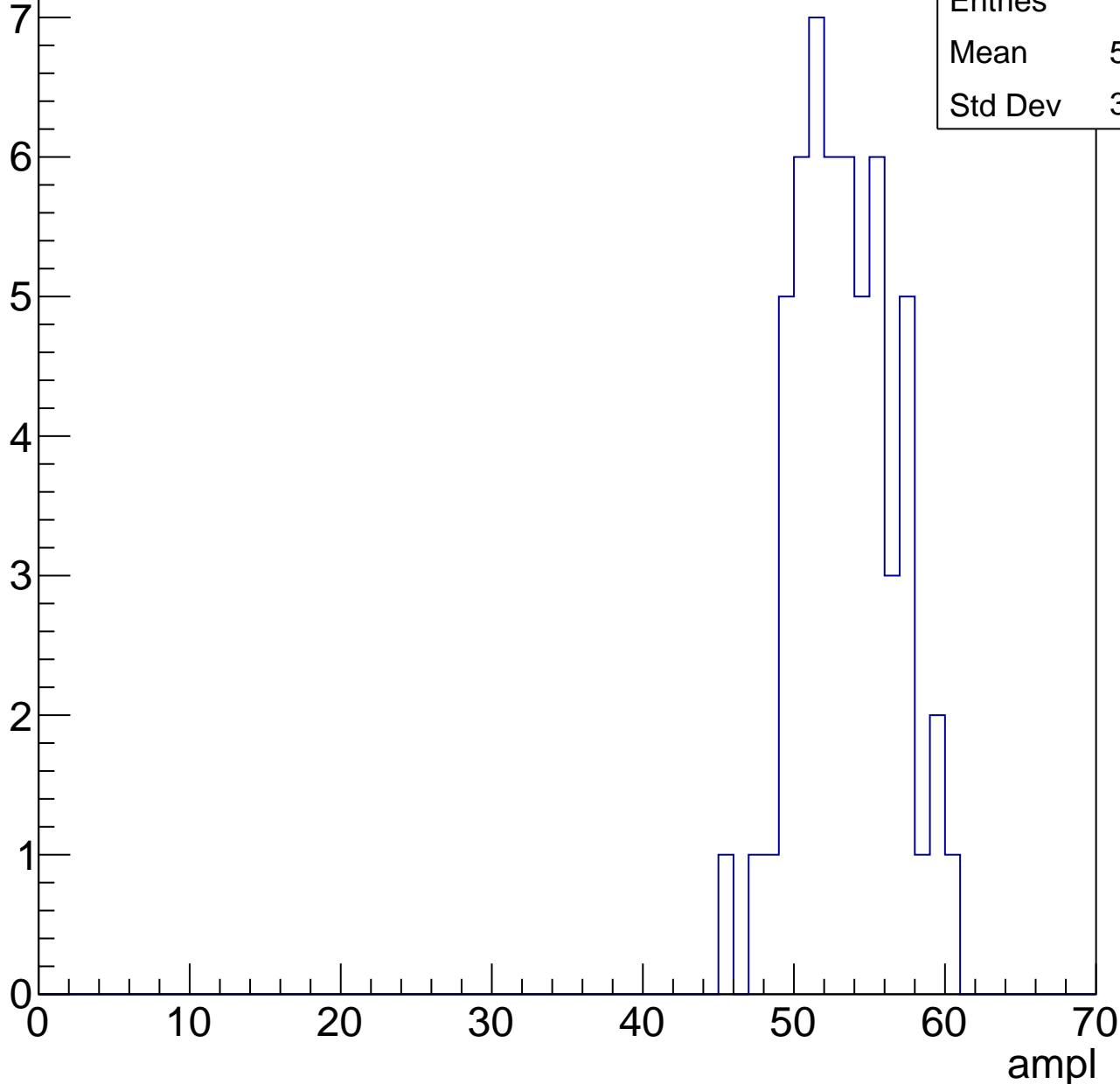


B1L003S, U20-ch38, adc4

calib_packv5_042523_0143.root, FC#13, port D2

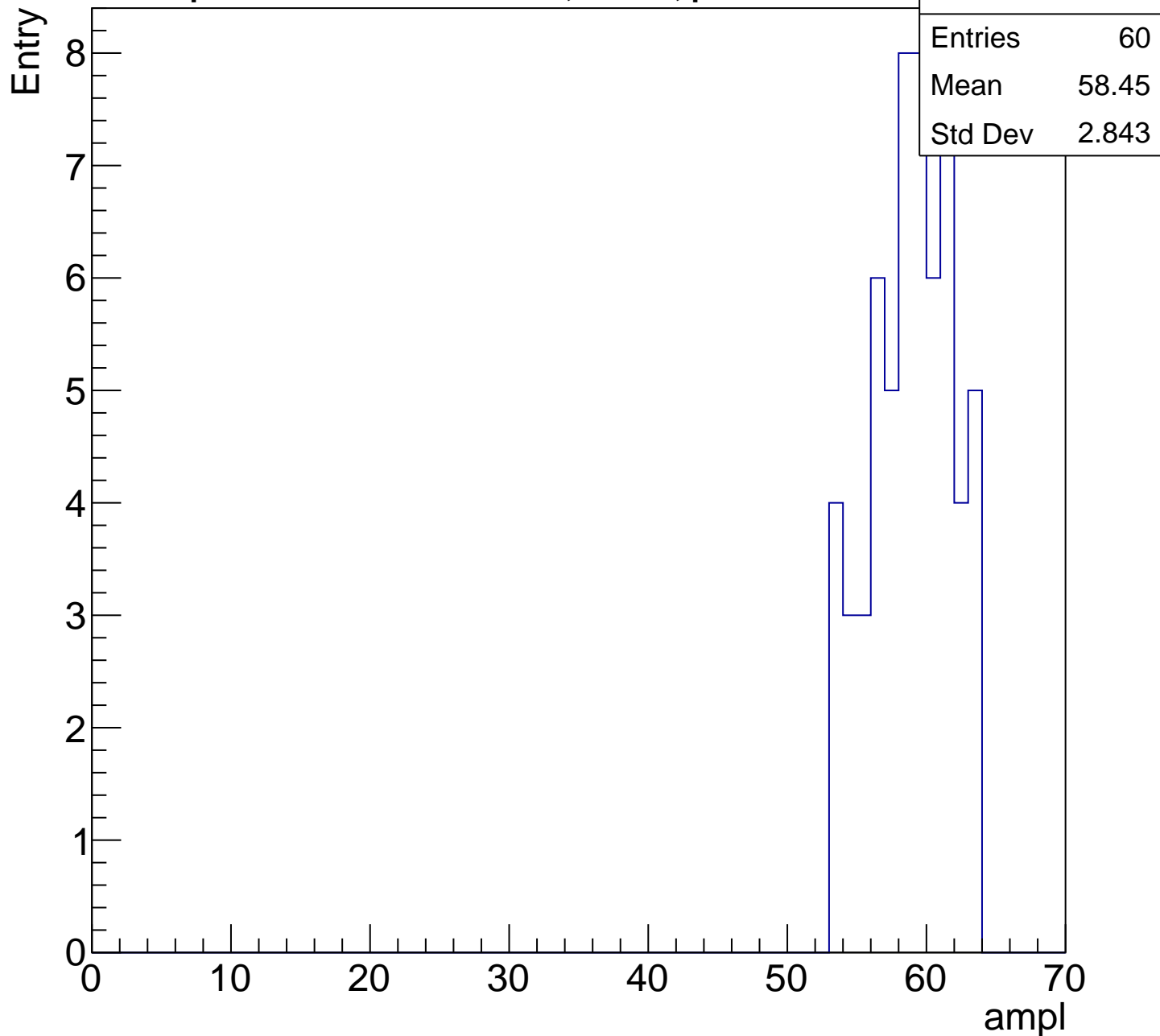
Entry

Entries	56
Mean	52.88
Std Dev	3.202



B1L003S, U20-ch38, adc5

calib_packv5_042523_0143.root, FC#13, port D2

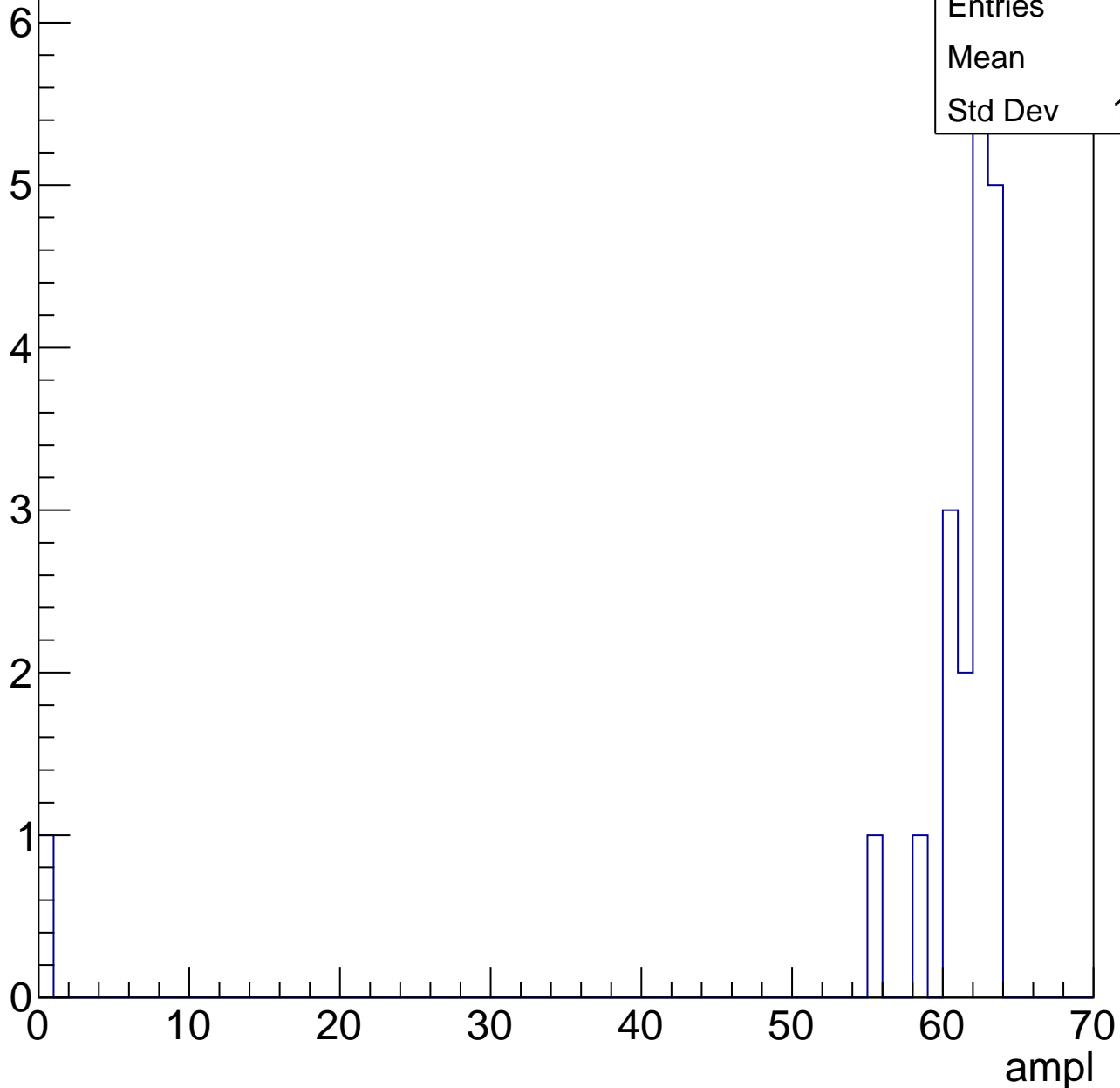


B1L003S, U20-ch38, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	58
Std Dev	13.81



B1L003S, U20-ch38, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L003S, U20-ch39, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	27.29
Std Dev	4.538

Gaus mean : 28.1794

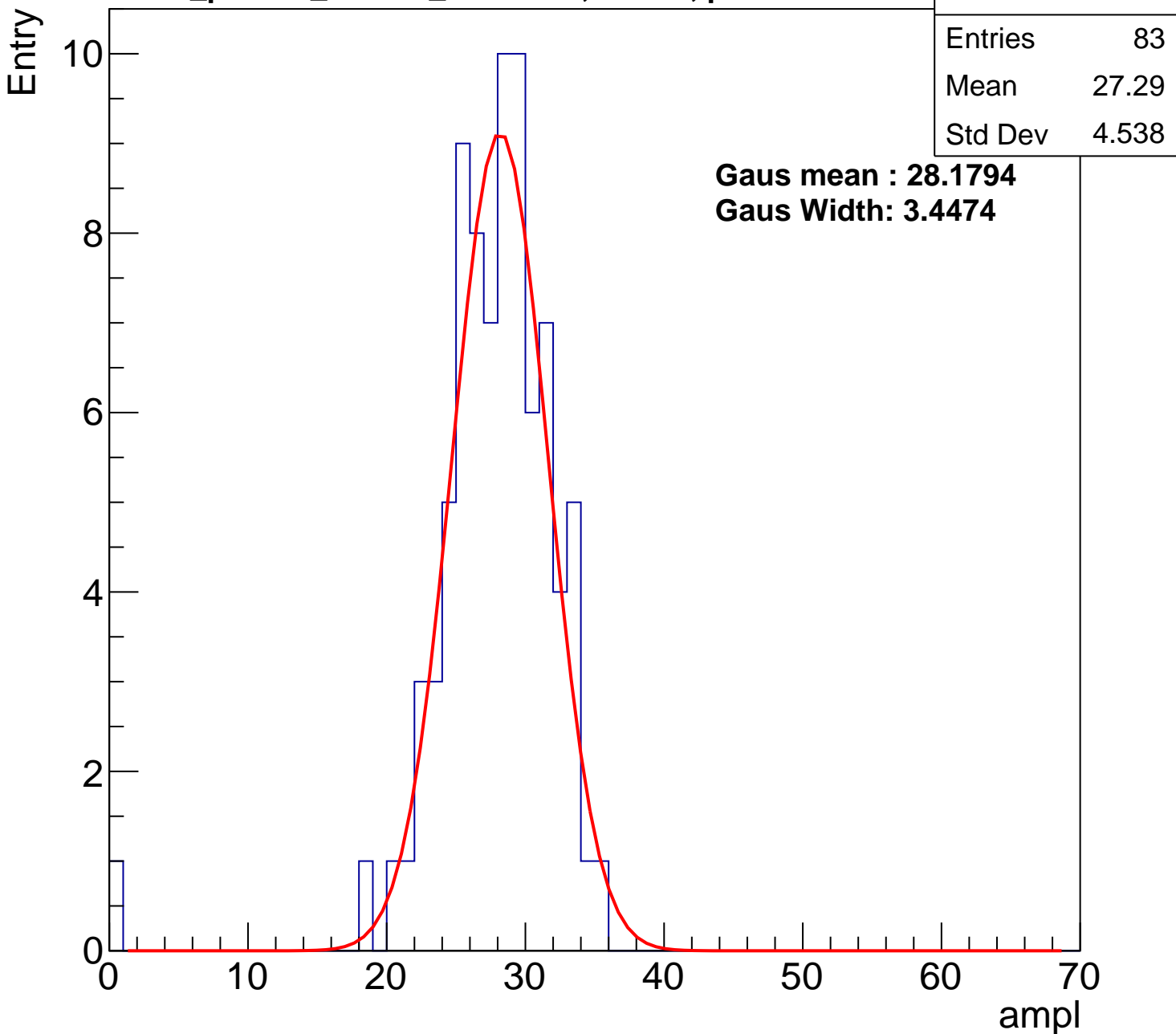
Gaus Width: 3.4474

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch39, adc1

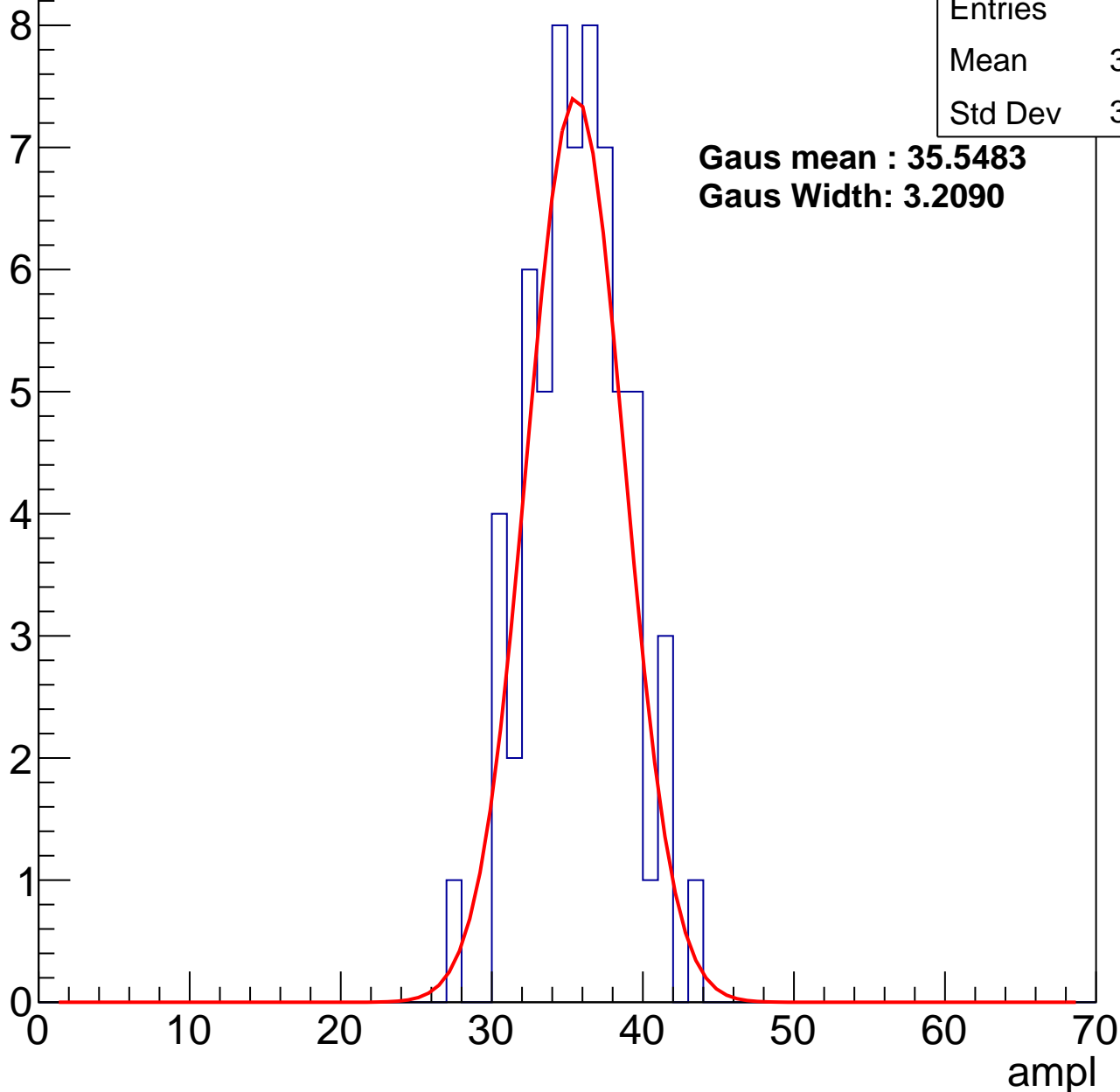
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.25
Std Dev	3.167

Gaus mean : 35.5483

Gaus Width: 3.2090



B1L003S, U20-ch39, adc2

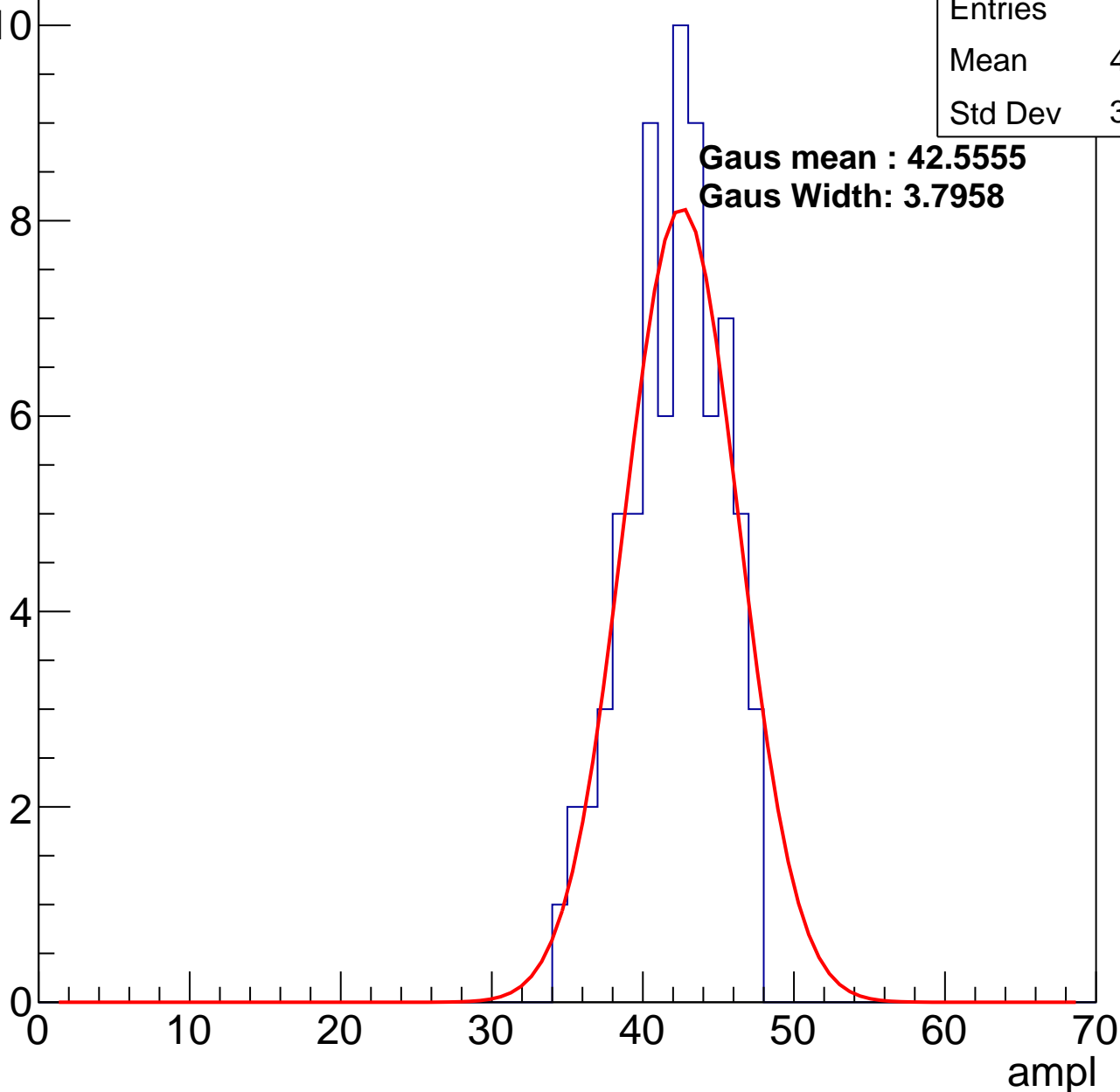
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	41.58
Std Dev	3.127

Gaus mean : 42.5555

Gaus Width: 3.7958



B1L003S, U20-ch39, adc3

calib_packv5_042523_0143.root, FC#13, port D2

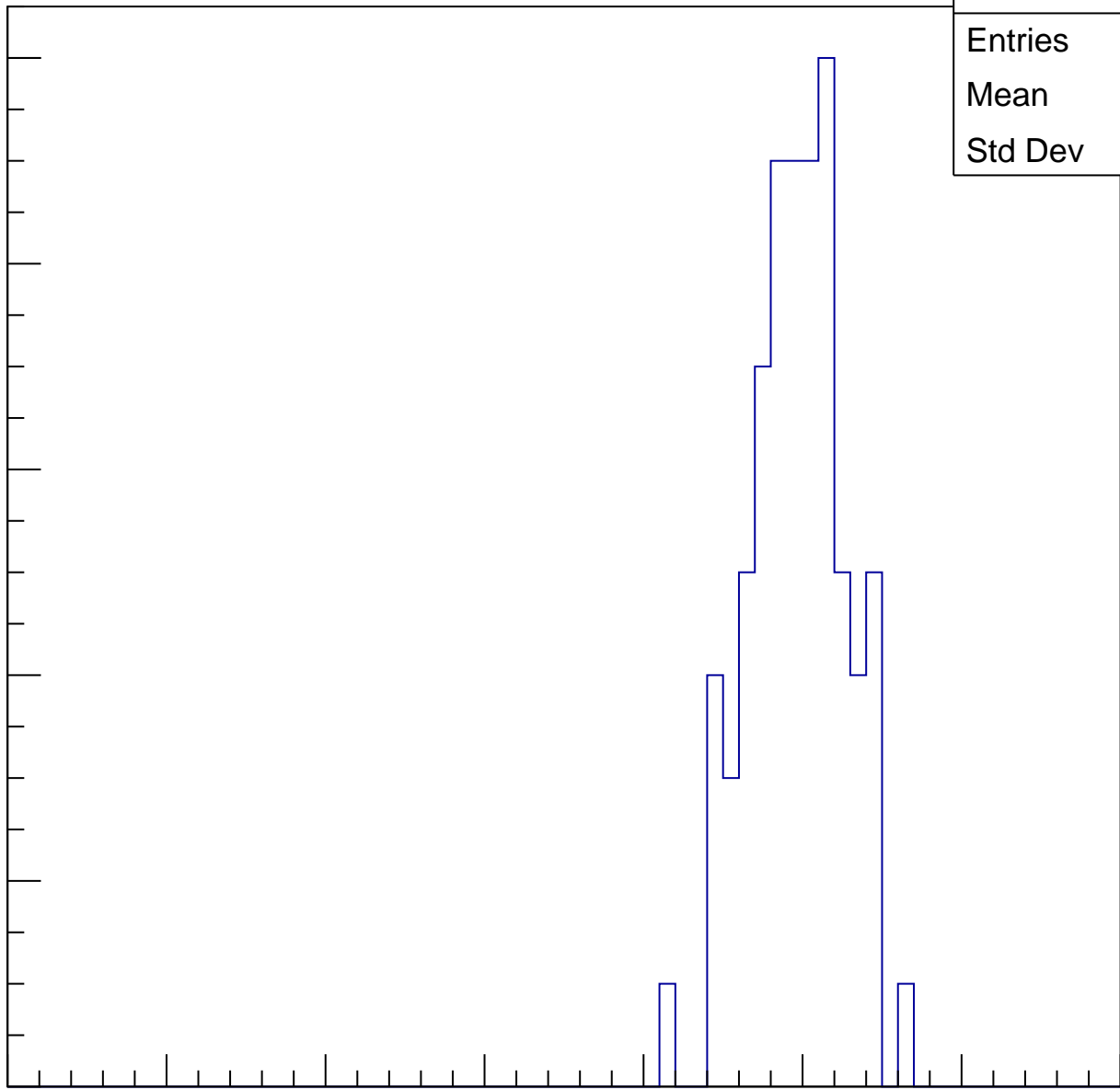
Entries	72
Mean	49.19
Std Dev	2.942

Entry

10
8
6
4
2
0

ampl

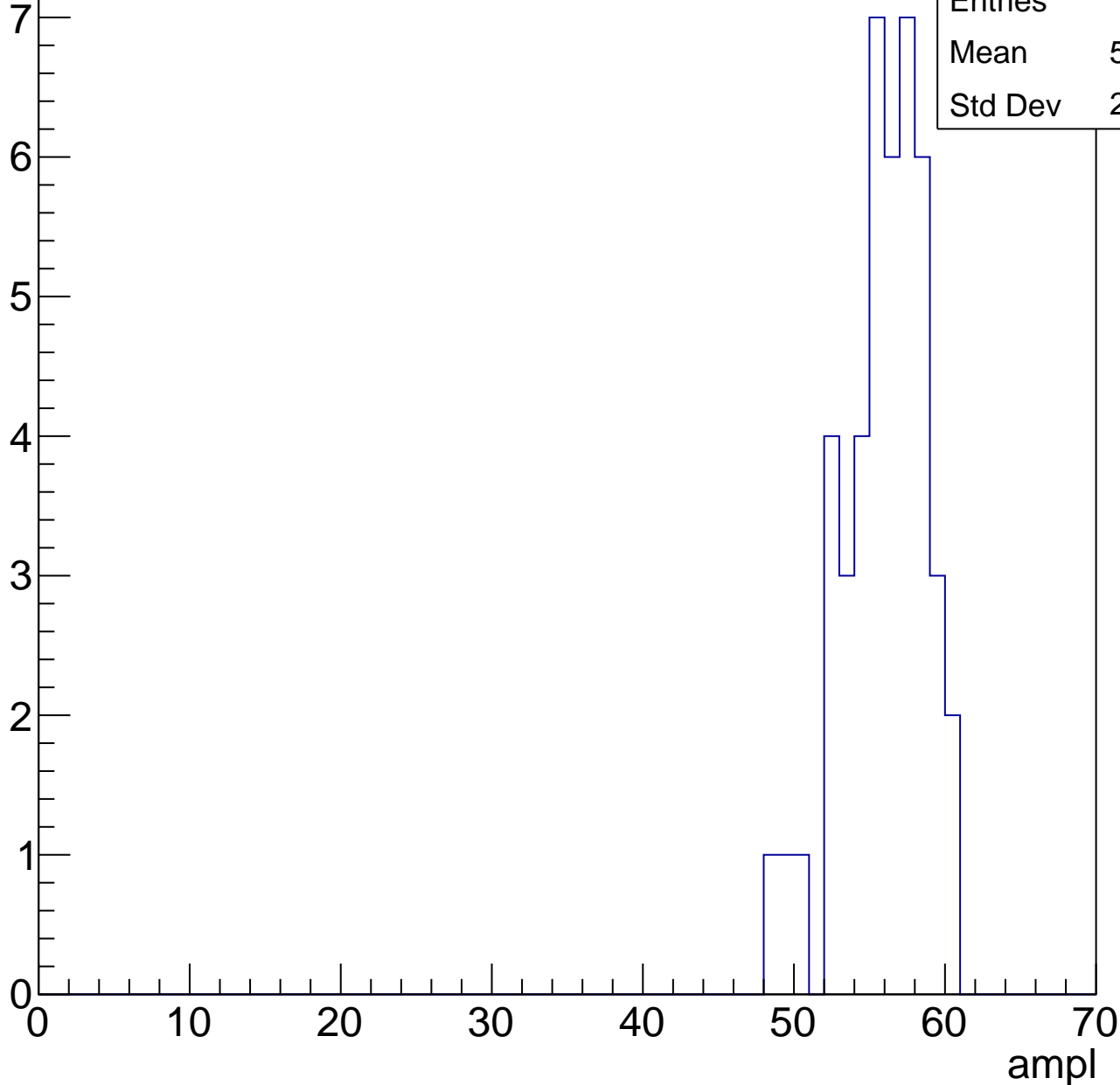
0 10 20 30 40 50 60 70



B1L003S, U20-ch39, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

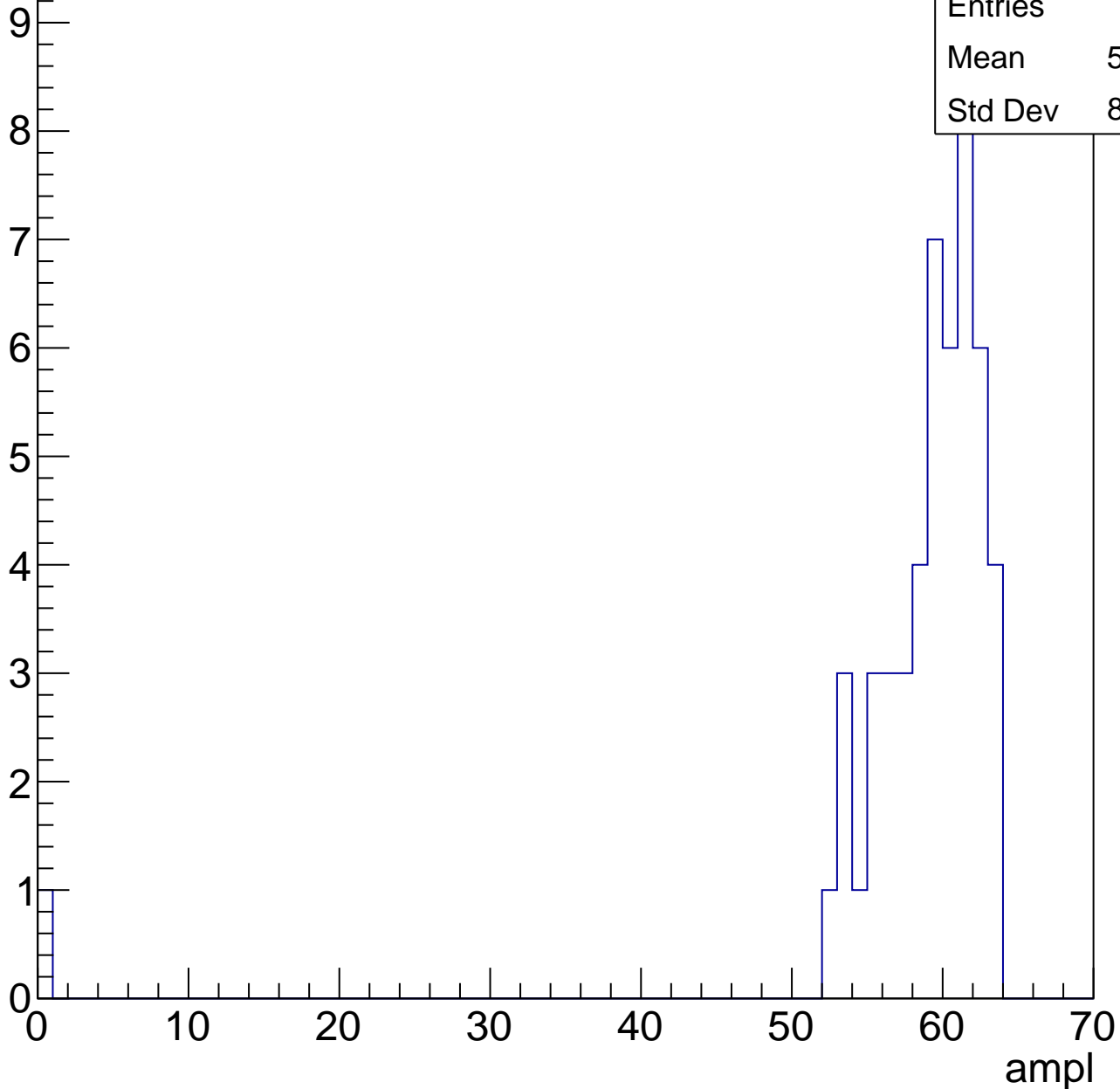


B1L003S, U20-ch39, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	57.78
Std Dev	8.676

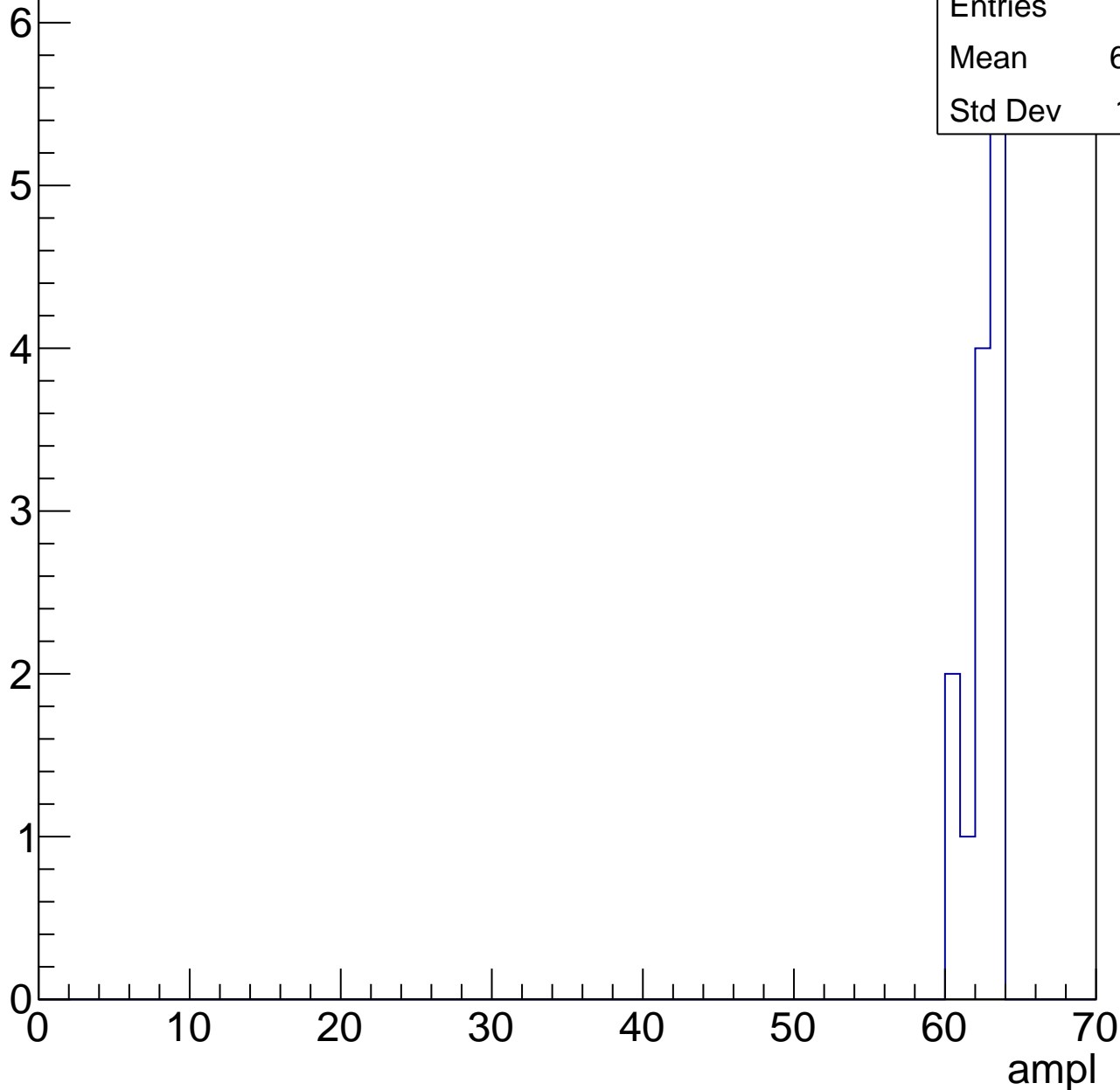


B1L003S, U20-ch39, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	62.08
Std Dev	1.071



B1L003S, U20-ch39, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch40, adc0

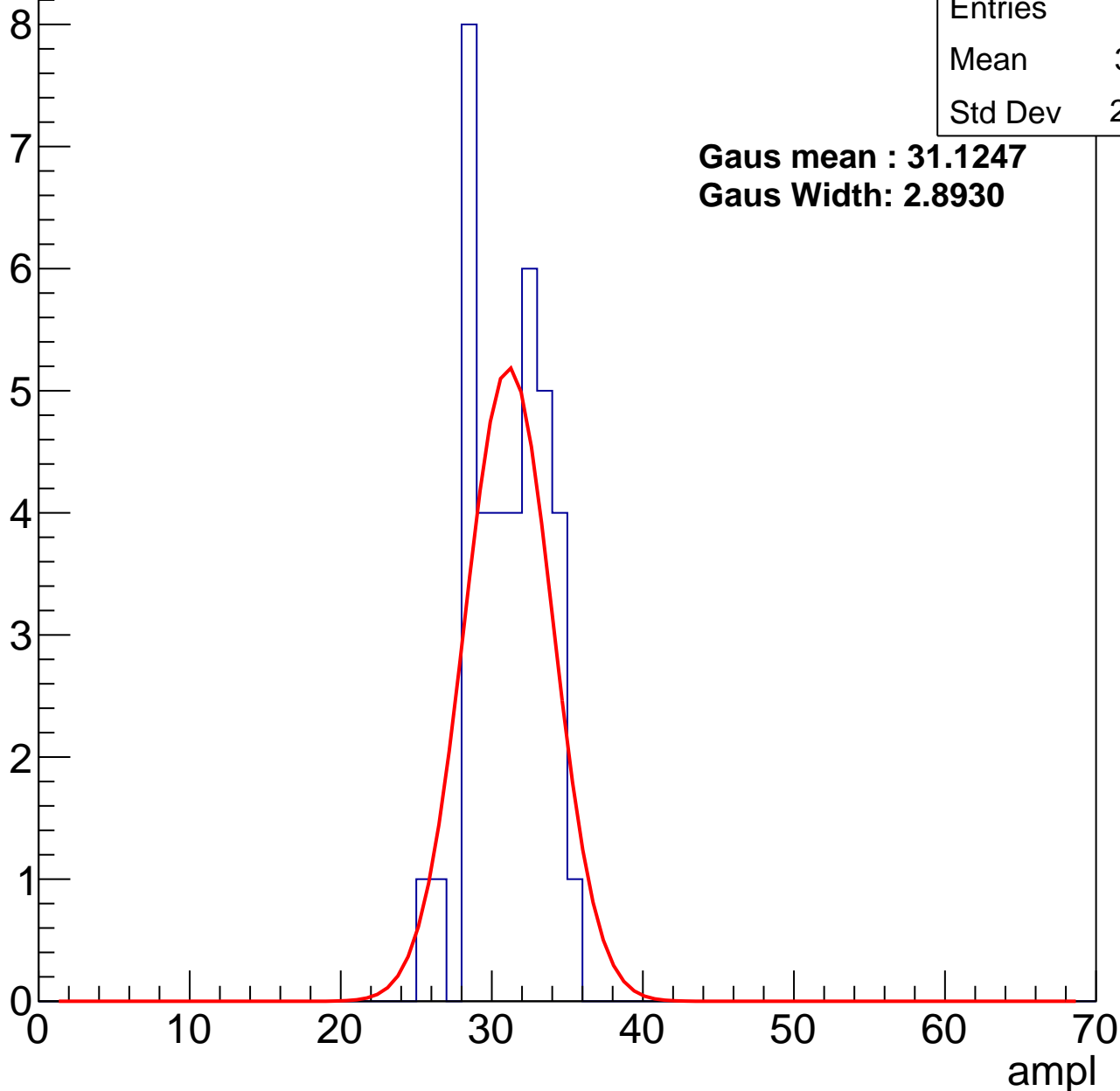
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	30.61
Std Dev	2.434

Gaus mean : 31.1247

Gaus Width: 2.8930



B1L003S, U20-ch40, adc1

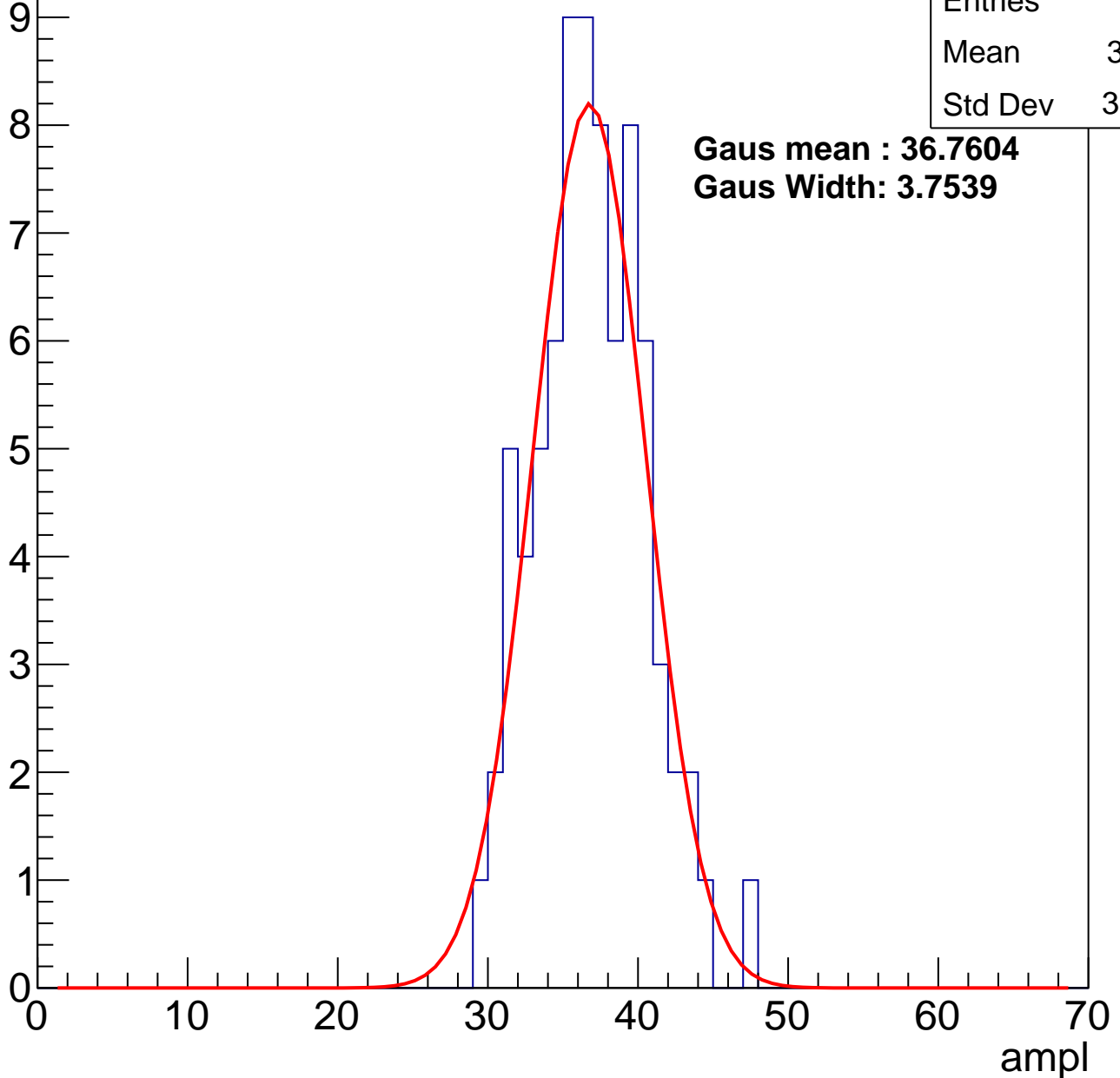
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	36.41
Std Dev	3.589

Gaus mean : 36.7604

Gaus Width: 3.7539



B1L003S, U20-ch40, adc2

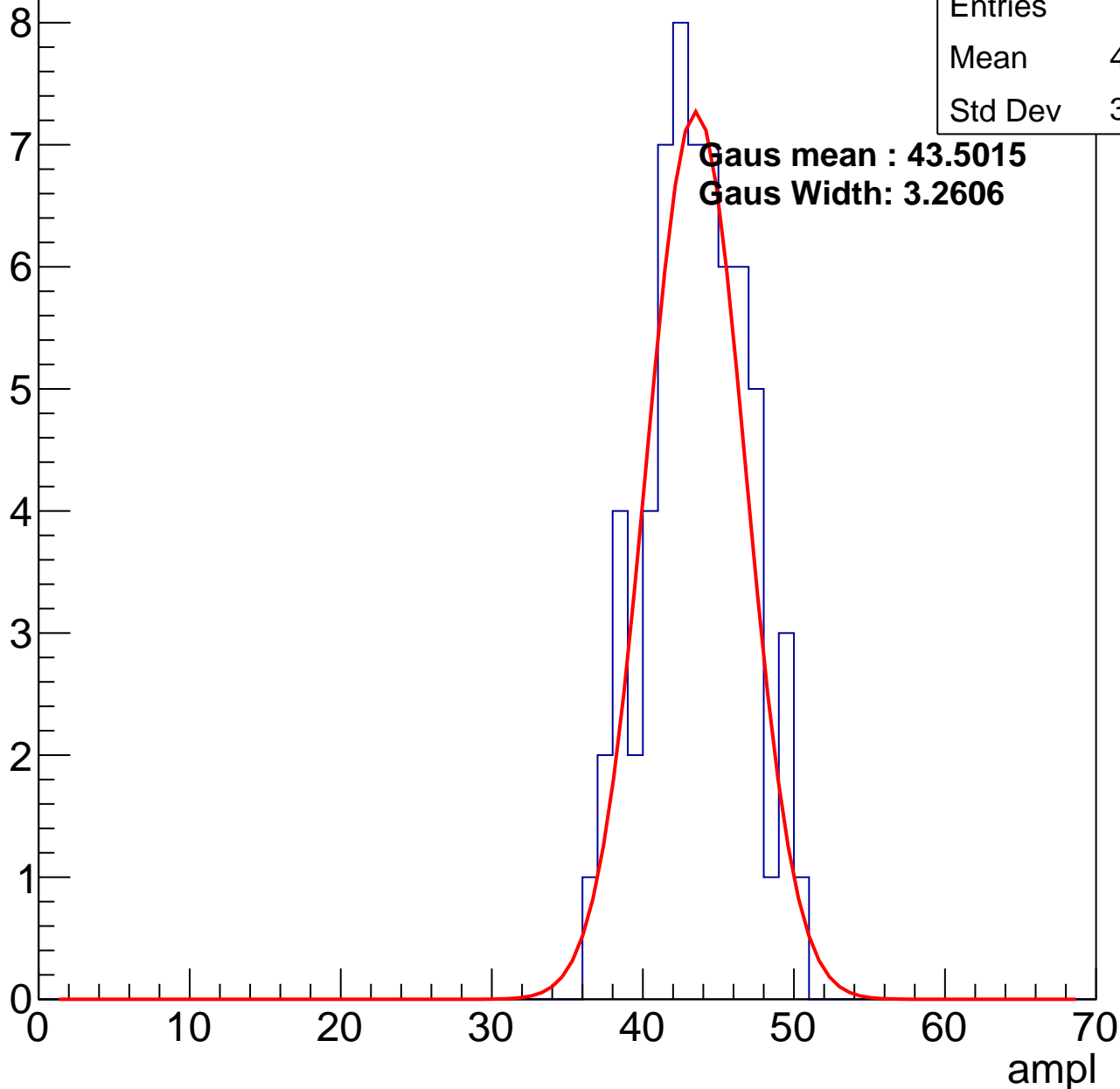
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.09
Std Dev	3.239

Gaus mean : 43.5015

Gaus Width: 3.2606

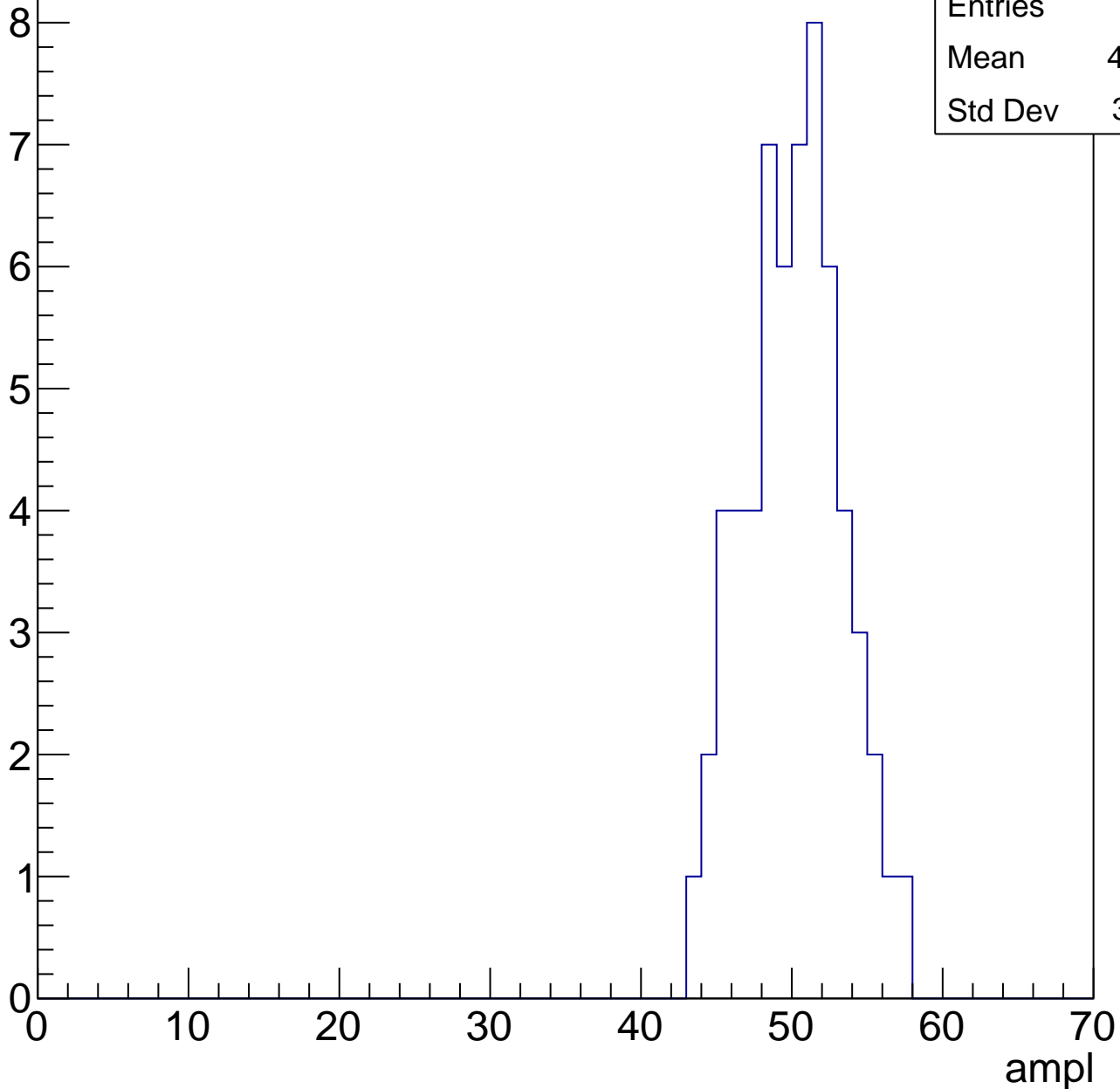


B1L003S, U20-ch40, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.67
Std Dev	3.161



B1L003S, U20-ch40, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 55.4

Std Dev 3.477

0

0

10

20

30

40

50

60

ampl

0

1

2

3

4

5

6

7

B1L003S, U20-ch40, adc5

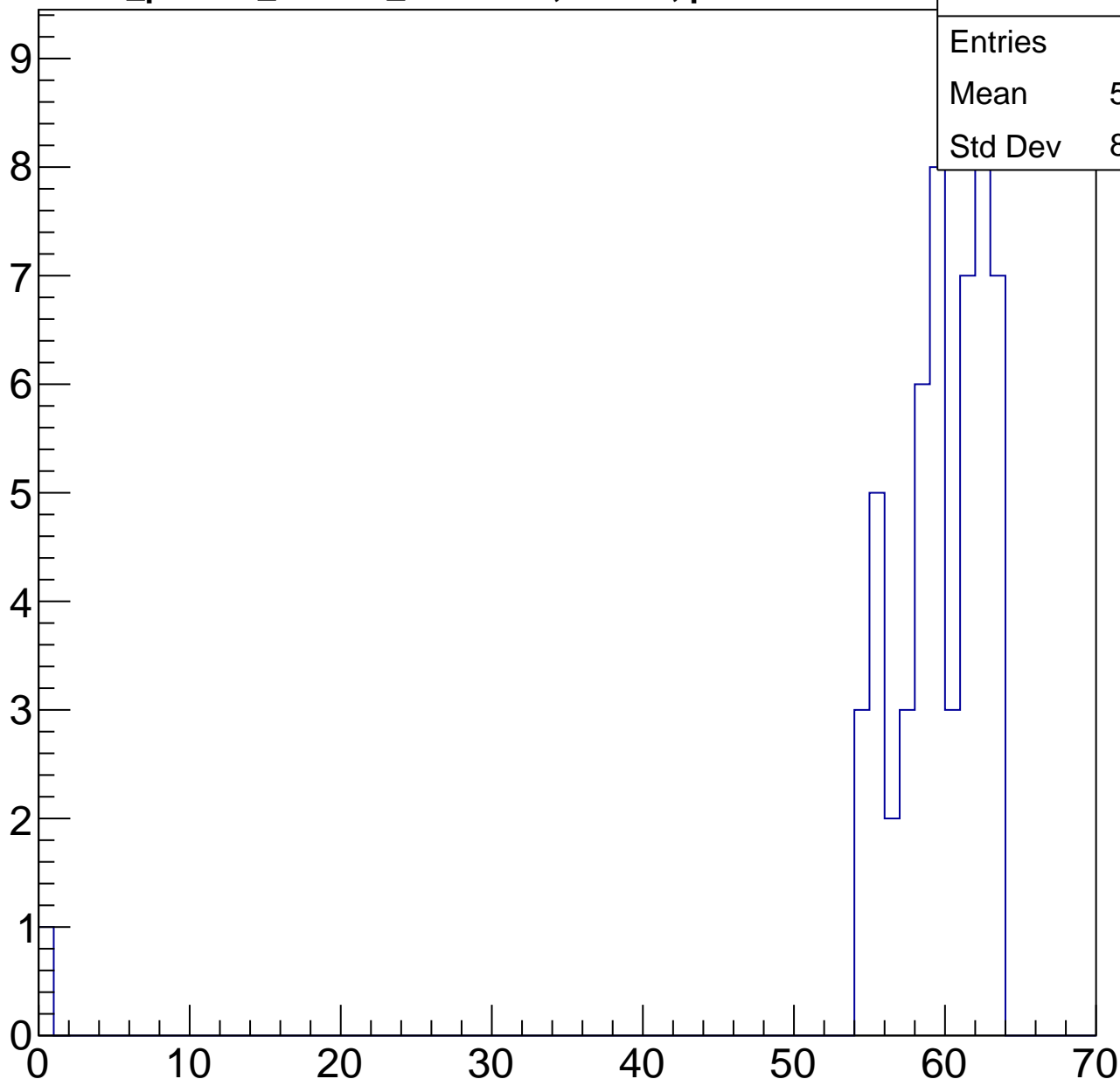
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.26
Std Dev	8.462

ampl



B1L003S, U20-ch40, adc6

calib_packv5_042523_0143.root, FC#13, port D2

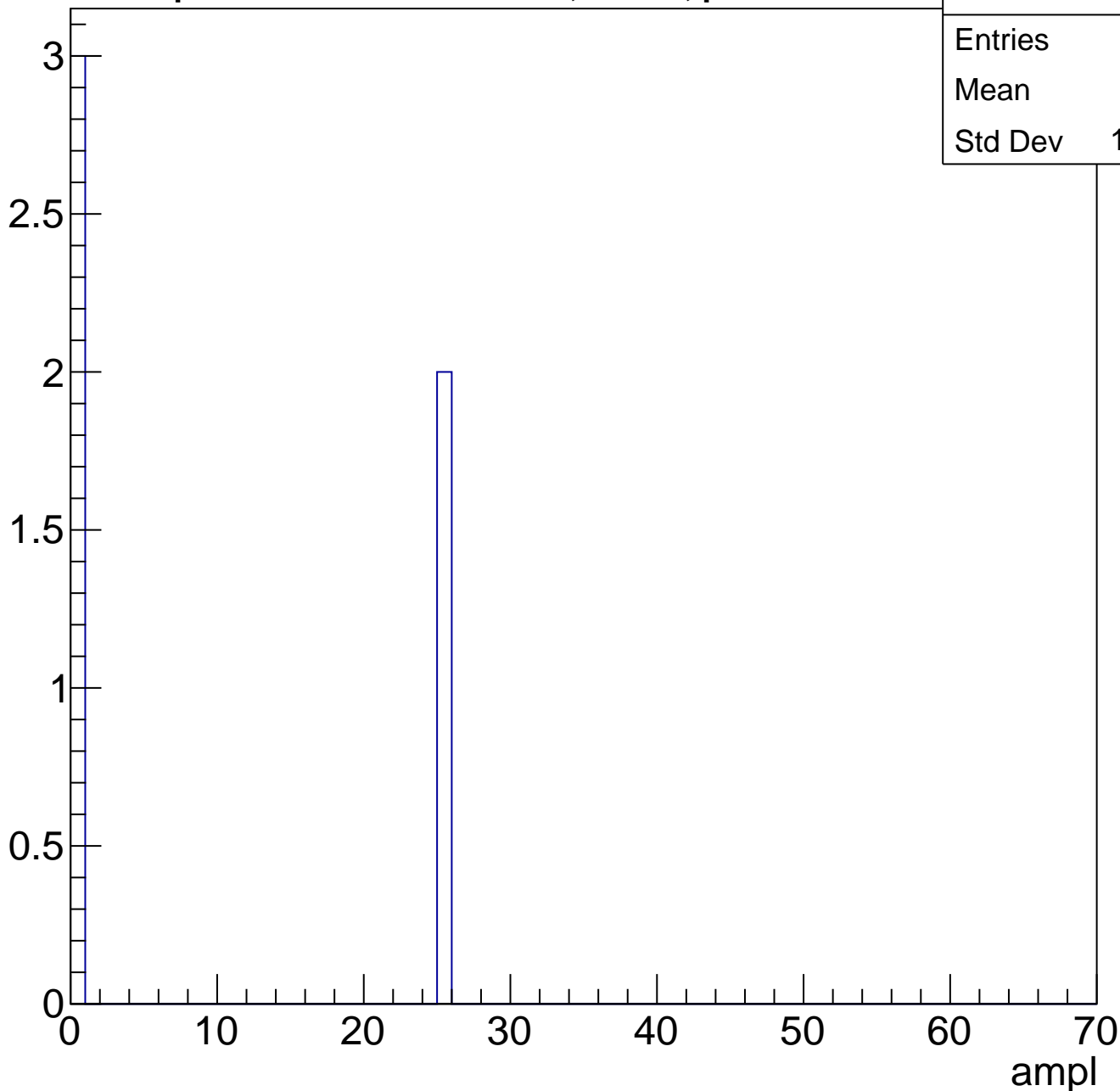
Entry



B1L003S, U20-ch40, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	10
Std Dev	12.25

B1L003S, U20-ch41, adc0

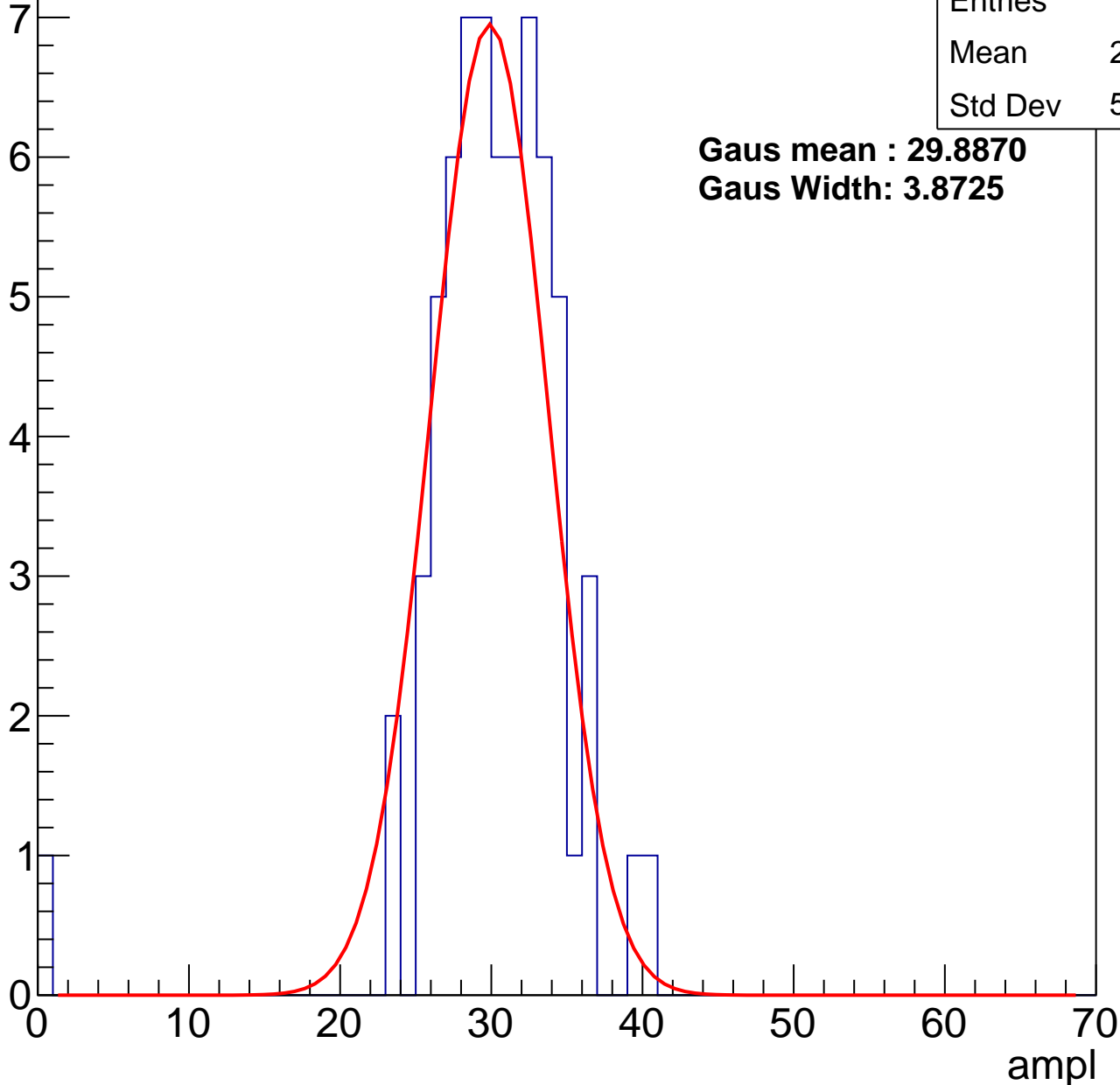
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	29.73
Std Dev	5.065

Gaus mean : 29.8870

Gaus Width: 3.8725



B1L003S, U20-ch41, adc1

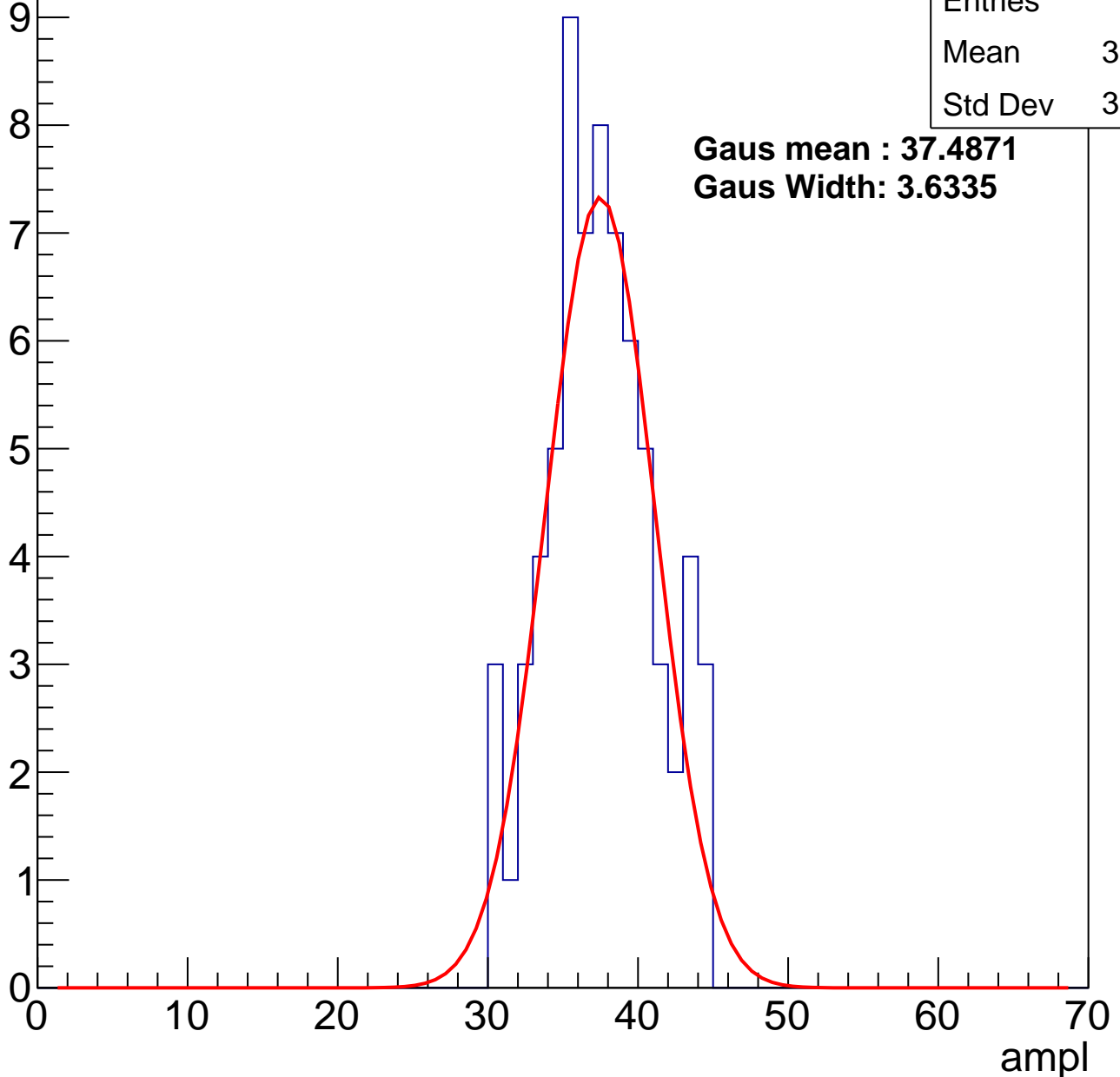
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.04
Std Dev	3.535

Gaus mean : 37.4871

Gaus Width: 3.6335



B1L003S, U20-ch41, adc2

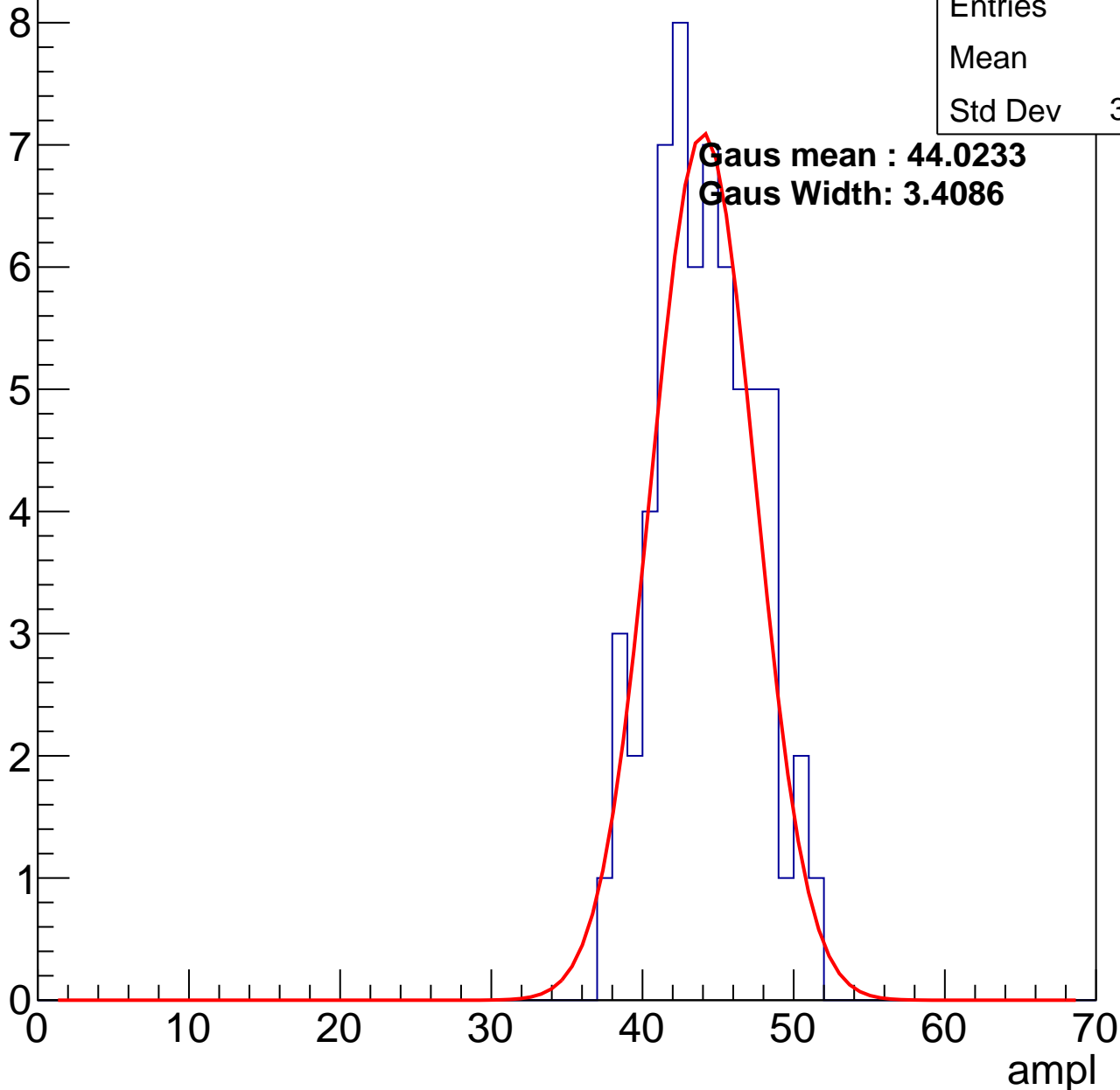
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.7
Std Dev	3.245

Gaus mean : 44.0233

Gaus Width: 3.4086

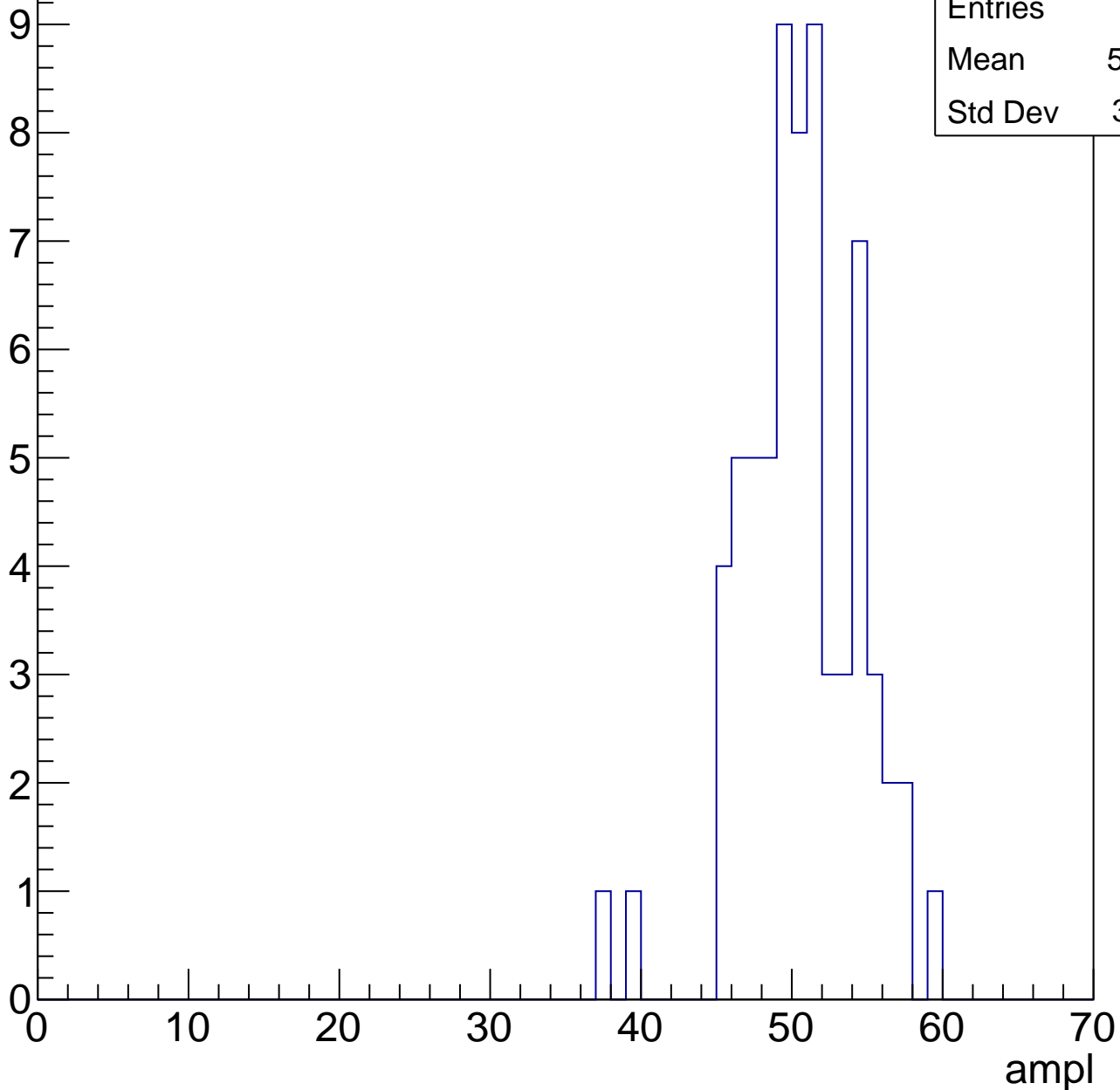


B1L003S, U20-ch41, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.06
Std Dev	3.891

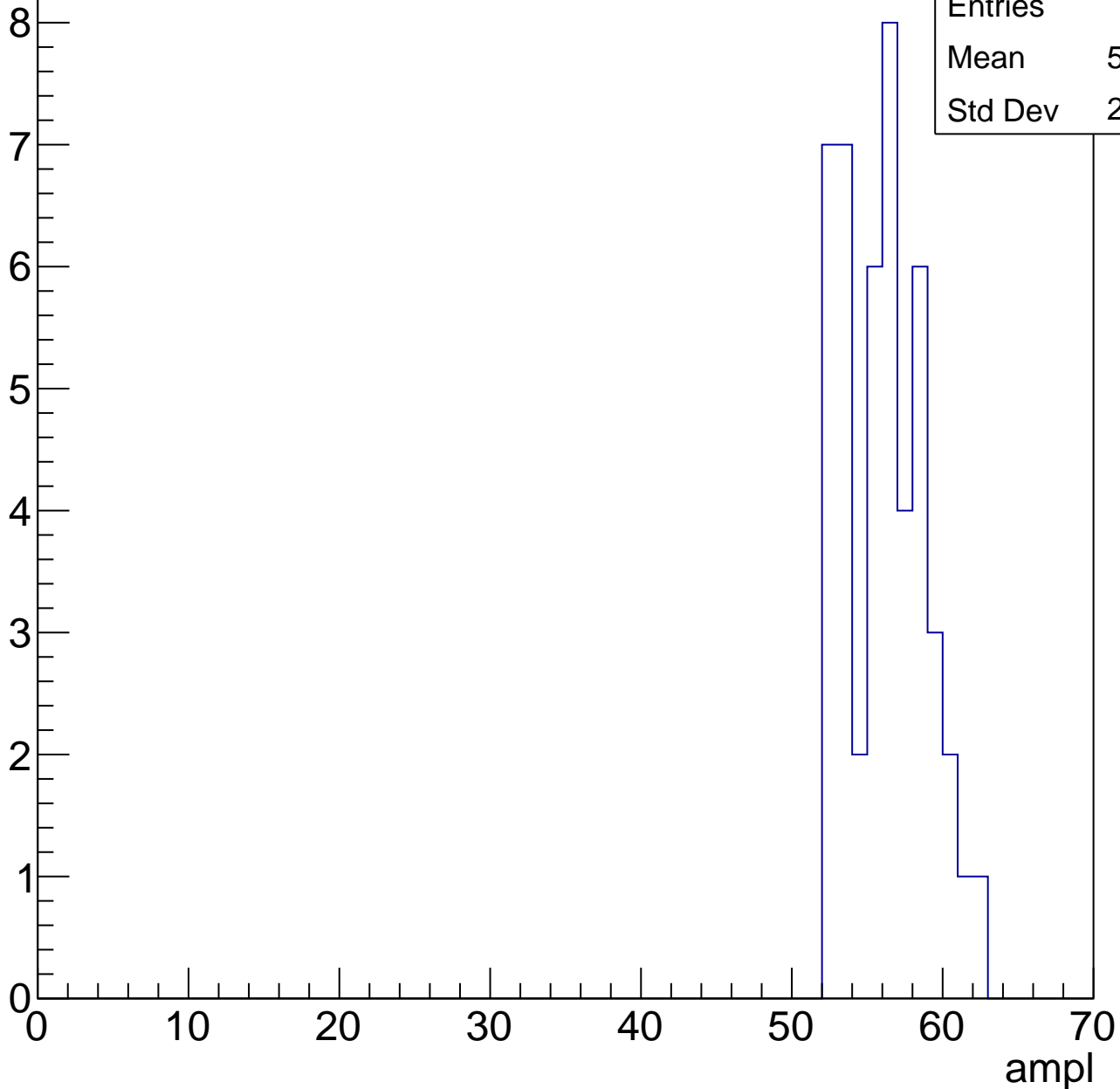


B1L003S, U20-ch41, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	55.68
Std Dev	2.659

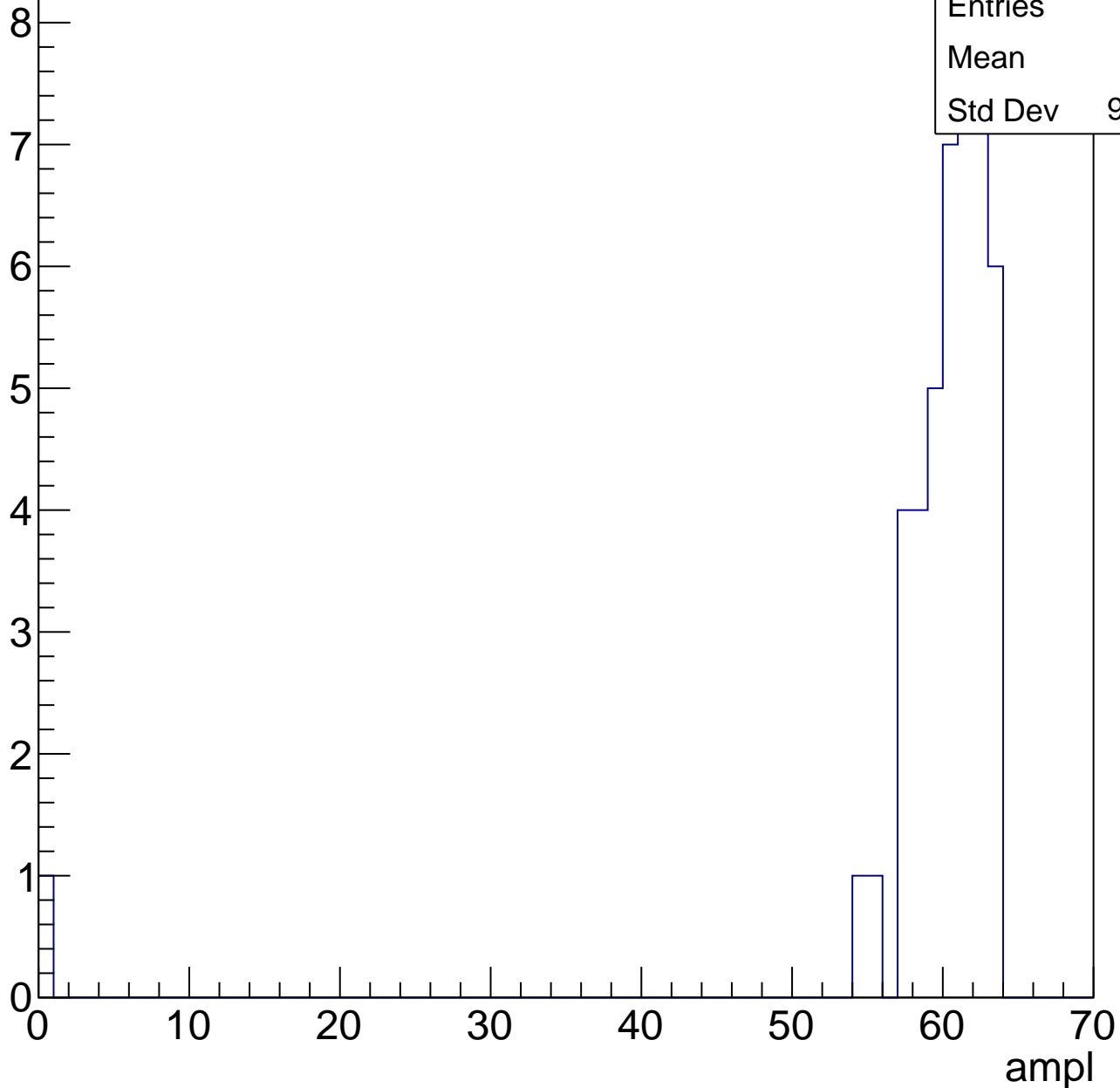


B1L003S, U20-ch41, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

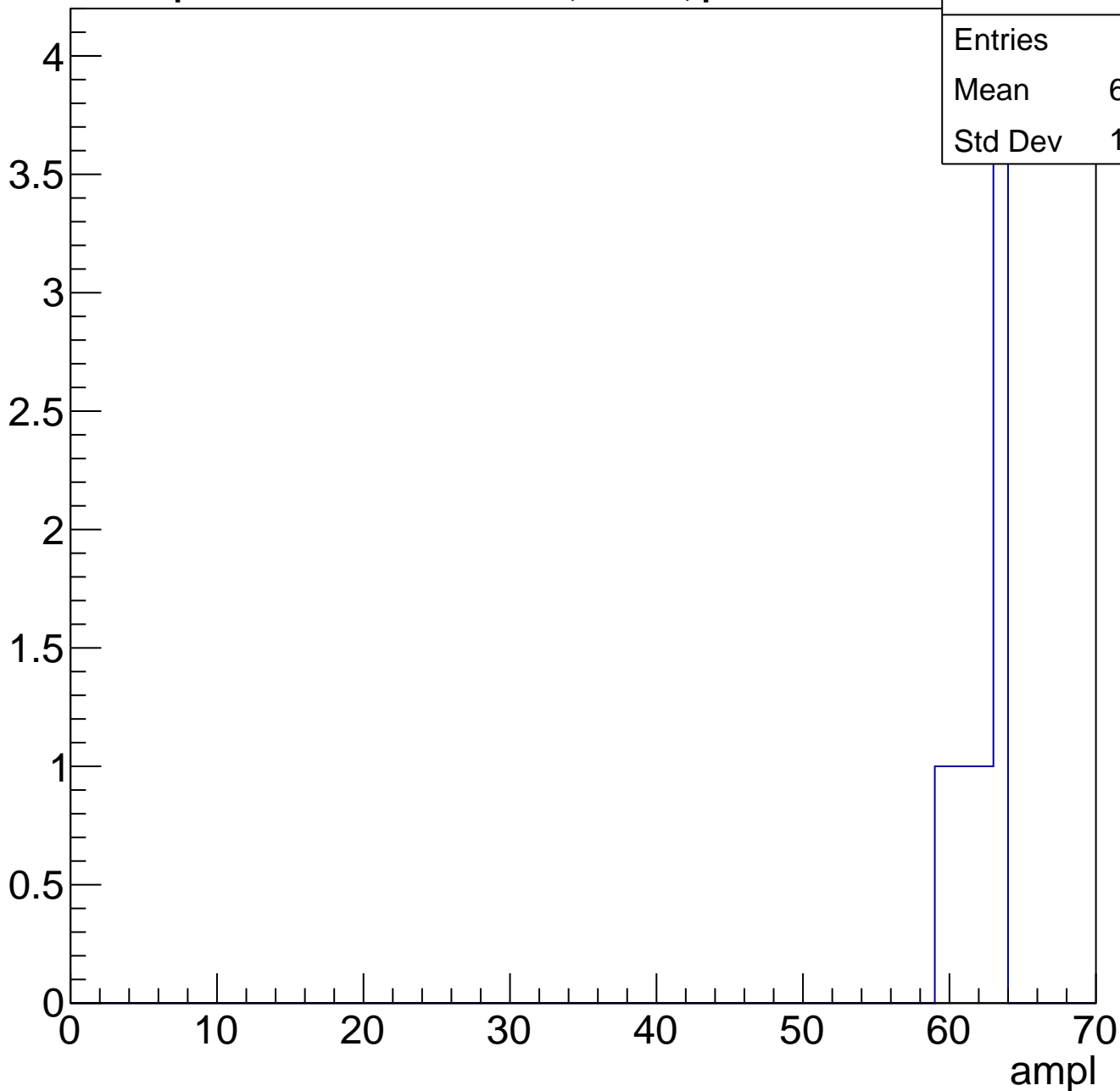
Entries	45
Mean	58.8
Std Dev	9.125



B1L003S, U20-ch41, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch41, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch42, adc0

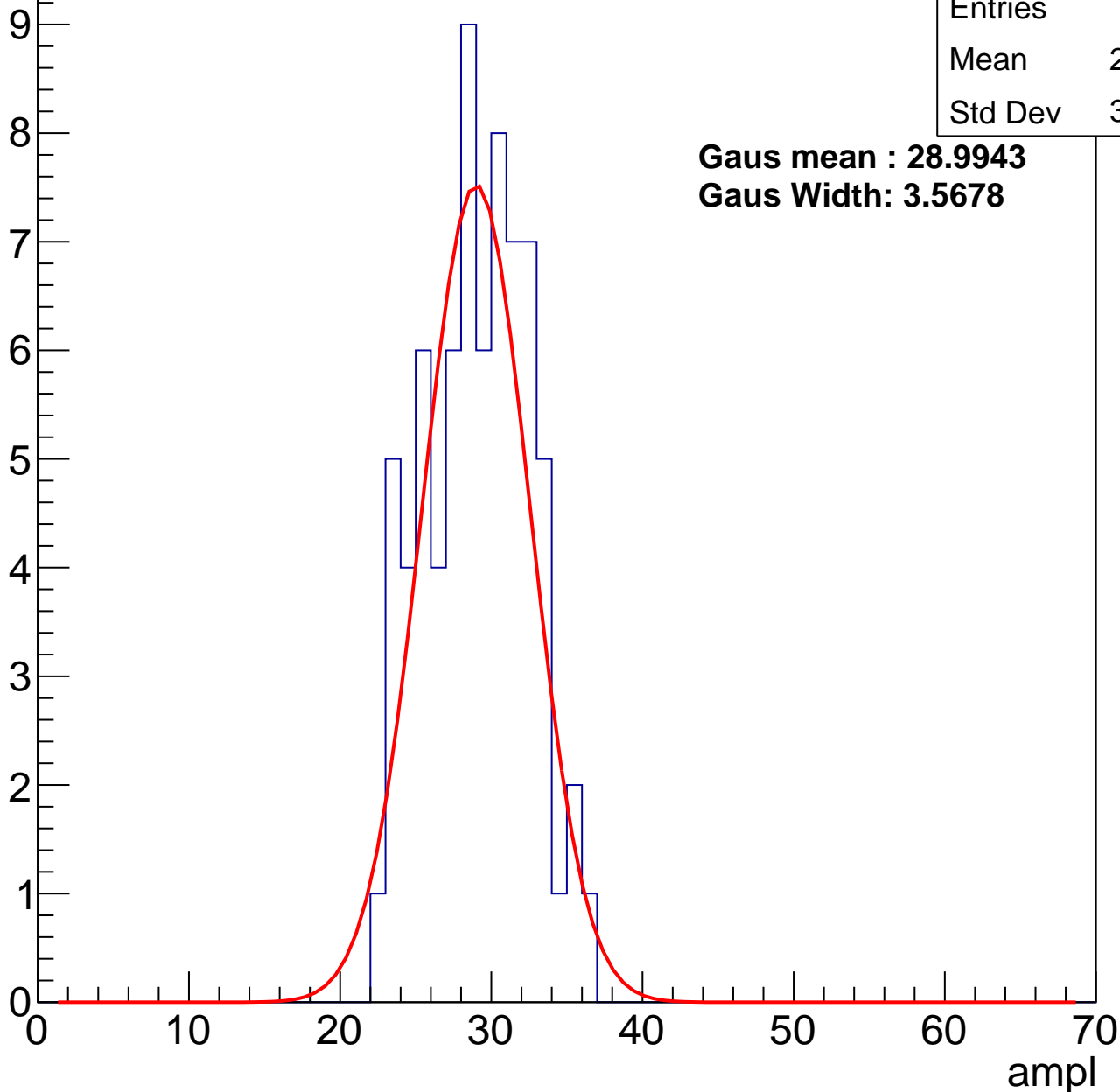
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	28.62
Std Dev	3.352

Gaus mean : 28.9943

Gaus Width: 3.5678



B1L003S, U20-ch42, adc1

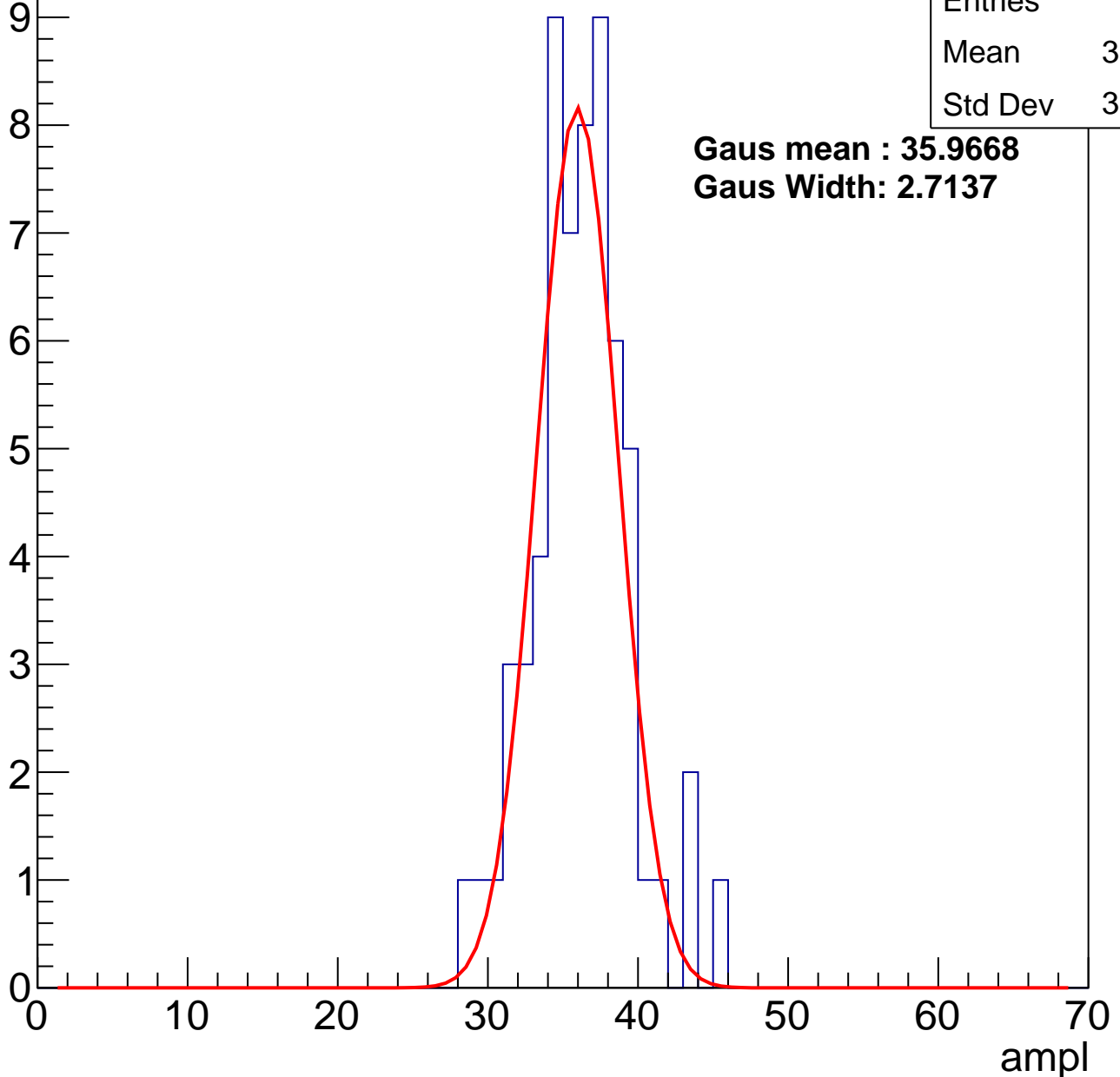
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.73
Std Dev	3.224

Gaus mean : 35.9668

Gaus Width: 2.7137



B1L003S, U20-ch42, adc2

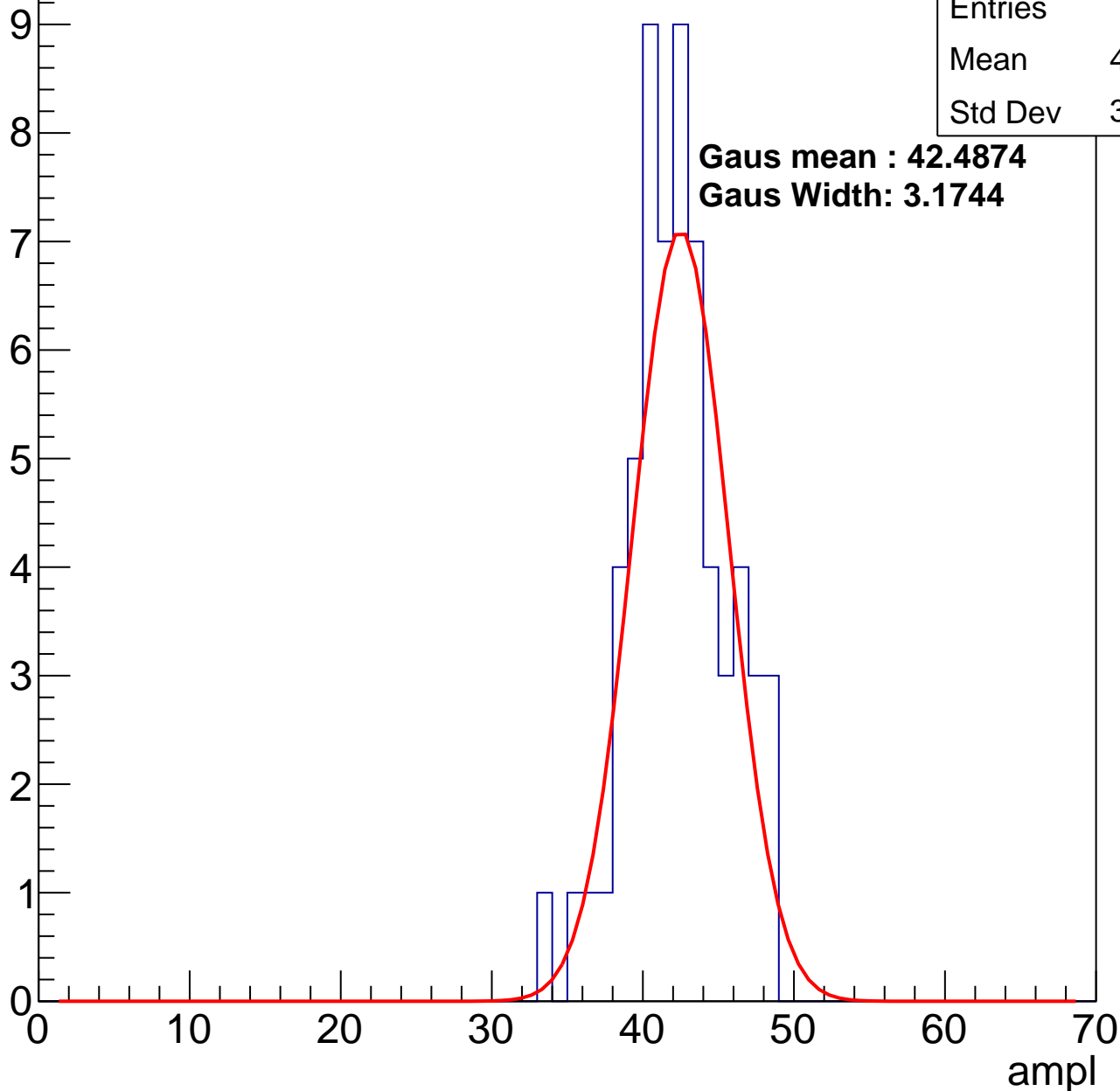
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	41.84
Std Dev	3.209

Gaus mean : 42.4874

Gaus Width: 3.1744

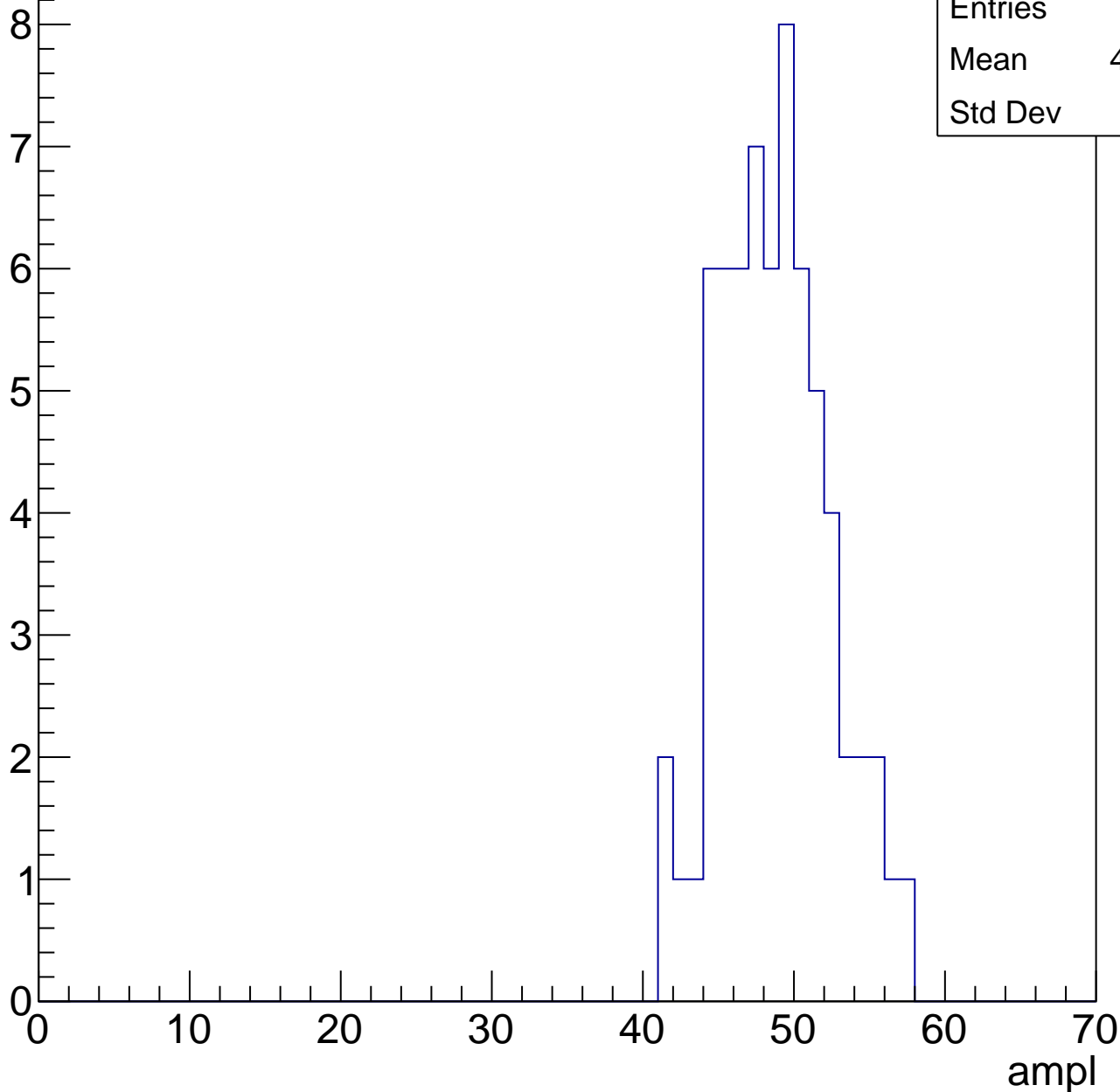


B1L003S, U20-ch42, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.27
Std Dev	3.57

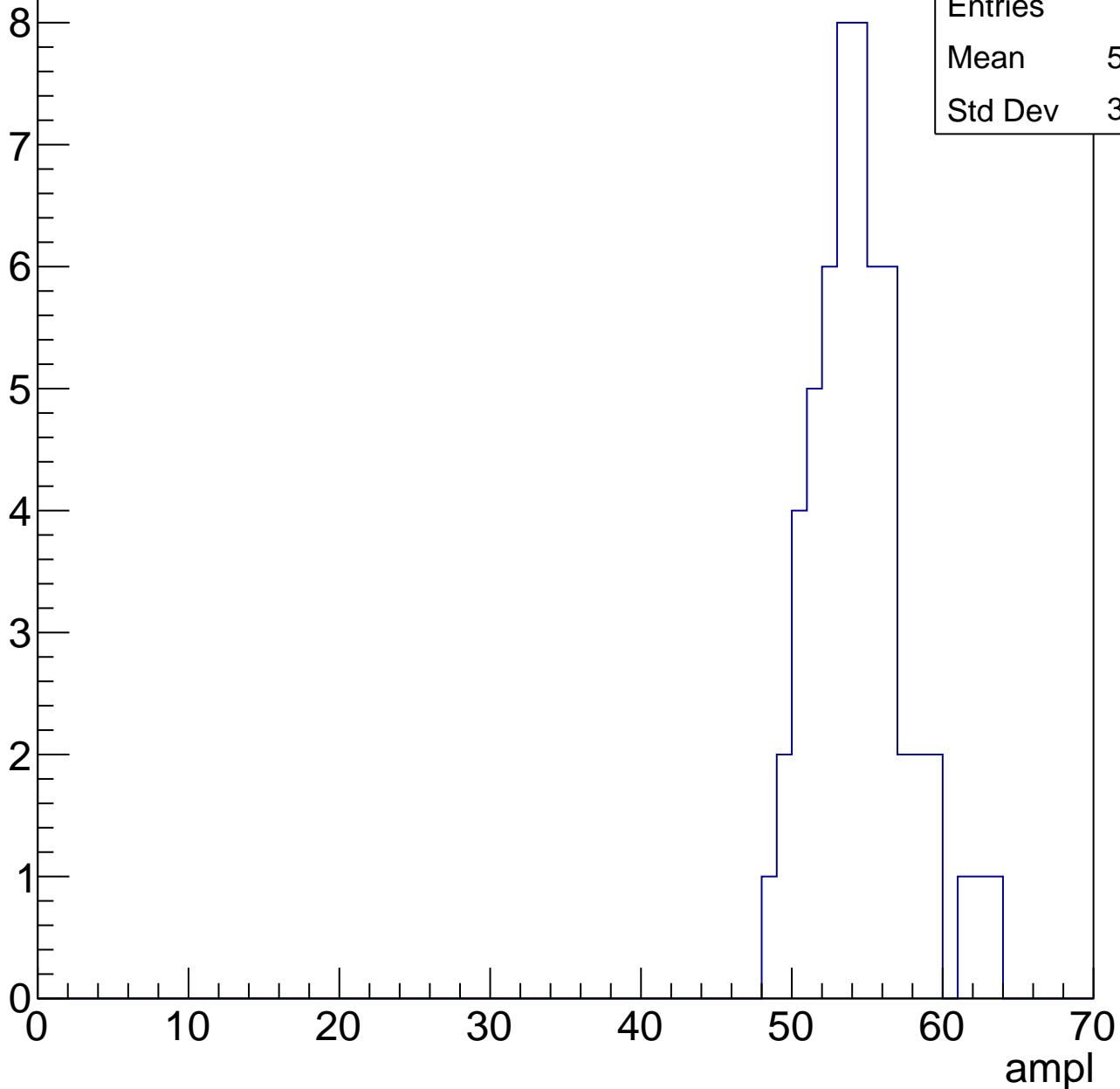


B1L003S, U20-ch42, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	53.98
Std Dev	3.165

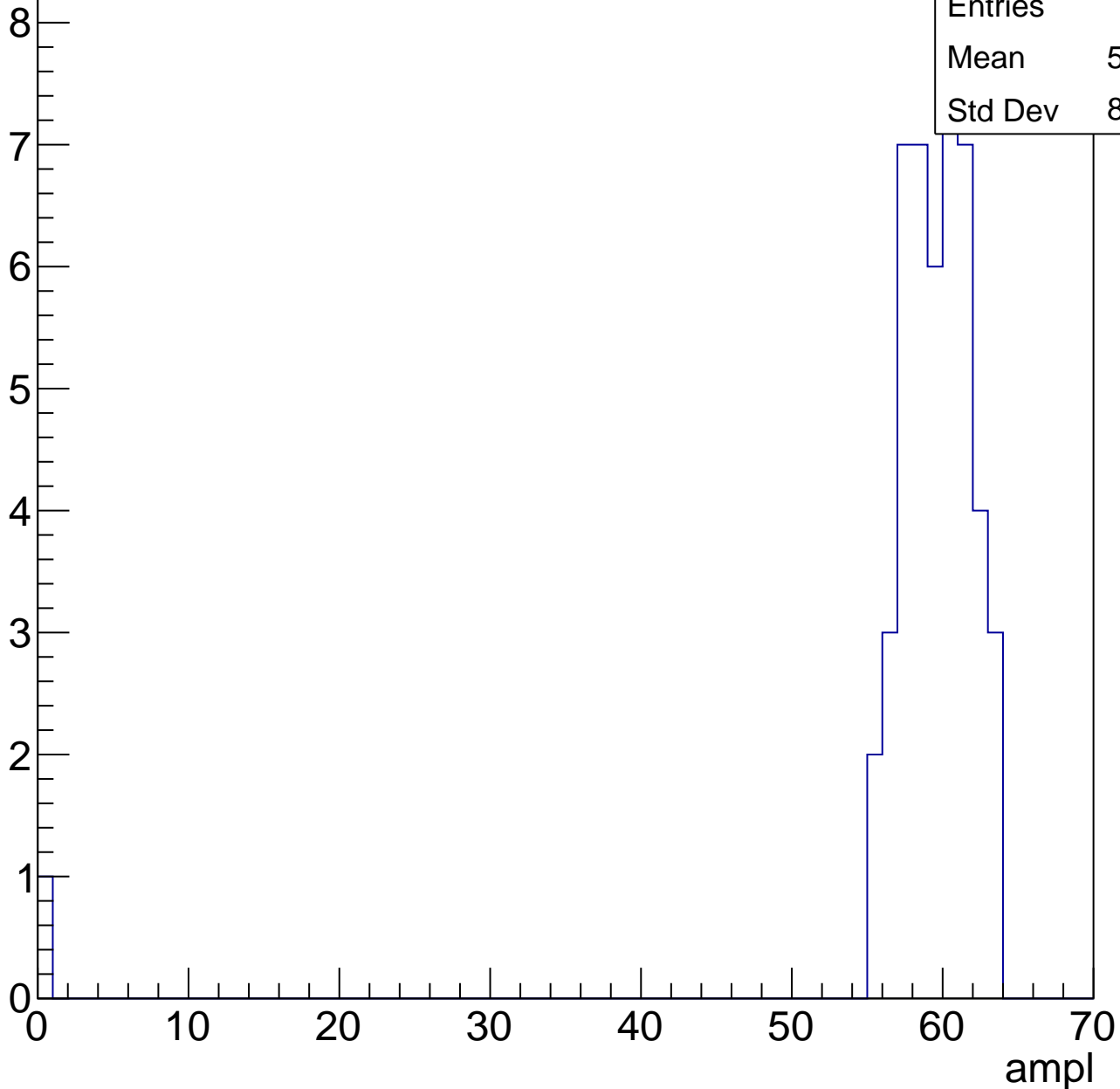


B1L003S, U20-ch42, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

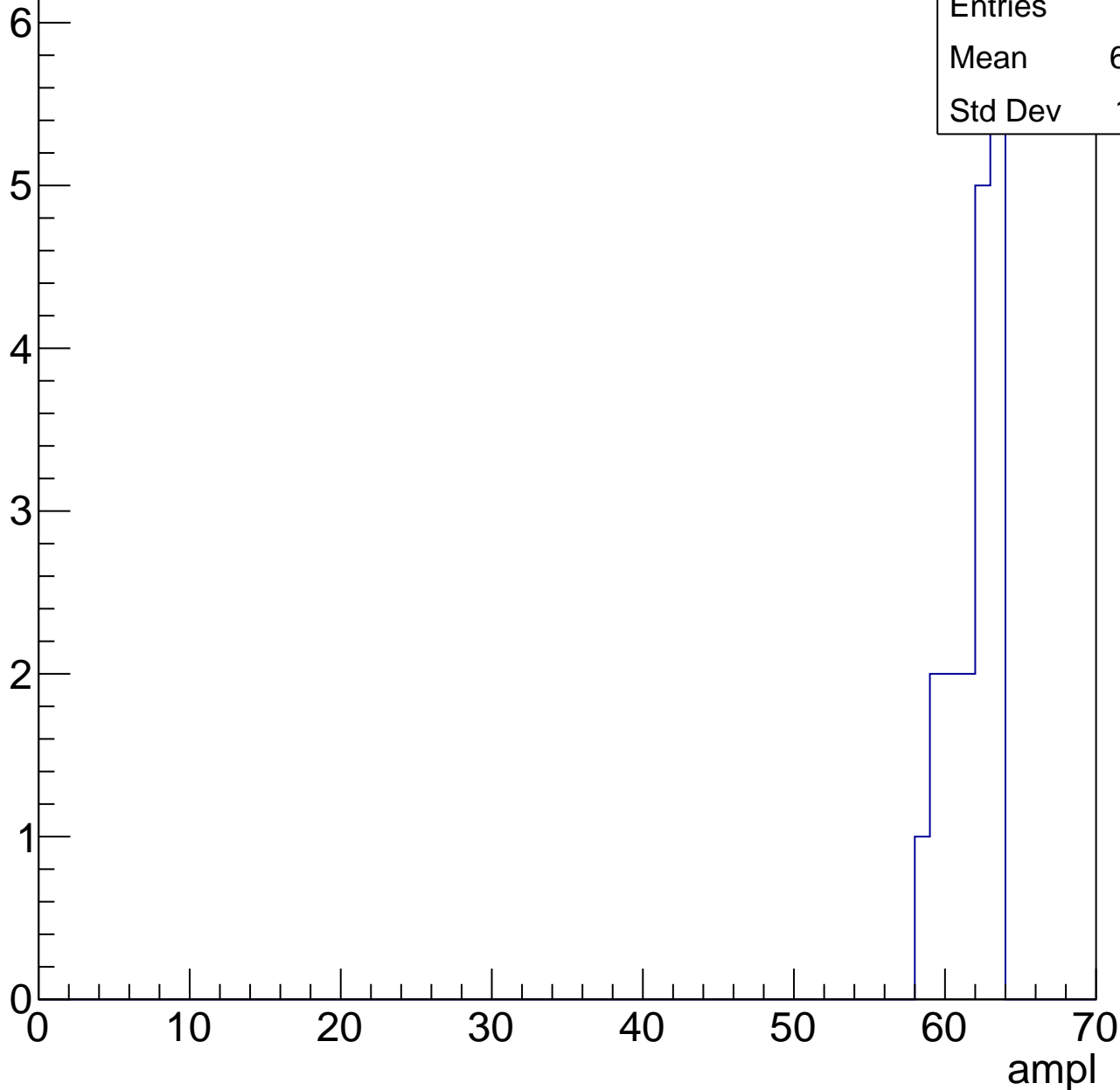
Entries	48
Mean	57.94
Std Dev	8.709



B1L003S, U20-ch42, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch42, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch43, adc0

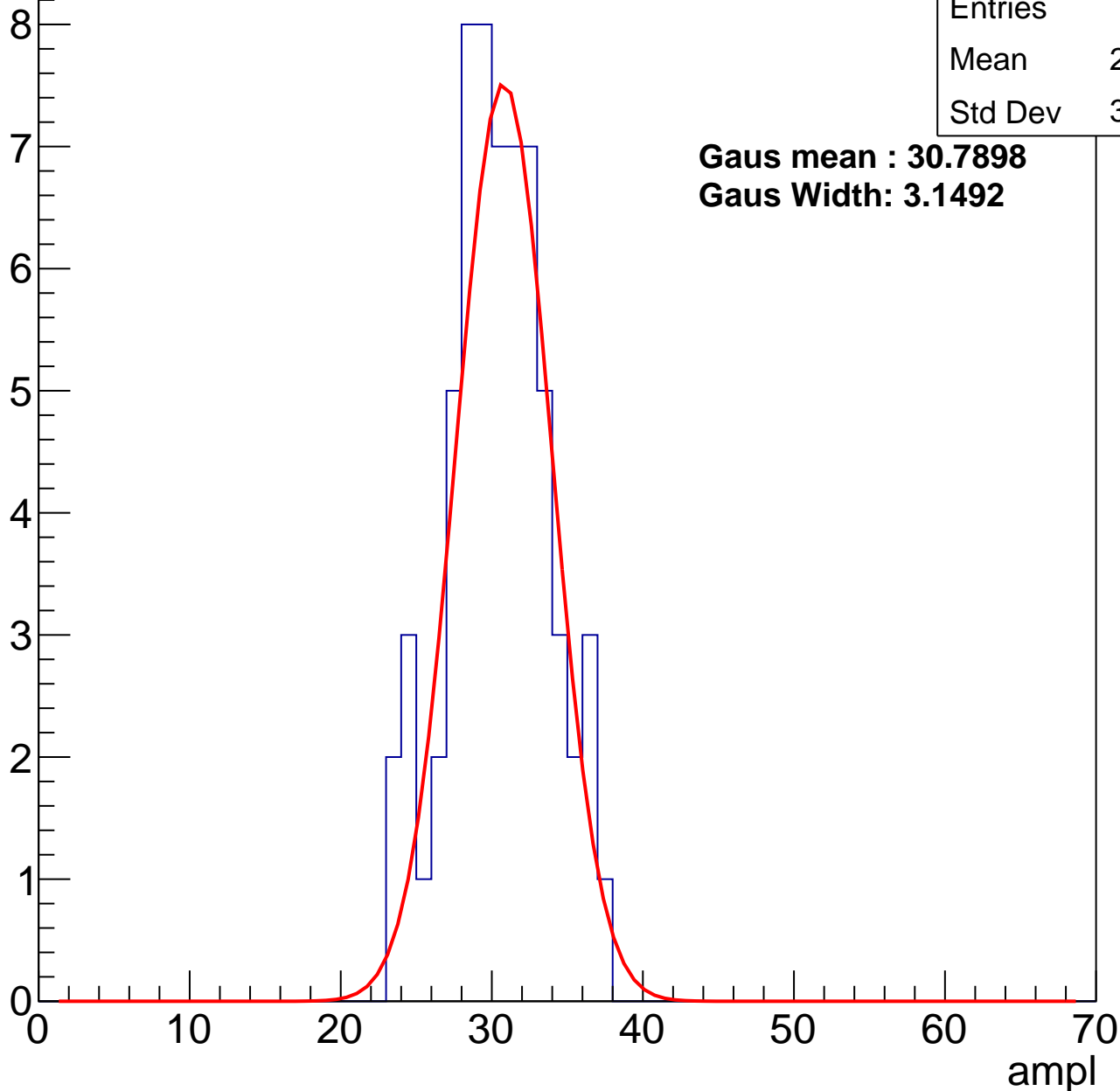
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.98
Std Dev	3.267

Gaus mean : 30.7898

Gaus Width: 3.1492



B1L003S, U20-ch43, adc1

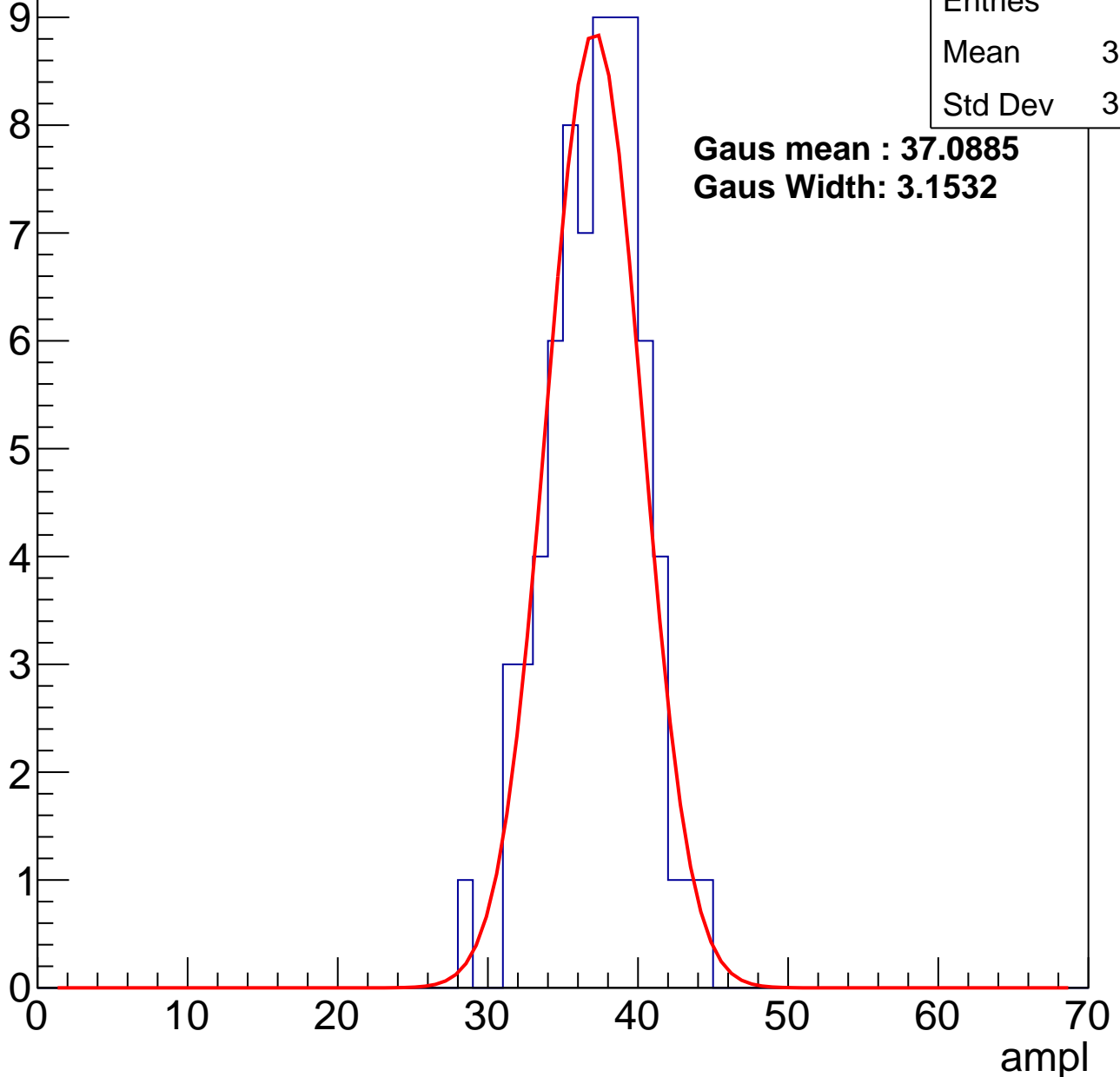
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.72
Std Dev	3.092

Gaus mean : 37.0885

Gaus Width: 3.1532



B1L003S, U20-ch43, adc2

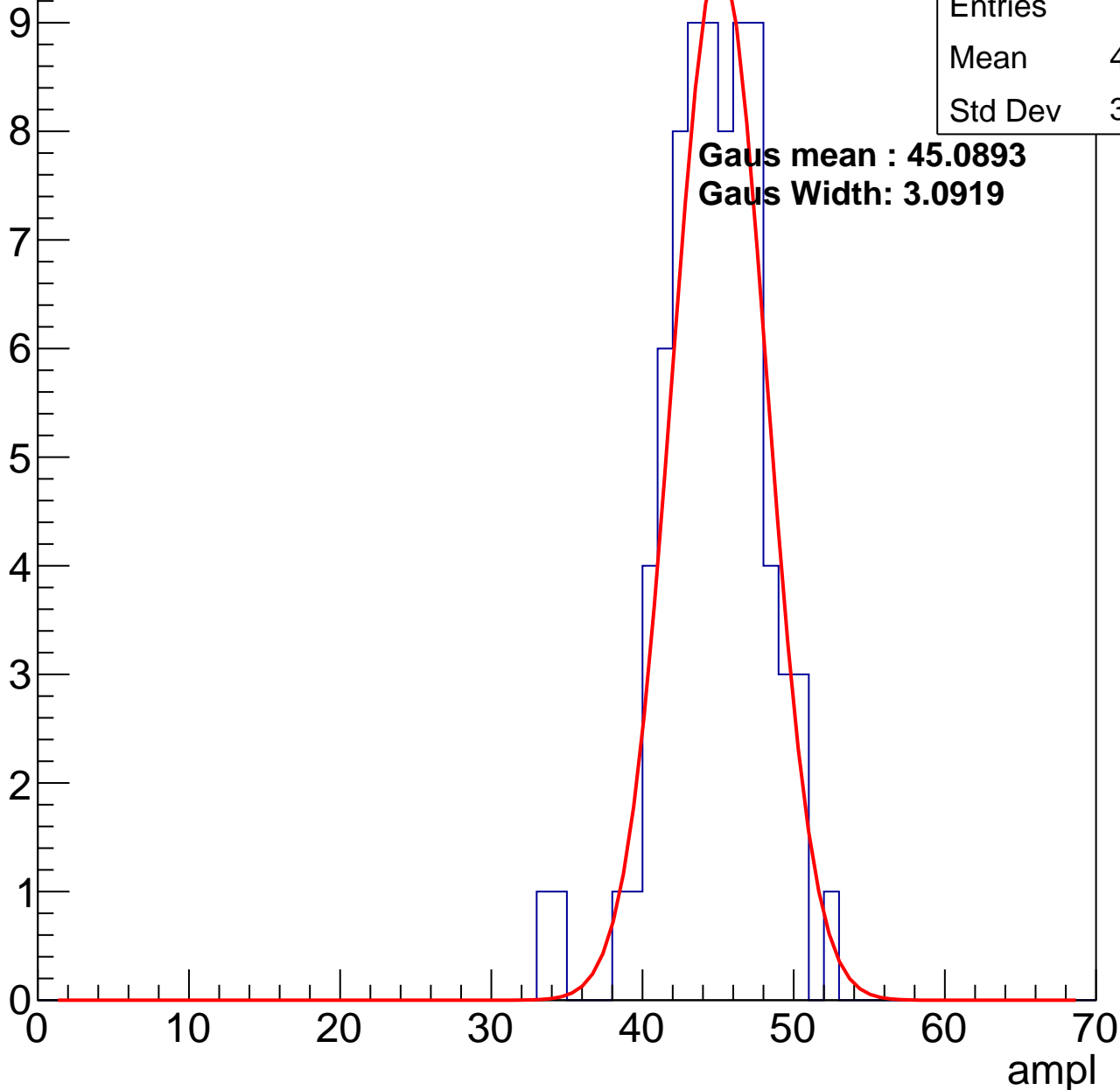
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	44.25
Std Dev	3.366

Gaus mean : 45.0893

Gaus Width: 3.0919

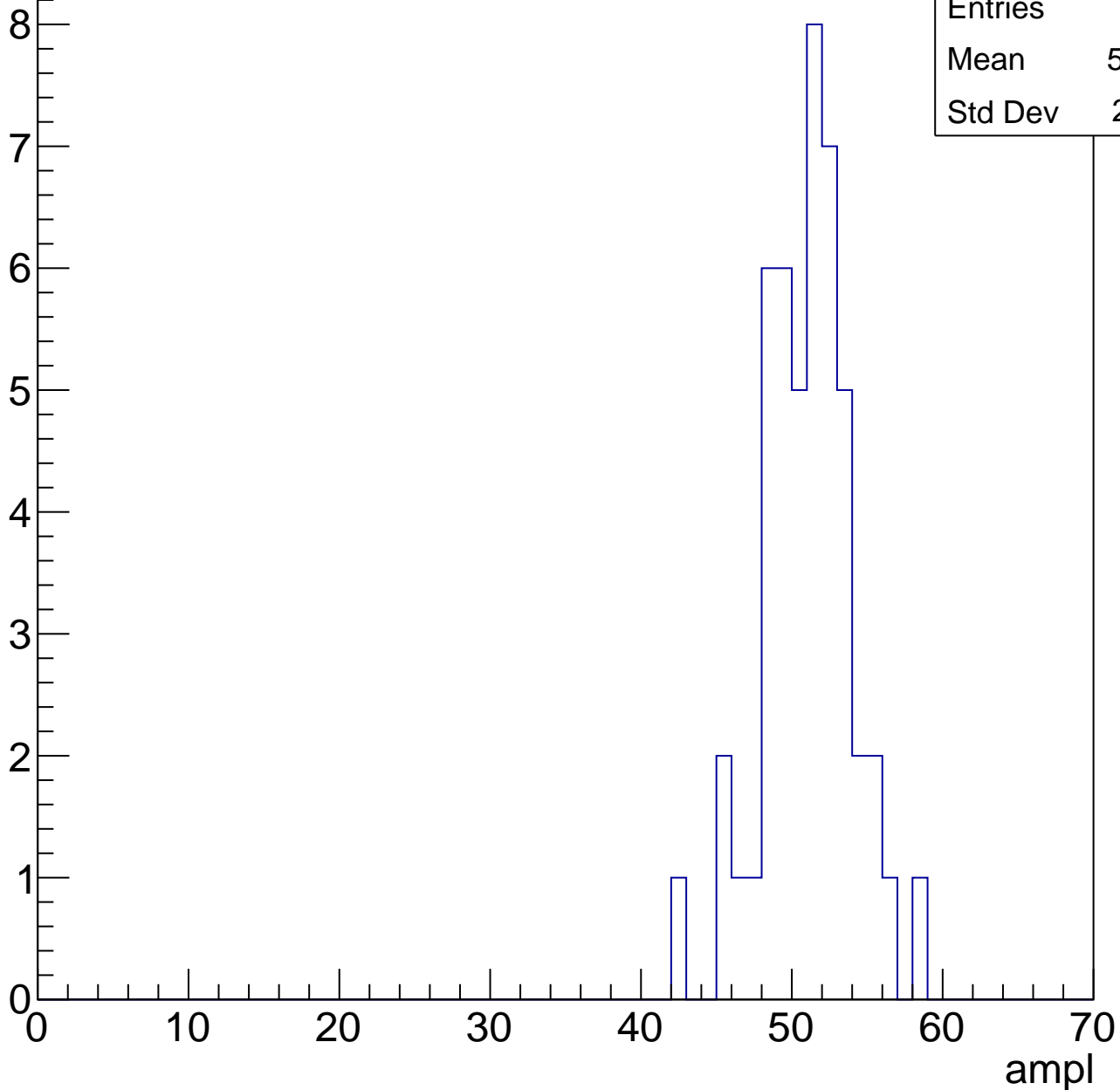


B1L003S, U20-ch43, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

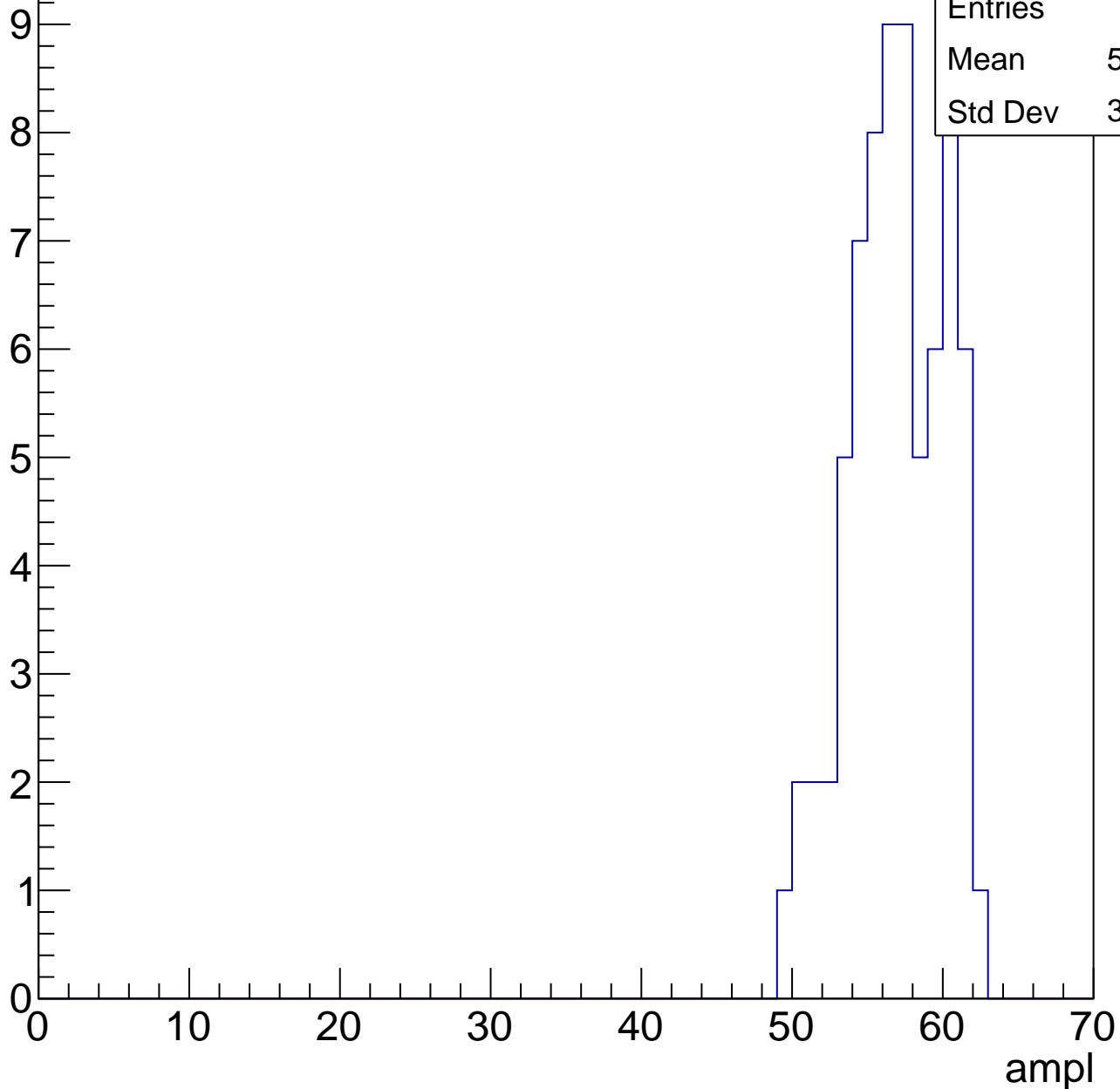
Entries	48
Mean	50.54
Std Dev	2.951



B1L003S, U20-ch43, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



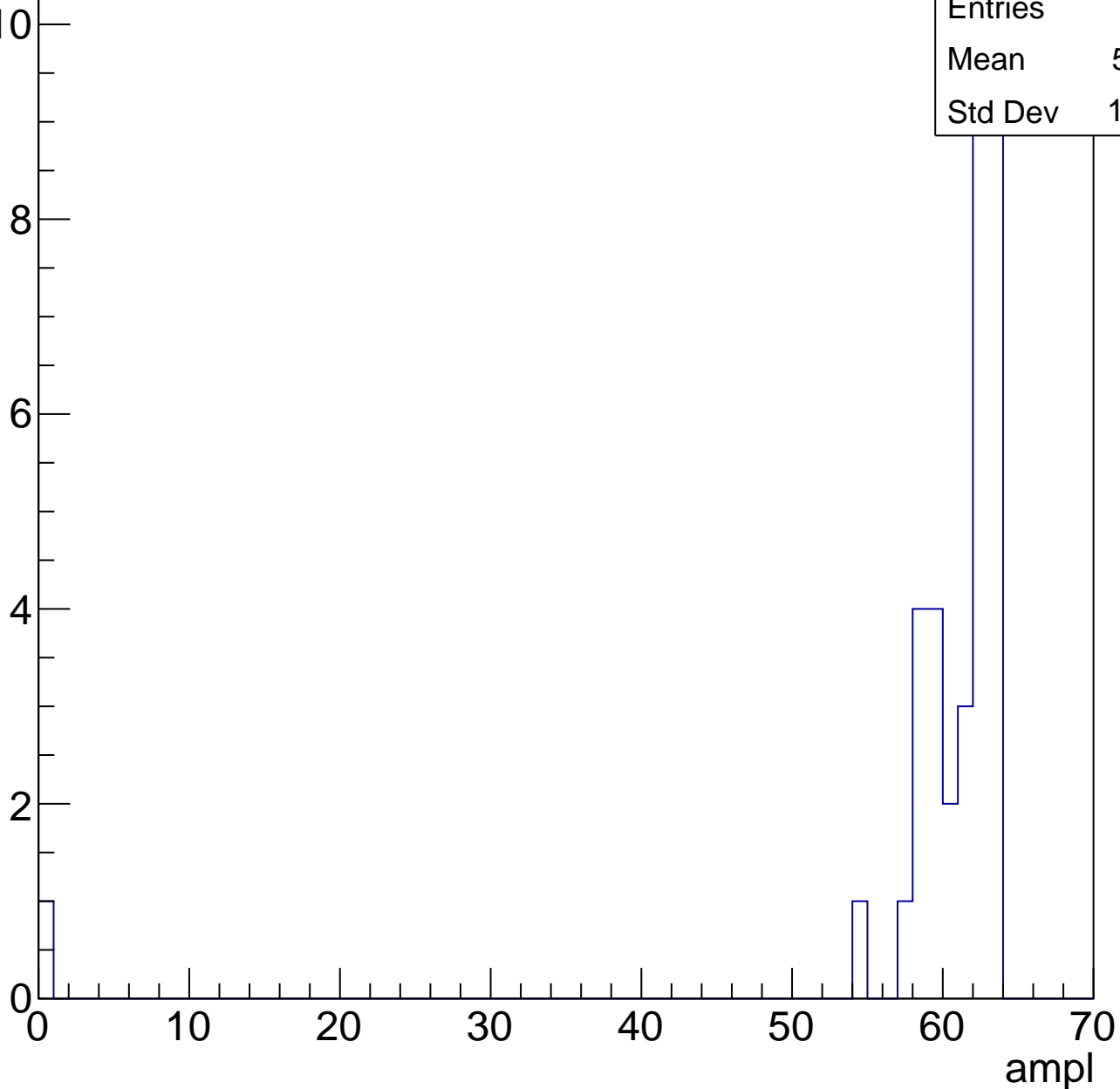
Entries	71
Mean	56.44
Std Dev	3.066

B1L003S, U20-ch43, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	59.11
Std Dev	10.37



B1L003S, U20-ch43, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch43, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch44, adc0

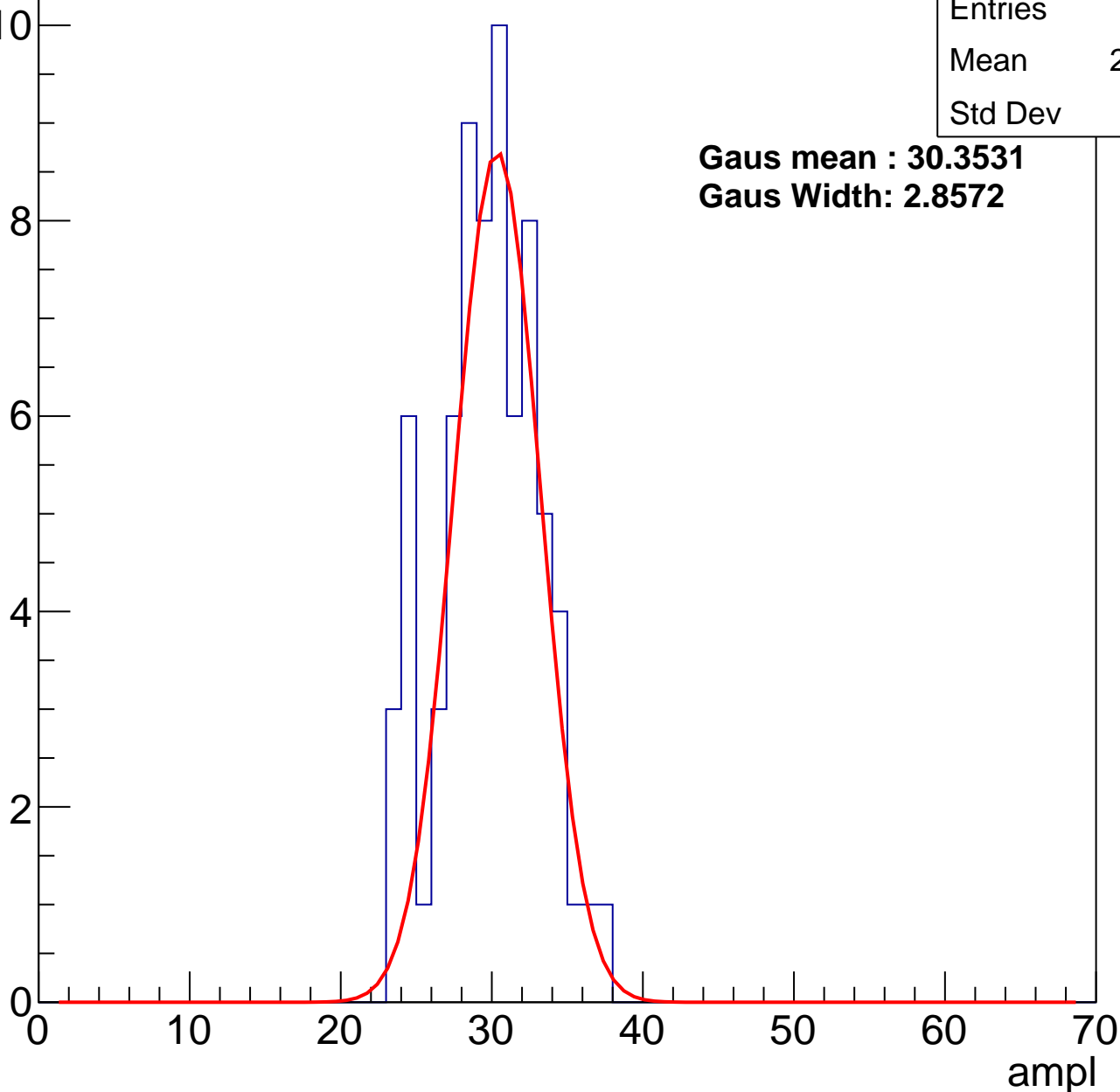
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.35
Std Dev	3.25

Gaus mean : 30.3531

Gaus Width: 2.8572



B1L003S, U20-ch44, adc1

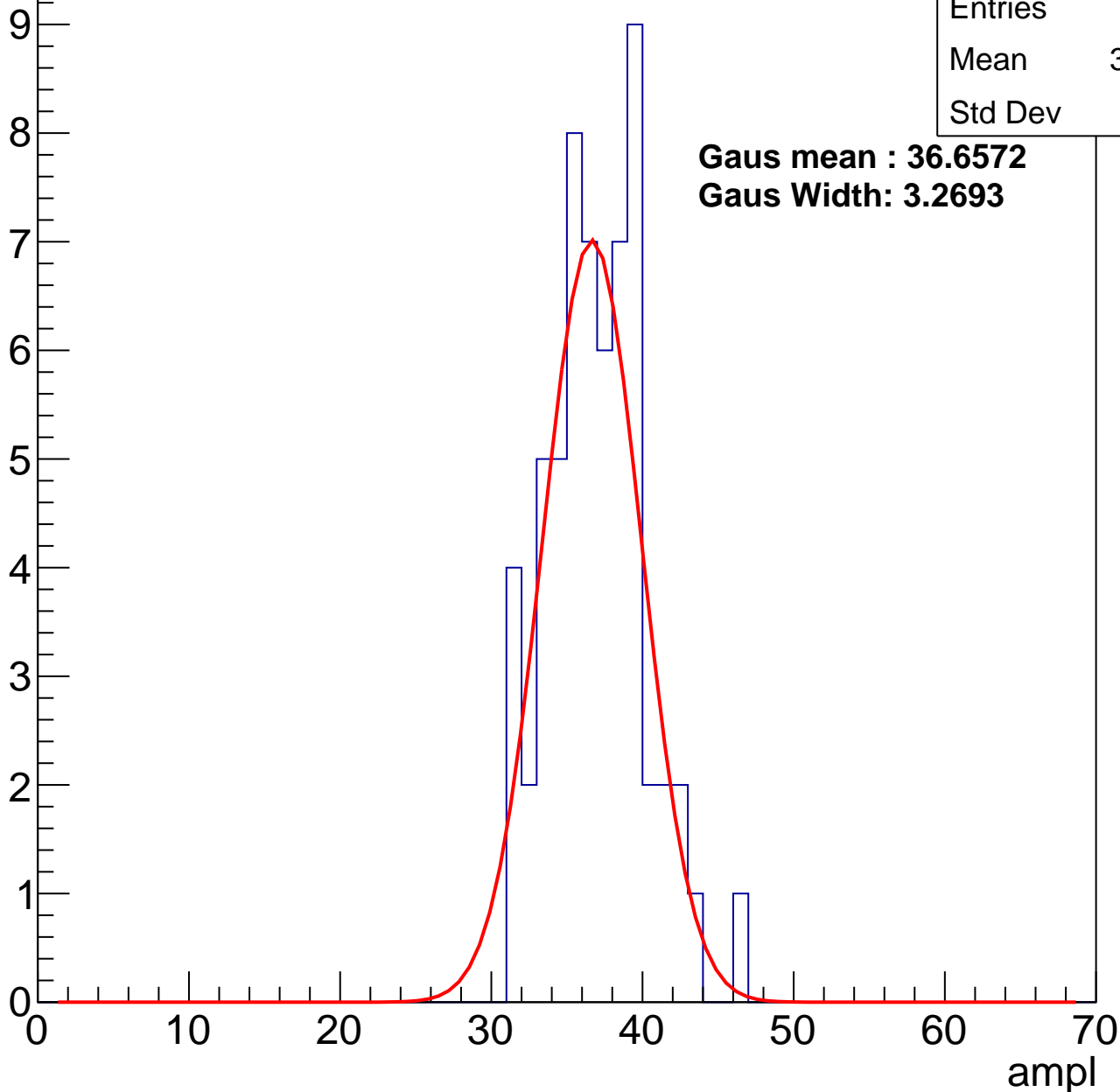
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.54
Std Dev	3.15

Gaus mean : 36.6572

Gaus Width: 3.2693



B1L003S, U20-ch44, adc2

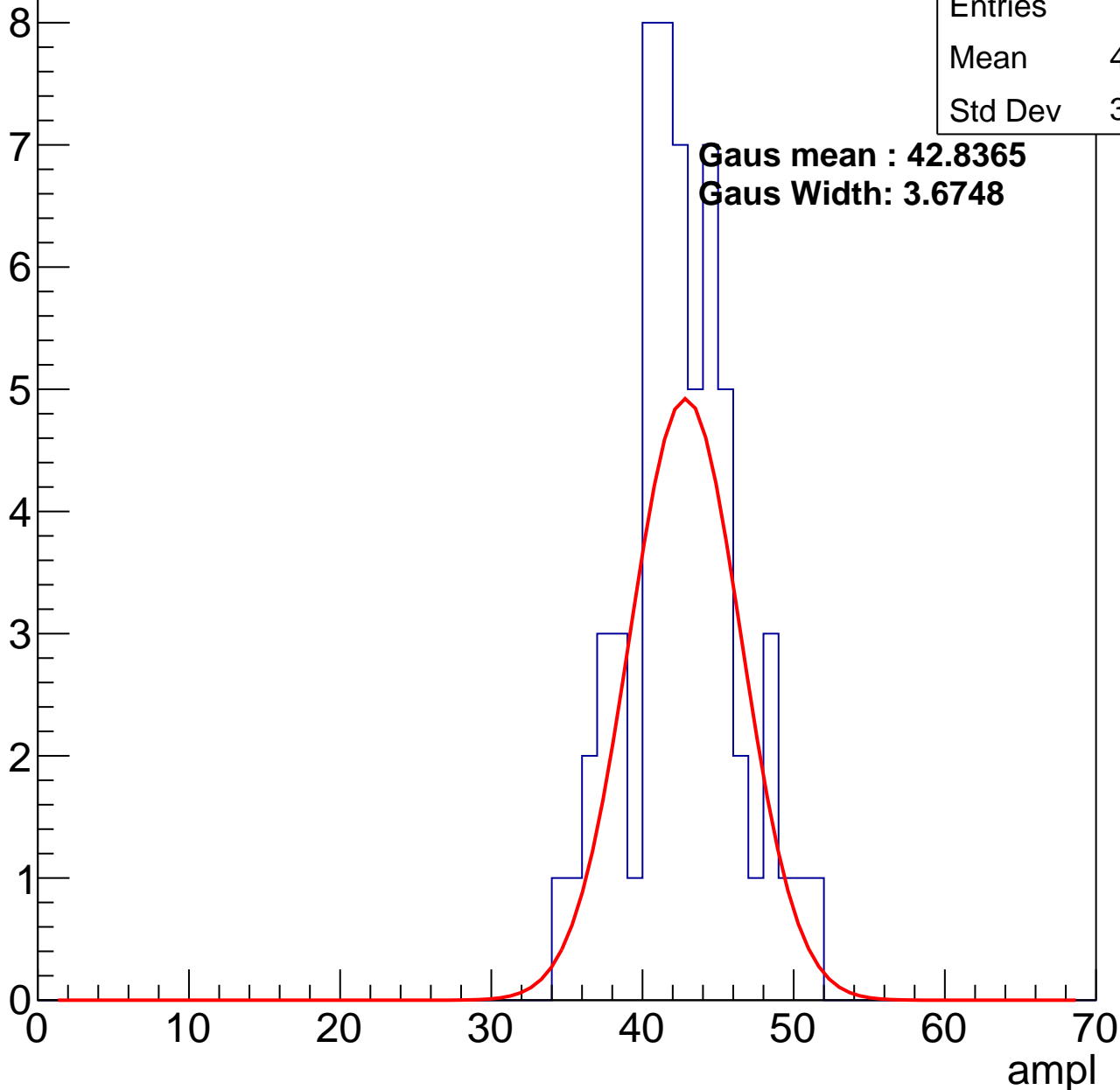
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.13
Std Dev	3.635

Gaus mean : 42.8365

Gaus Width: 3.6748

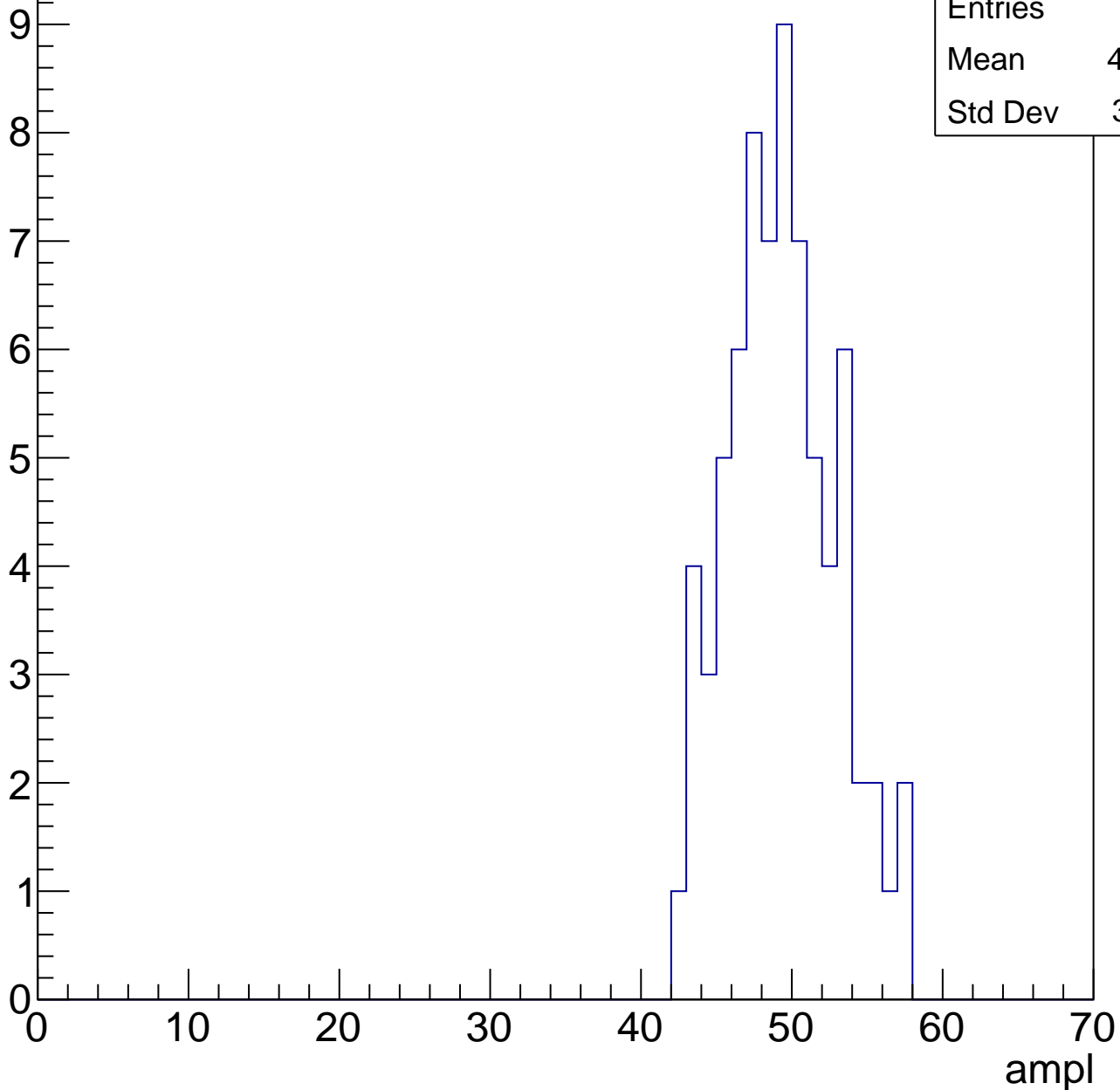


B1L003S, U20-ch44, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.88
Std Dev	3.531

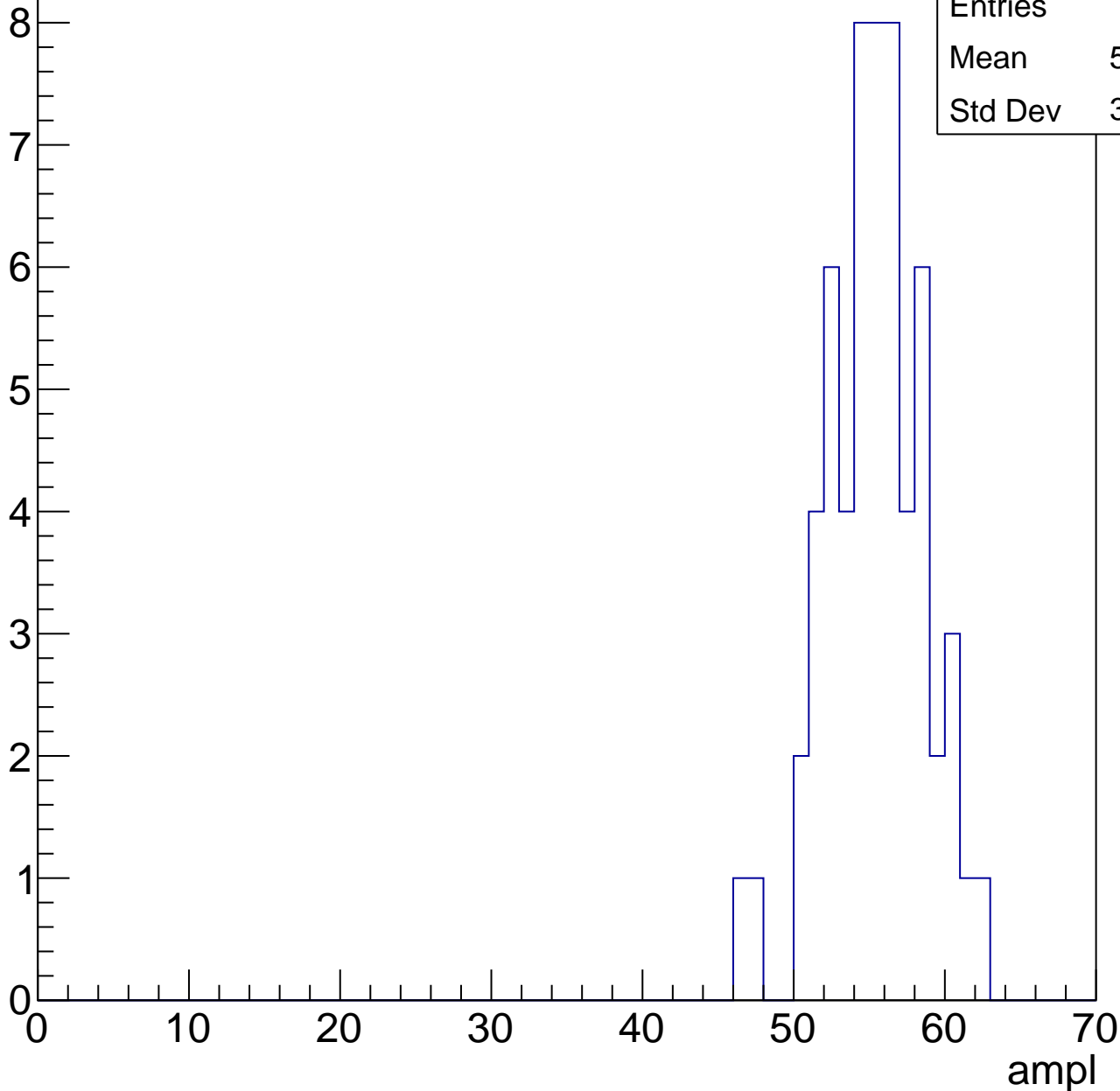


B1L003S, U20-ch44, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.88
Std Dev	3.205

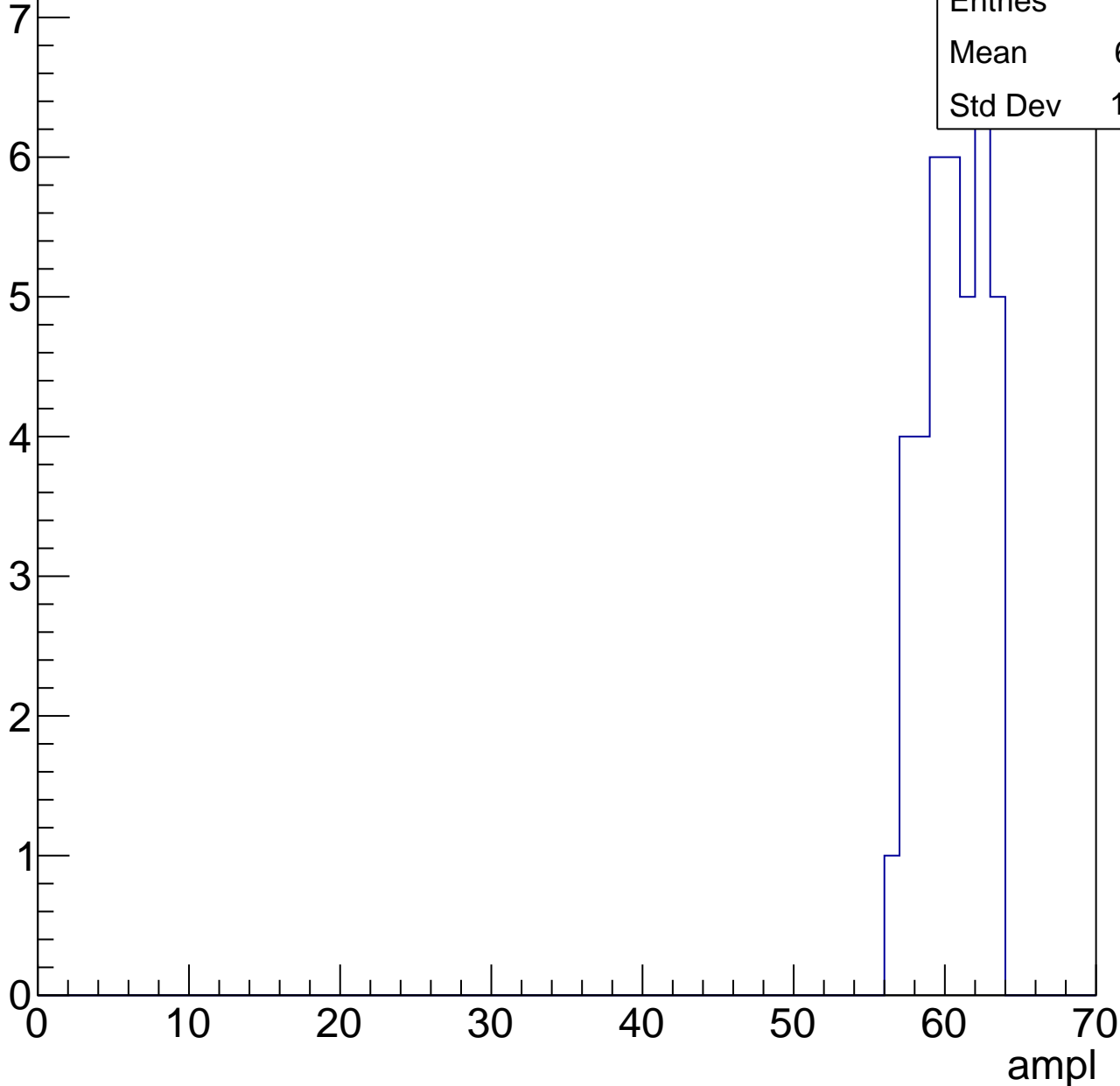


B1L003S, U20-ch44, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	60.11
Std Dev	1.997

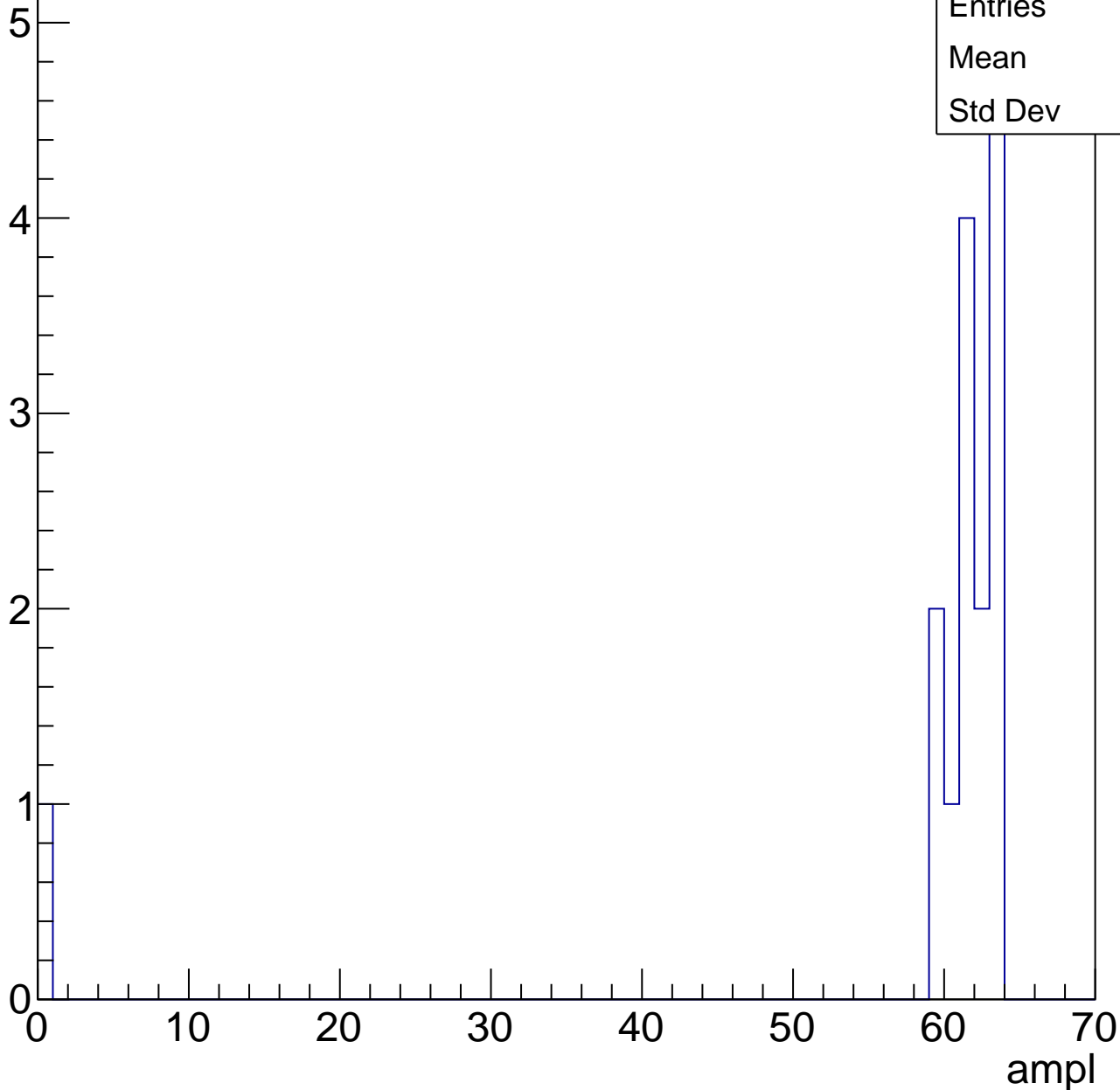


B1L003S, U20-ch44, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	57.4
Std Dev	15.4



B1L003S, U20-ch44, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch45, adc0

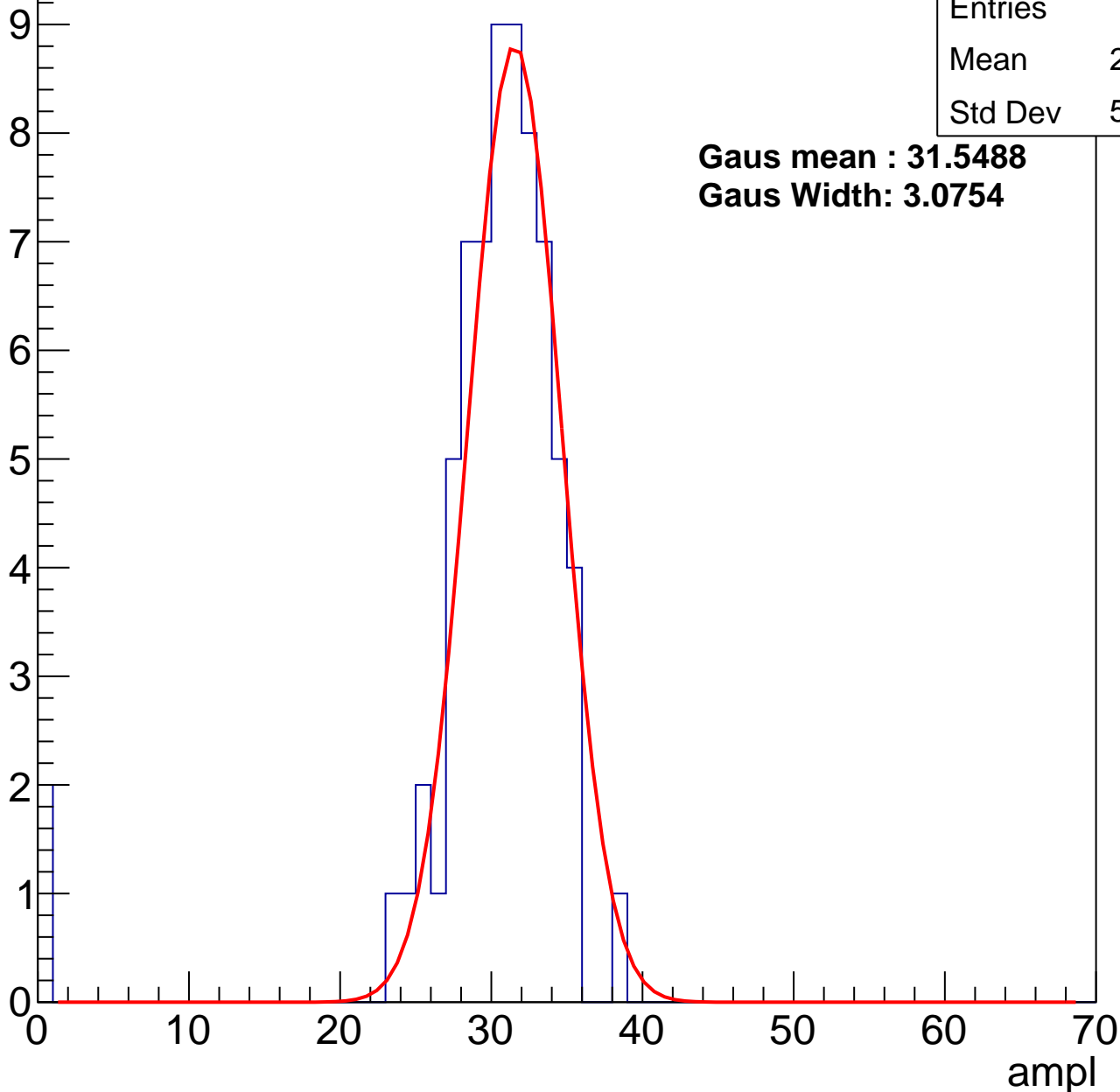
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.58
Std Dev	5.857

Gaus mean : 31.5488

Gaus Width: 3.0754



B1L003S, U20-ch45, adc1

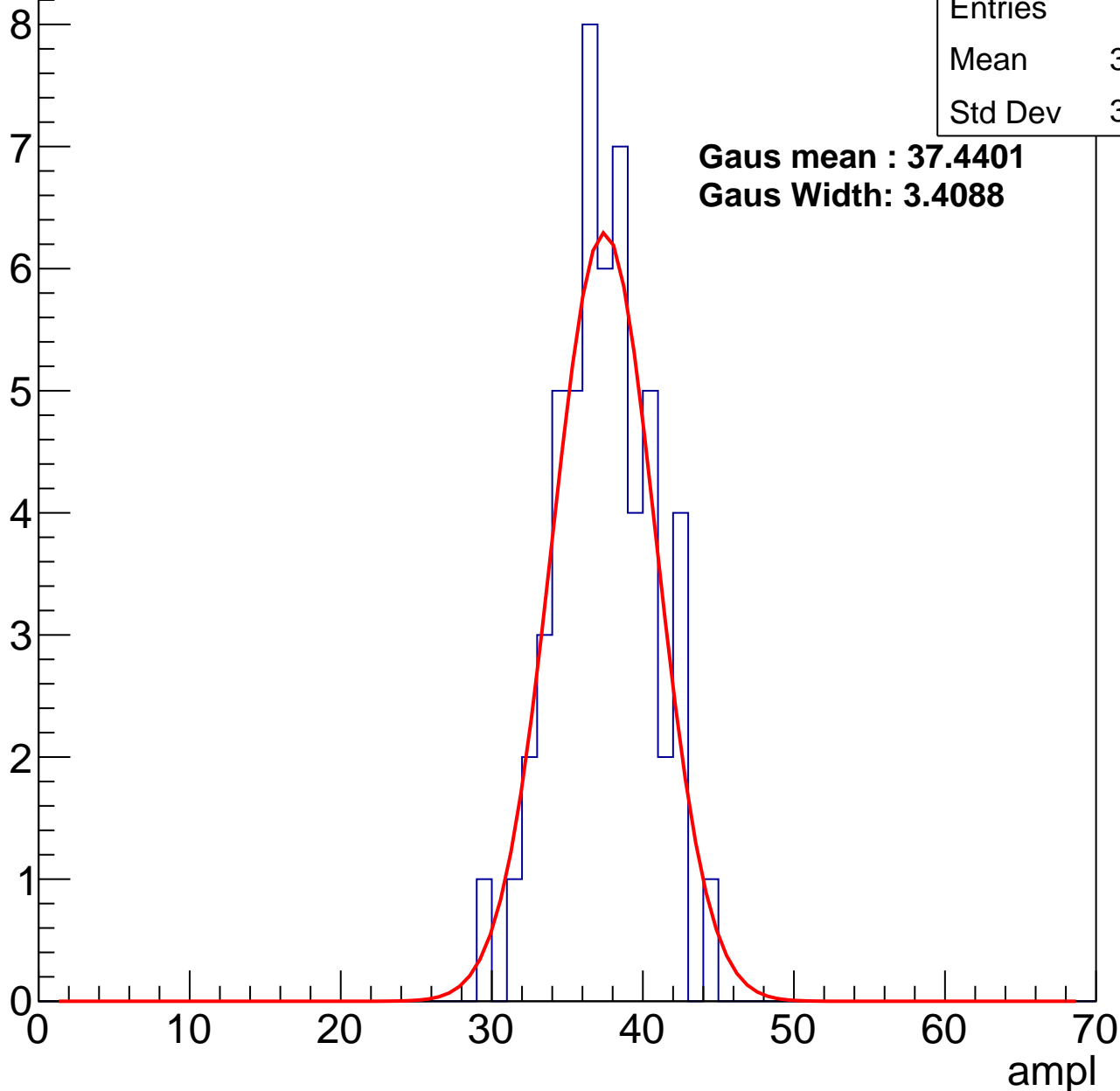
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.93
Std Dev	3.102

Gaus mean : 37.4401

Gaus Width: 3.4088



B1L003S, U20-ch45, adc2

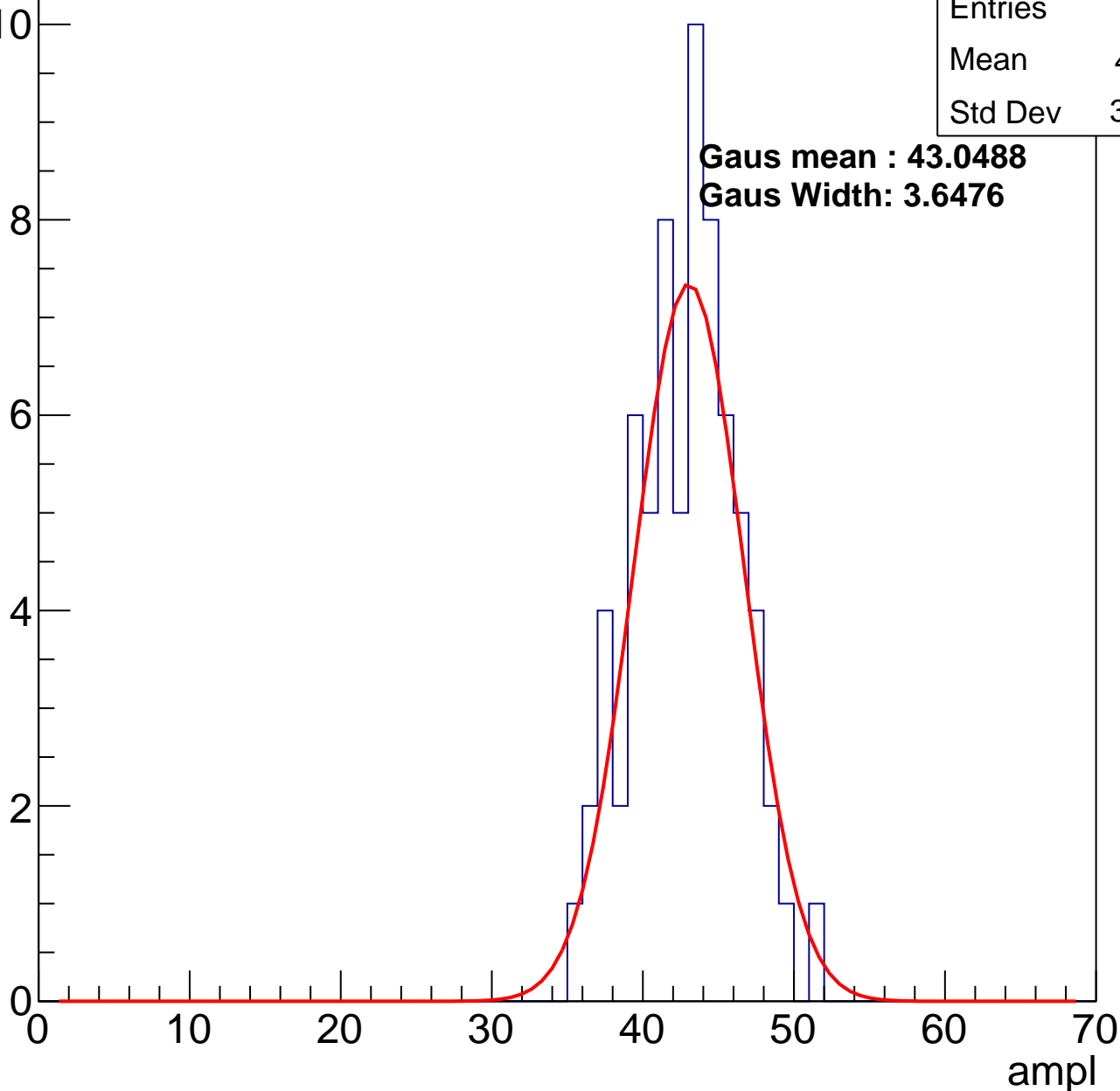
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	42.41
Std Dev	3.383

Gaus mean : 43.0488

Gaus Width: 3.6476

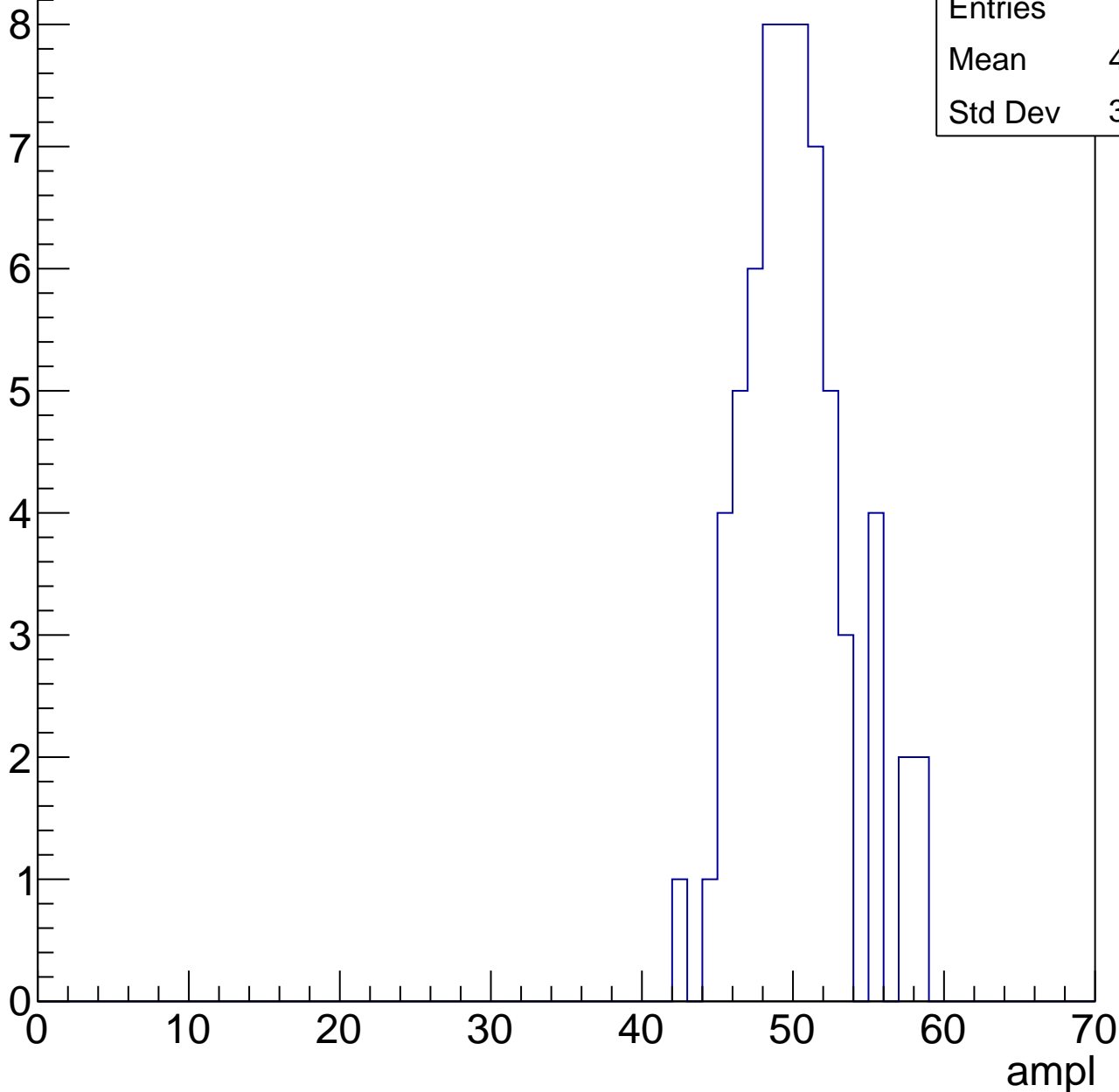


B1L003S, U20-ch45, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.69
Std Dev	3.418



B1L003S, U20-ch45, adc4

calib_packv5_042523_0143.root, FC#13, port D2

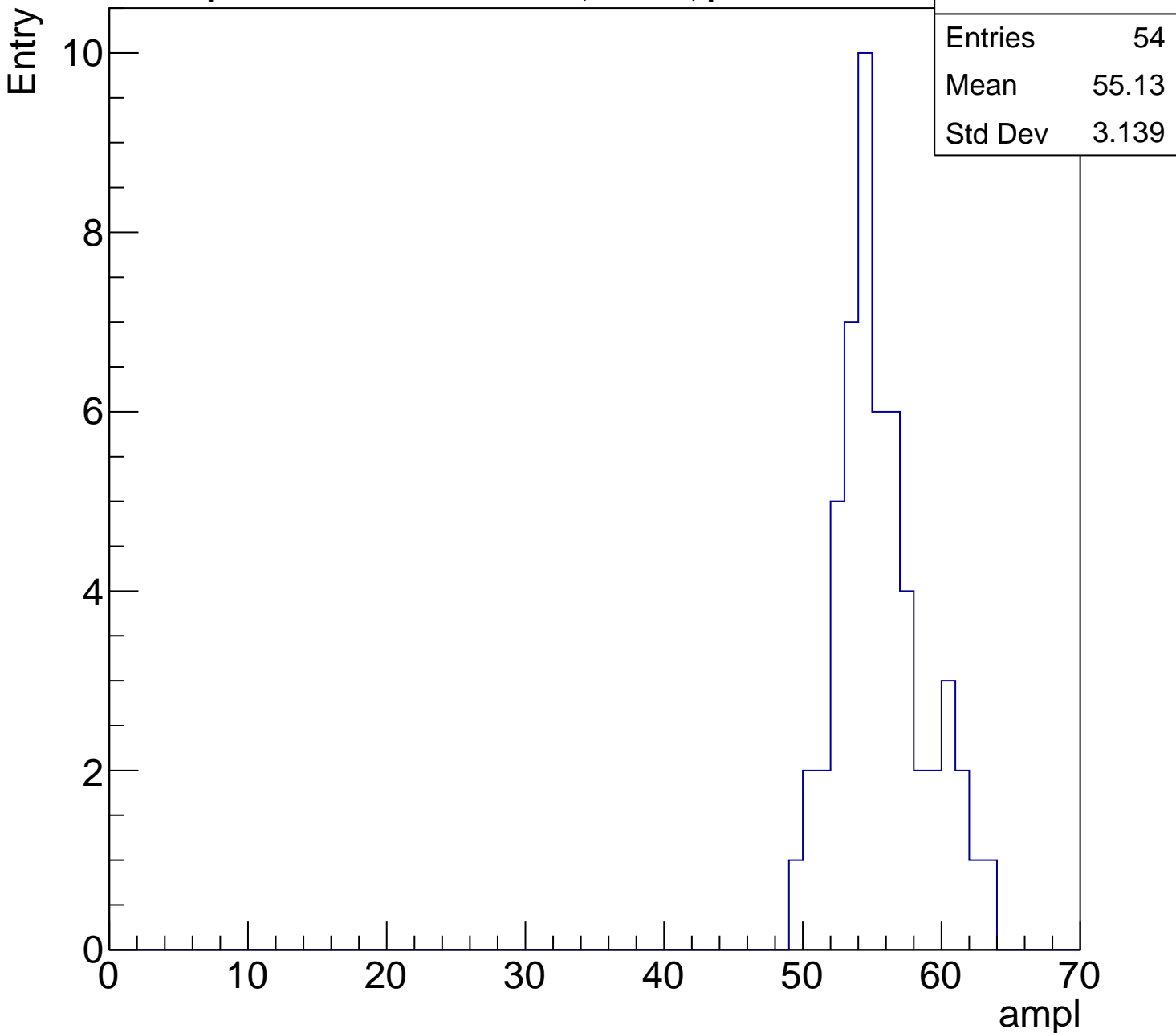
Entries	54
Mean	55.13
Std Dev	3.139

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch45, adc5

calib_packv5_042523_0143.root, FC#13, port D2

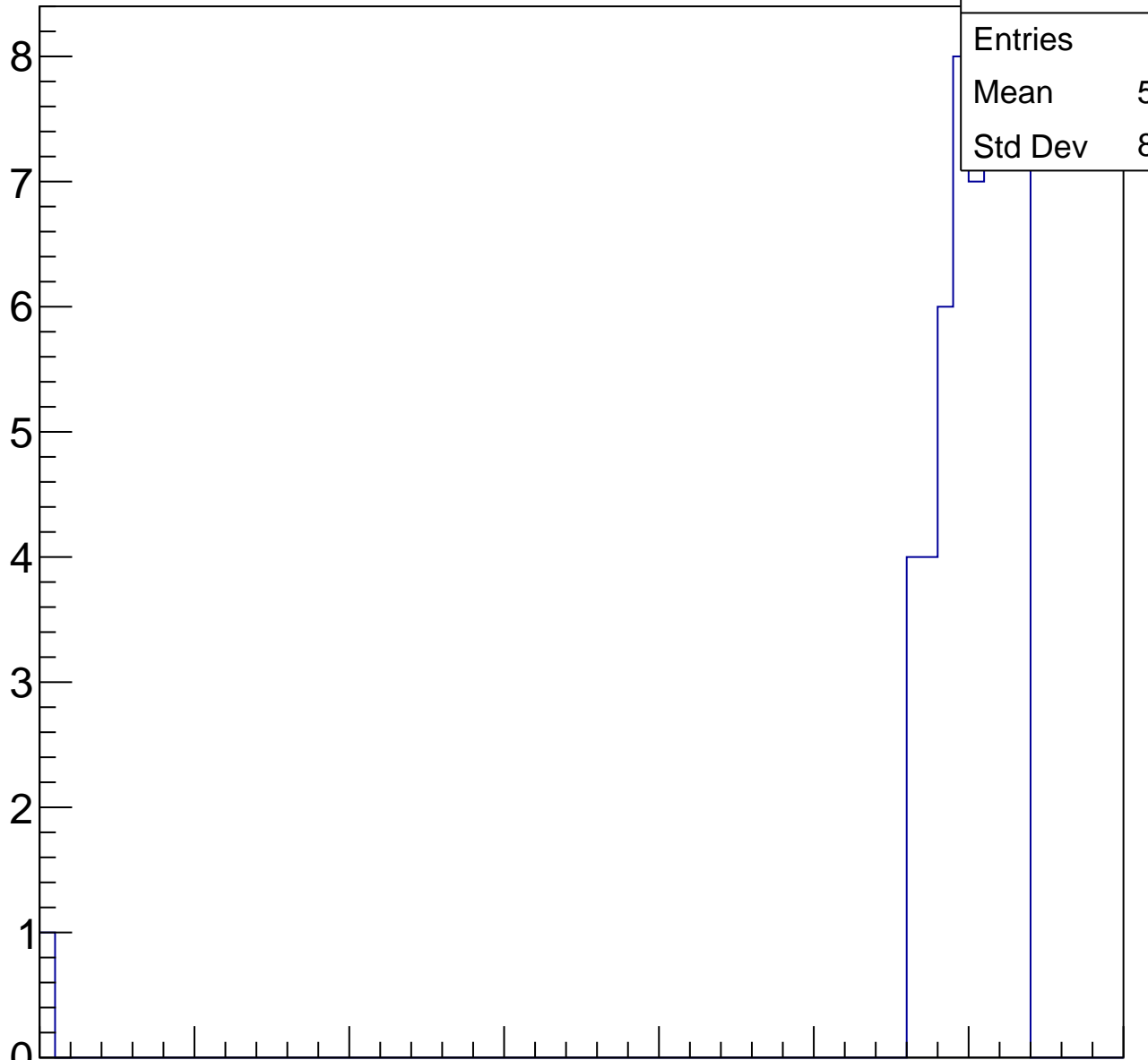
Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.89
Std Dev	8.364

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch45, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch45, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L003S, U20-ch46, adc0

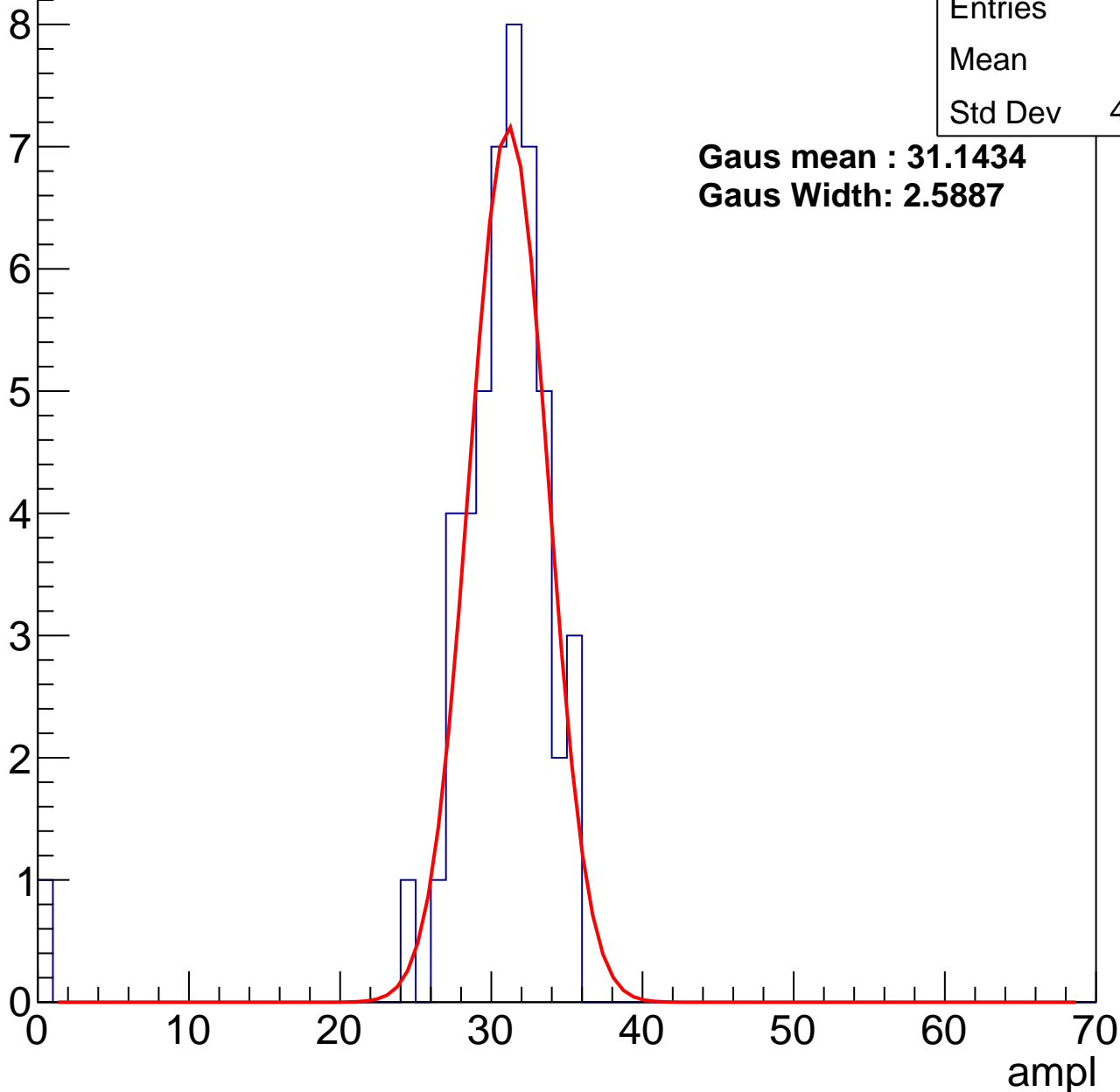
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	29.9
Std Dev	4.993

Gaus mean : 31.1434

Gaus Width: 2.5887



B1L003S, U20-ch46, adc1

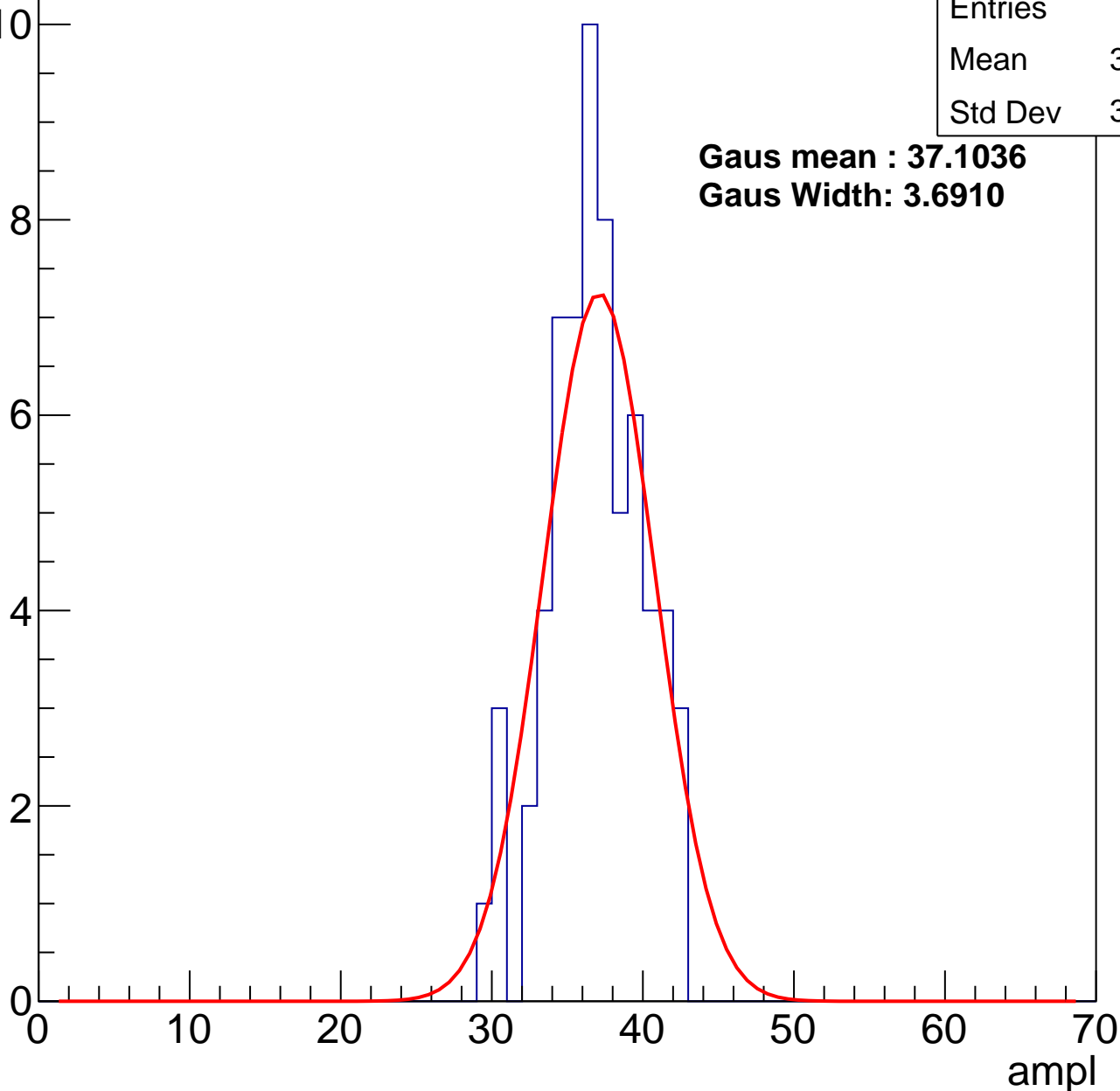
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.38
Std Dev	3.075

Gaus mean : 37.1036

Gaus Width: 3.6910



B1L003S, U20-ch46, adc2

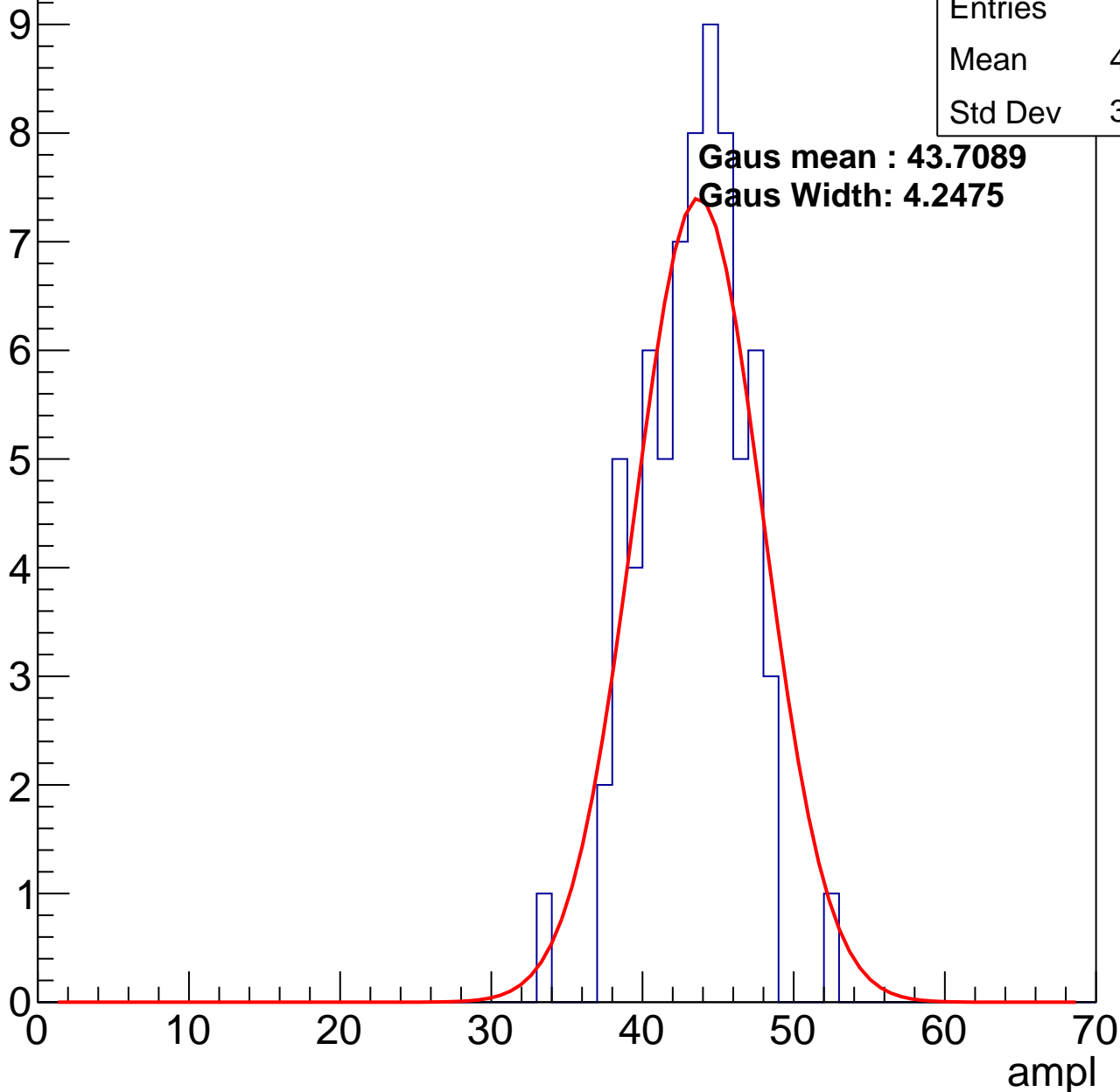
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	42.86
Std Dev	3.335

Gaus mean : 43.7089

Gaus Width: 4.2475

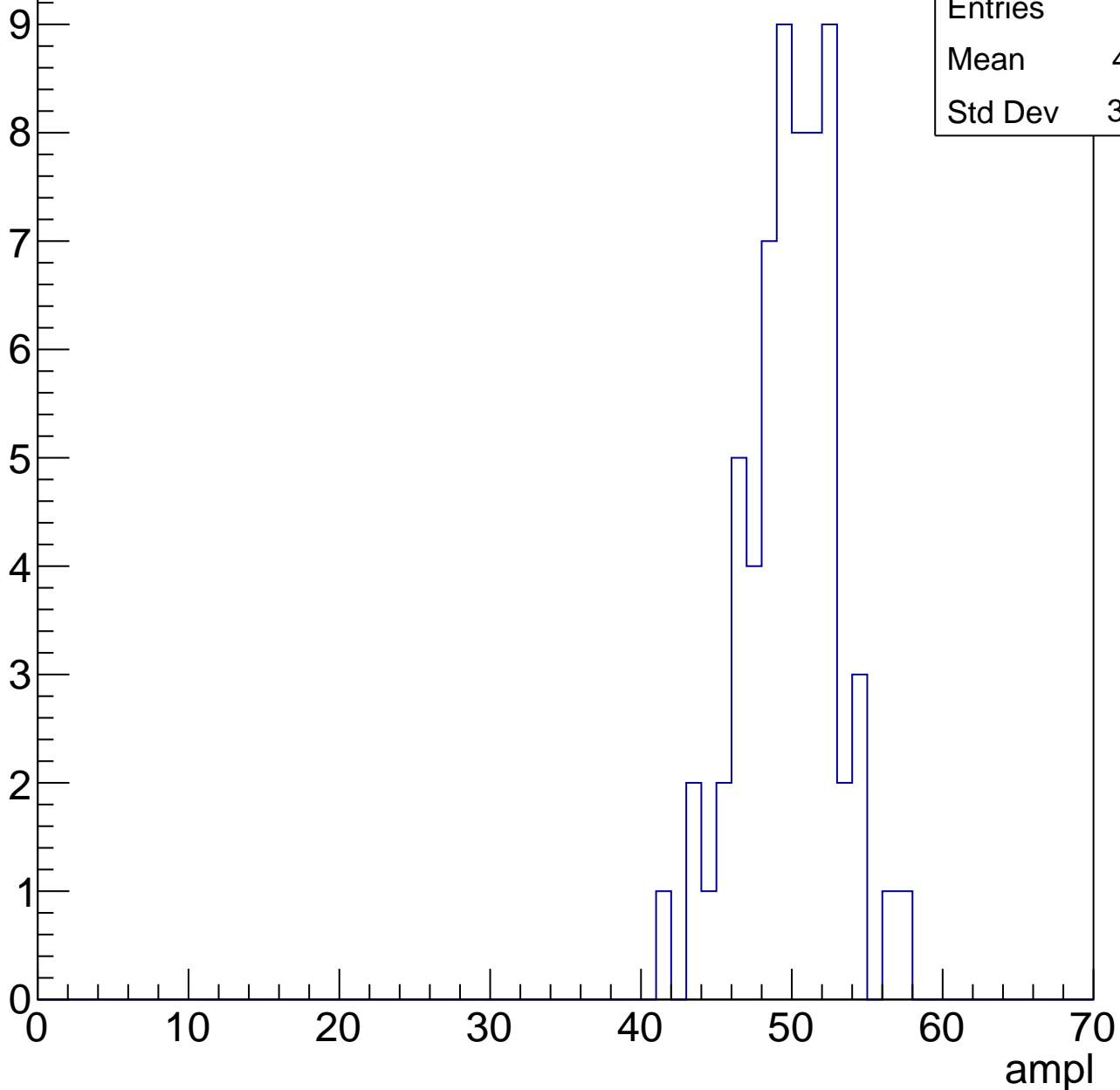


B1L003S, U20-ch46, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

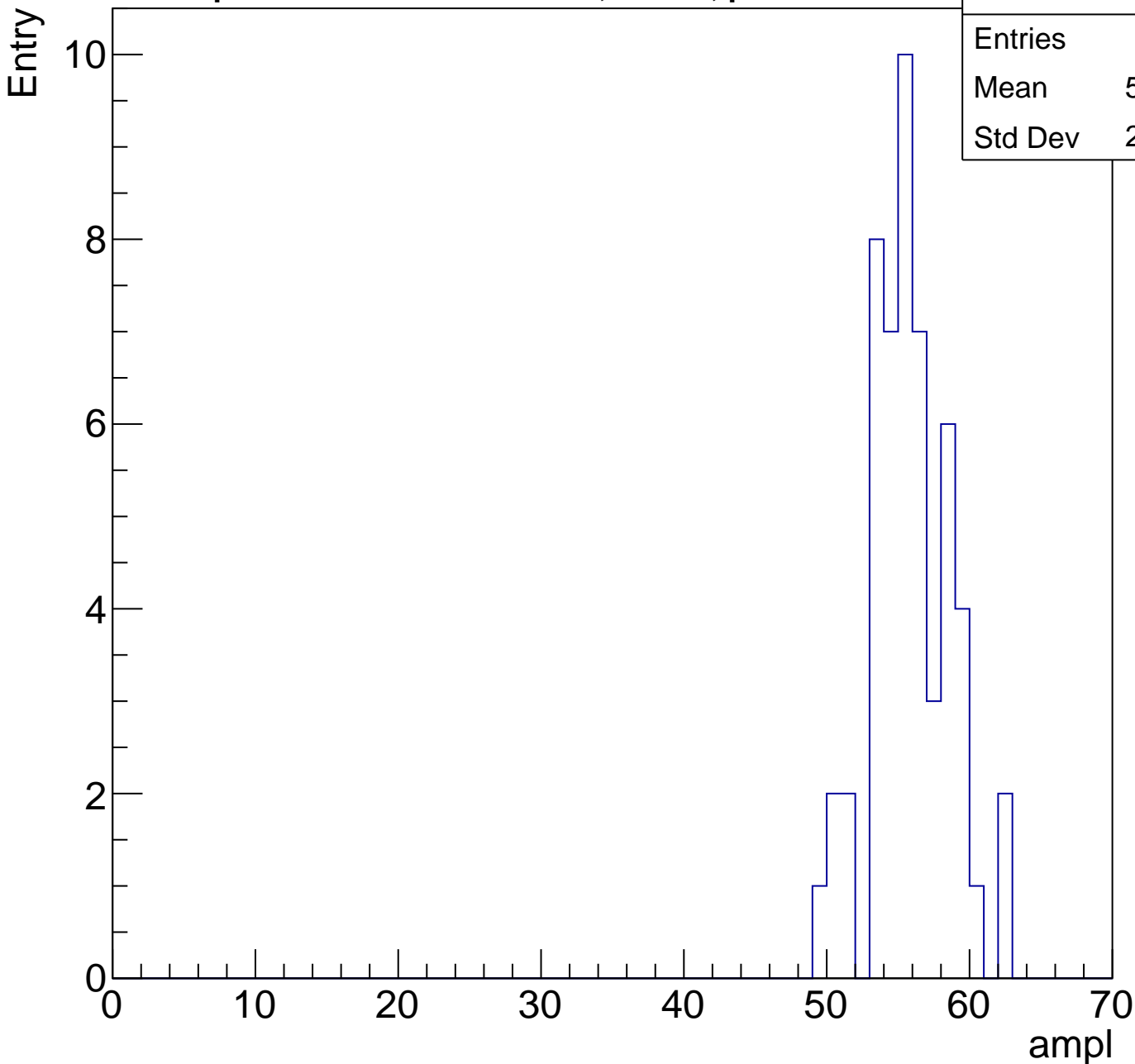
Entries	63
Mean	49.41
Std Dev	3.064



B1L003S, U20-ch46, adc4

calib_packv5_042523_0143.root, FC#13, port D2

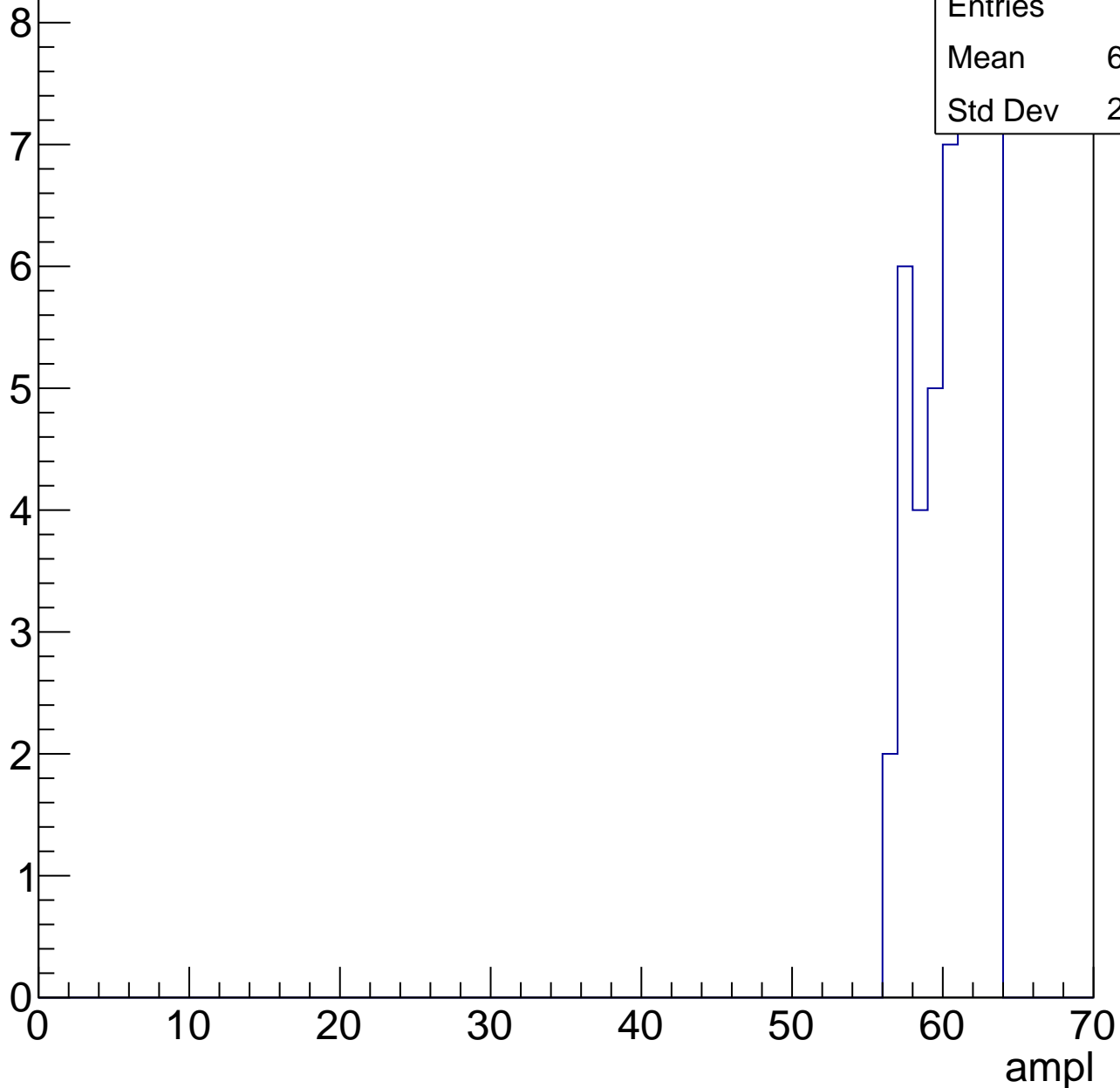
Entries	53
Mean	55.36
Std Dev	2.782



B1L003S, U20-ch46, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	48
Mean	60.19
Std Dev	2.128

B1L003S, U20-ch46, adc6

calib_packv5_042523_0143.root, FC#13, port D2

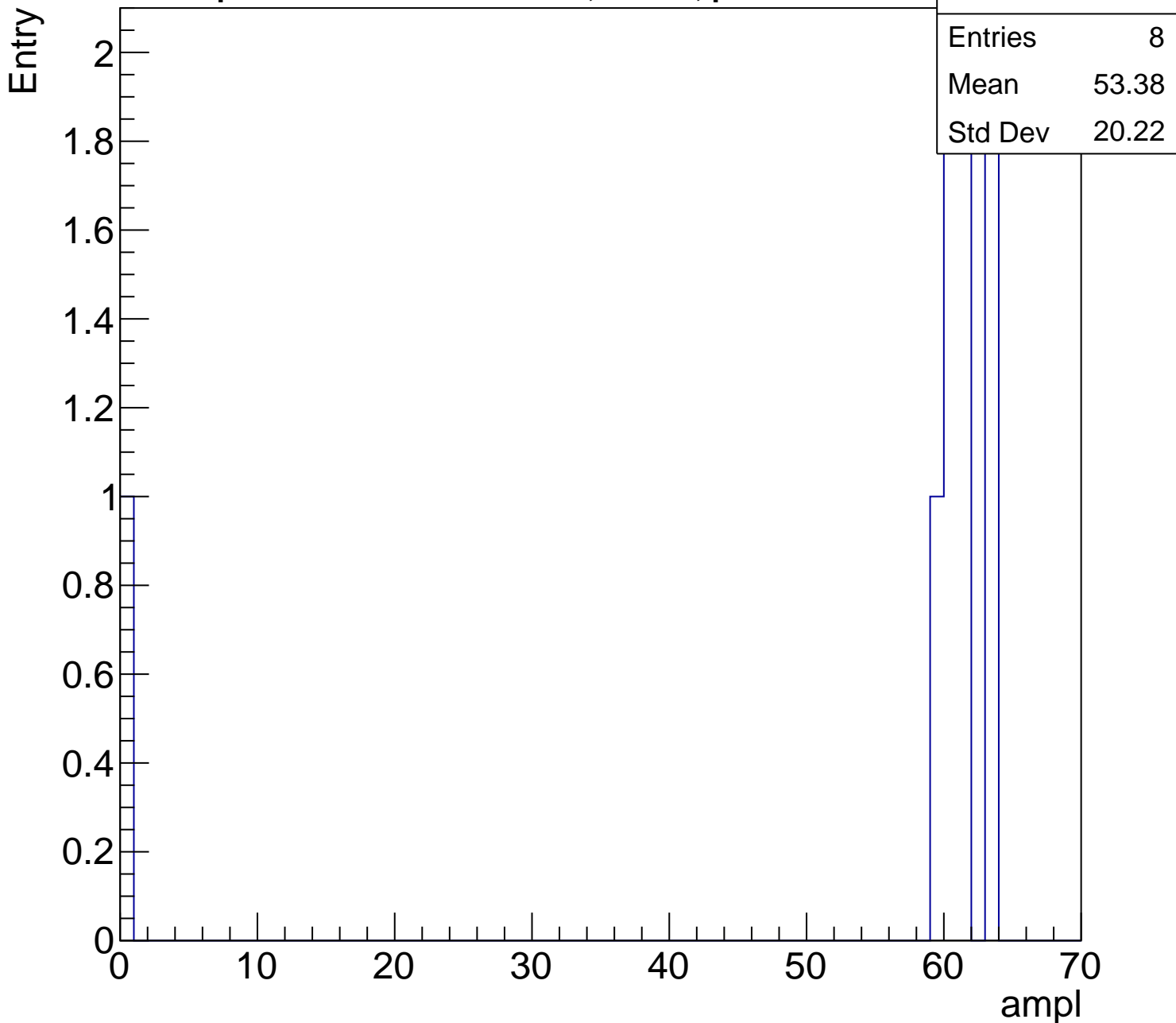
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.38
Std Dev	20.22

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch46, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch47, adc0

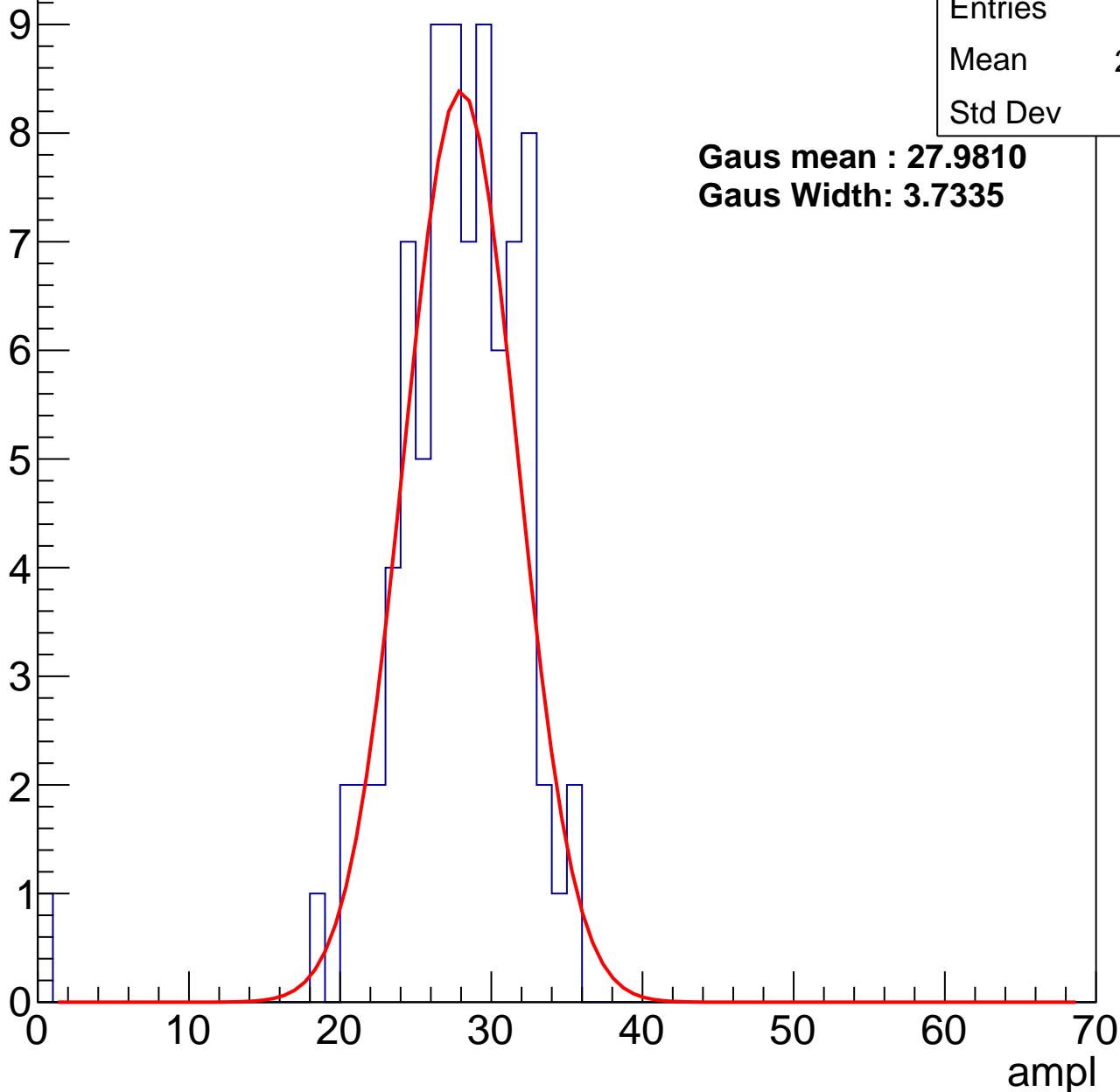
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	27.21
Std Dev	4.68

Gaus mean : 27.9810

Gaus Width: 3.7335



B1L003S, U20-ch47, adc1

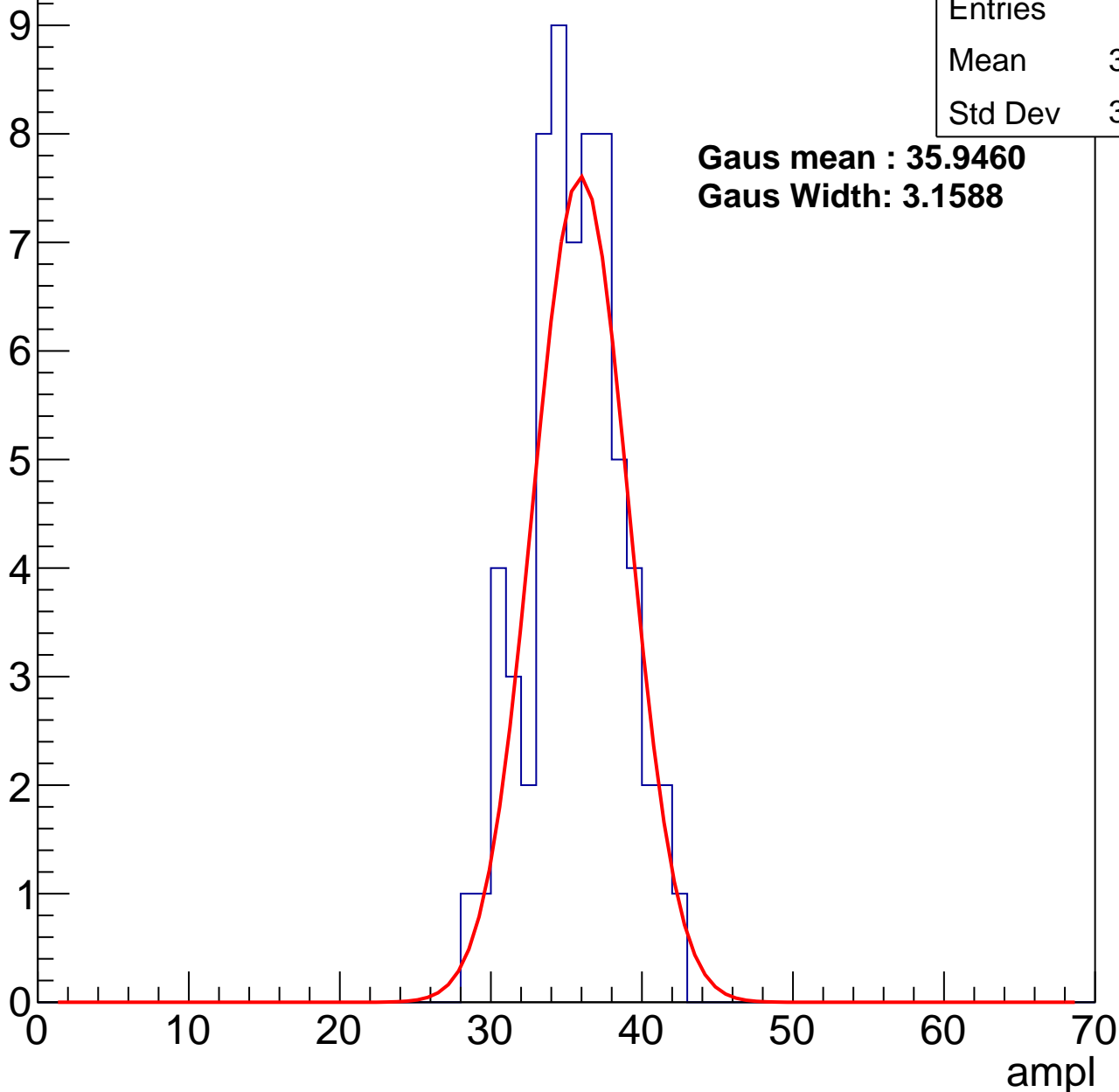
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.12
Std Dev	3.066

Gaus mean : 35.9460

Gaus Width: 3.1588



B1L003S, U20-ch47, adc2

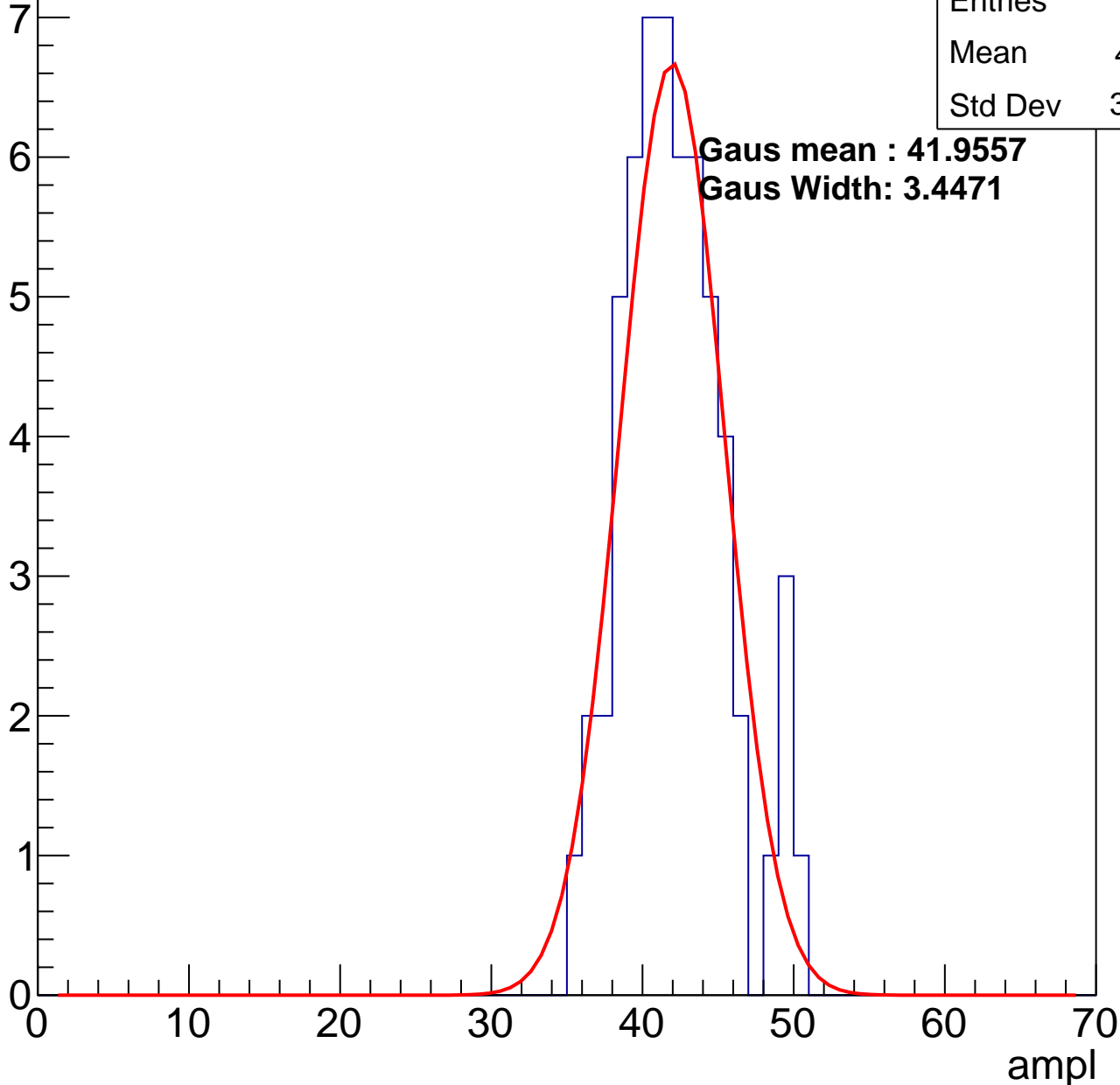
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	41.71
Std Dev	3.414

Gaus mean : 41.9557

Gaus Width: 3.4471

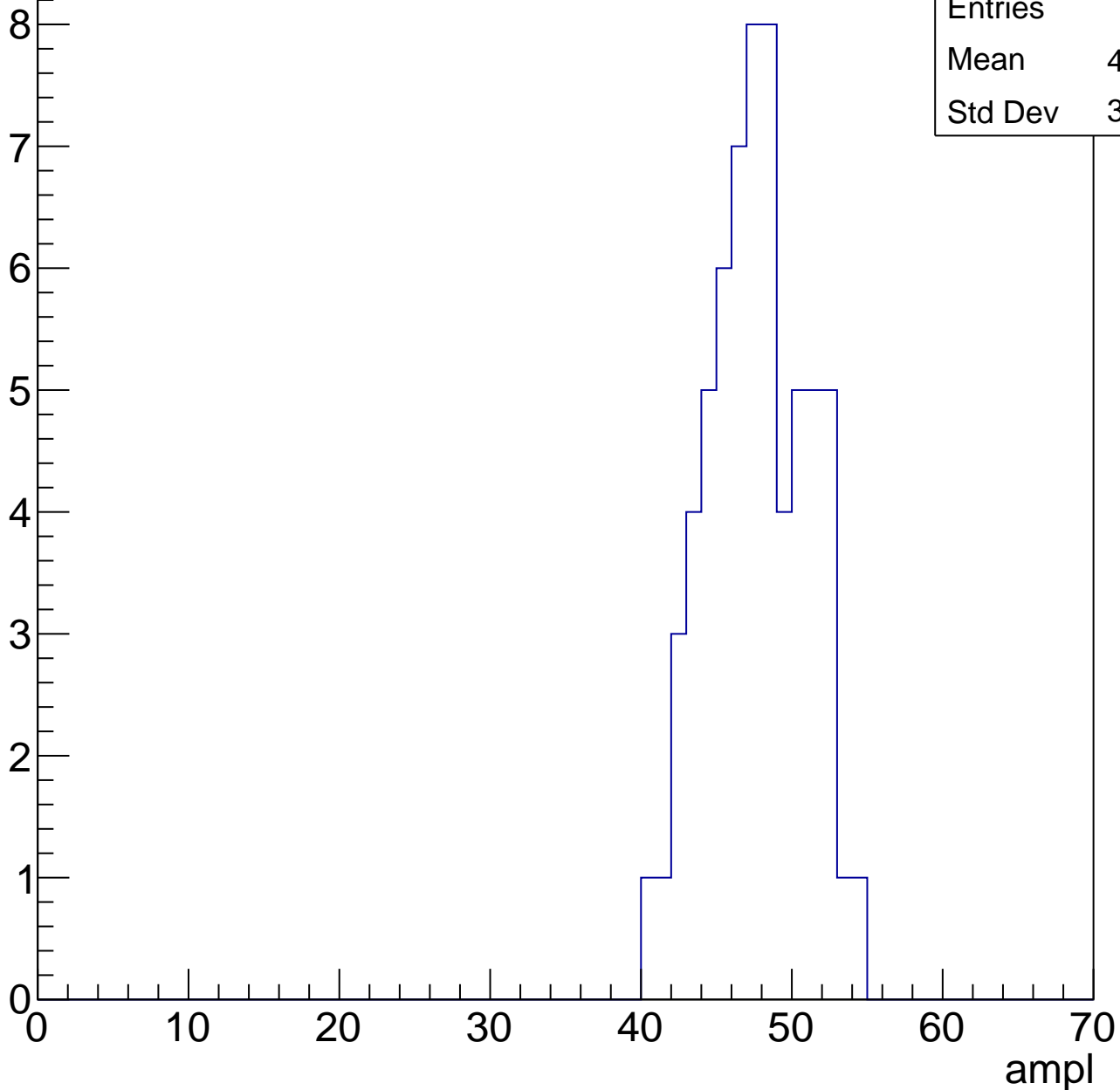


B1L003S, U20-ch47, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47.17
Std Dev	3.204

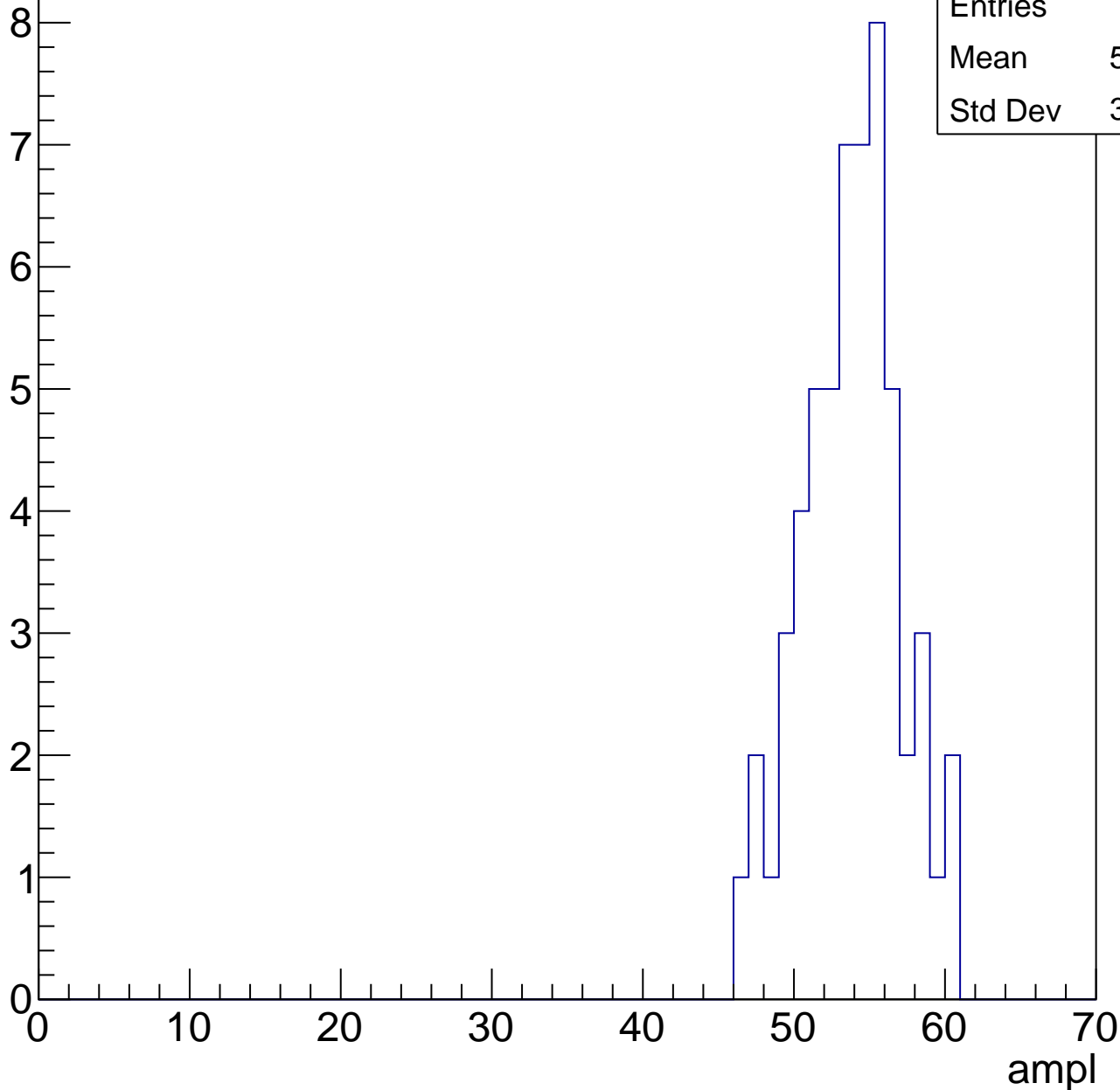


B1L003S, U20-ch47, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	53.32
Std Dev	3.202



B1L003S, U20-ch47, adc5

calib_packv5_042523_0143.root, FC#13, port D2

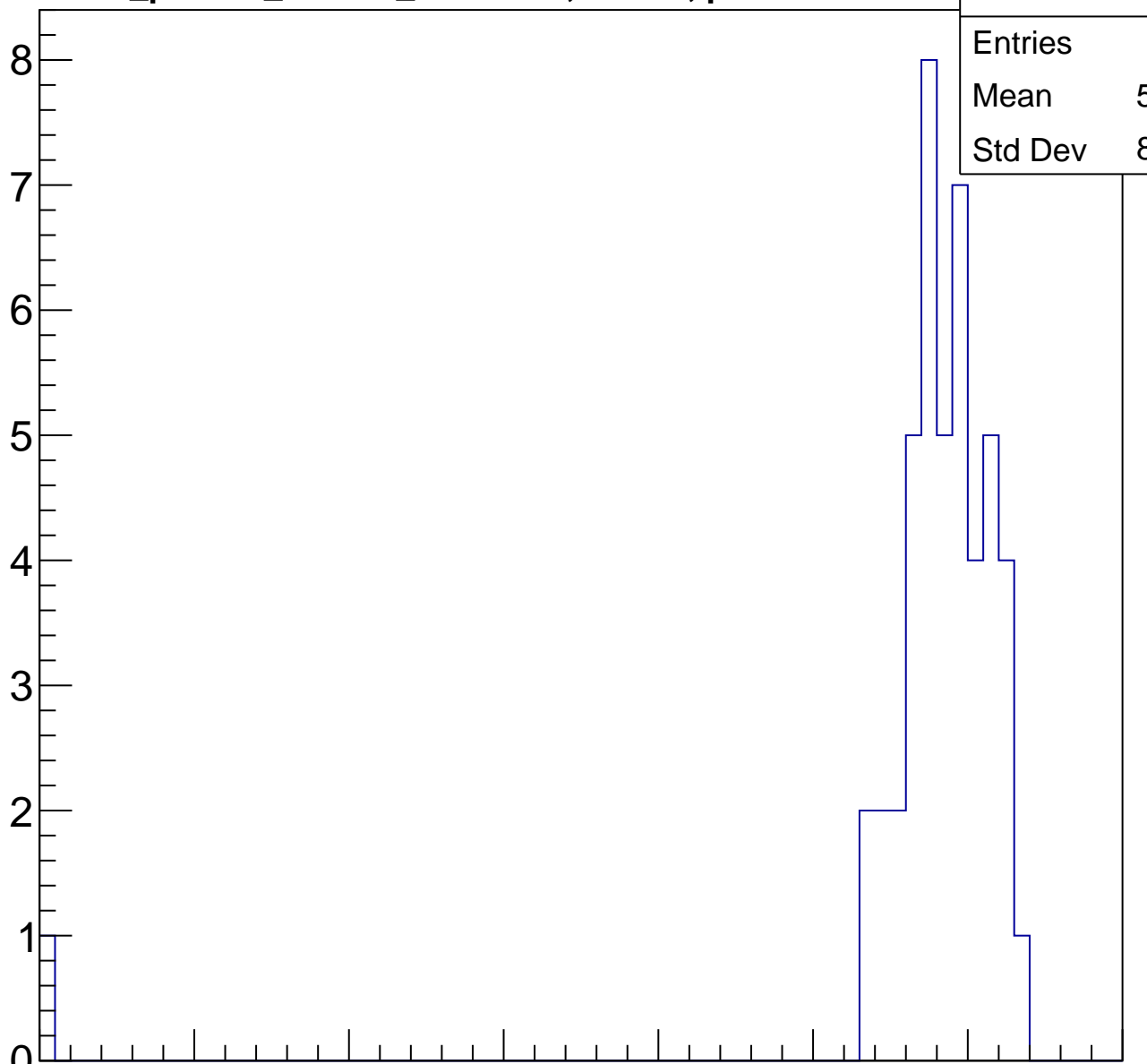
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	56.93
Std Dev	8.843

ampl

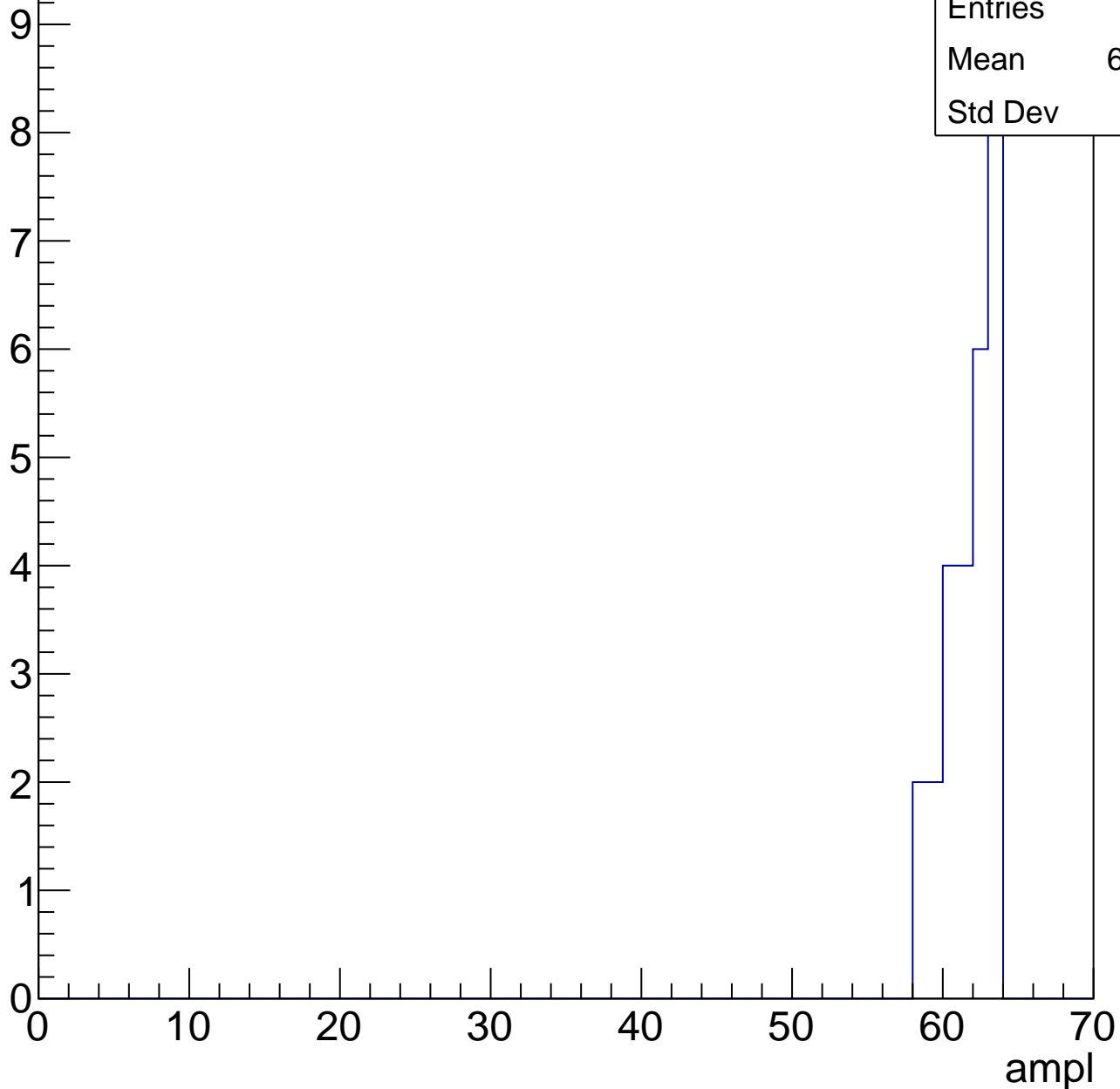
0 10 20 30 40 50 60 70



B1L003S, U20-ch47, adc6

calib_packv5_042523_0143.root, FC#13, port D2

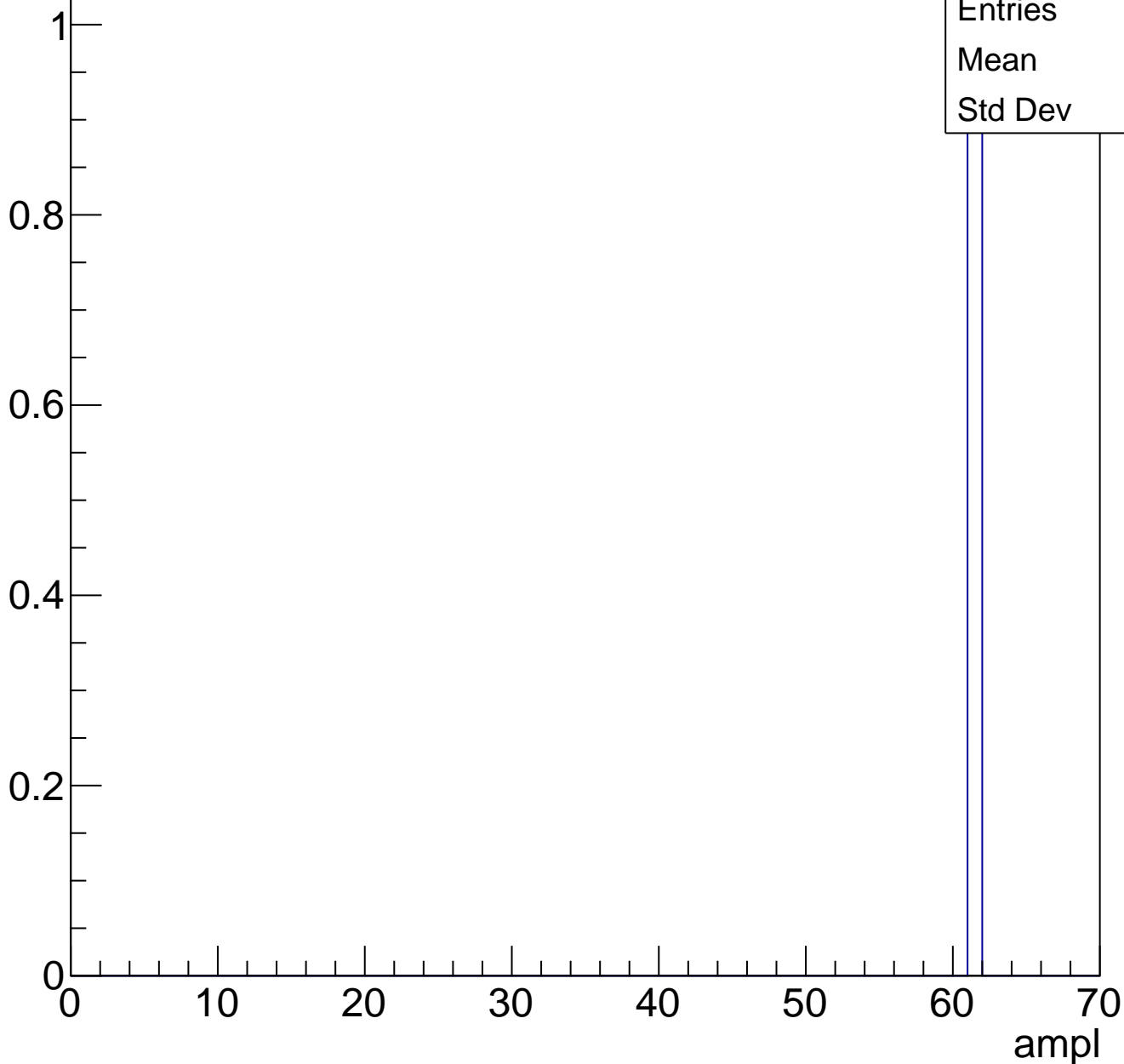
Entry



B1L003S, U20-ch47, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch48, adc0

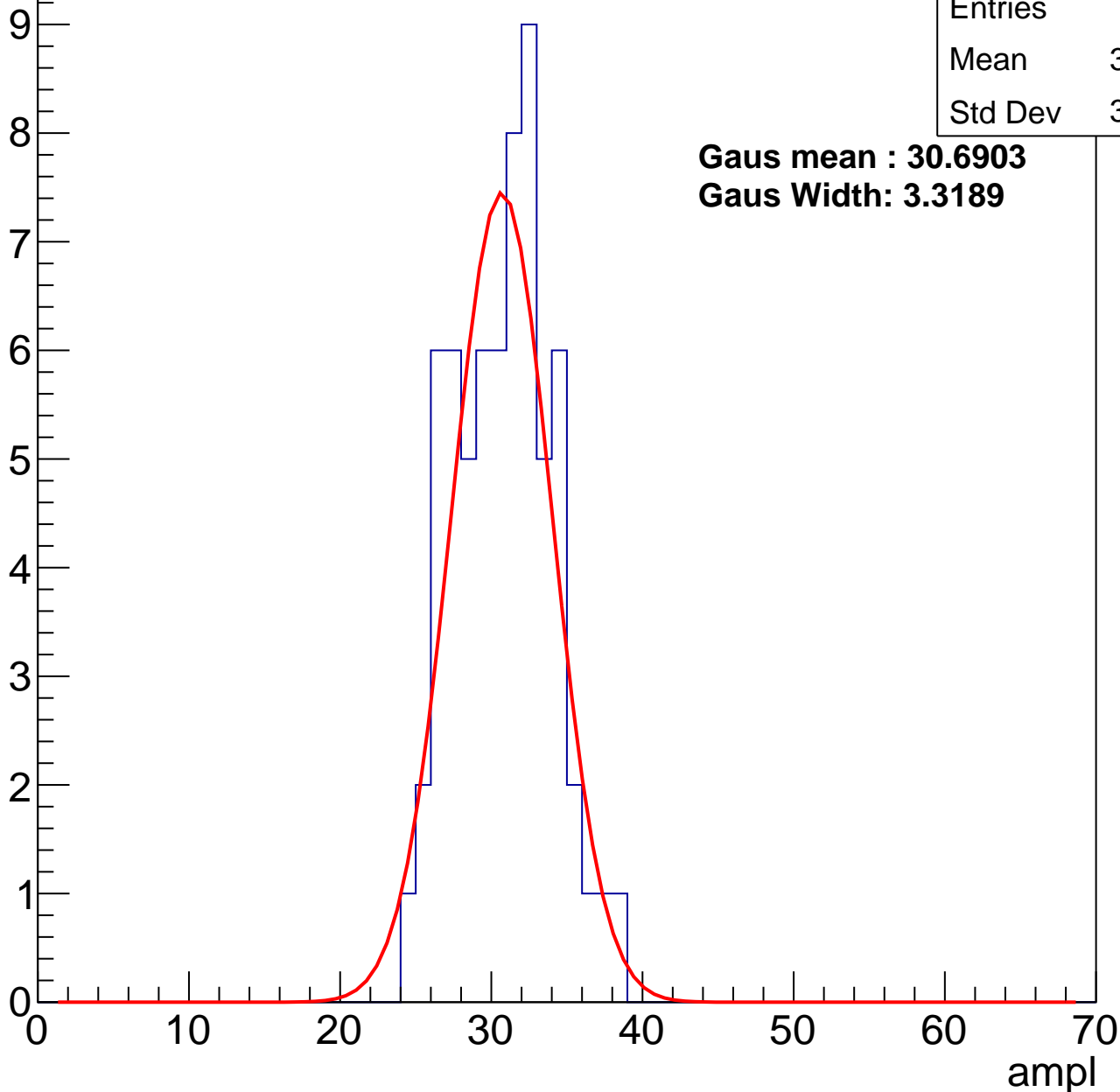
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.34
Std Dev	3.134

Gaus mean : 30.6903

Gaus Width: 3.3189



B1L003S, U20-ch48, adc1

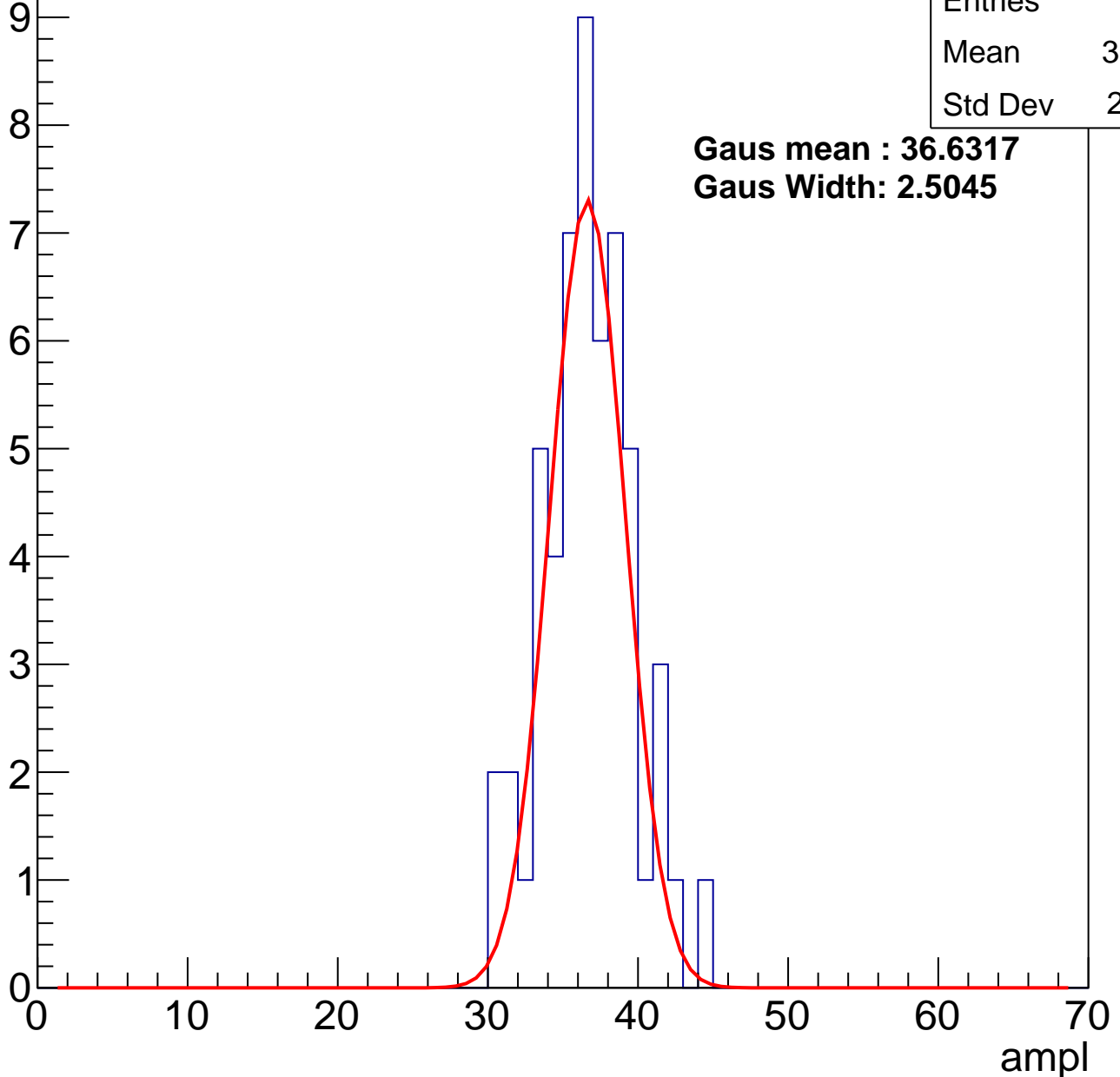
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.22
Std Dev	2.961

Gaus mean : 36.6317

Gaus Width: 2.5045



B1L003S, U20-ch48, adc2

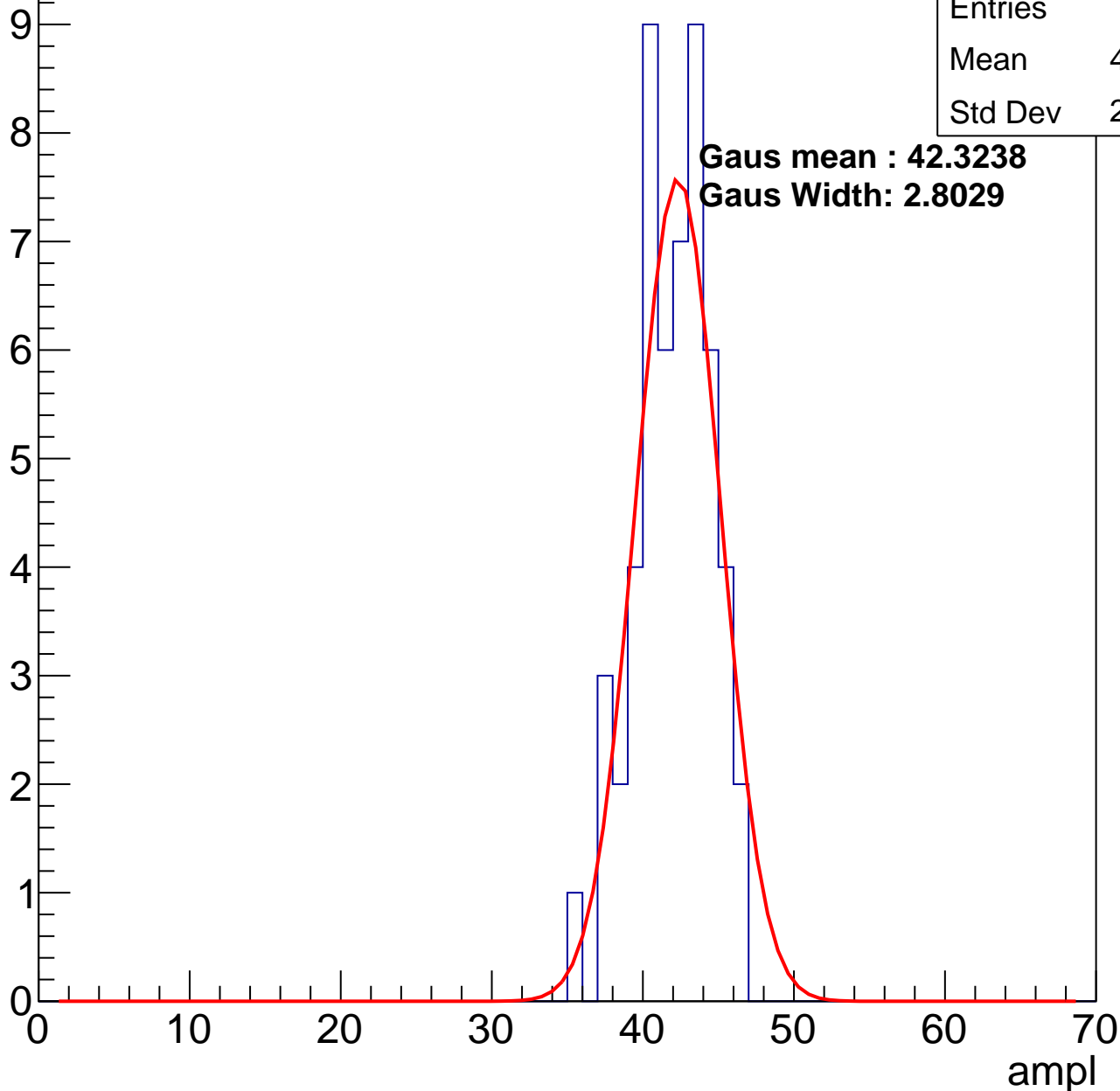
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	41.53
Std Dev	2.469

Gaus mean : 42.3238

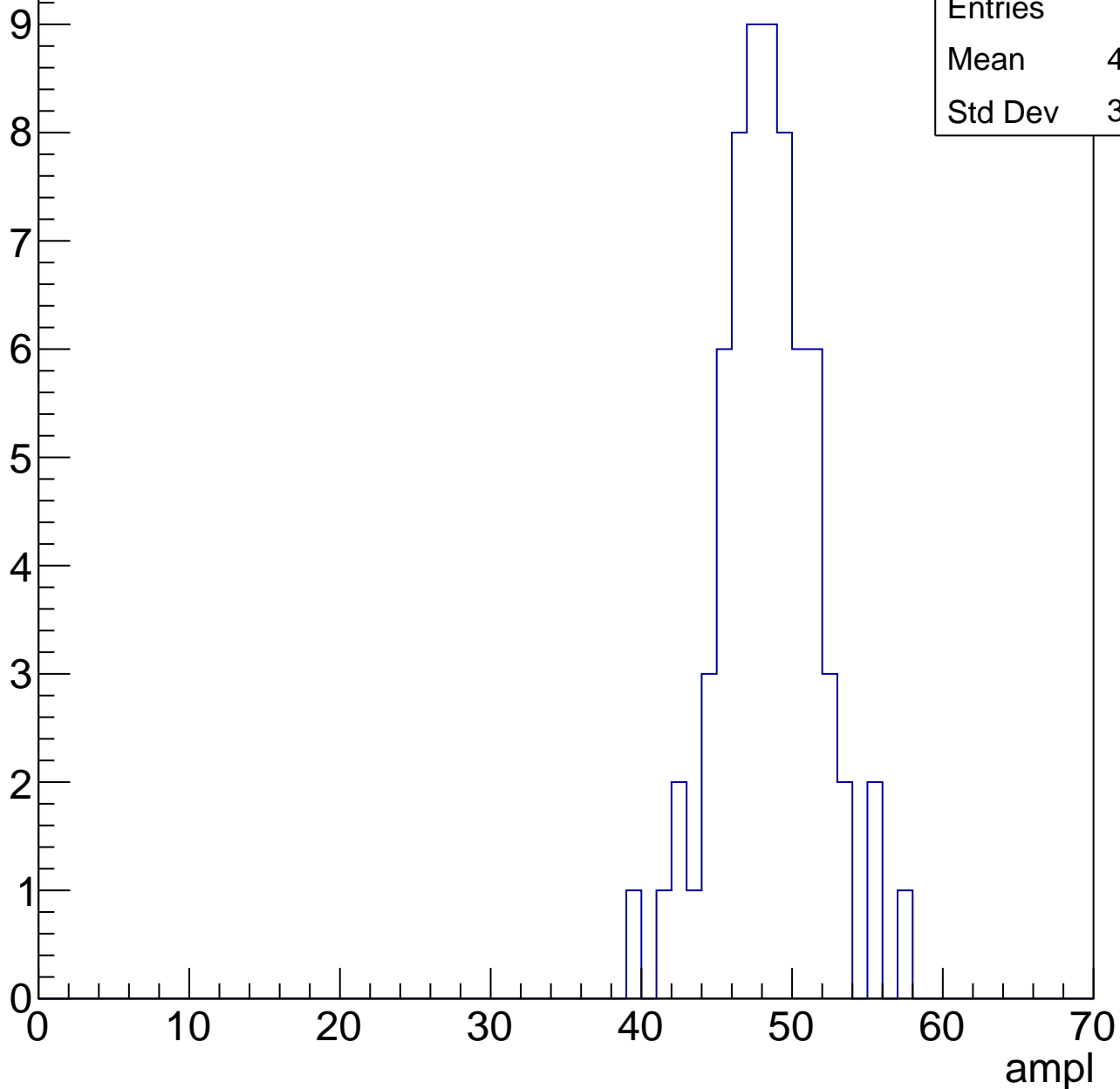
Gaus Width: 2.8029



B1L003S, U20-ch48, adc3

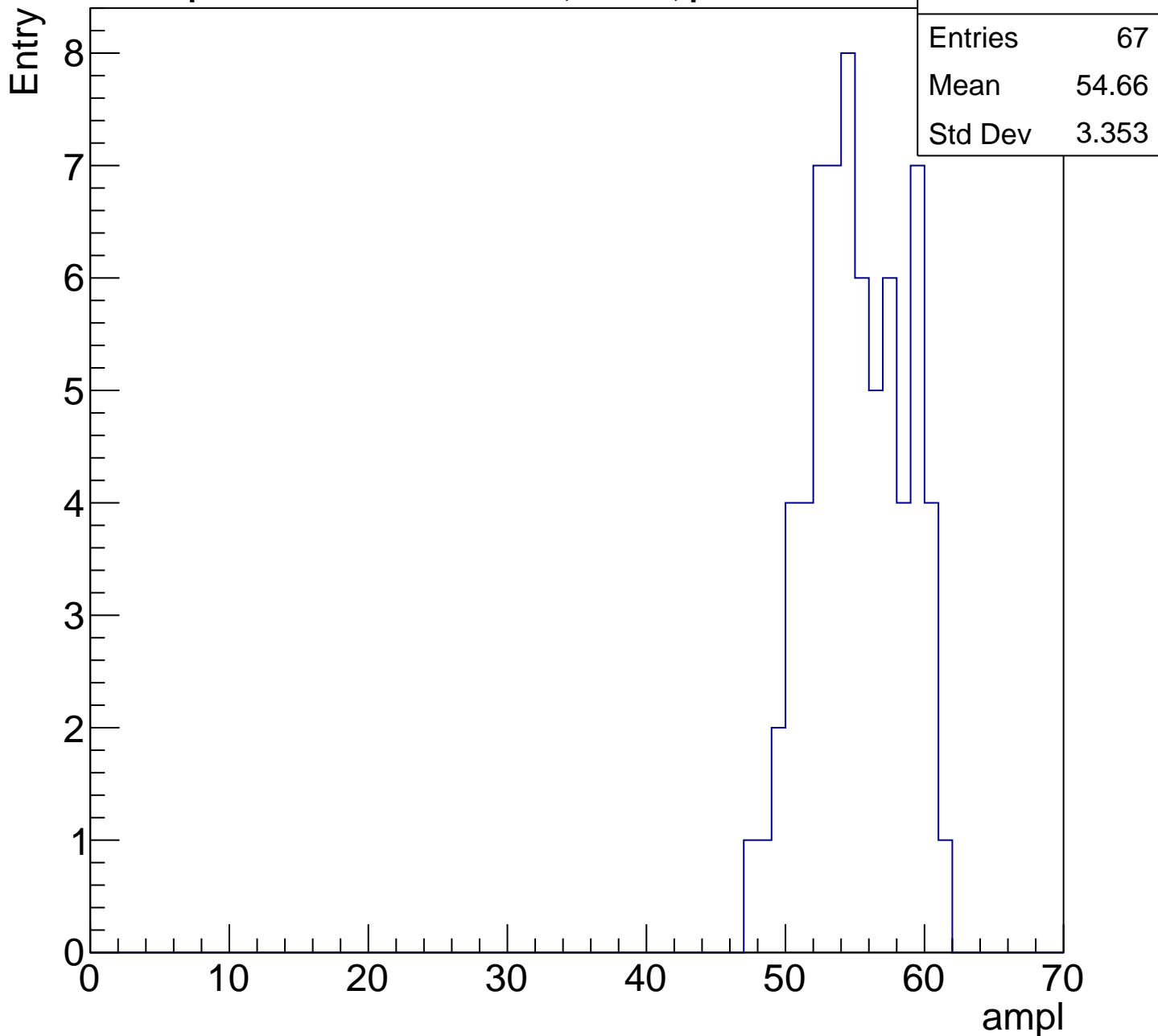
calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch48, adc4

calib_packv5_042523_0143.root, FC#13, port D2

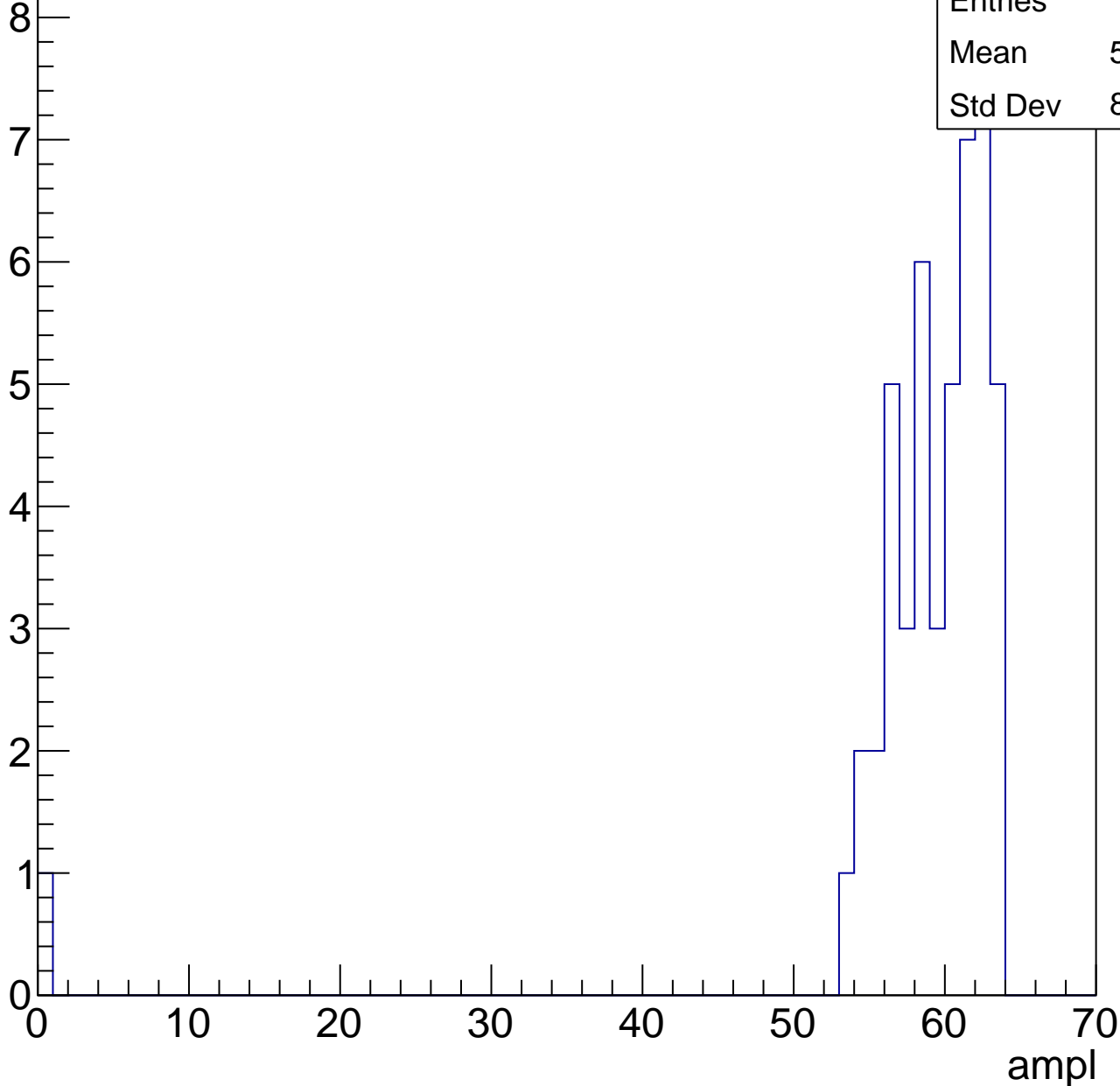


B1L003S, U20-ch48, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	58.02
Std Dev	8.899

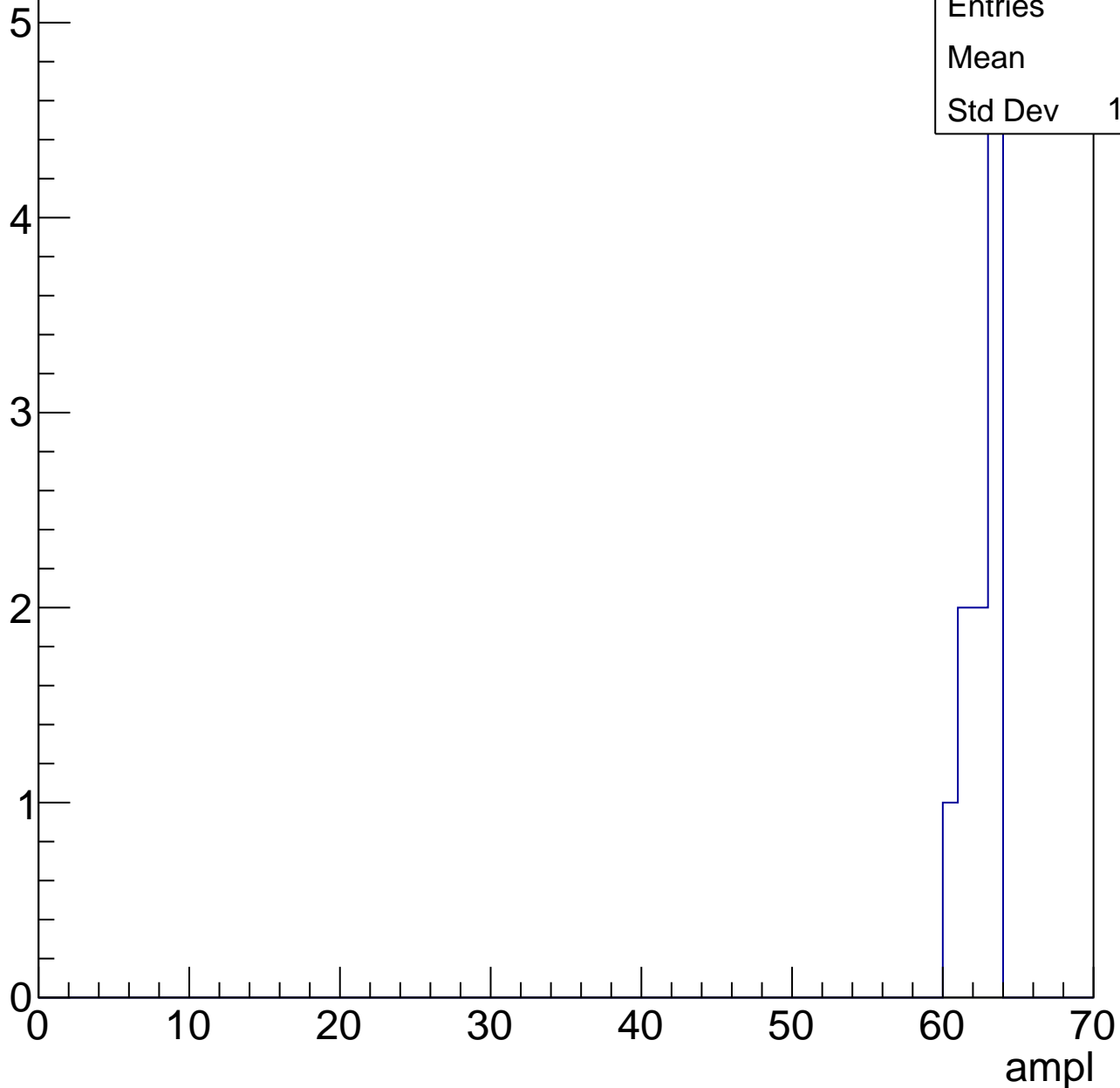


B1L003S, U20-ch48, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	62.1
Std Dev	1.044



B1L003S, U20-ch48, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L003S, U20-ch49, adc0

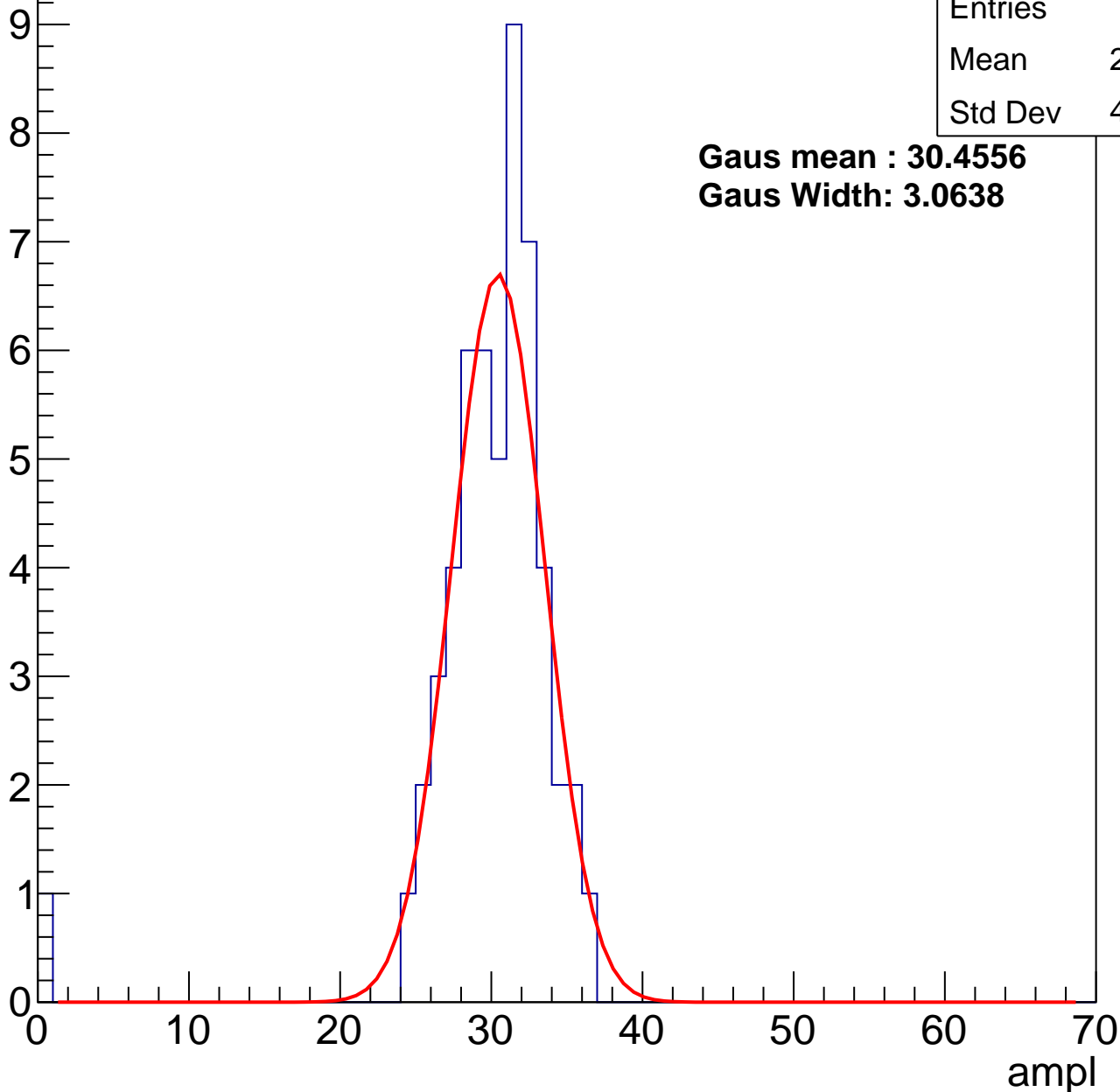
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	29.45
Std Dev	4.905

Gaus mean : 30.4556

Gaus Width: 3.0638



B1L003S, U20-ch49, adc1

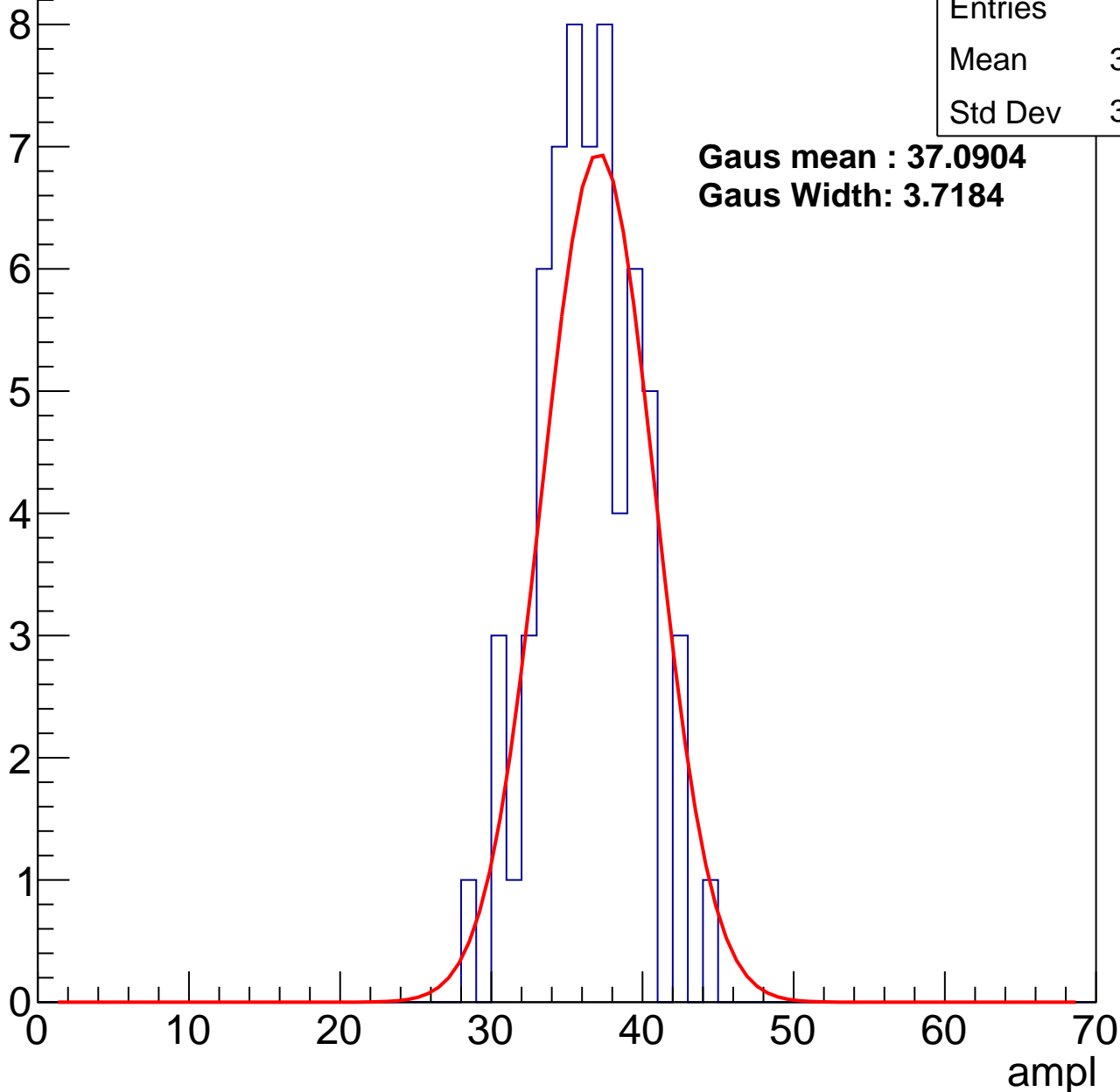
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.95
Std Dev	3.249

Gaus mean : 37.0904

Gaus Width: 3.7184



B1L003S, U20-ch49, adc2

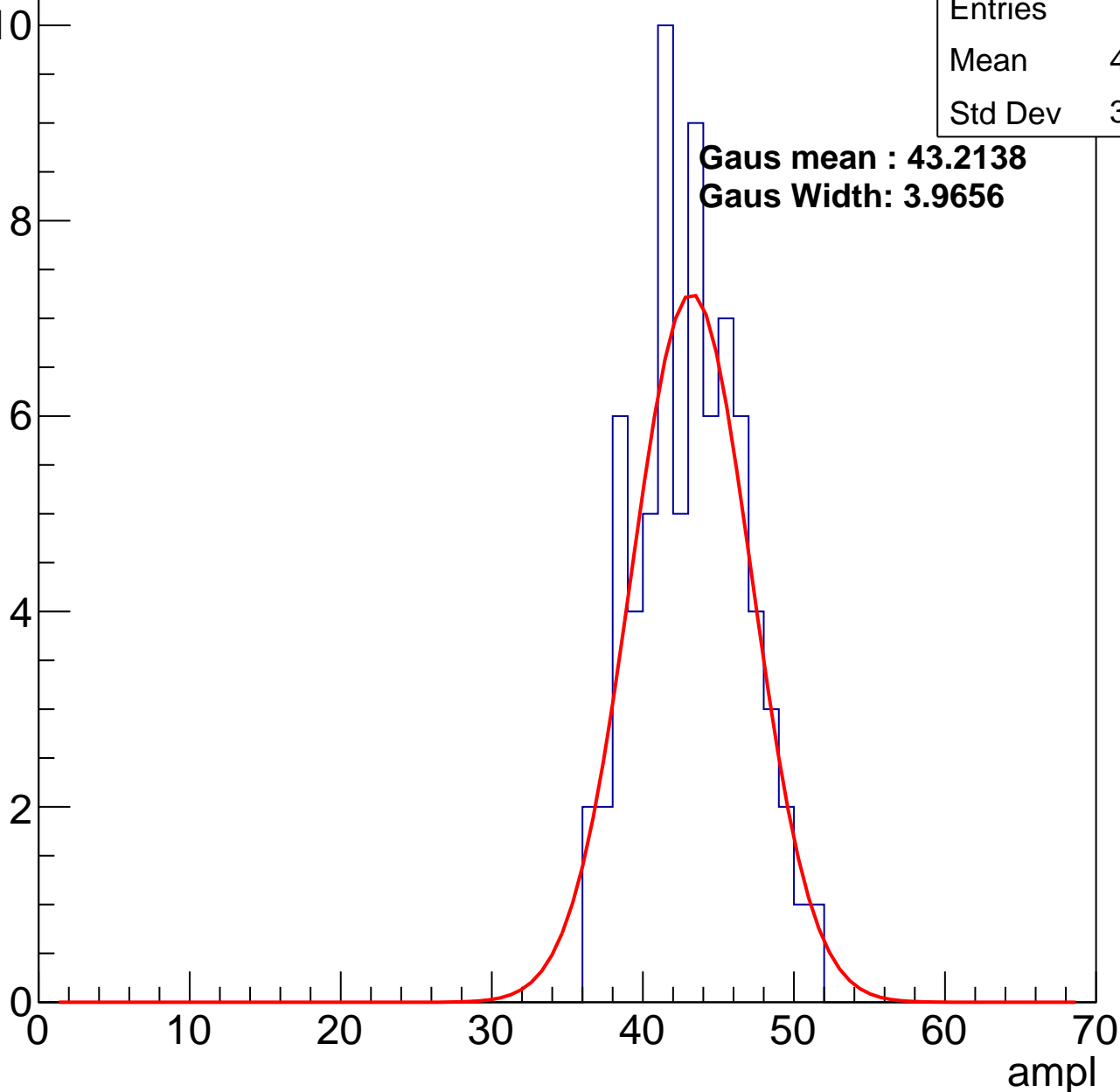
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.78
Std Dev	3.477

Gaus mean : 43.2138

Gaus Width: 3.9656

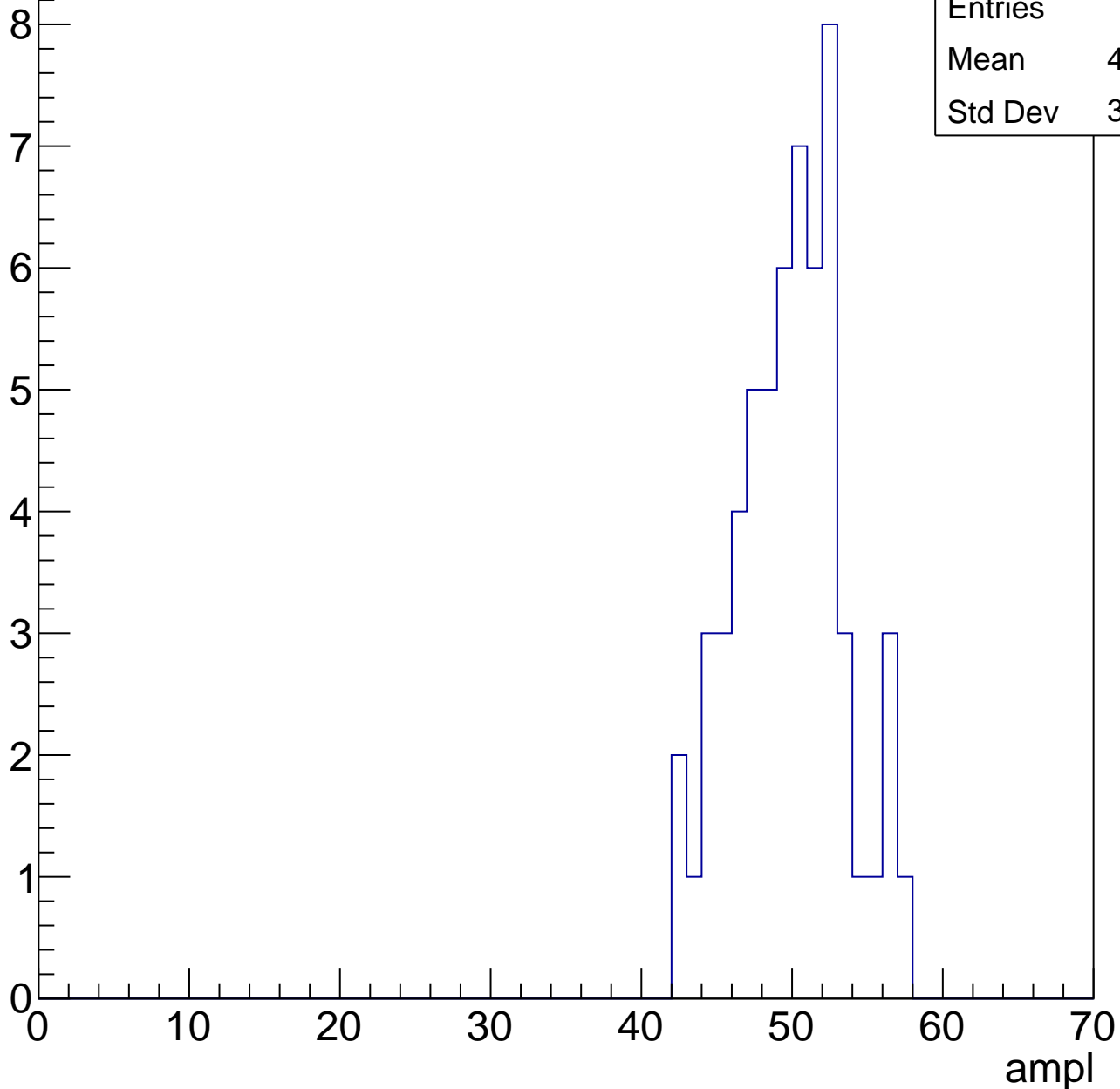


B1L003S, U20-ch49, adc3

calib_packv5_042523_0143.root, FC#13, port D2

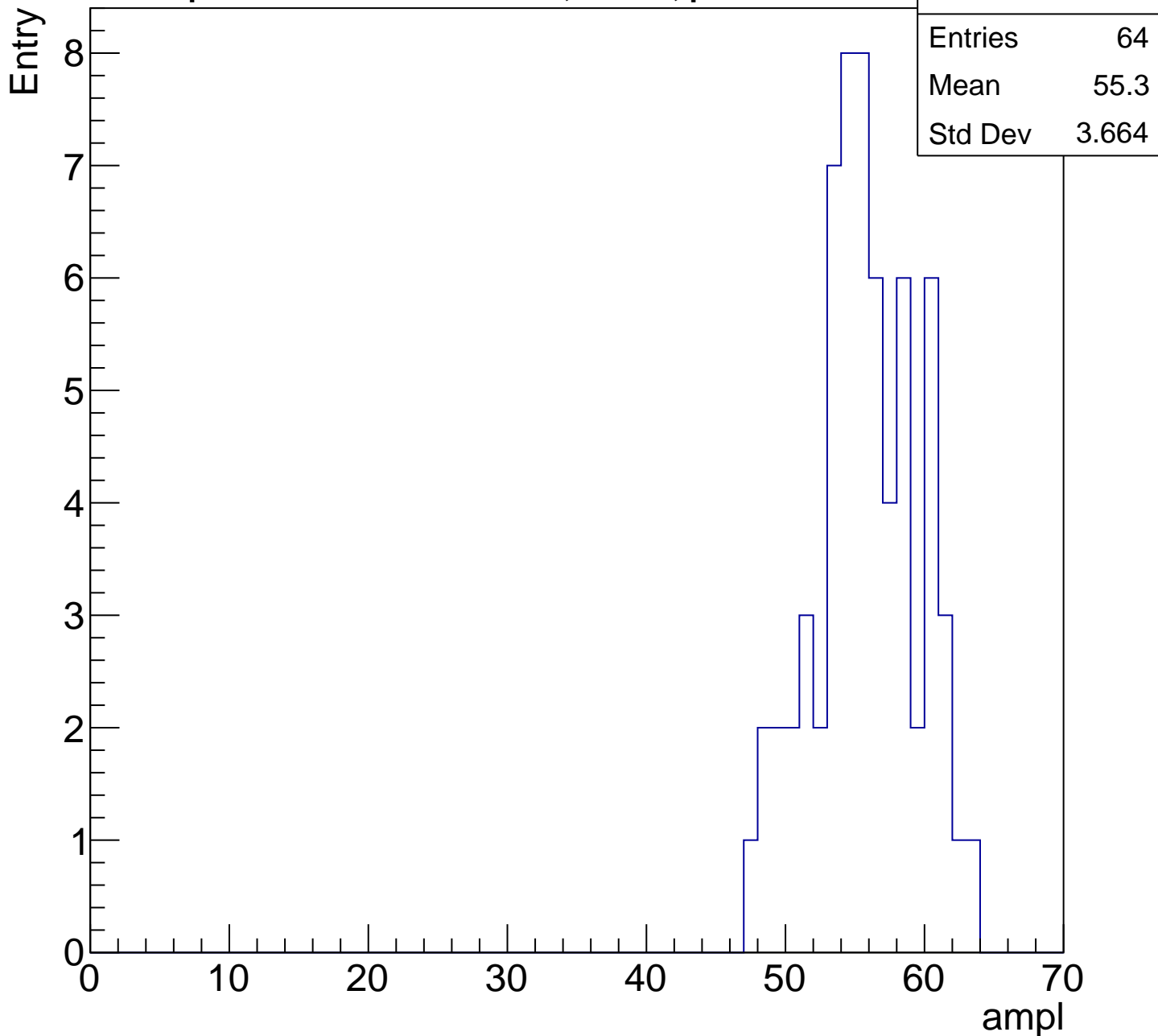
Entry

Entries	59
Mean	49.36
Std Dev	3.526



B1L003S, U20-ch49, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch49, adc5

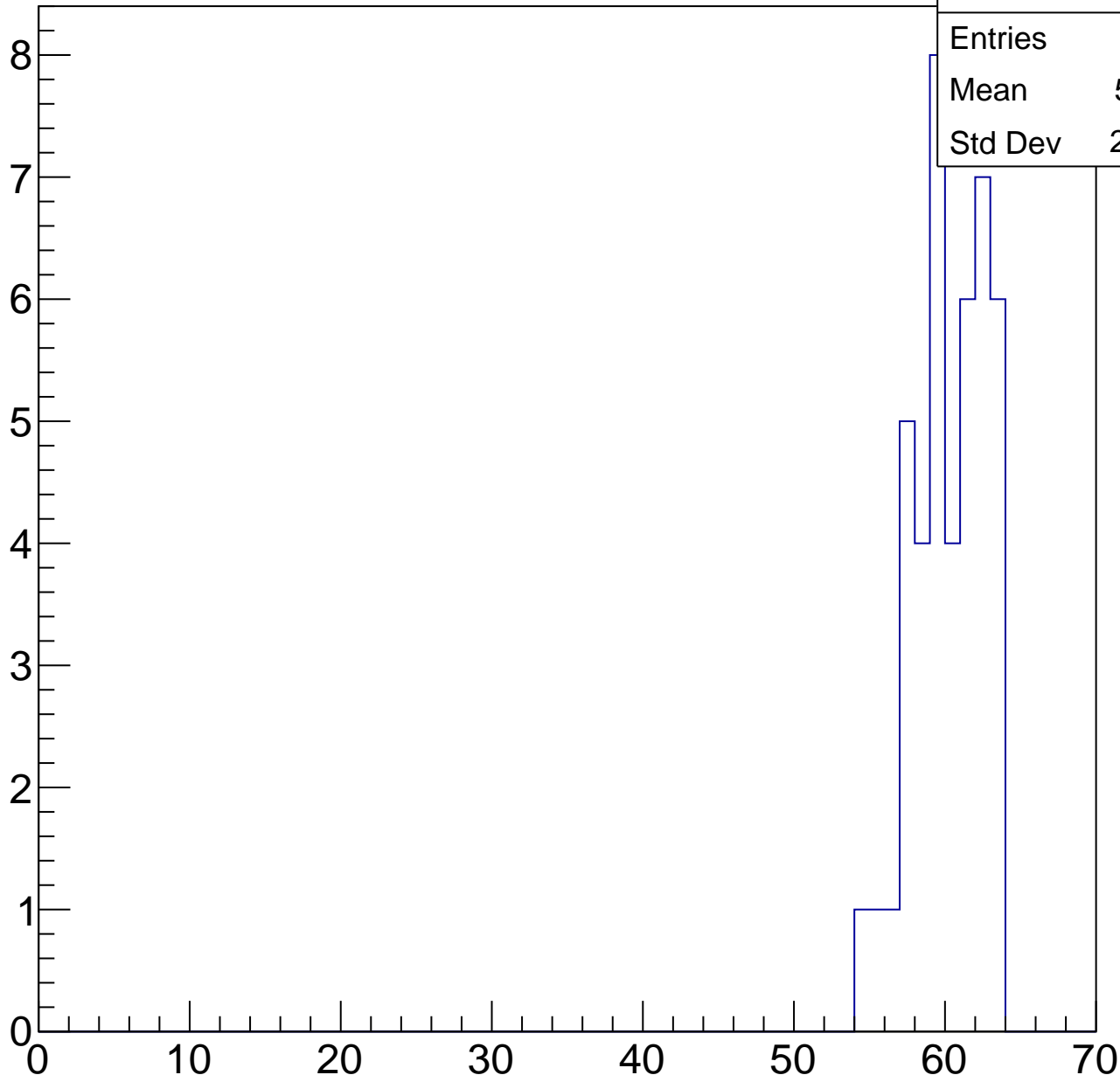
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.81
Std Dev	2.325

ampl



B1L003S, U20-ch49, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

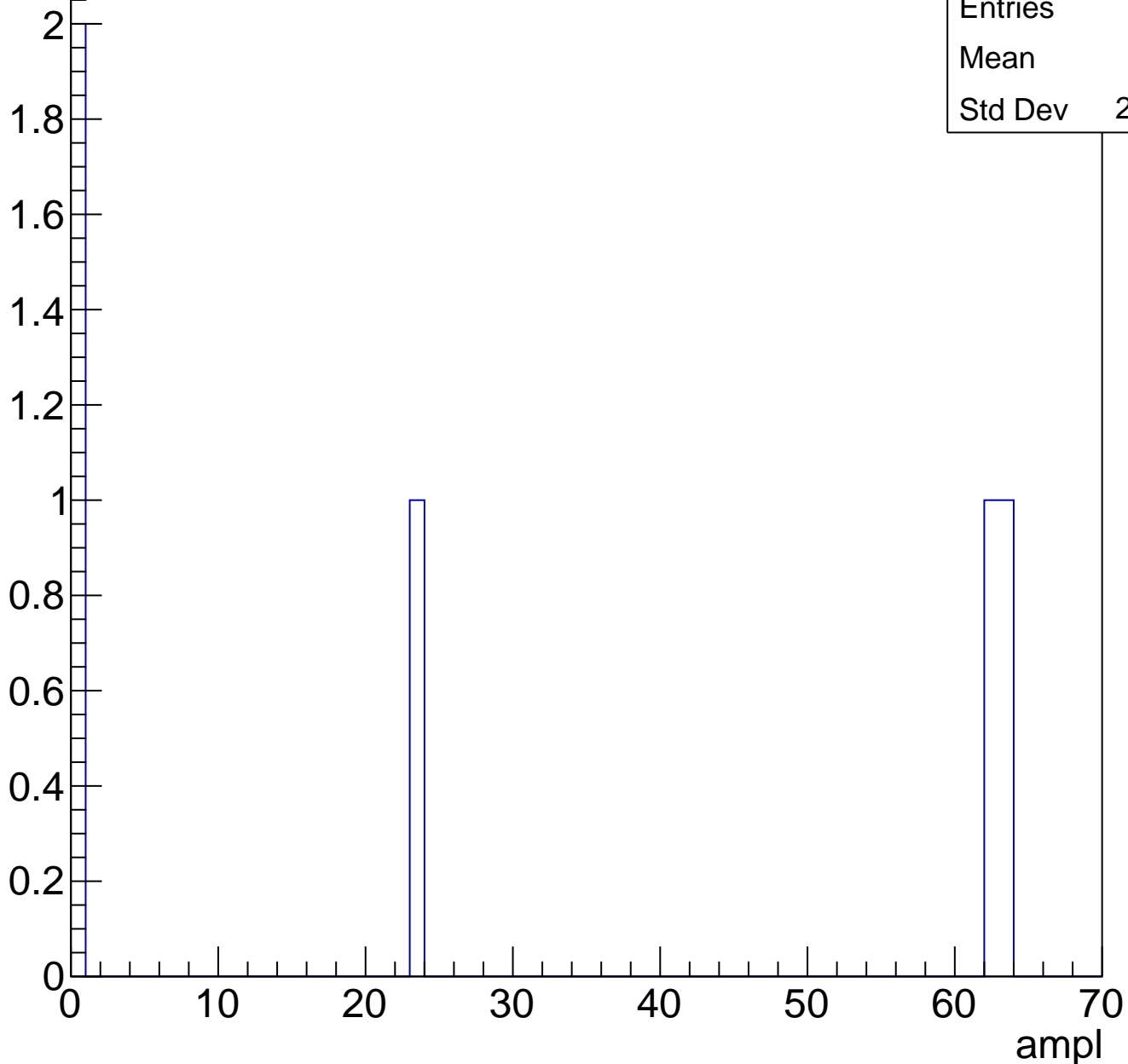


Entries	5
Mean	49.8
Std Dev	24.91

B1L003S, U20-ch49, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	29.6
Std Dev	28.15

B1L003S, U20-ch50, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	30.29
Std Dev	3.551

Gaus mean : 30.6266

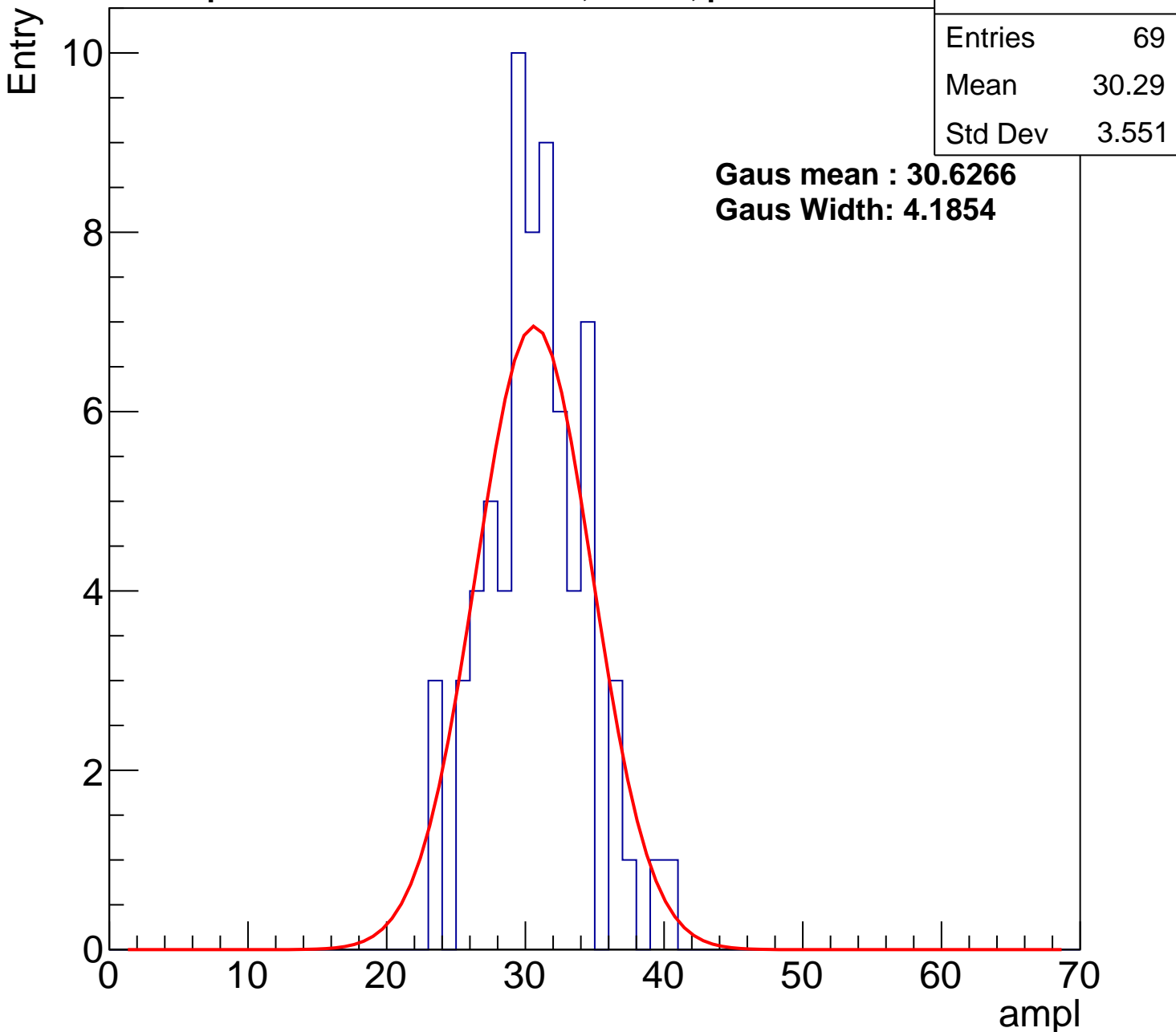
Gaus Width: 4.1854

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch50, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	36.58
Std Dev	3.032

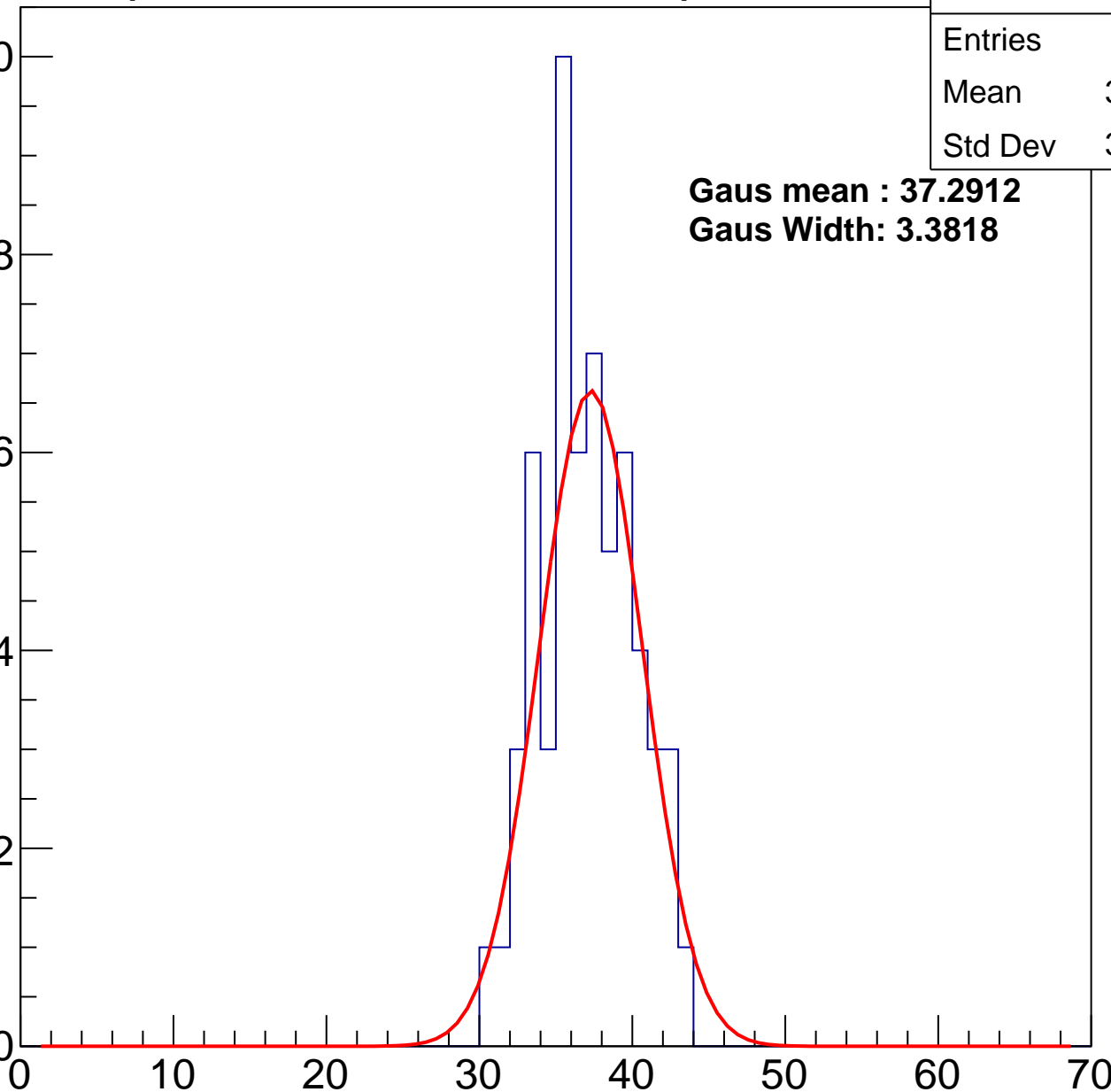
Gaus mean : 37.2912

Gaus Width: 3.3818

Entry

10
8
6
4
2
0

ampl



B1L003S, U20-ch50, adc2

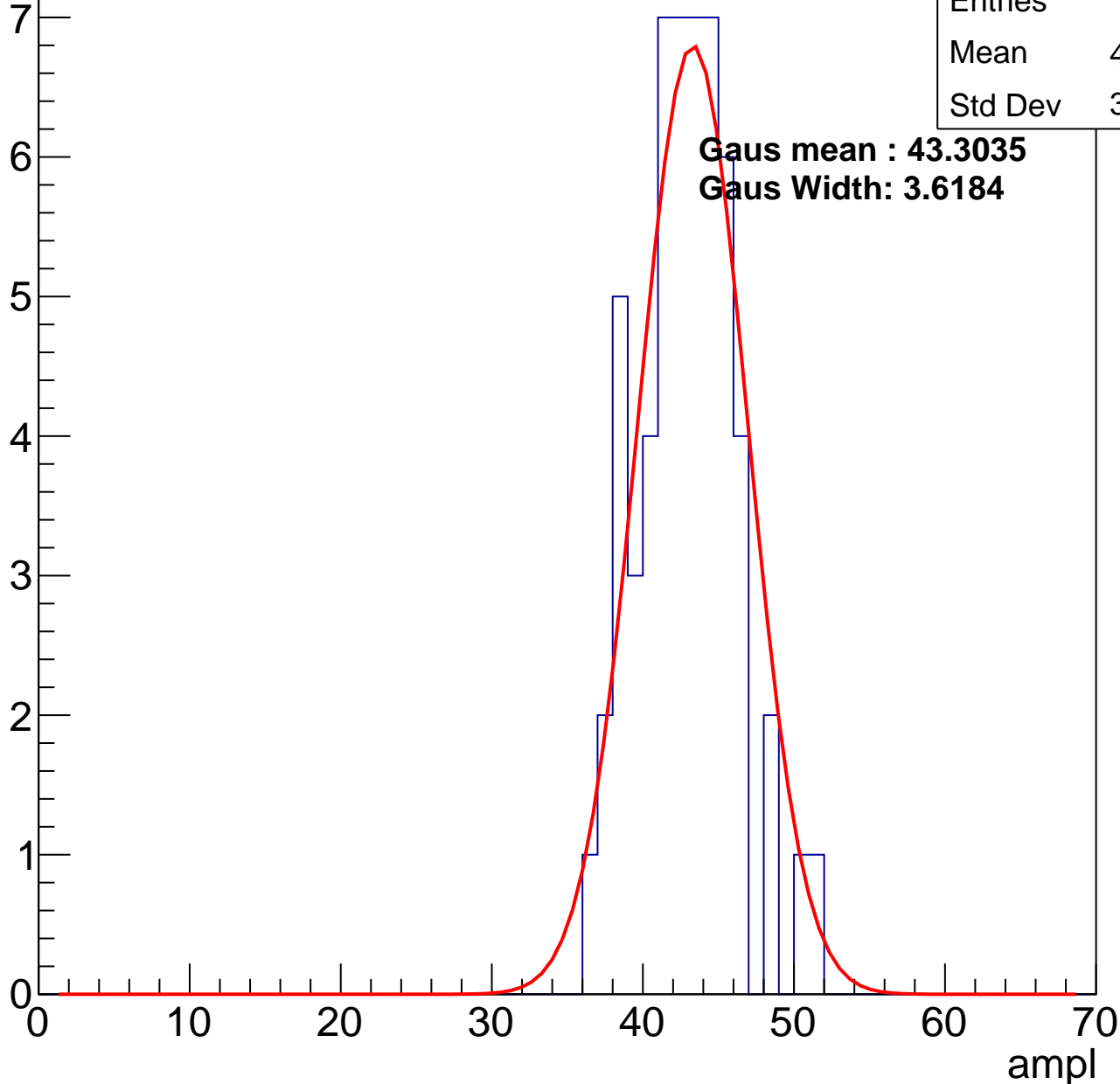
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.42
Std Dev	3.179

Gaus mean : 43.3035

Gaus Width: 3.6184

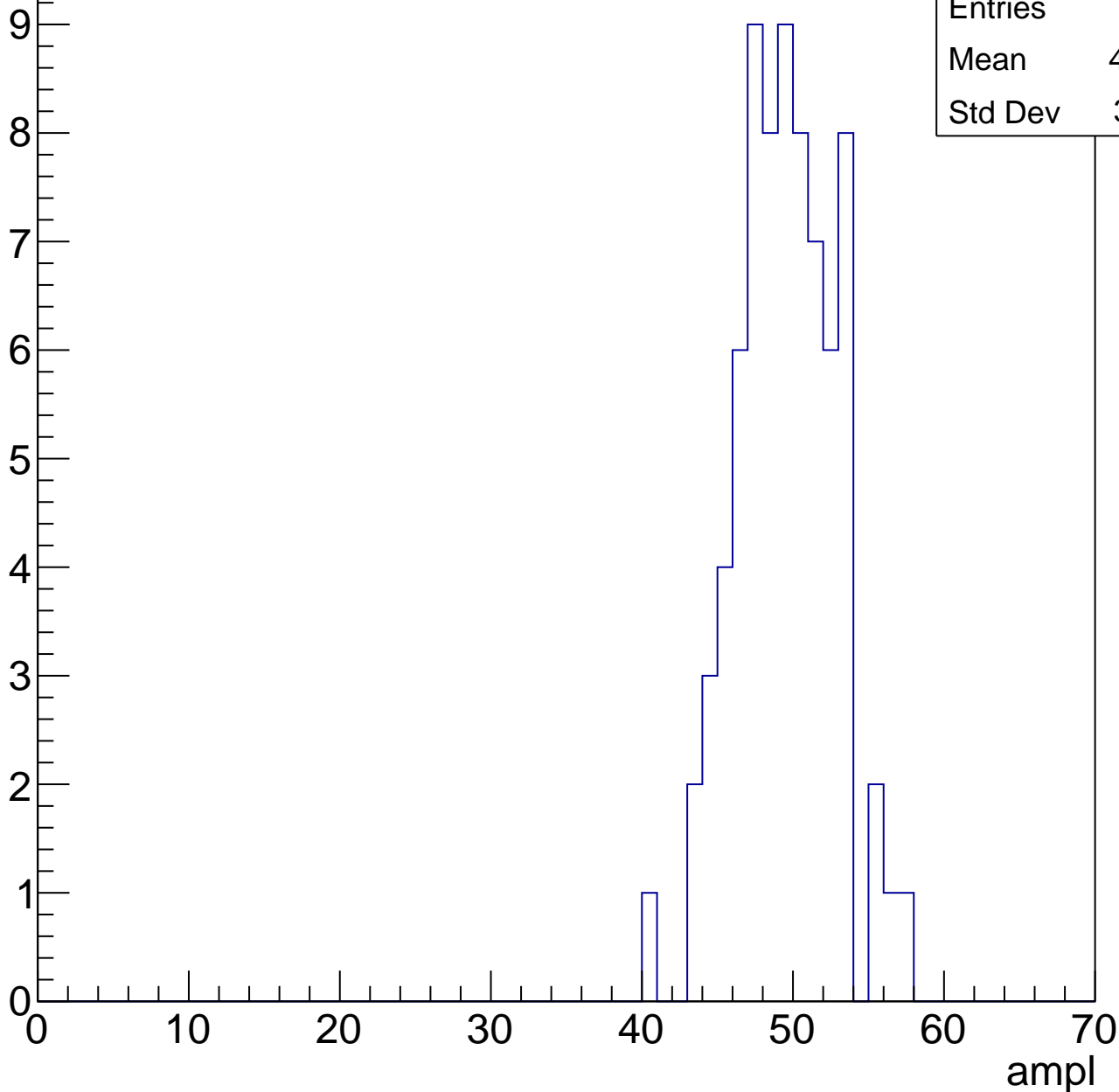


B1L003S, U20-ch50, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	49.04
Std Dev	3.251

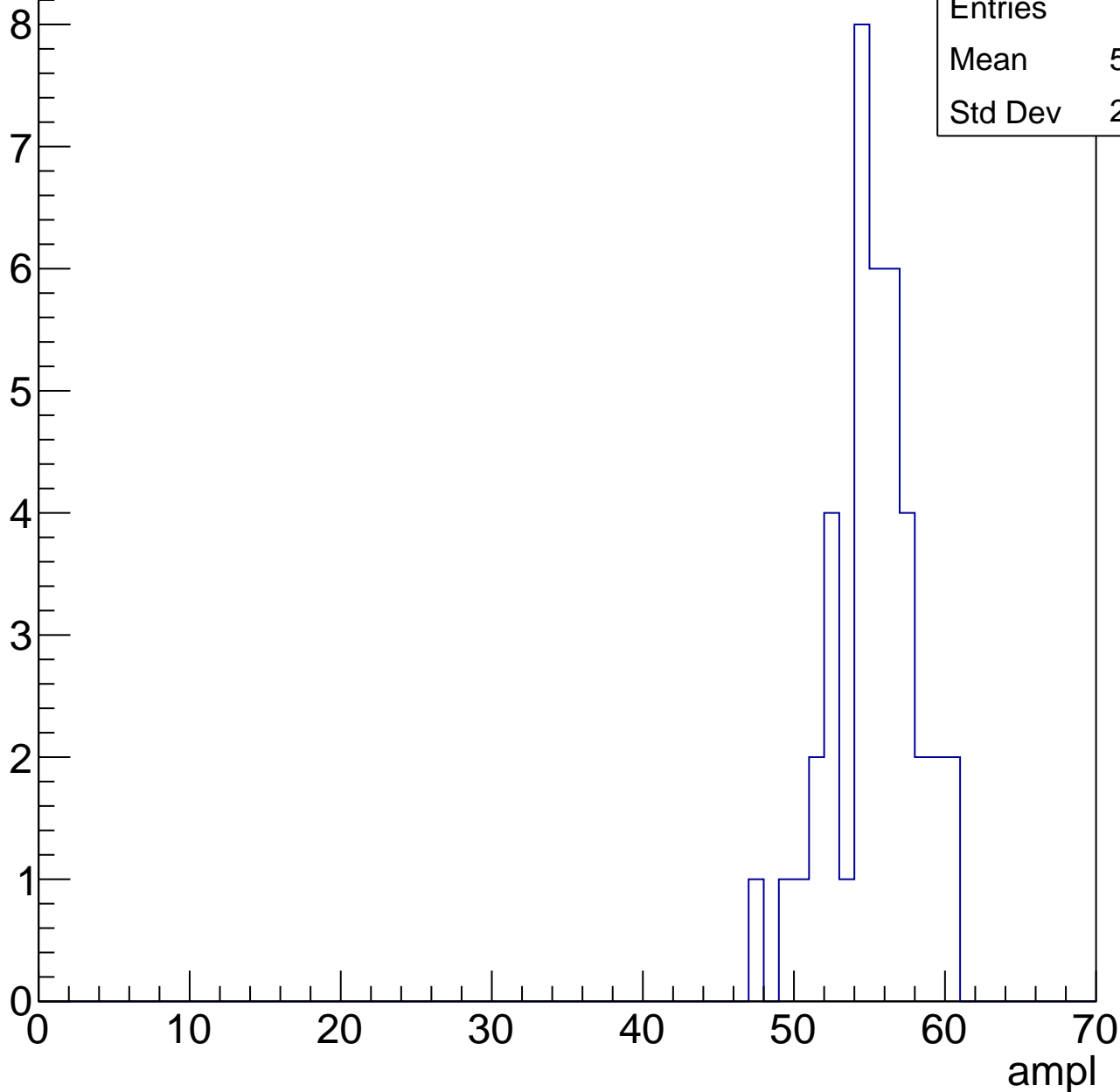


B1L003S, U20-ch50, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	54.73
Std Dev	2.846

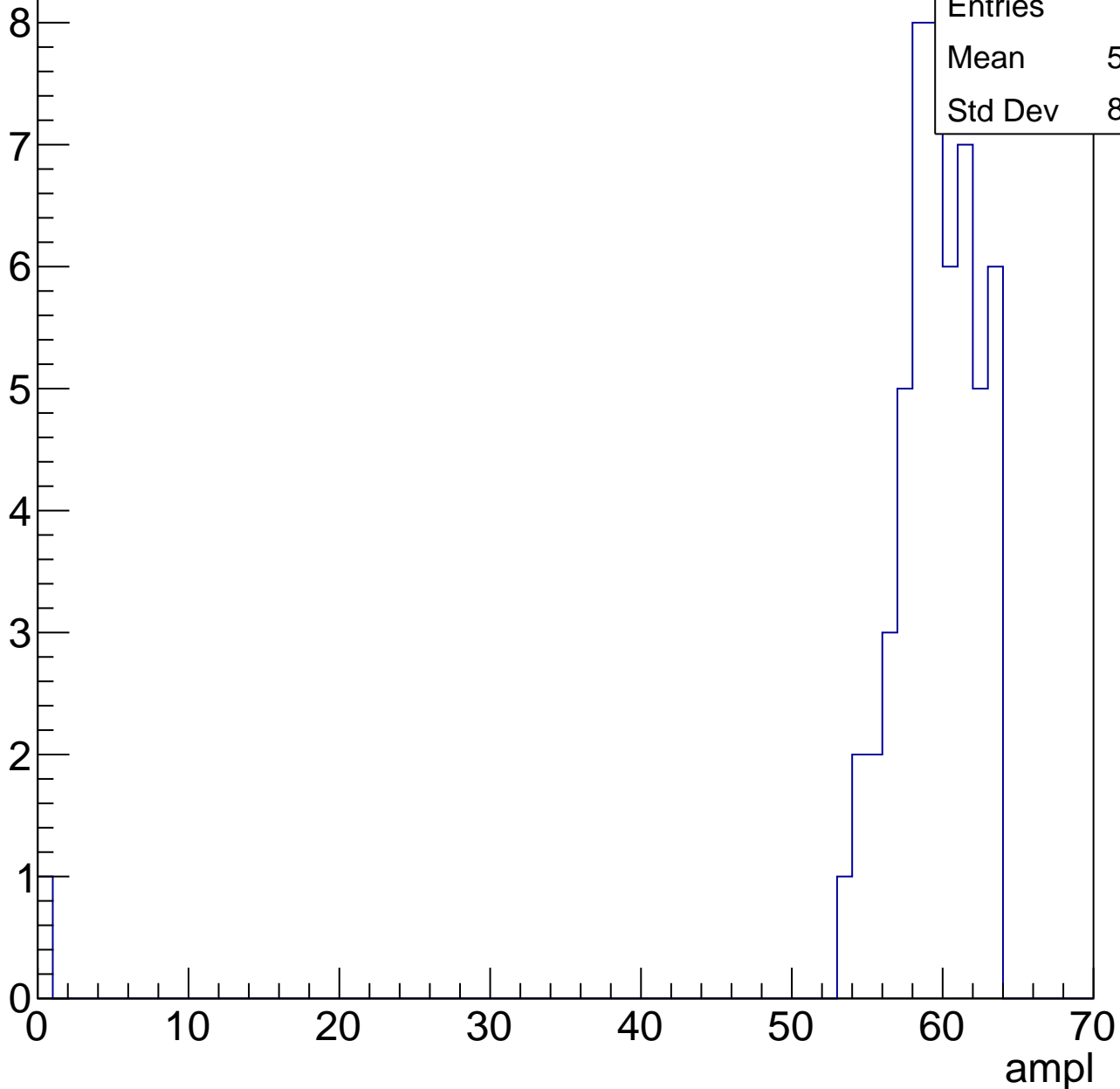


B1L003S, U20-ch50, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.06
Std Dev	8.368

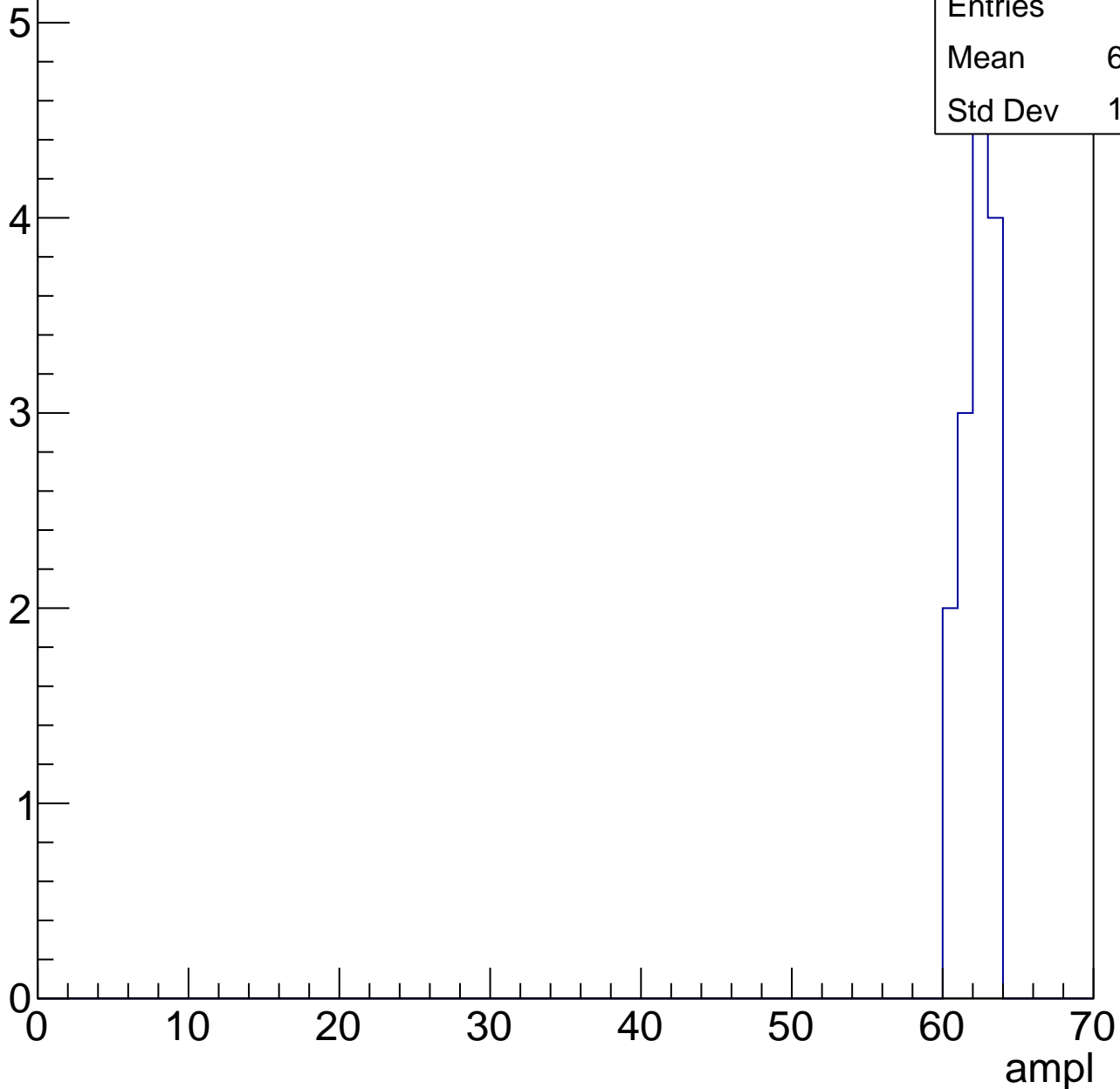


B1L003S, U20-ch50, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.79
Std Dev	1.013



B1L003S, U20-ch50, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch51, adc0

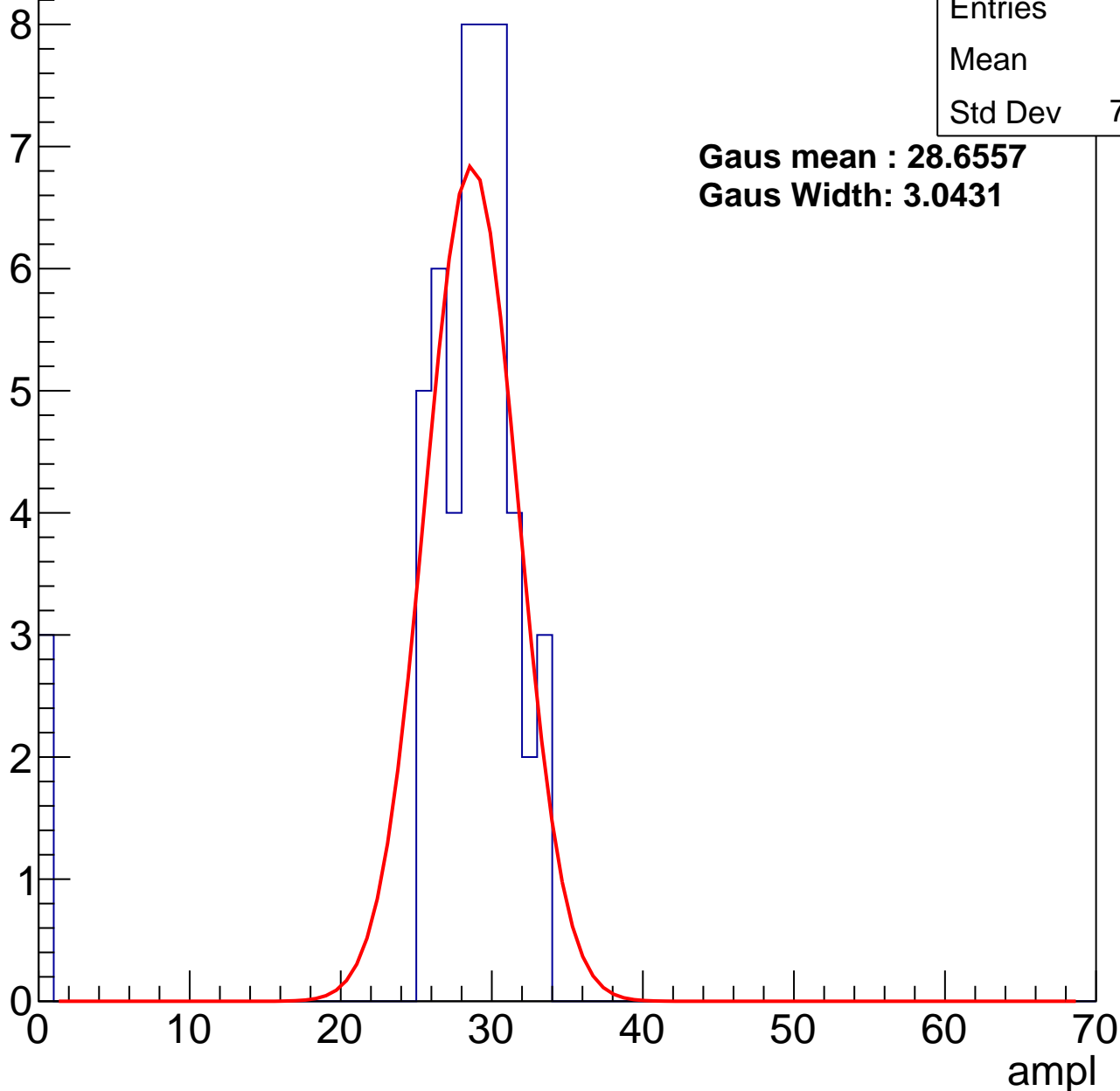
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	26.9
Std Dev	7.066

Gaus mean : 28.6557

Gaus Width: 3.0431



B1L003S, U20-ch51, adc1

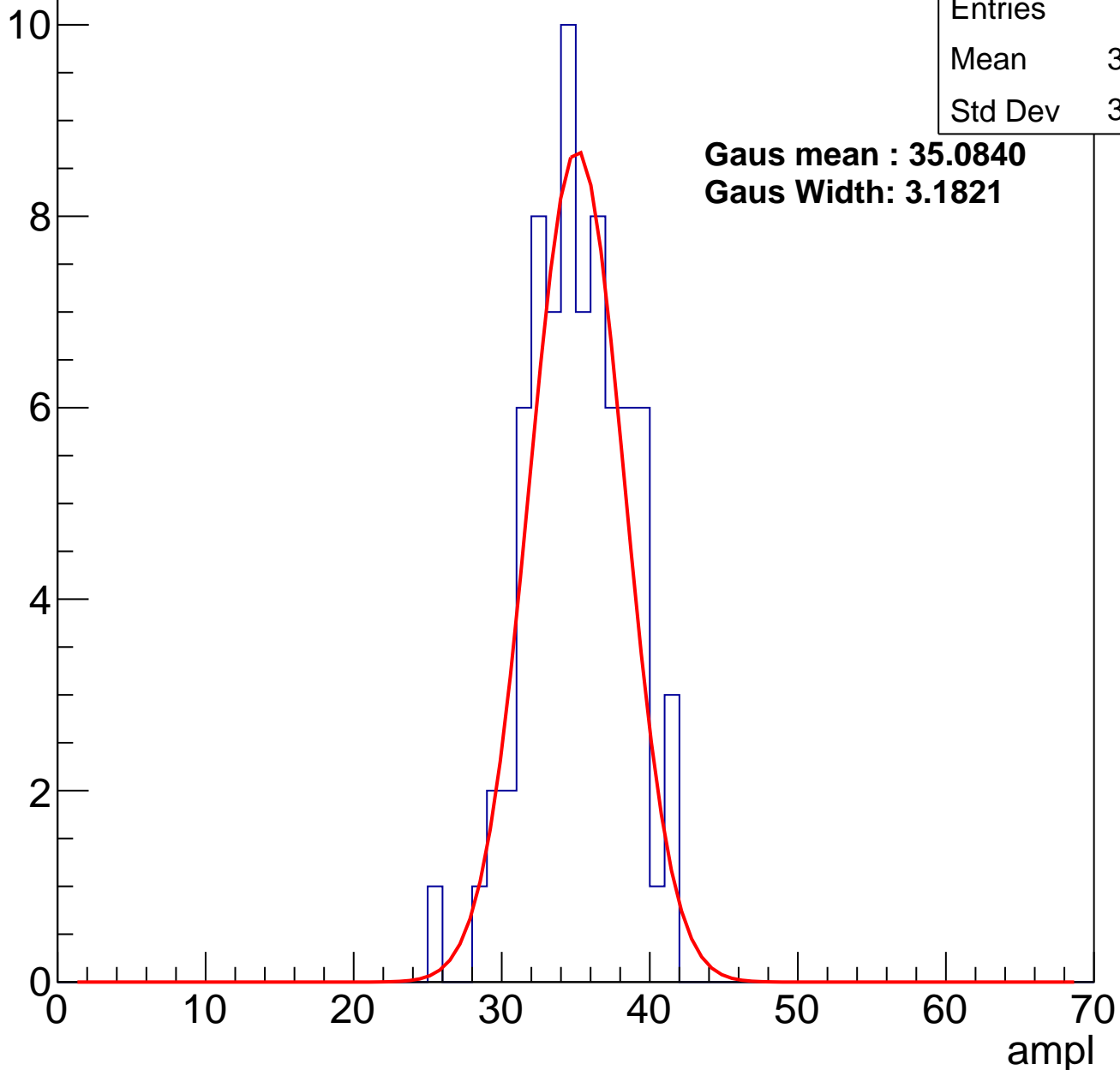
calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	34.65
Std Dev	3.253

Gaus mean : 35.0840

Gaus Width: 3.1821

Entry



B1L003S, U20-ch51, adc2

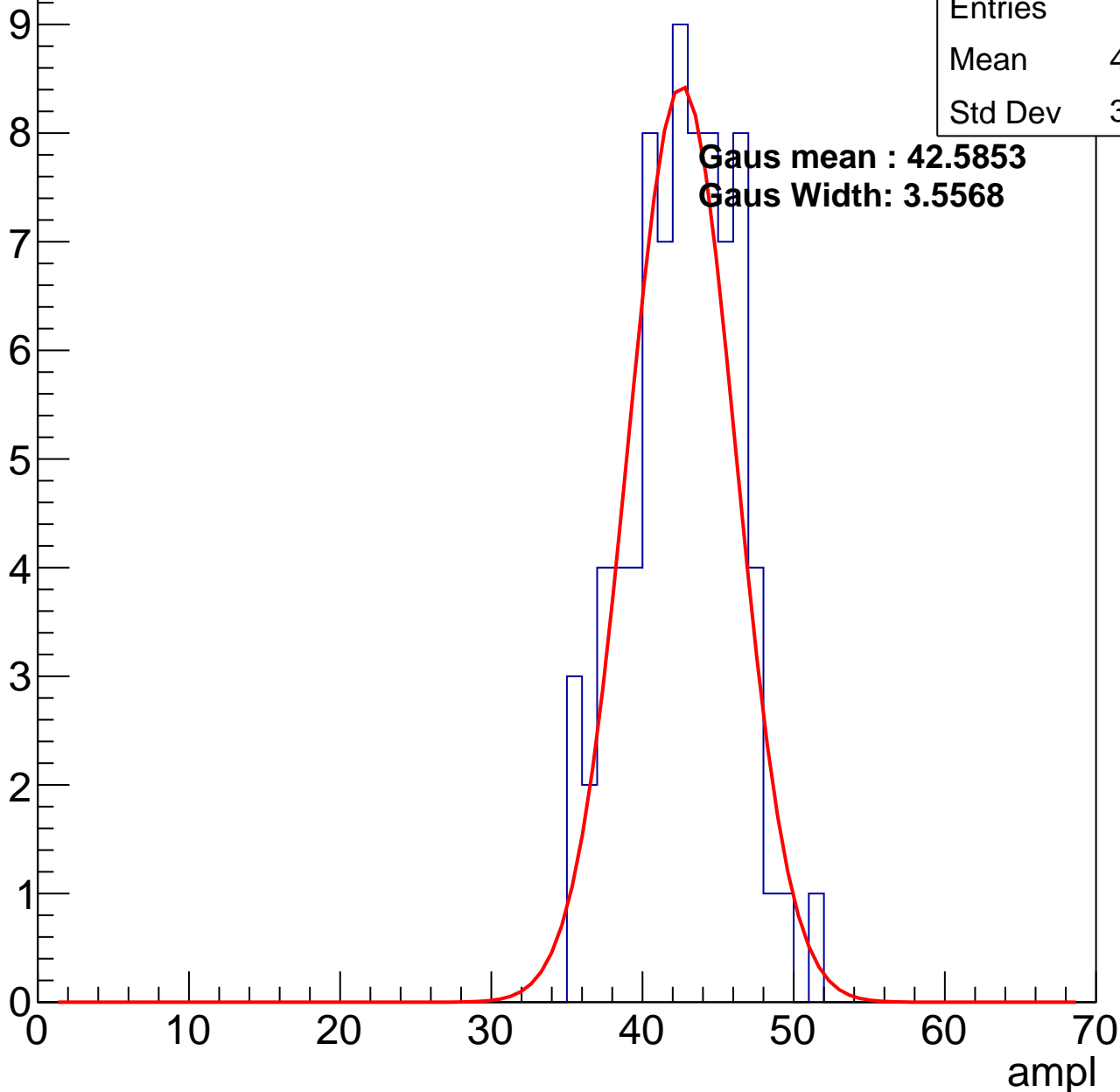
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	42.19
Std Dev	3.472

Gaus mean : 42.5853

Gaus Width: 3.5568

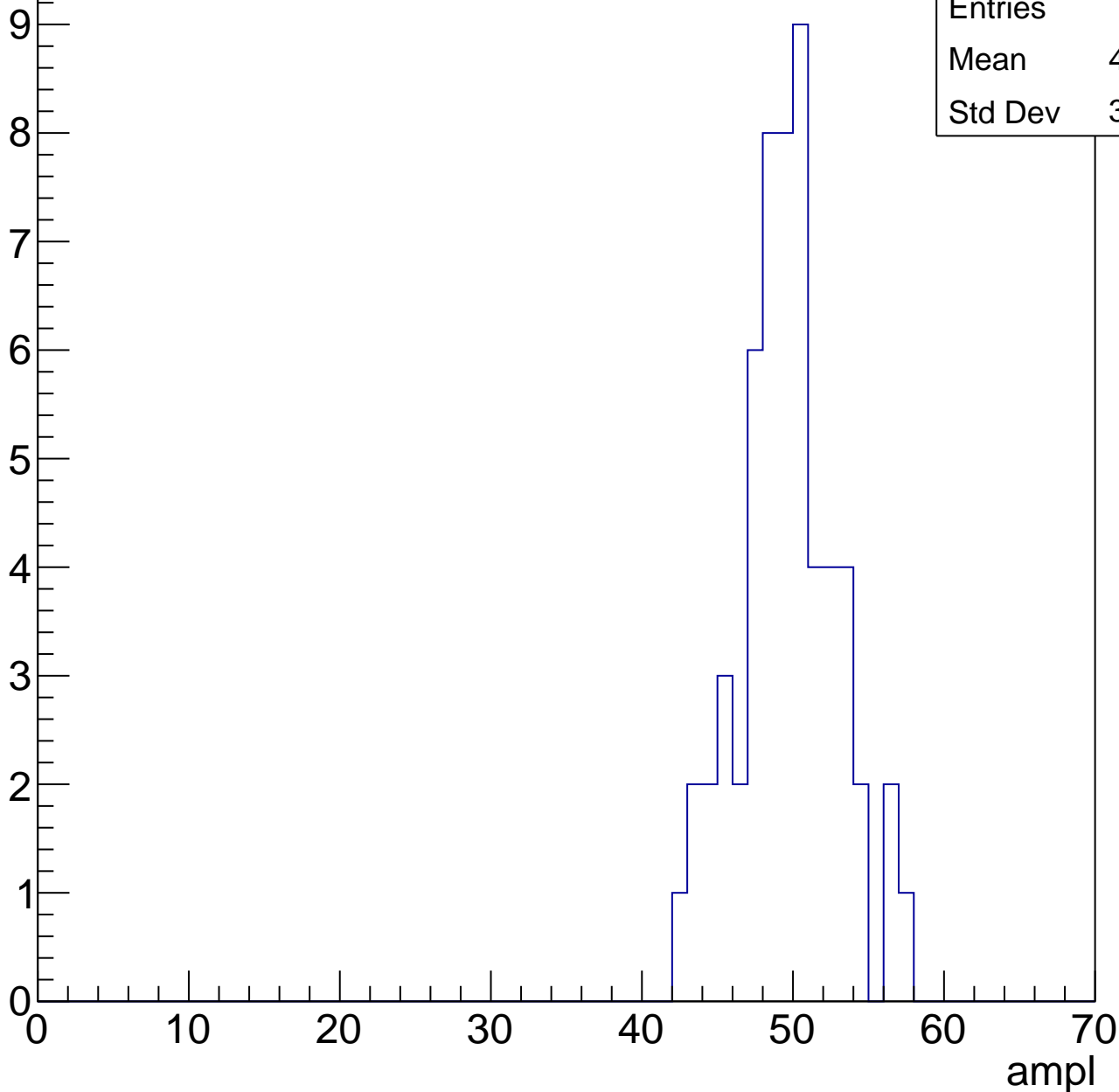


B1L003S, U20-ch51, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

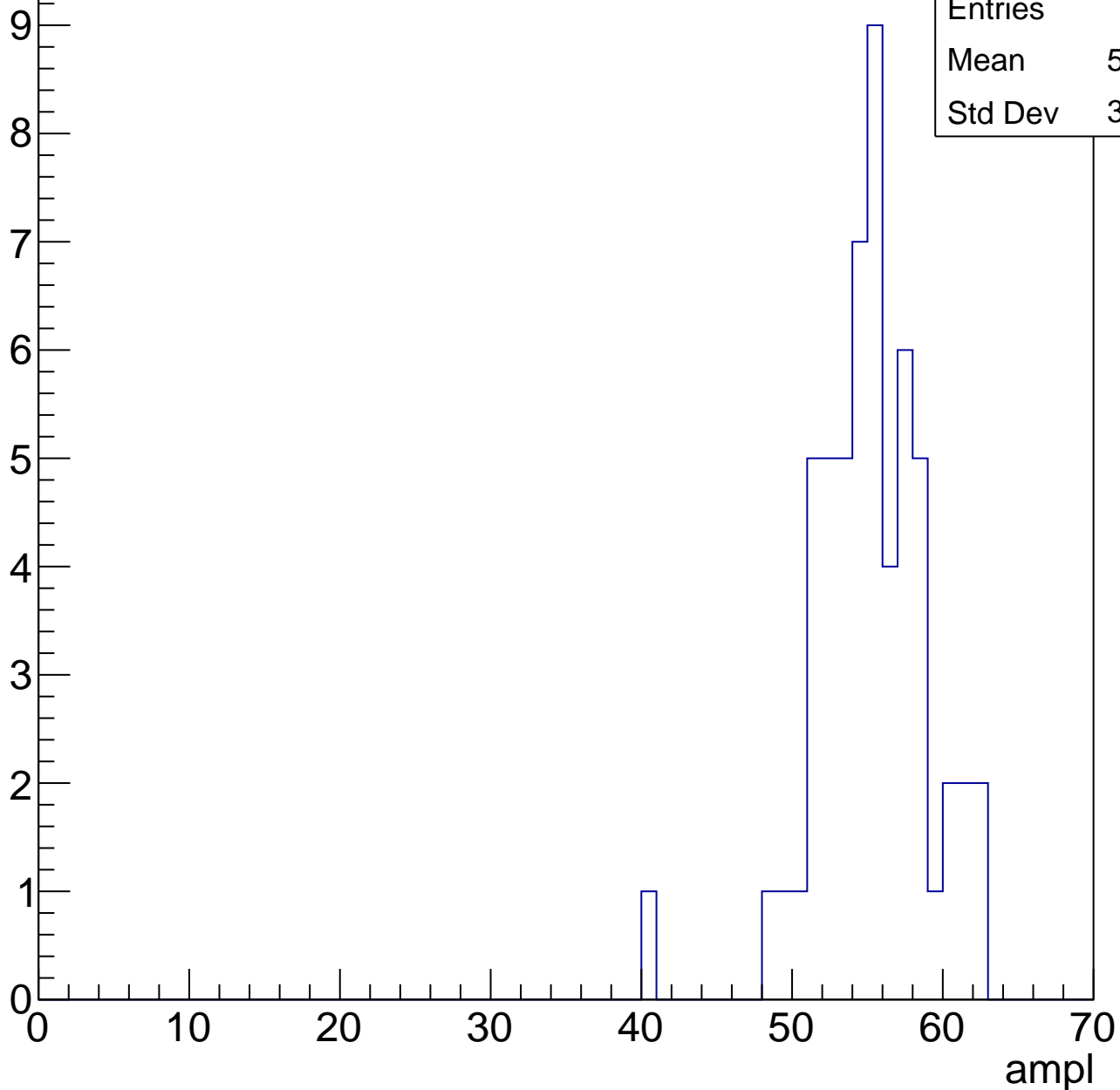
Entries	58
Mean	49.17
Std Dev	3.228



B1L003S, U20-ch51, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch51, adc5

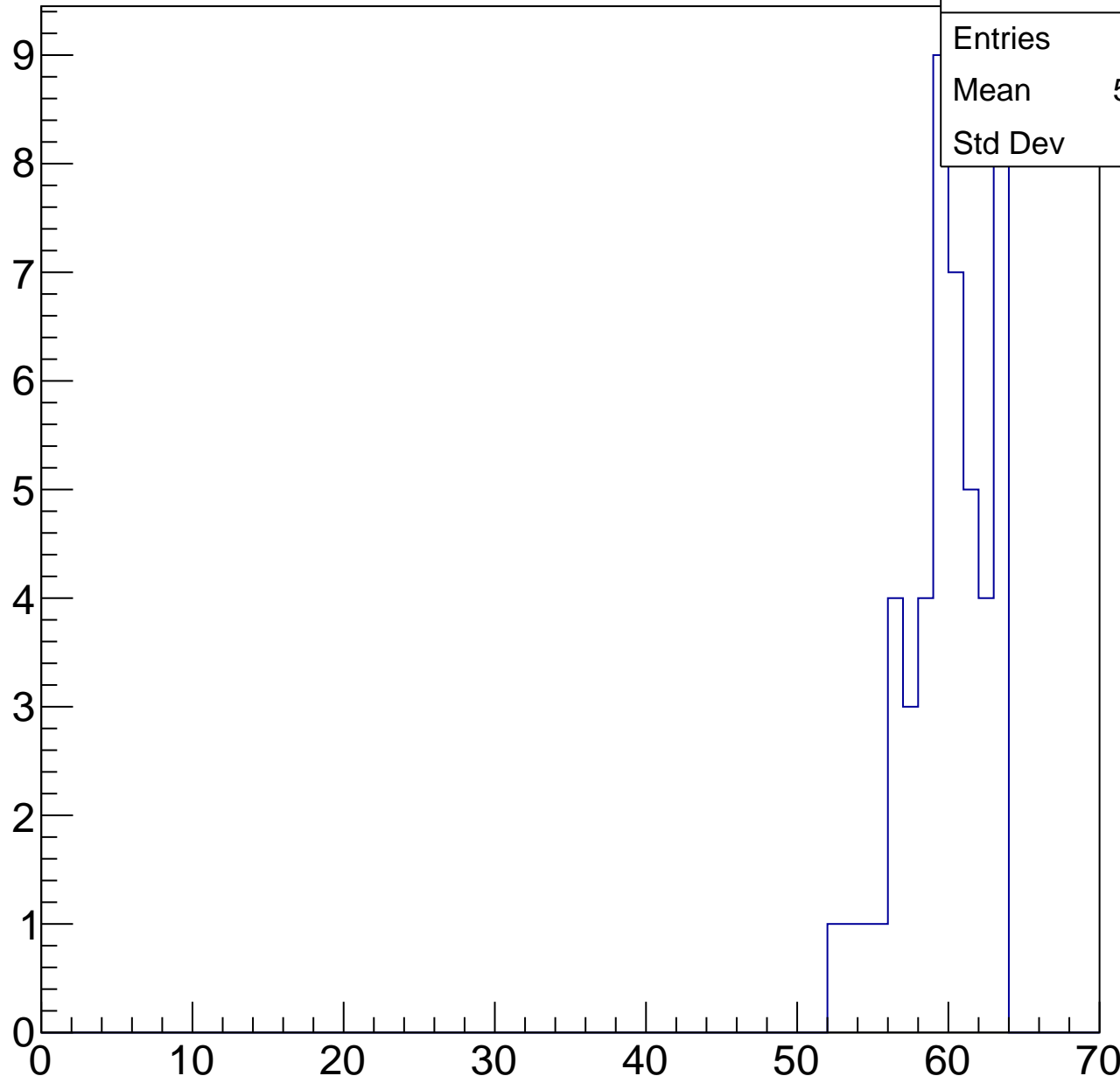
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.35
Std Dev	2.75

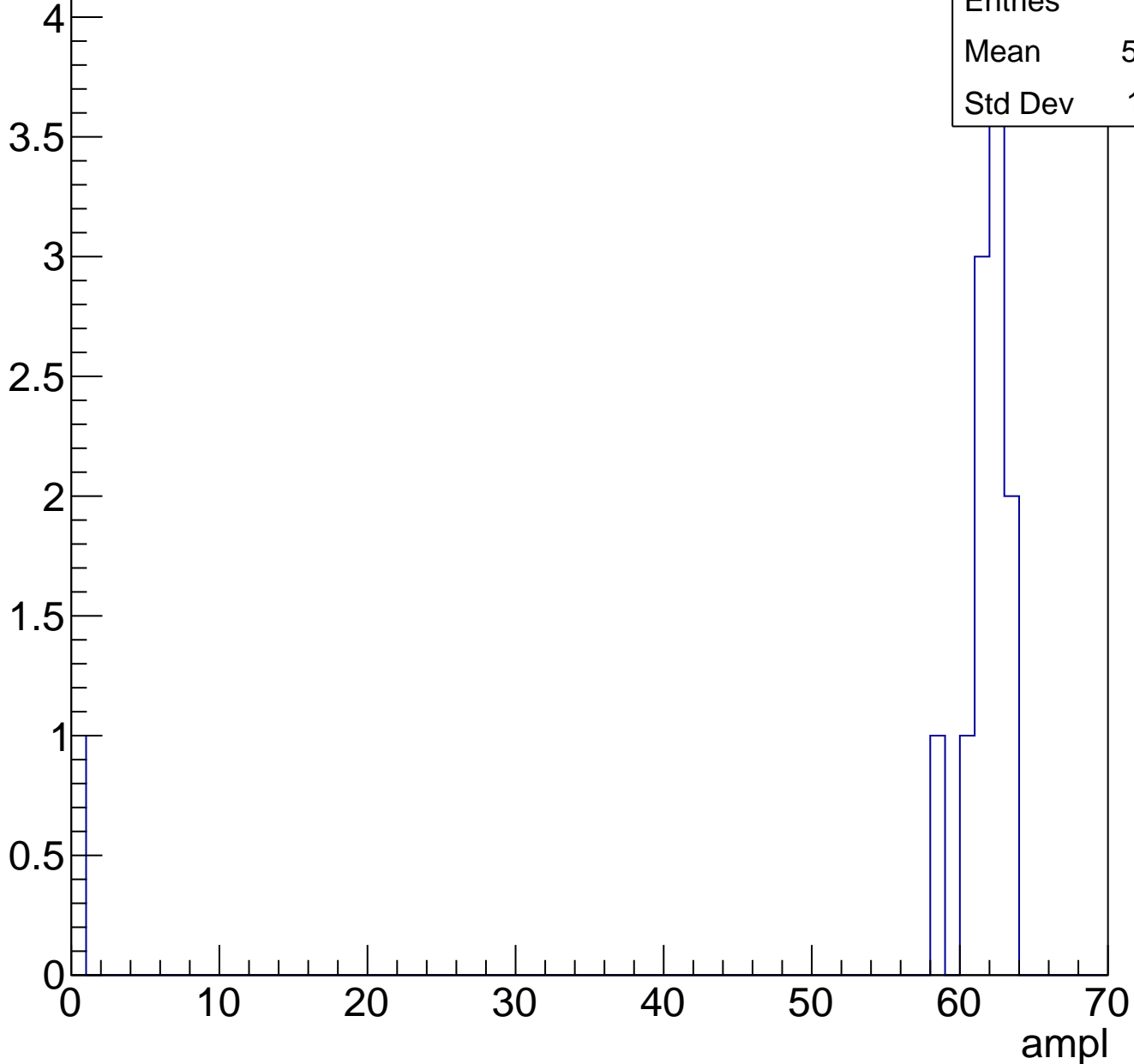
ampl



B1L003S, U20-ch51, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch51, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L003S, U20-ch52, adc0

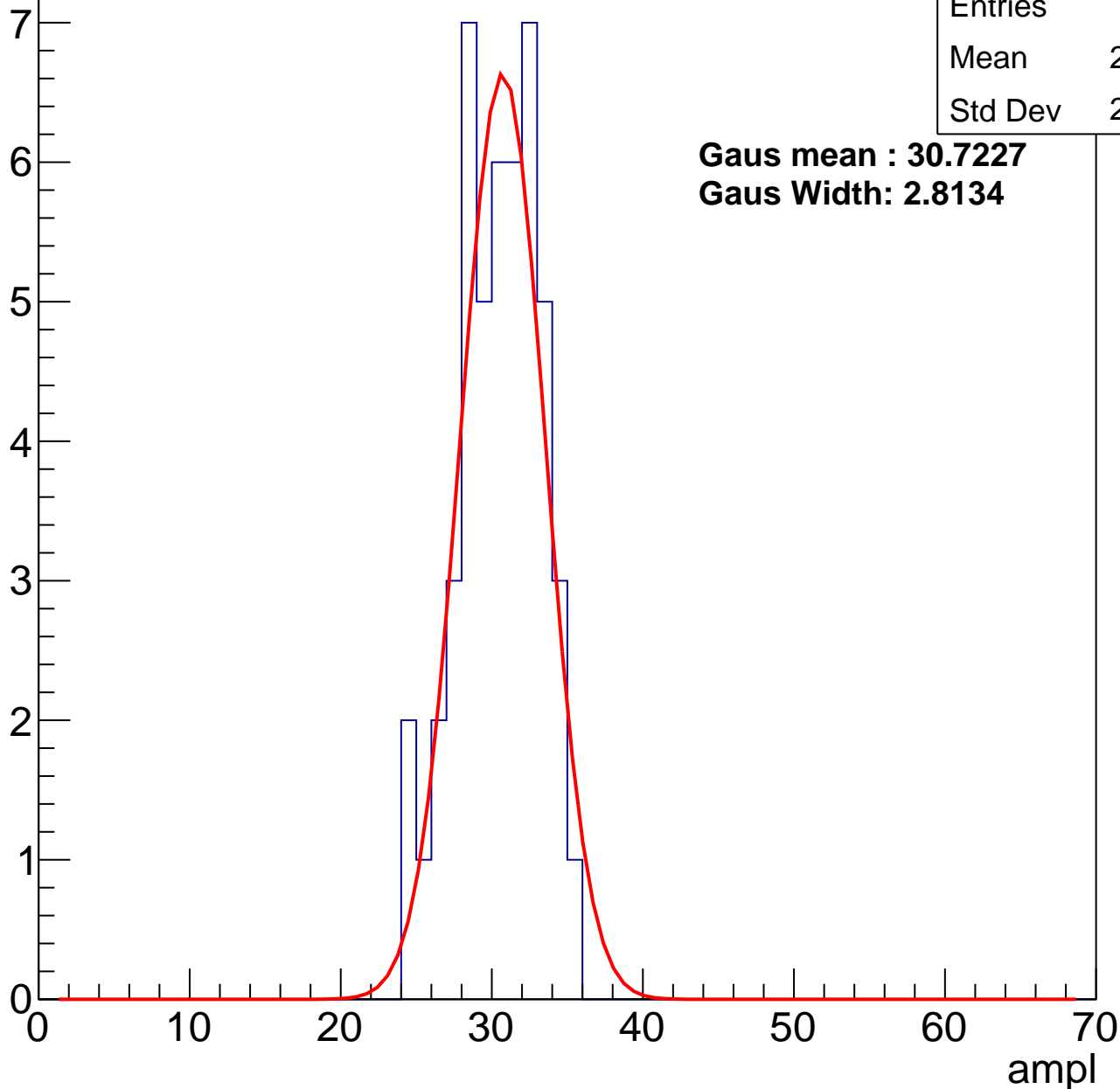
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	29.98
Std Dev	2.665

Gaus mean : 30.7227

Gaus Width: 2.8134



B1L003S, U20-ch52, adc1

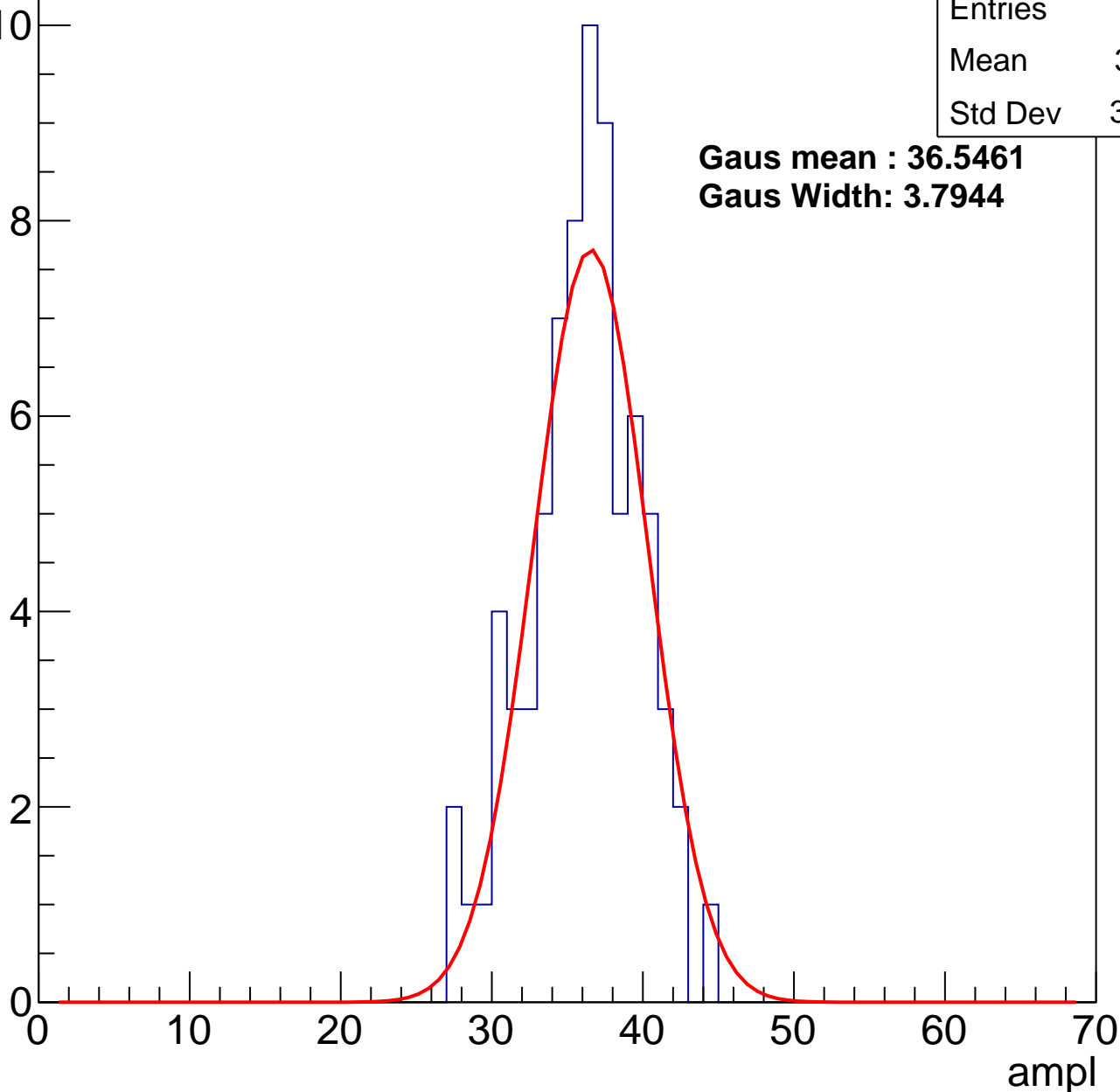
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	35.61
Std Dev	3.625

Gaus mean : 36.5461

Gaus Width: 3.7944



B1L003S, U20-ch52, adc2

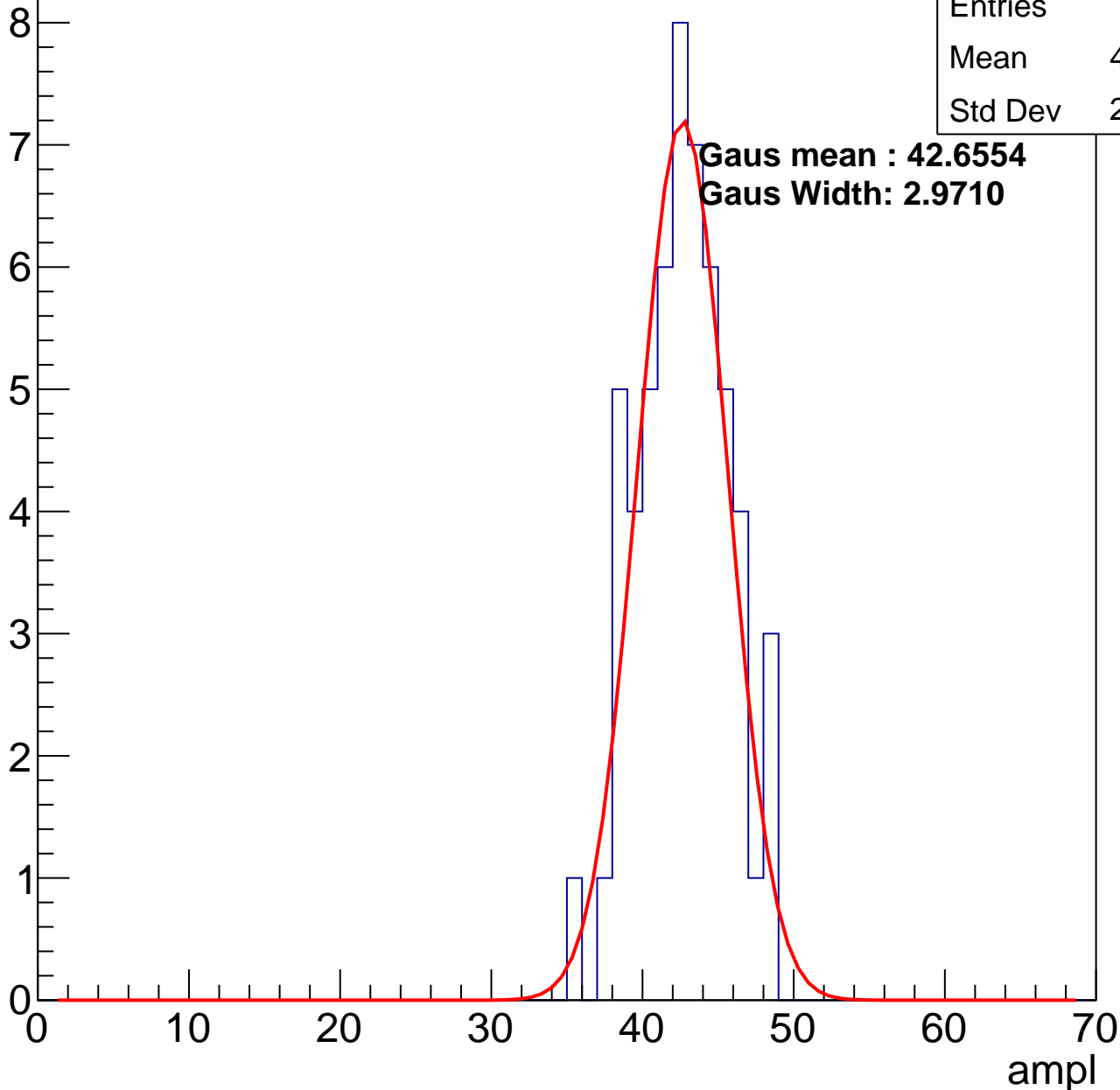
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.23
Std Dev	2.946

Gaus mean : 42.6554

Gaus Width: 2.9710

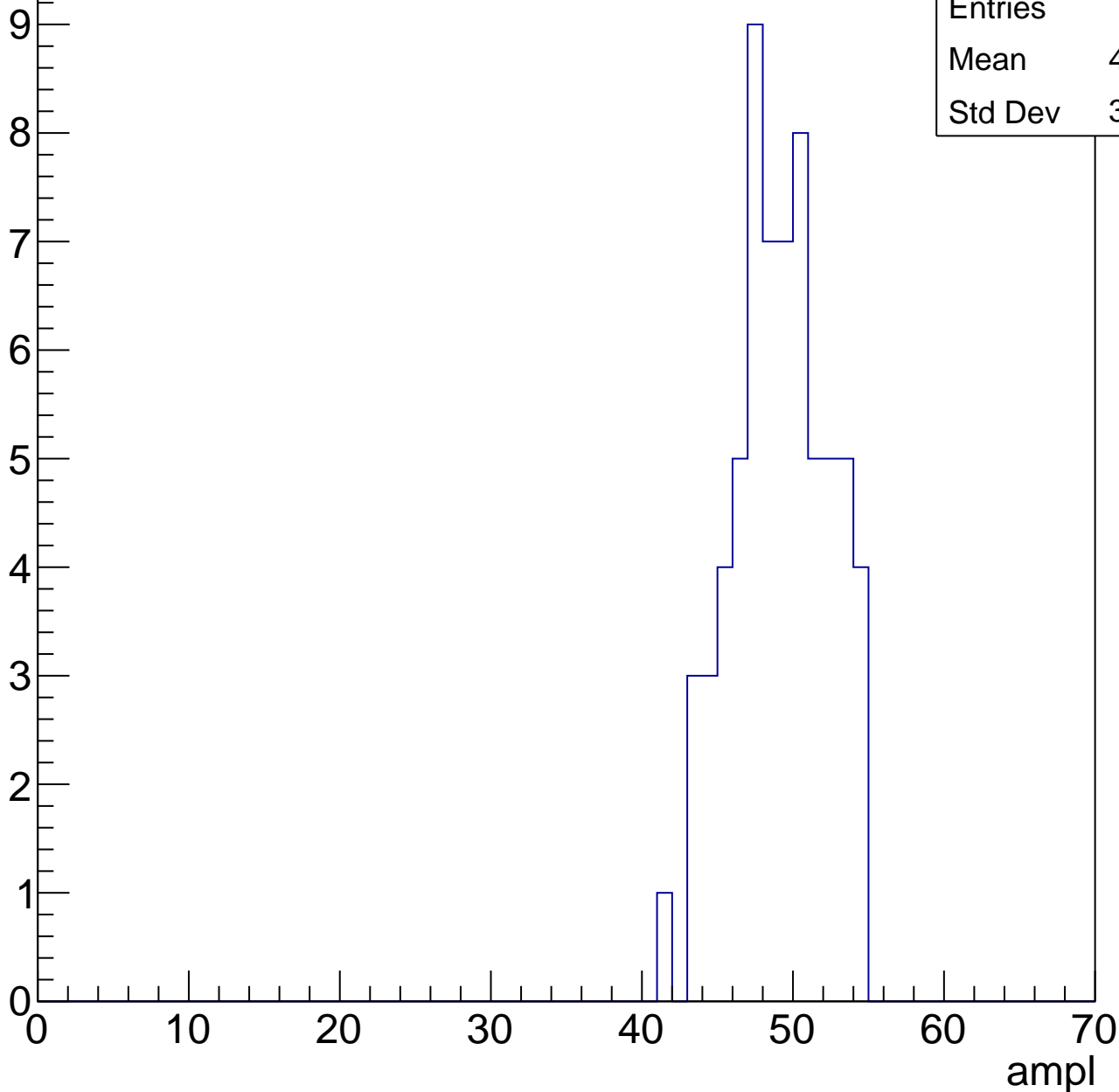


B1L003S, U20-ch52, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.64
Std Dev	3.122

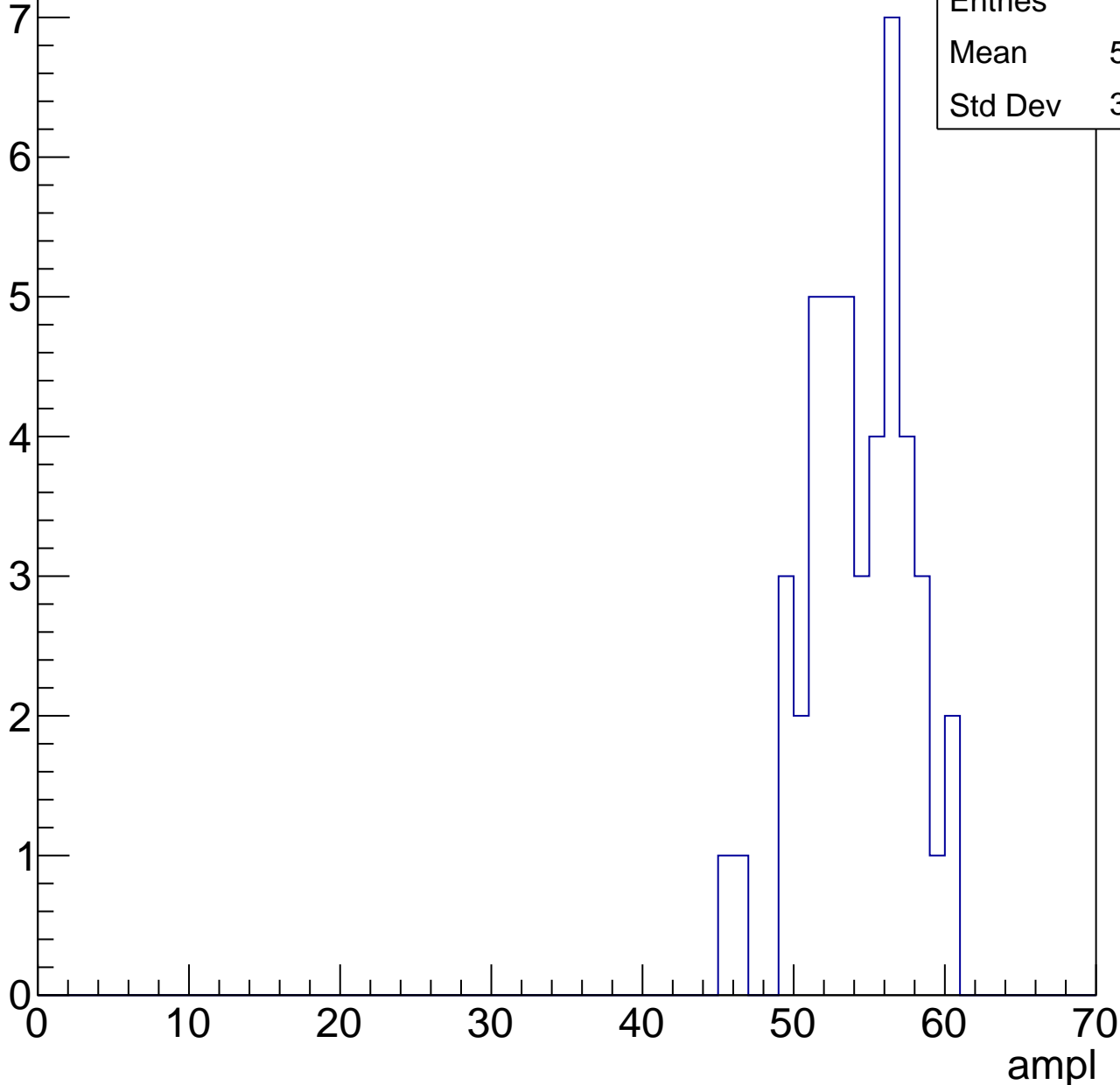


B1L003S, U20-ch52, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	53.76
Std Dev	3.402



B1L003S, U20-ch52, adc5

calib_packv5_042523_0143.root, FC#13, port D2

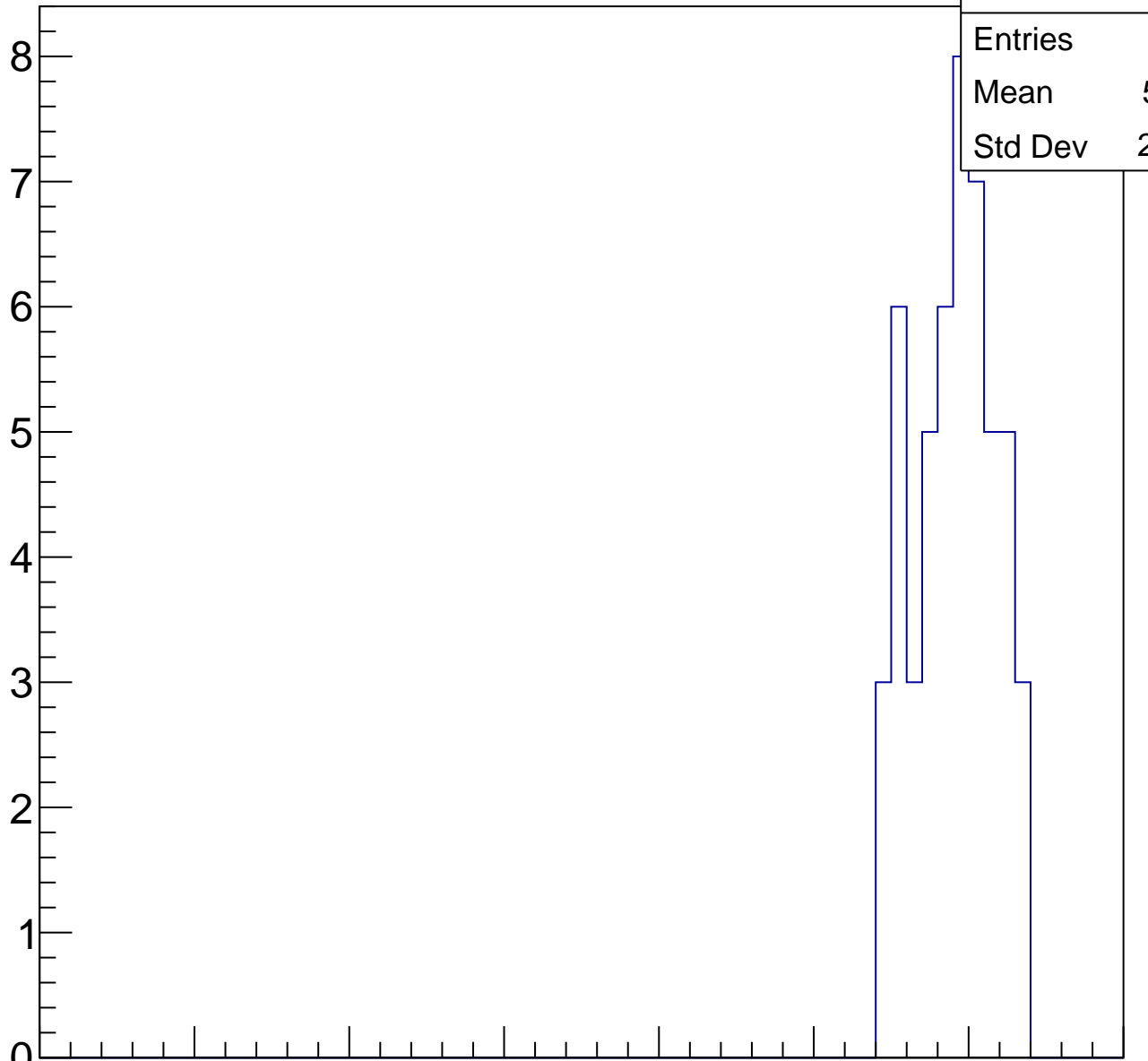
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.61
Std Dev	2.567

ampl

0 10 20 30 40 50 60 70

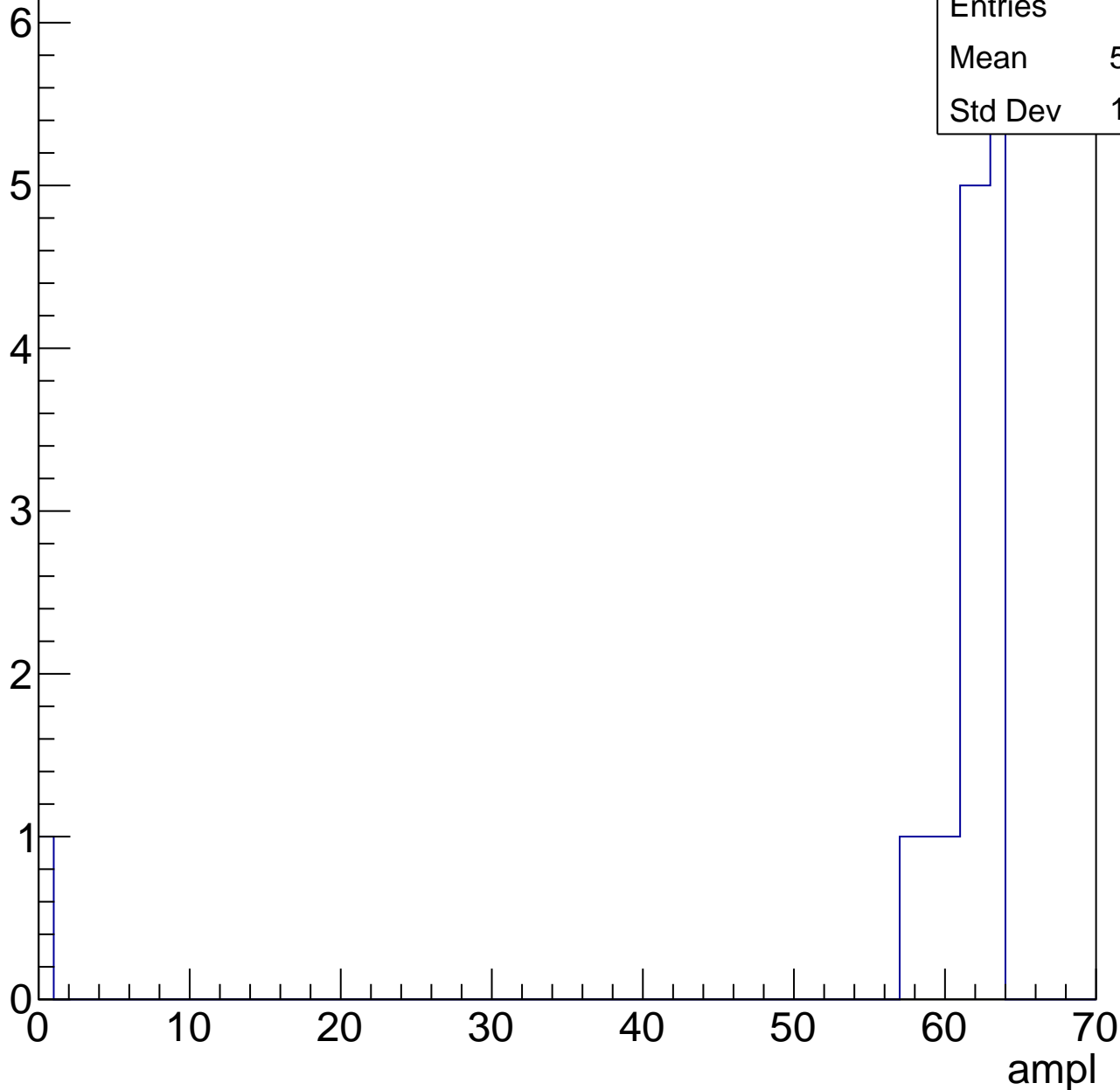


B1L003S, U20-ch52, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

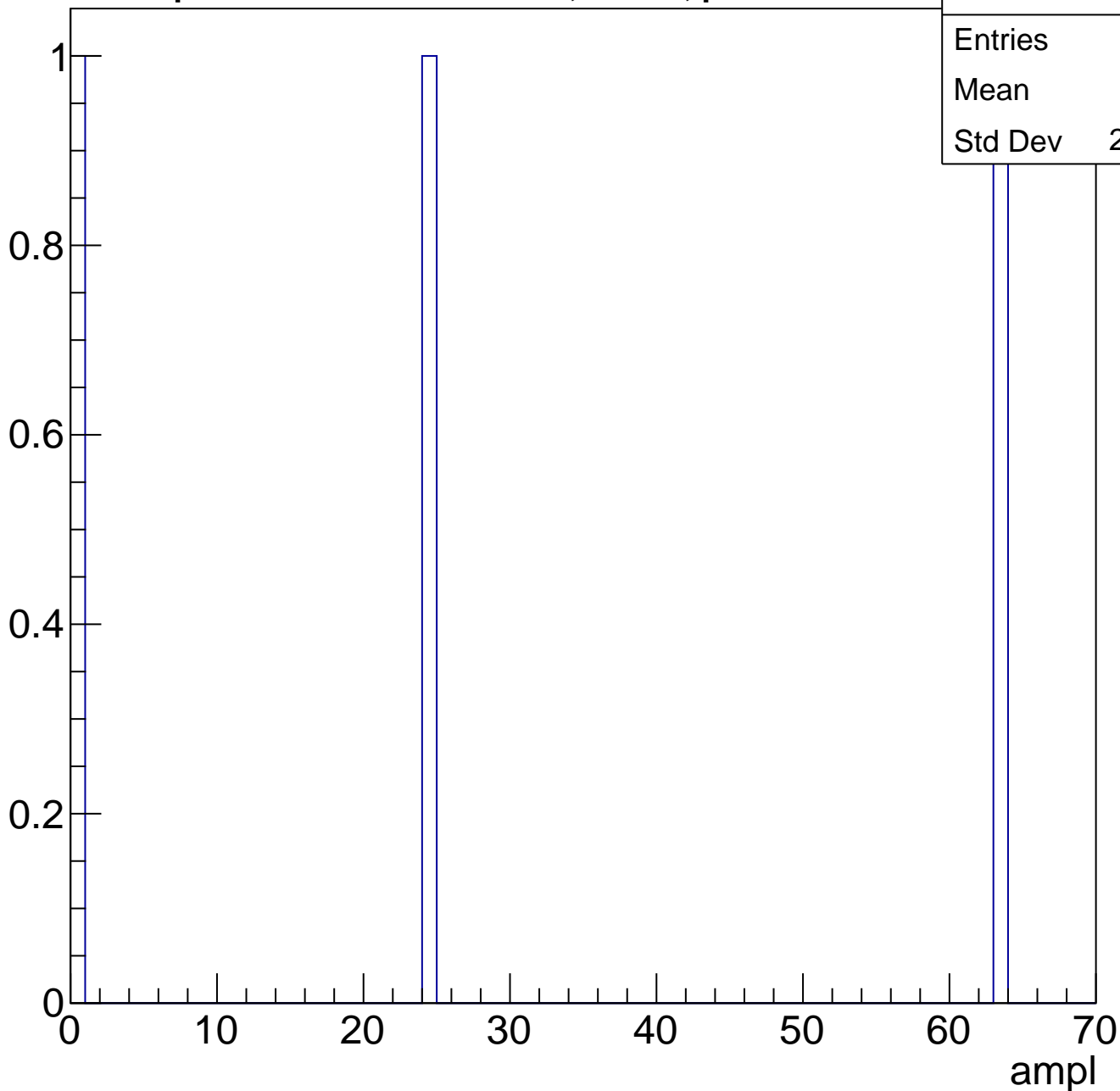
Entries	21
Mean	58.43
Std Dev	13.17



B1L003S, U20-ch52, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch53, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	27.12
Std Dev	6.294

Gaus mean : 28.7083

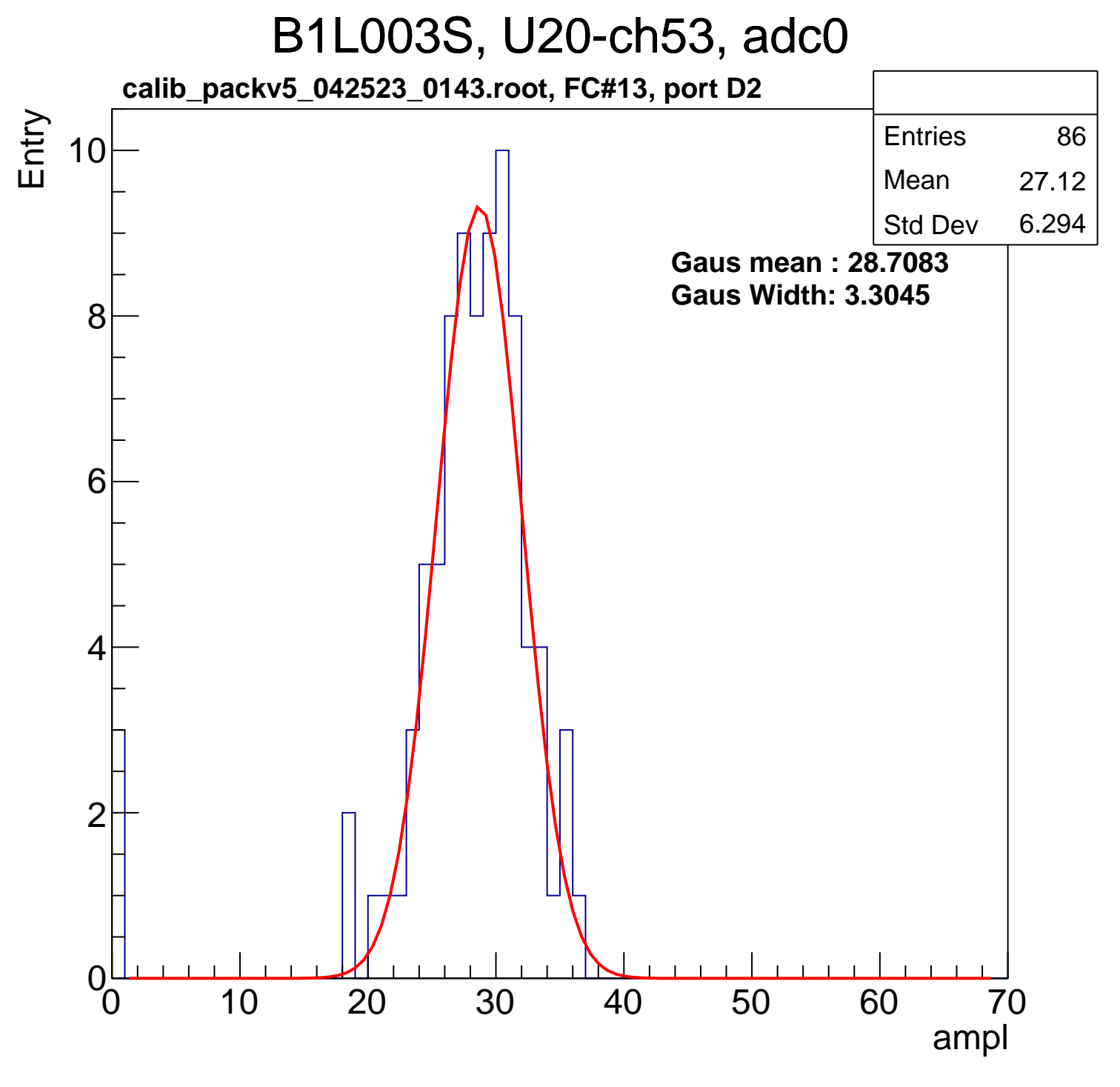
Gaus Width: 3.3045

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch53, adc1

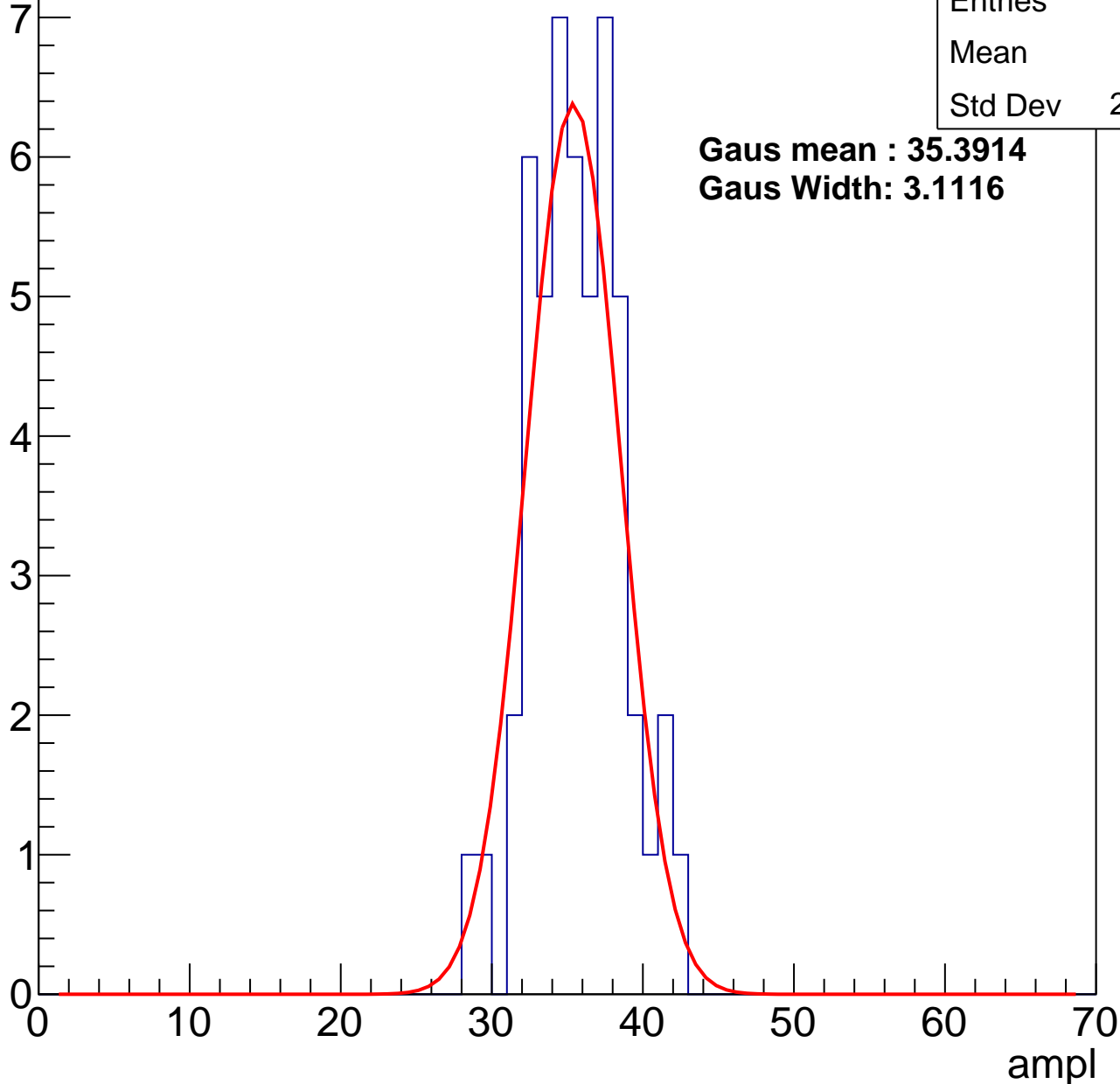
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	35.2
Std Dev	2.977

Gaus mean : 35.3914

Gaus Width: 3.1116



B1L003S, U20-ch53, adc2

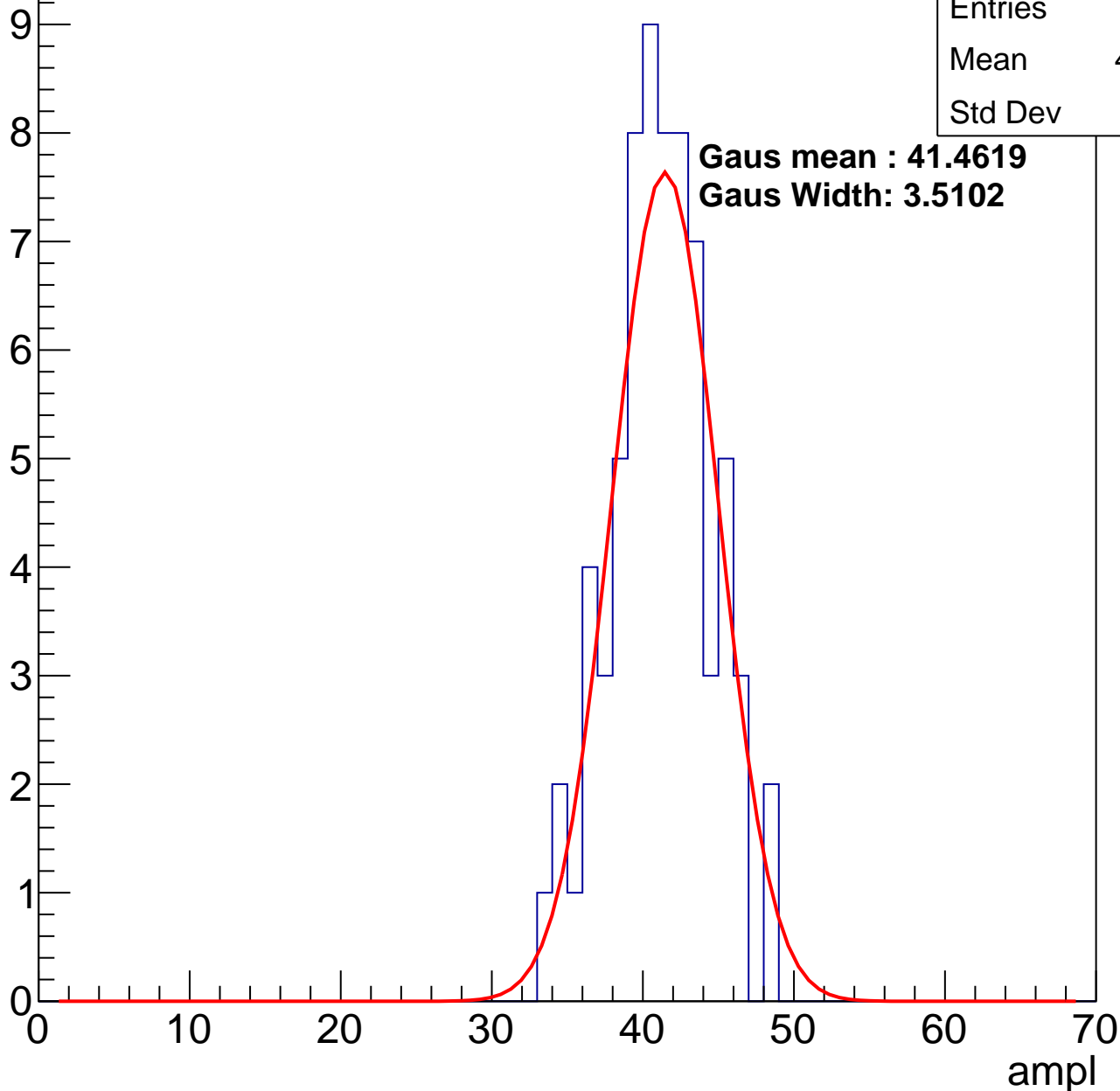
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	40.71
Std Dev	3.28

Gaus mean : 41.4619

Gaus Width: 3.5102

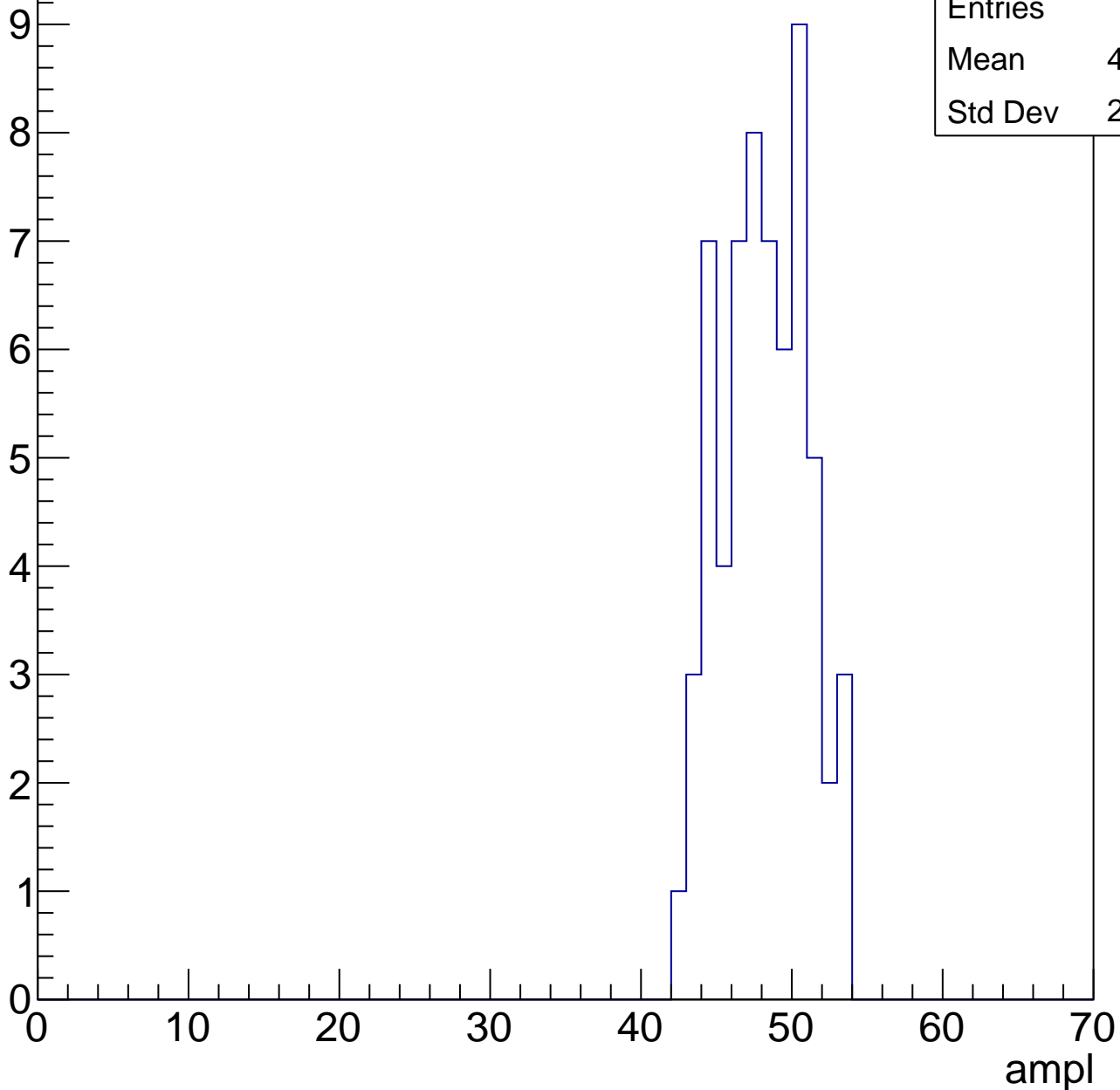


B1L003S, U20-ch53, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	47.66
Std Dev	2.788

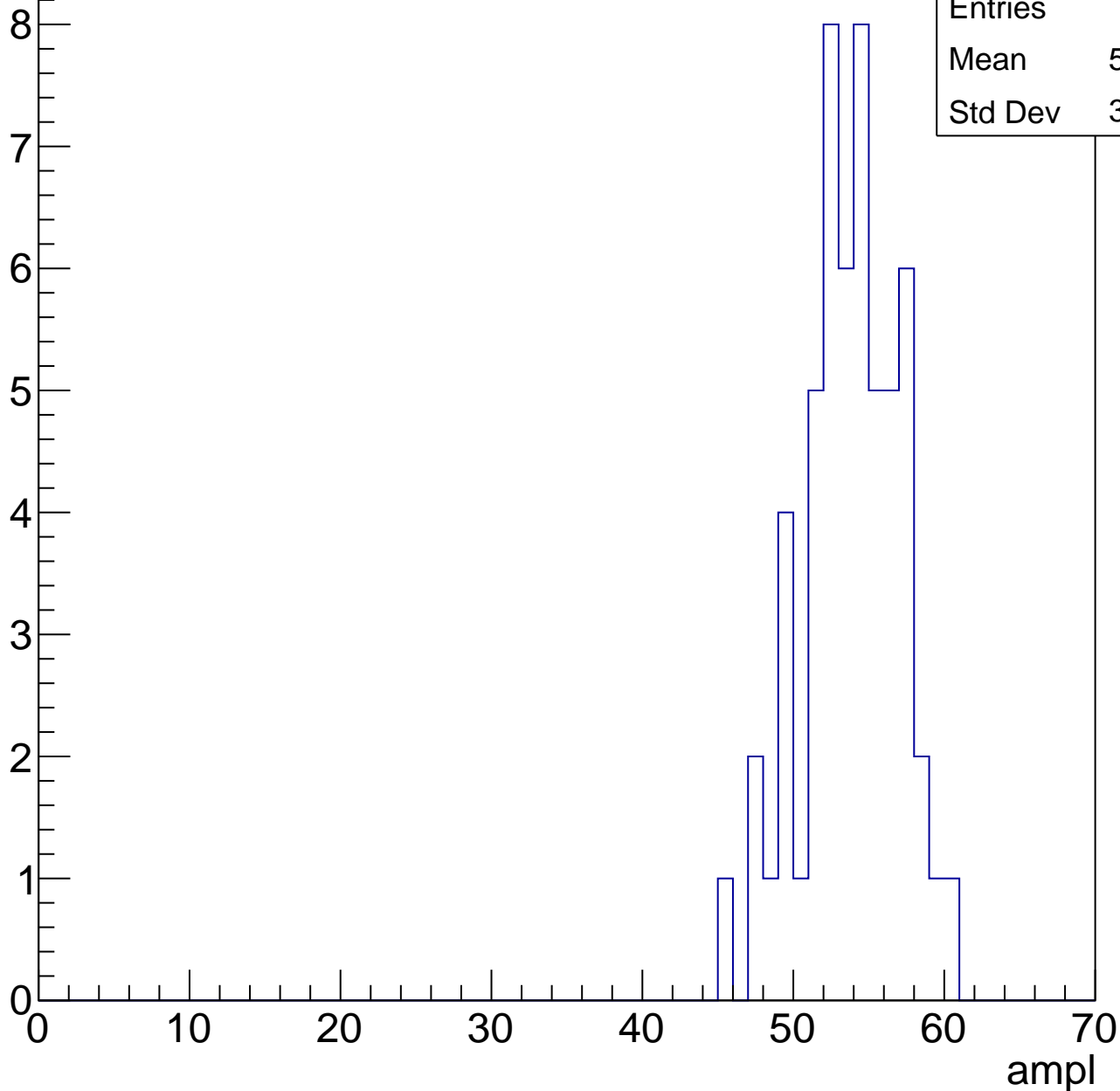


B1L003S, U20-ch53, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

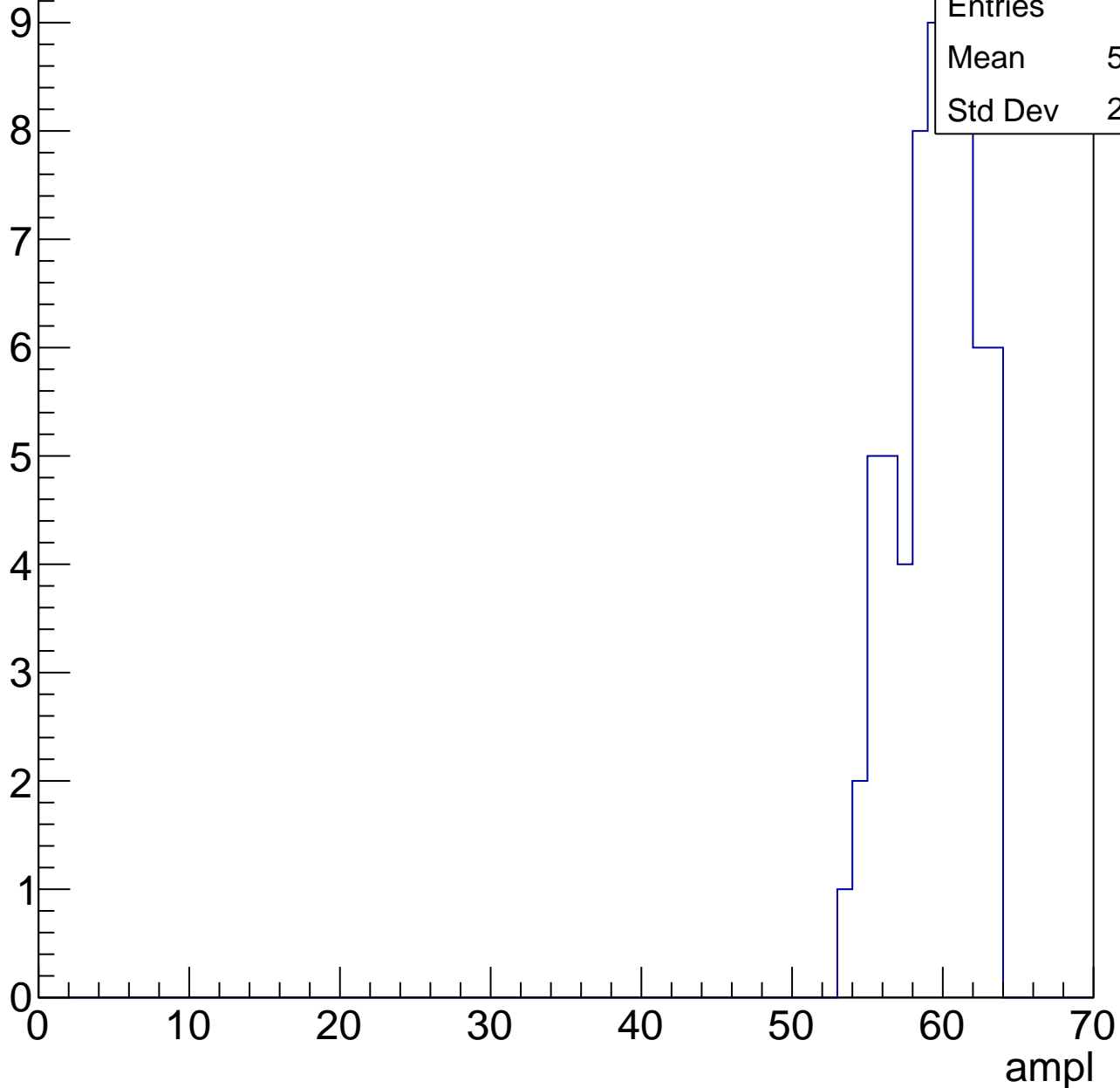
Entries	56
Mean	53.32
Std Dev	3.163



B1L003S, U20-ch53, adc5

calib_packv5_042523_0143.root, FC#13, port D2

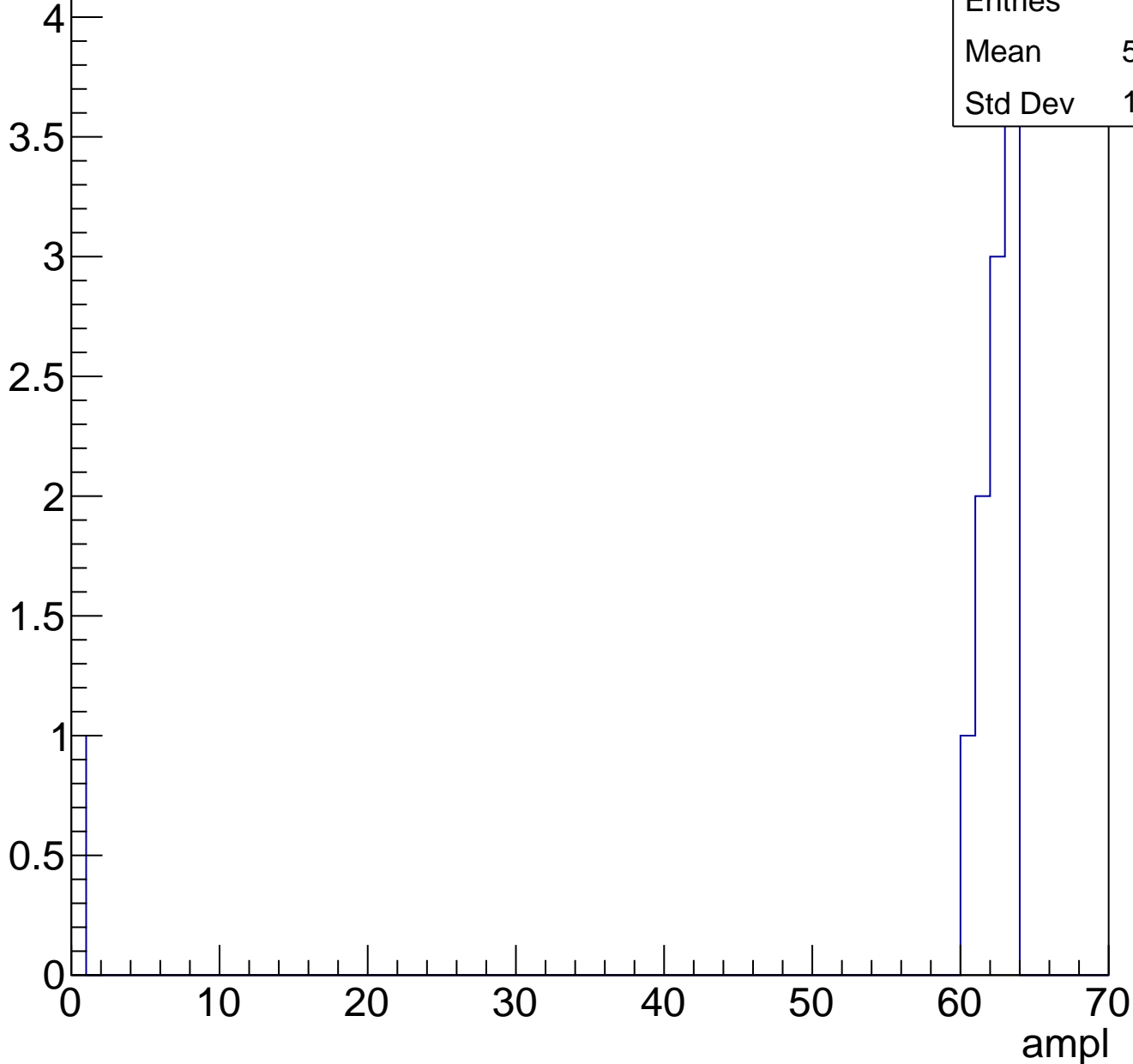
Entry



B1L003S, U20-ch53, adc6

calib_packv5_042523_0143.root, FC#13, port D2

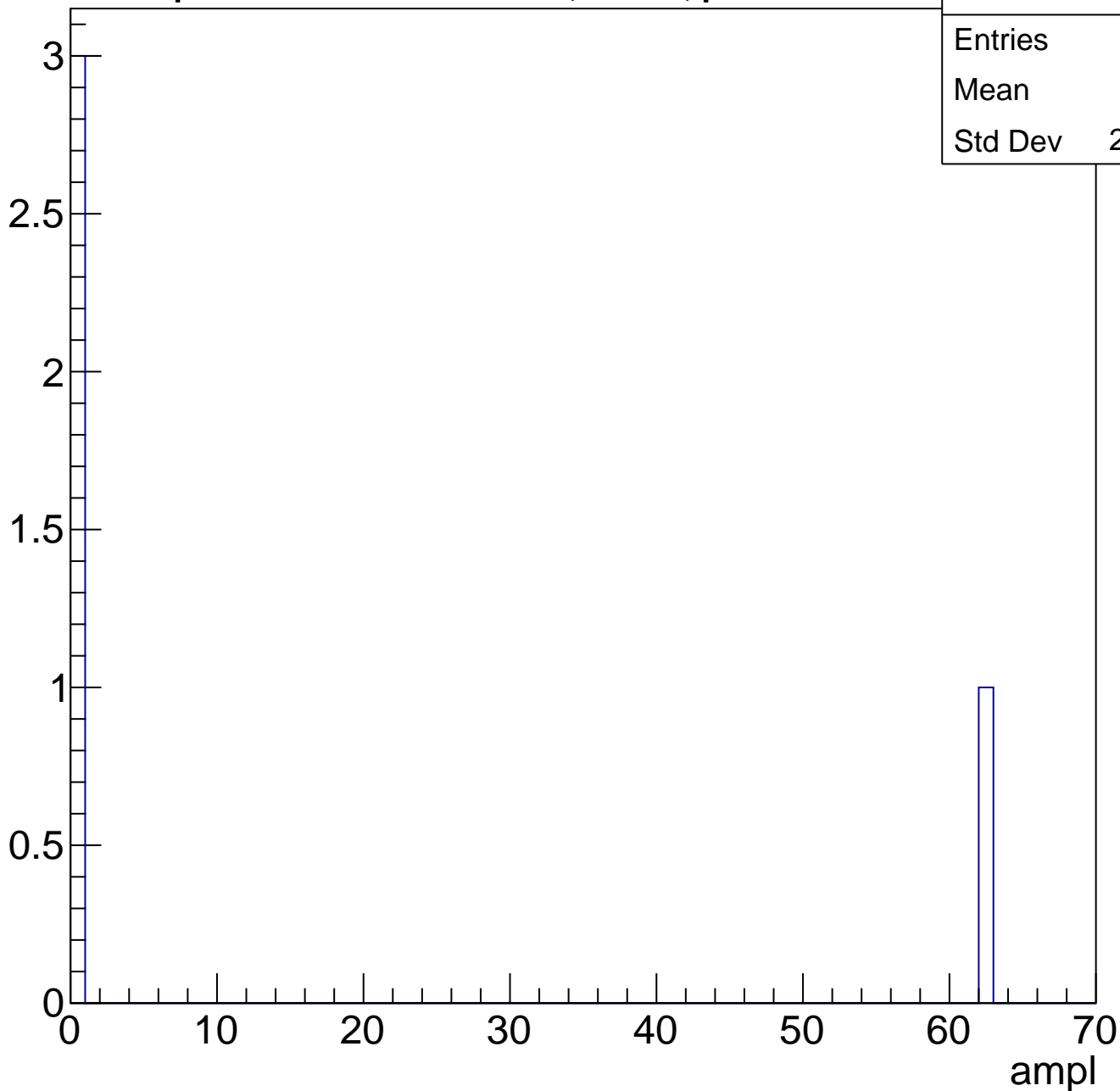
Entry



B1L003S, U20-ch53, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch54, adc0

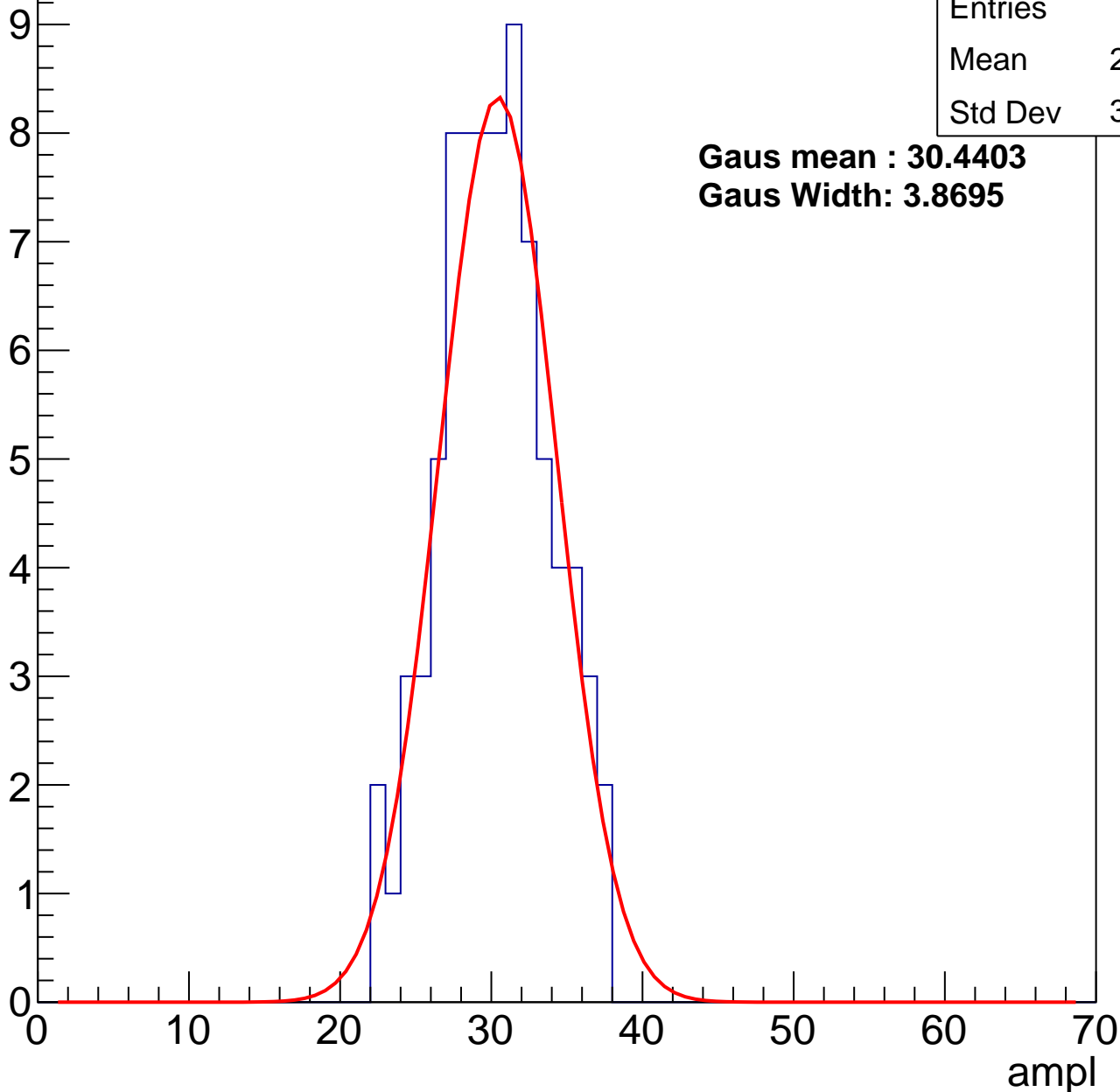
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.77
Std Dev	3.535

Gaus mean : 30.4403

Gaus Width: 3.8695



B1L003S, U20-ch54, adc1

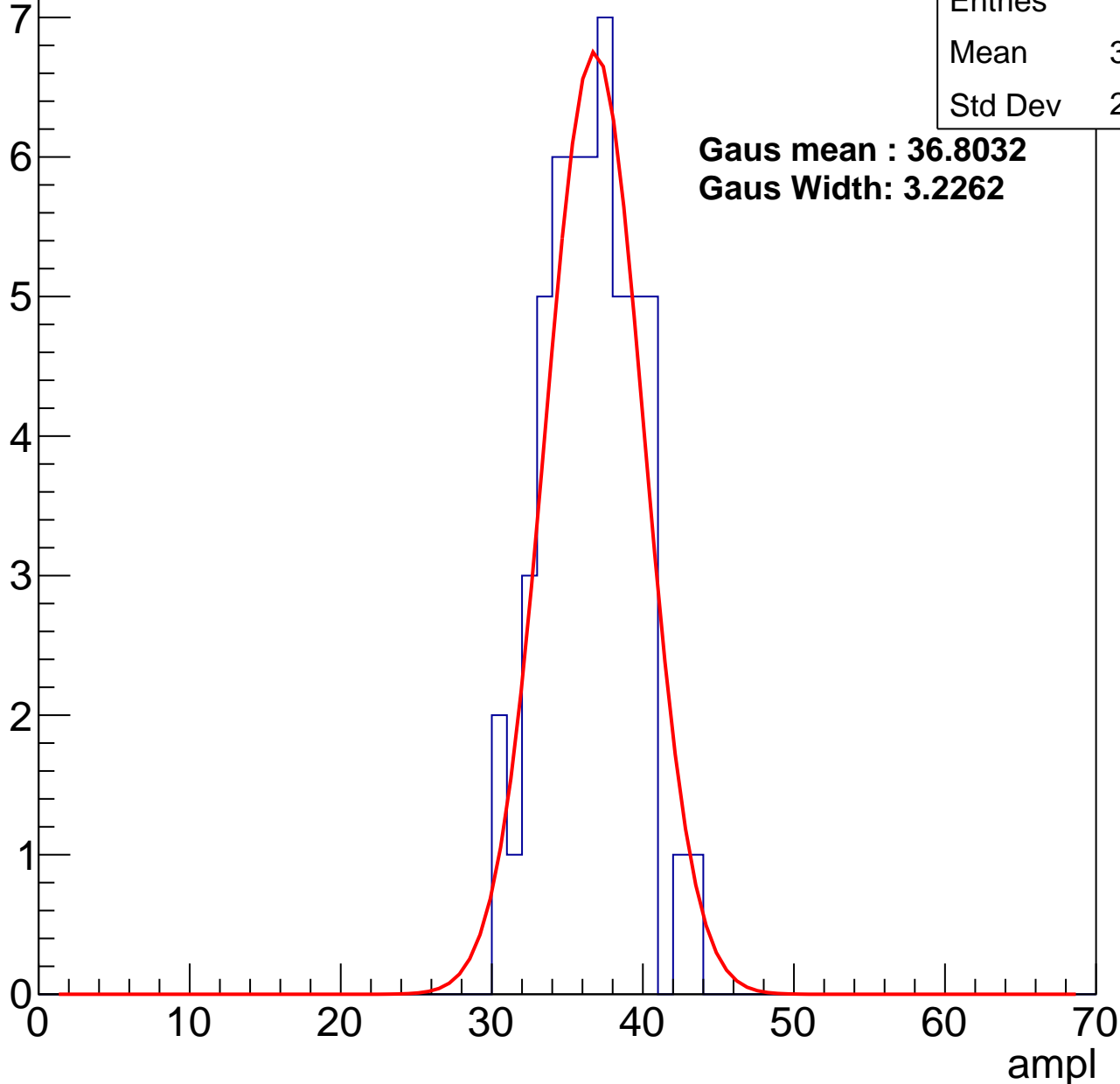
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.06
Std Dev	2.936

Gaus mean : 36.8032

Gaus Width: 3.2262



B1L003S, U20-ch54, adc2

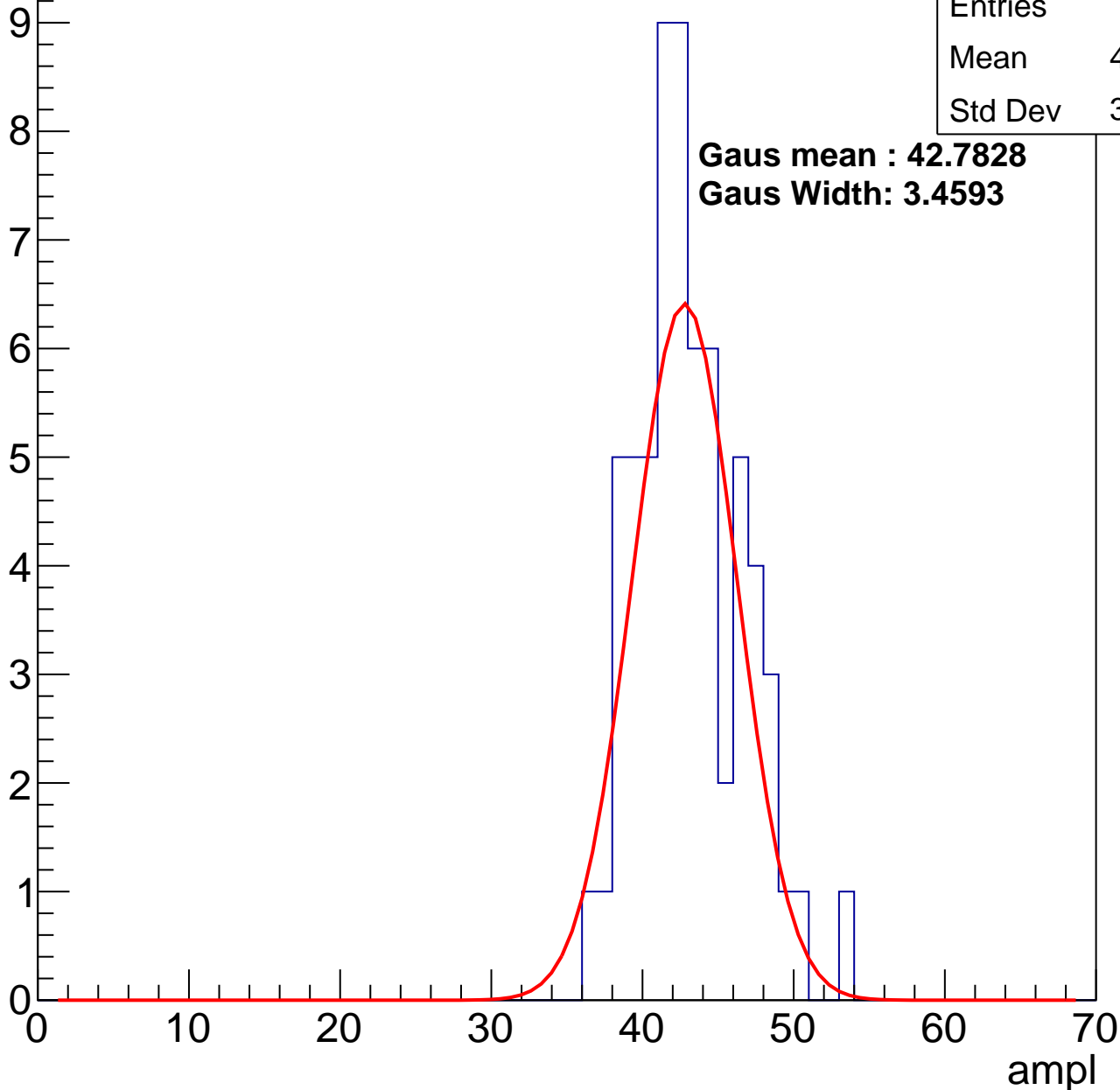
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.67
Std Dev	3.437

Gaus mean : 42.7828

Gaus Width: 3.4593

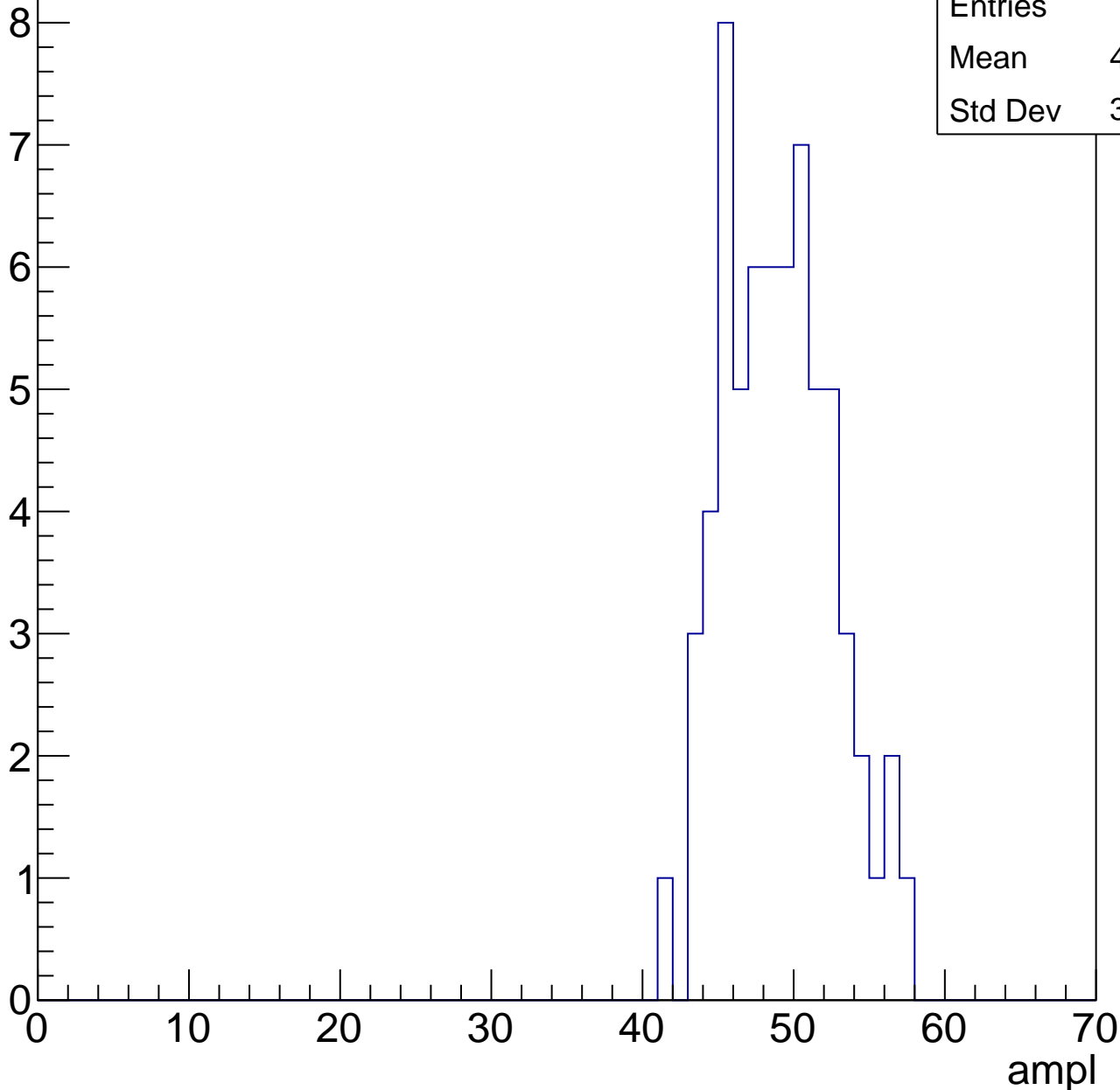


B1L003S, U20-ch54, adc3

calib_packv5_042523_0143.root, FC#13, port D2

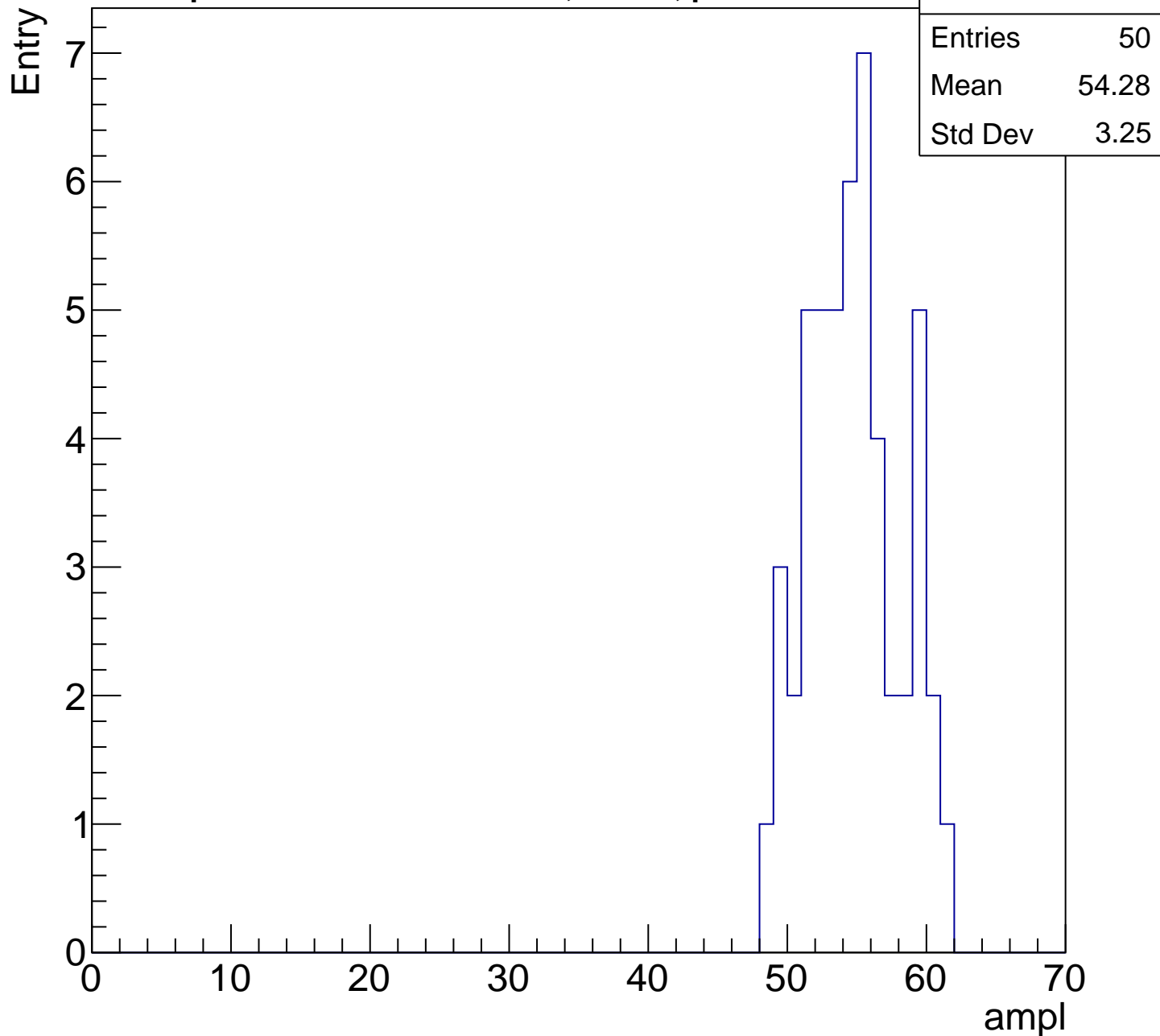
Entry

Entries	65
Mean	48.55
Std Dev	3.574



B1L003S, U20-ch54, adc4

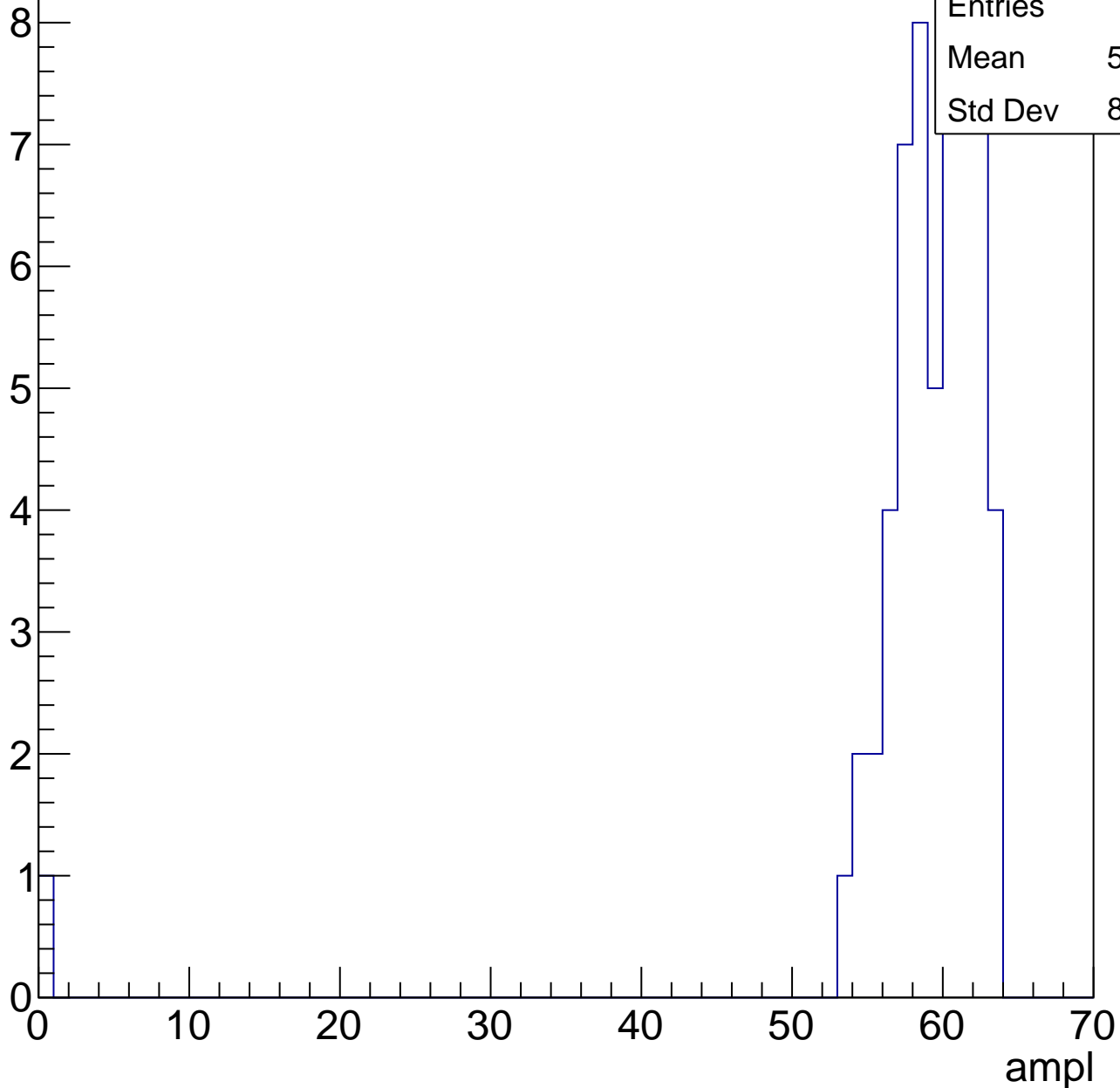
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch54, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

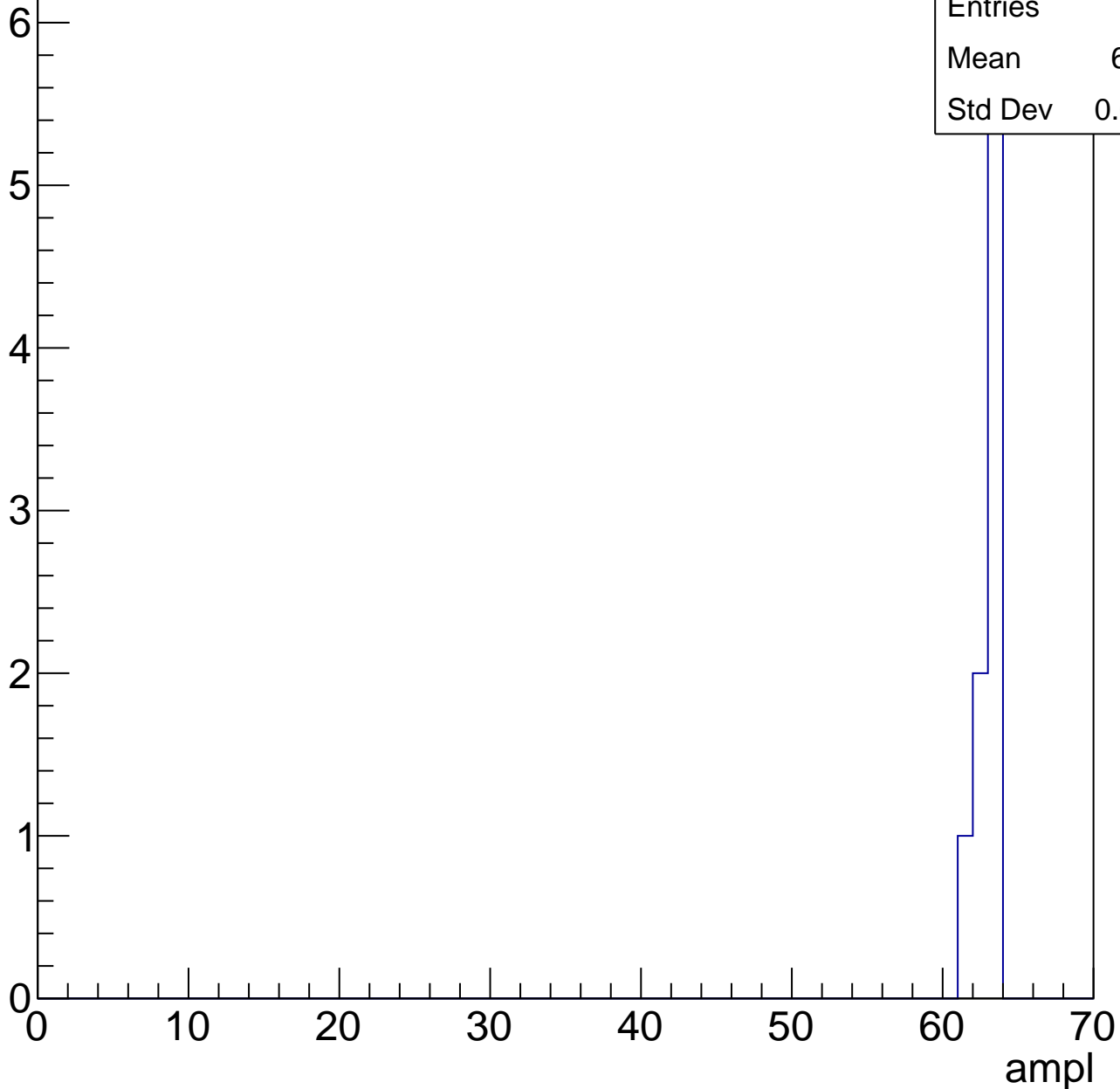


B1L003S, U20-ch54, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	62.56
Std Dev	0.6849



B1L003S, U20-ch54, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch55, adc0

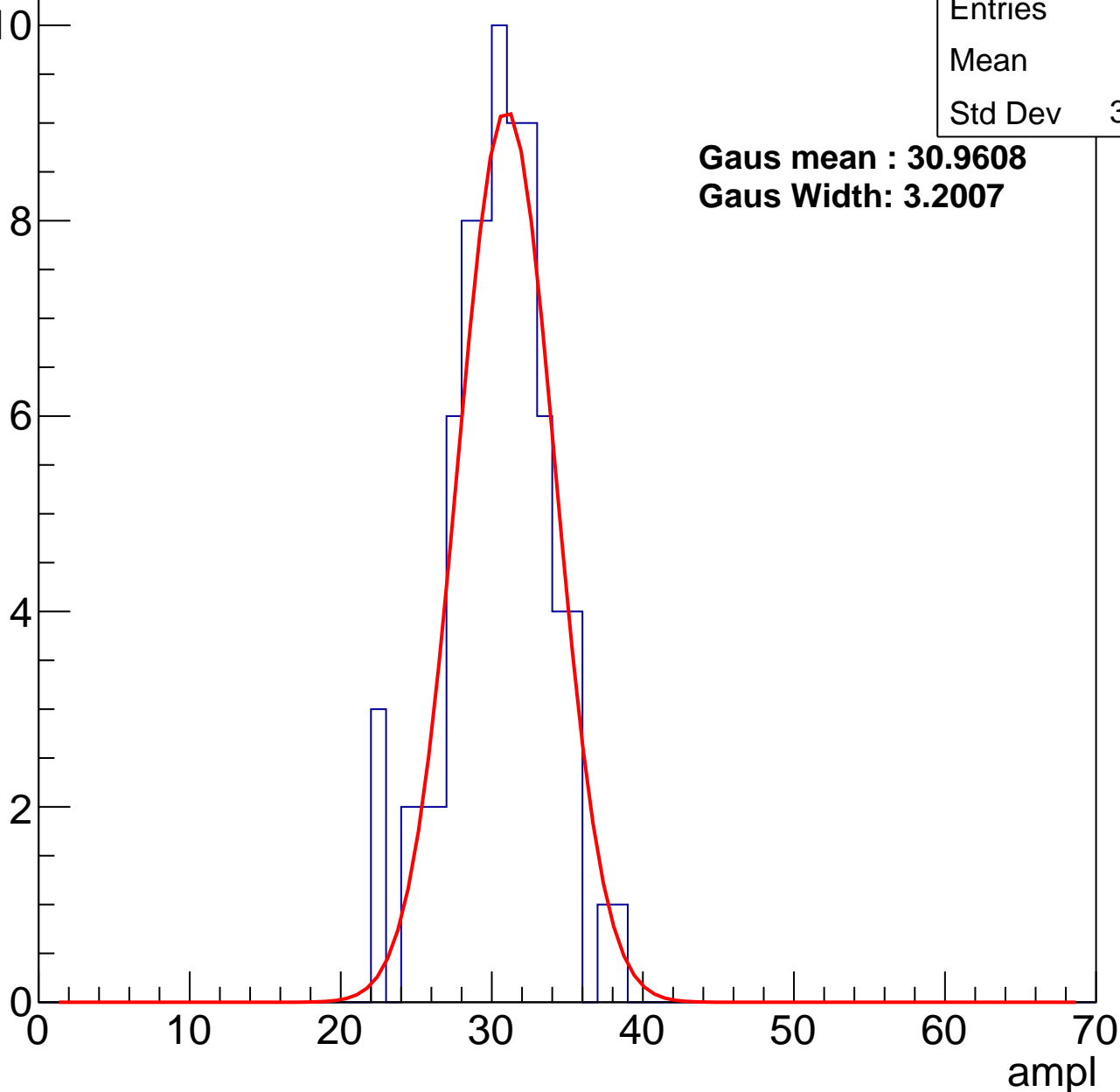
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30
Std Dev	3.298

Gaus mean : 30.9608

Gaus Width: 3.2007



B1L003S, U20-ch55, adc1

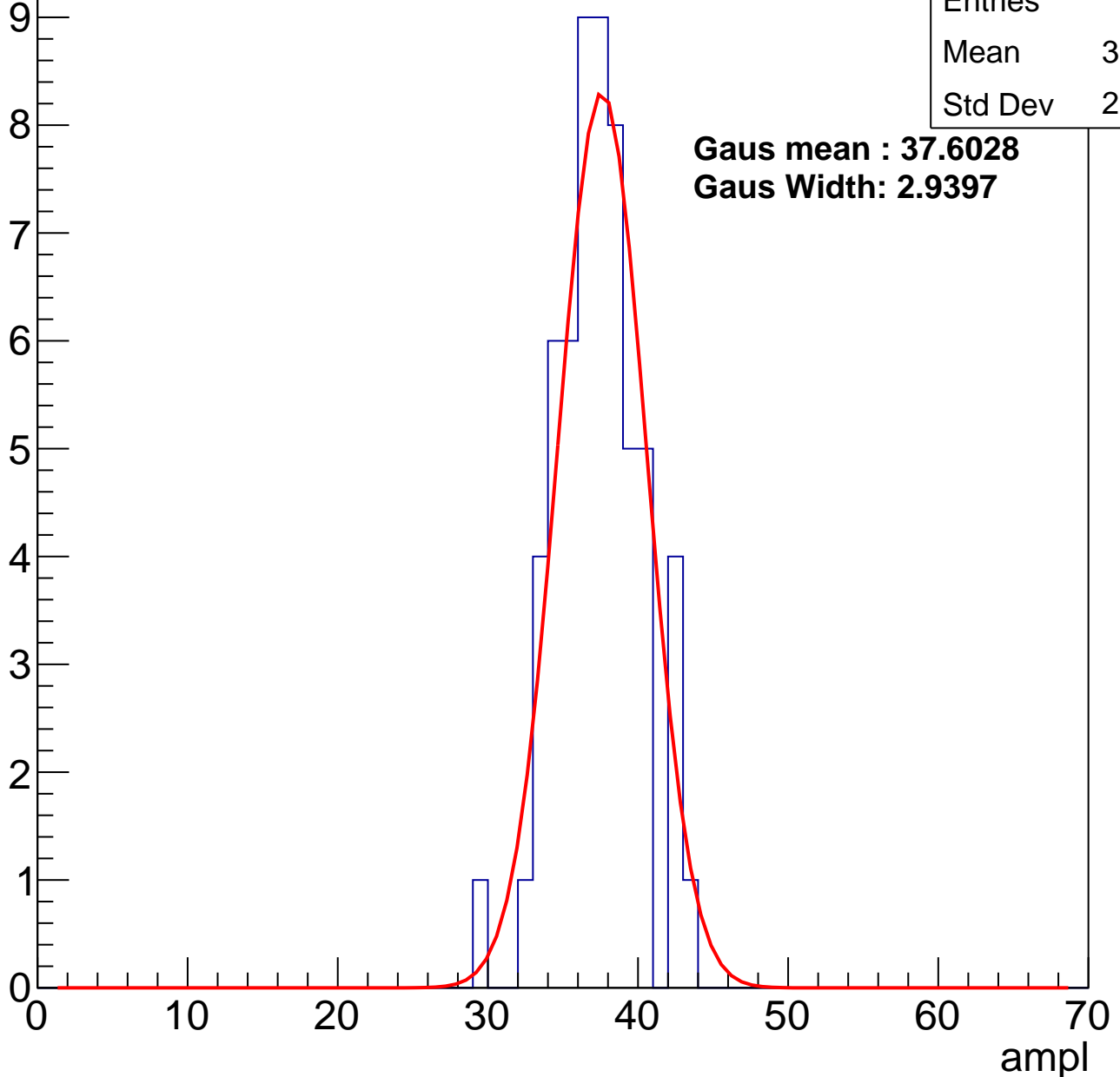
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.85
Std Dev	2.754

Gaus mean : 37.6028

Gaus Width: 2.9397



B1L003S, U20-ch55, adc2

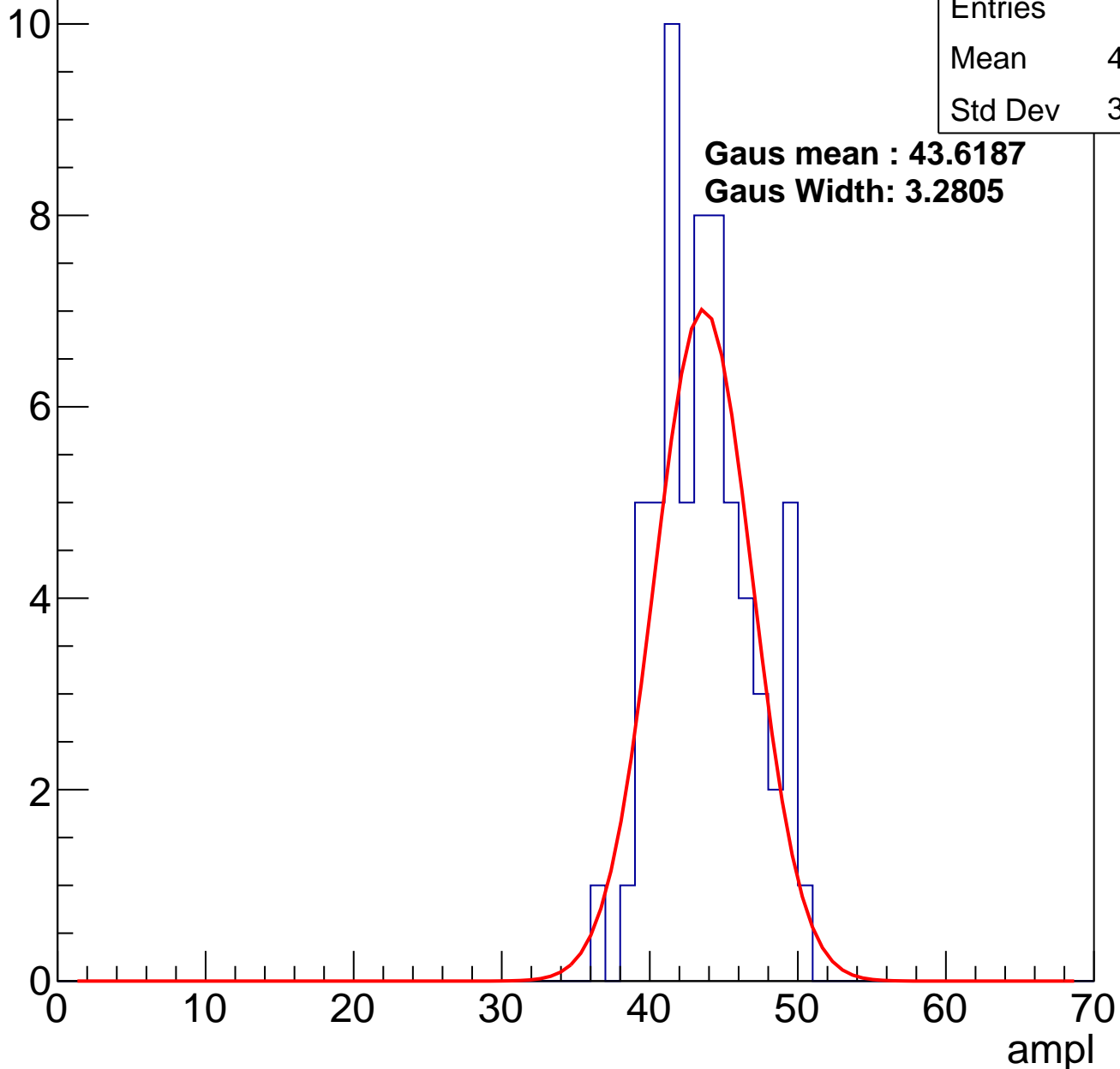
calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	43.27
Std Dev	3.163

Gaus mean : 43.6187

Gaus Width: 3.2805

Entry

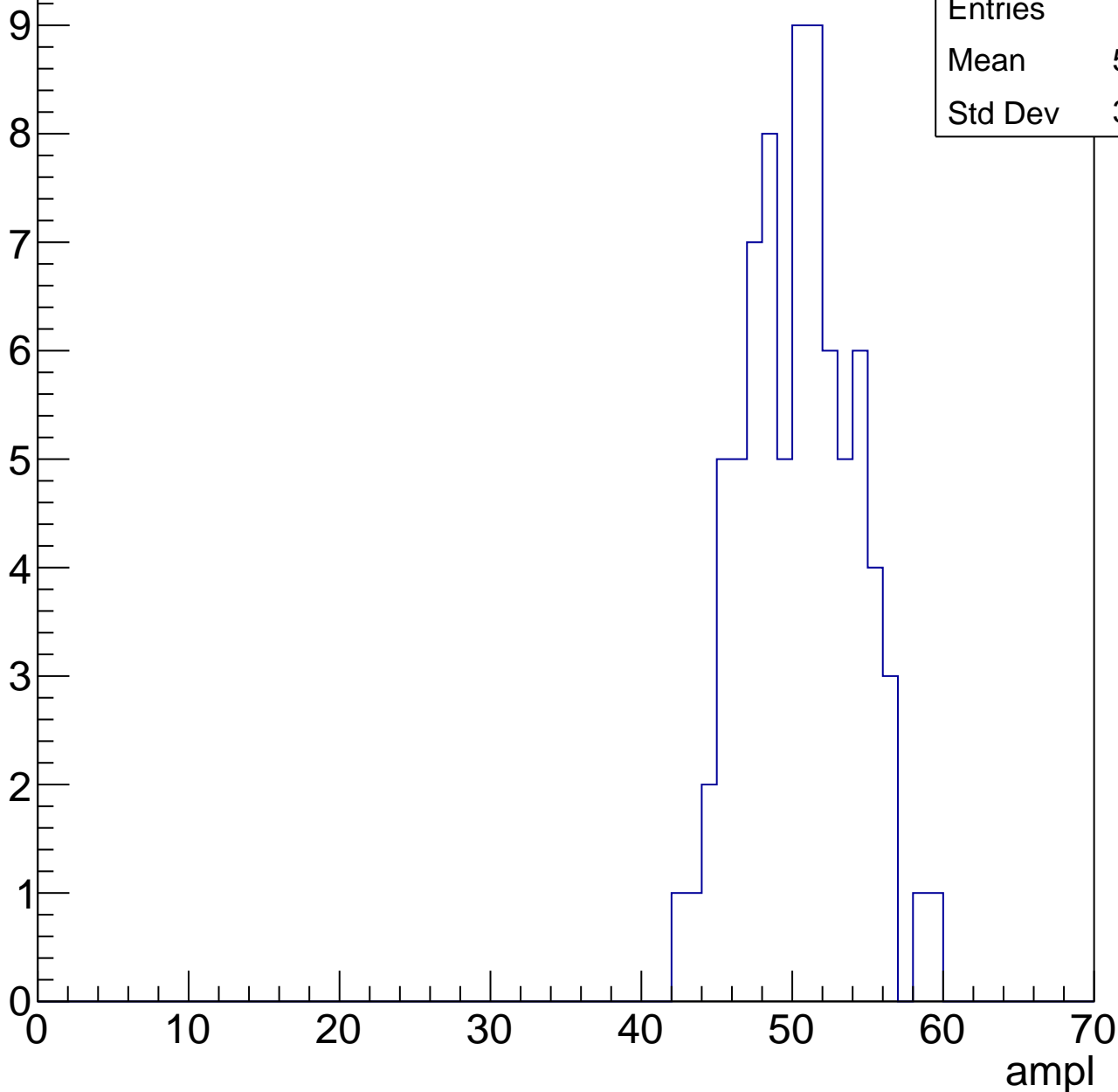


B1L003S, U20-ch55, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	50.01
Std Dev	3.611

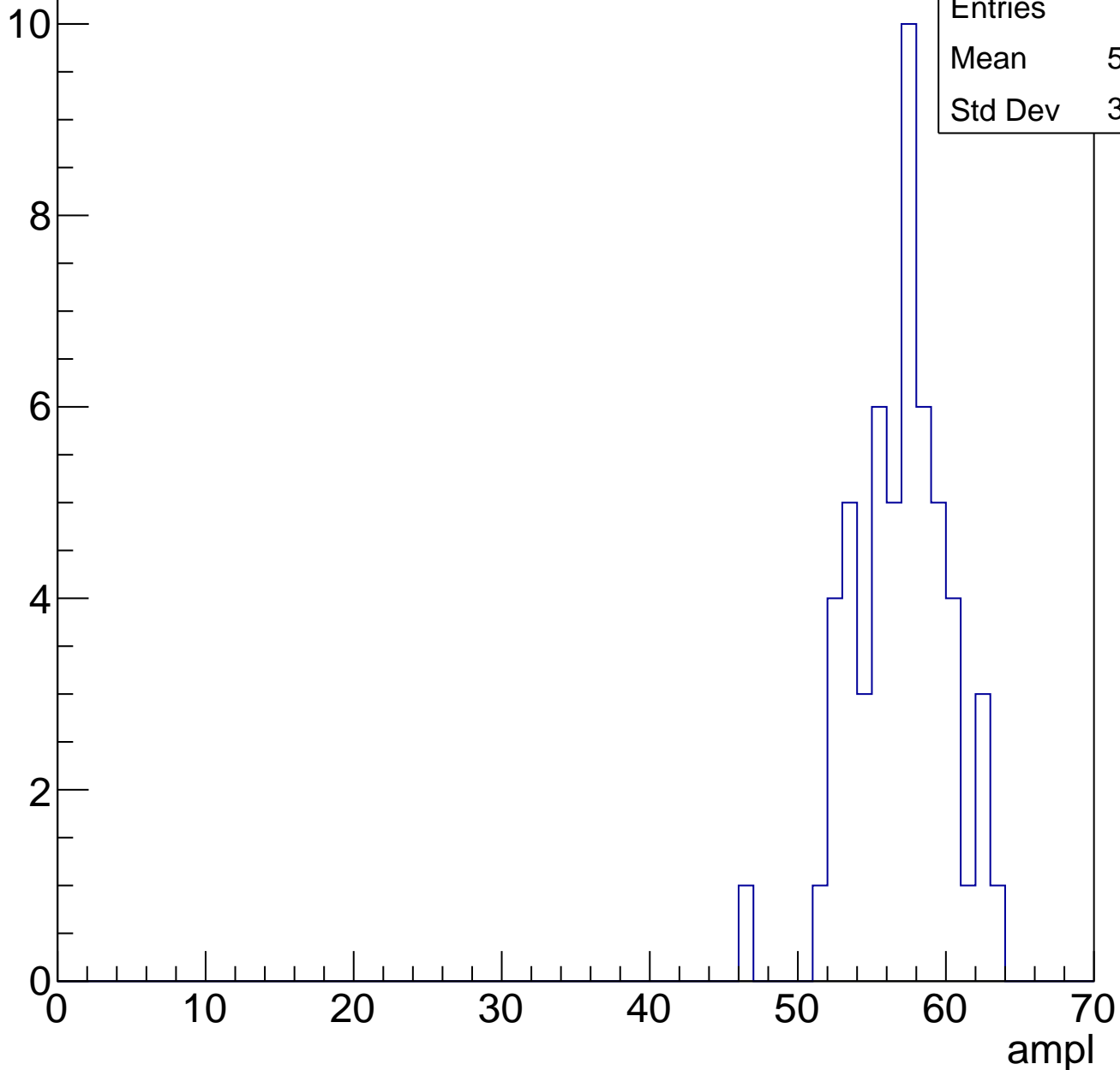


B1L003S, U20-ch55, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	56.45
Std Dev	3.207

Entry

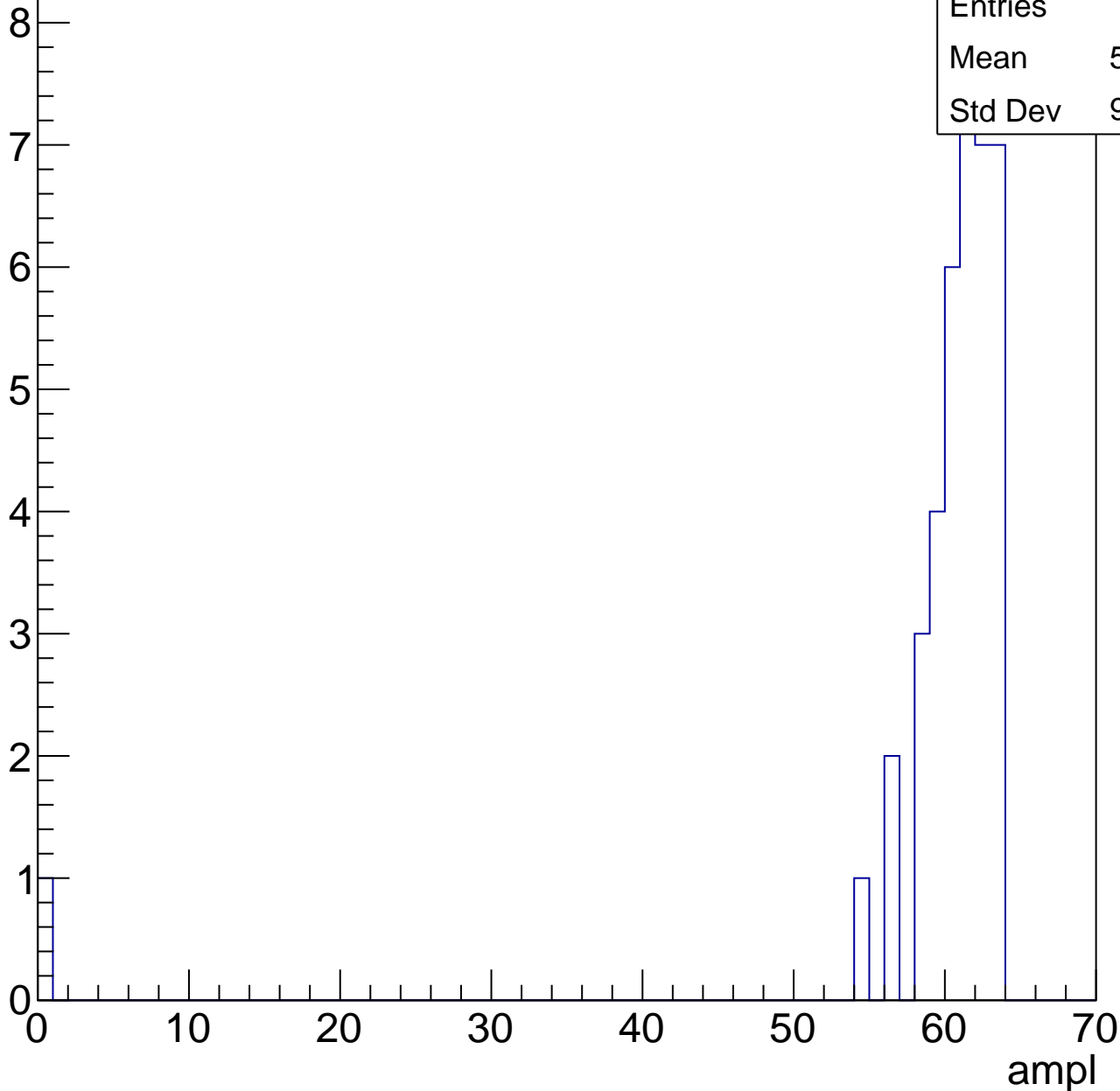


B1L003S, U20-ch55, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.95
Std Dev	9.793



B1L003S, U20-ch55, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch55, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch56, adc0

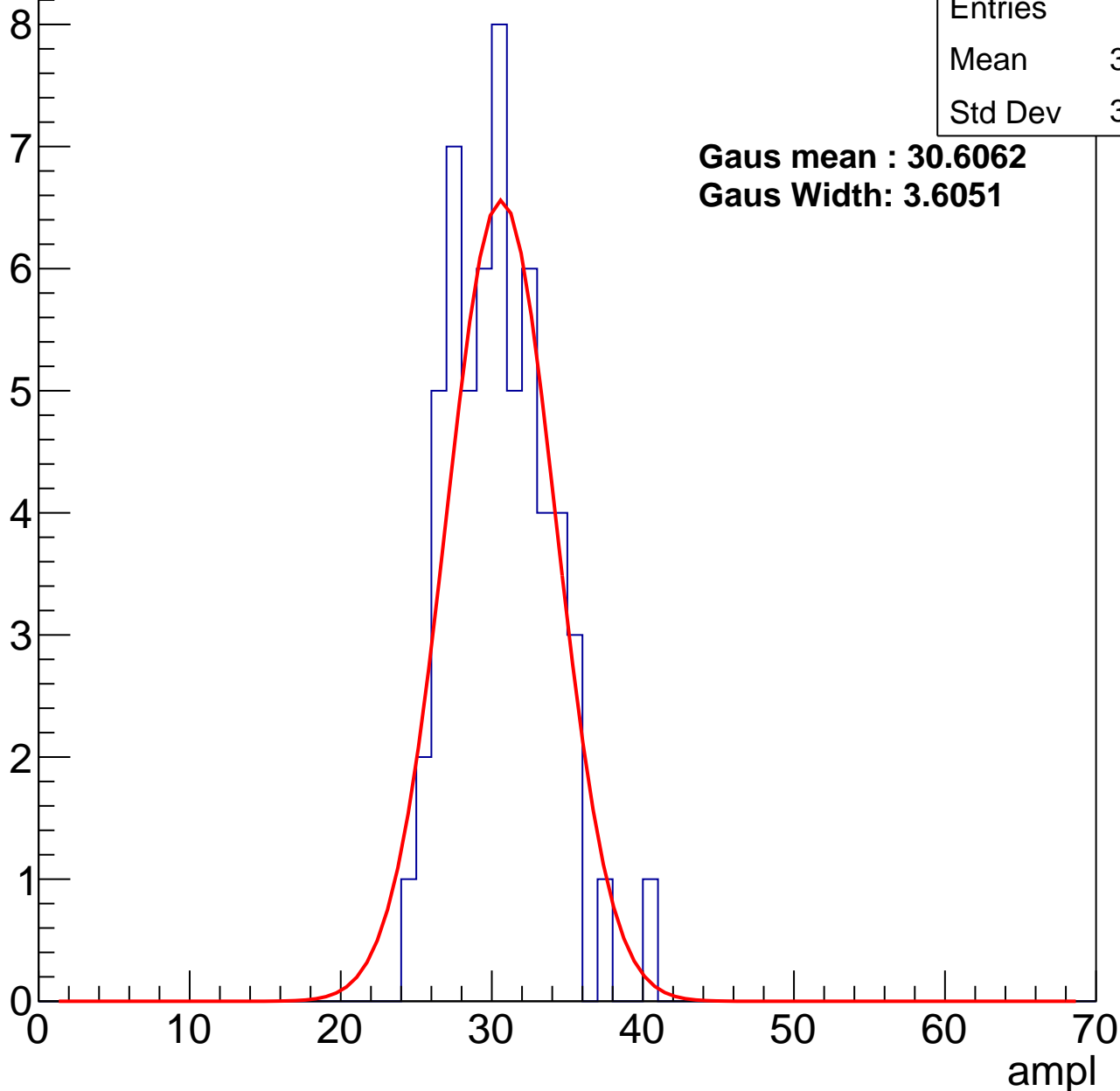
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.07
Std Dev	3.237

Gaus mean : 30.6062

Gaus Width: 3.6051



B1L003S, U20-ch56, adc1

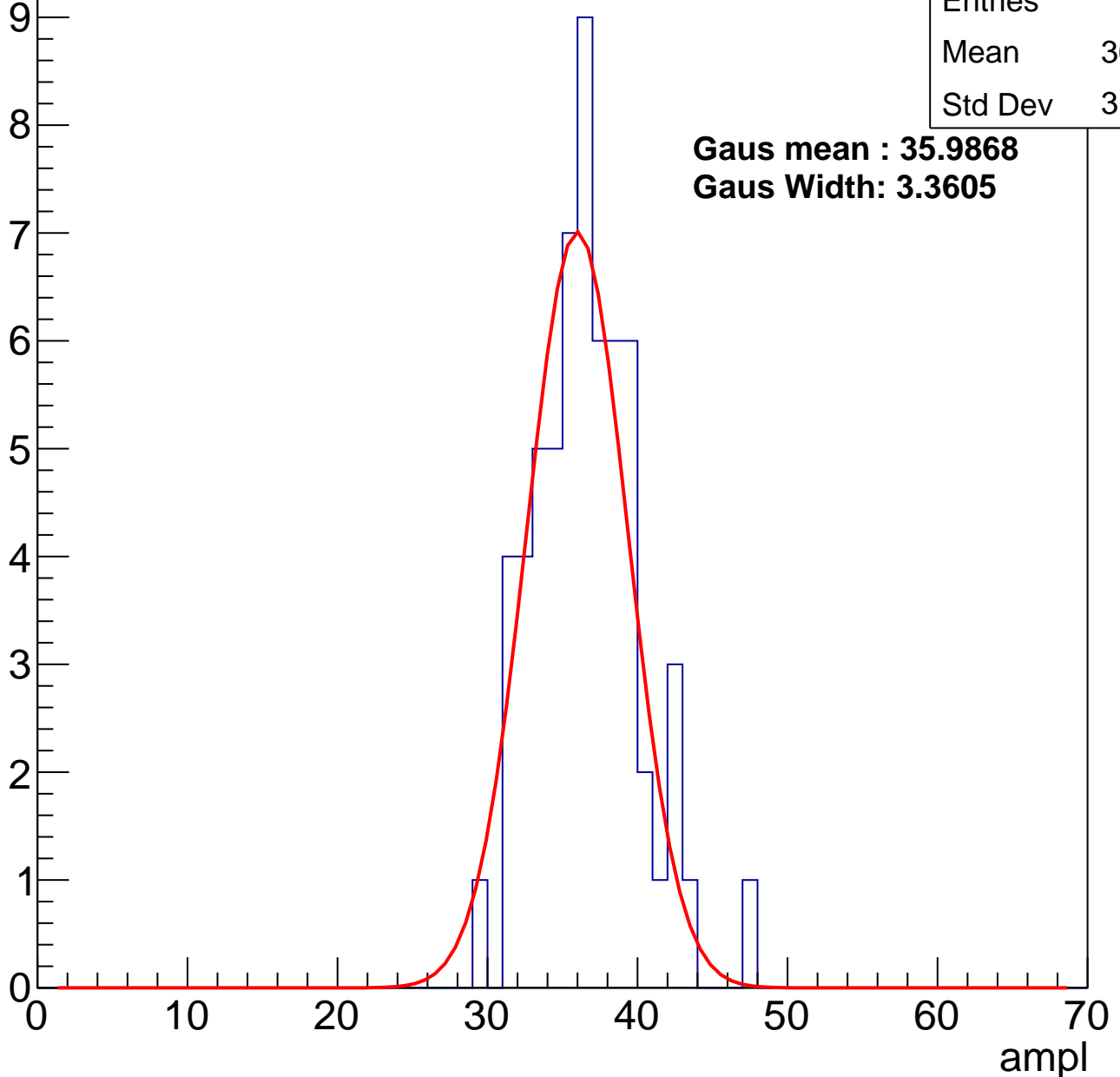
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.16
Std Dev	3.393

Gaus mean : 35.9868

Gaus Width: 3.3605



B1L003S, U20-ch56, adc2

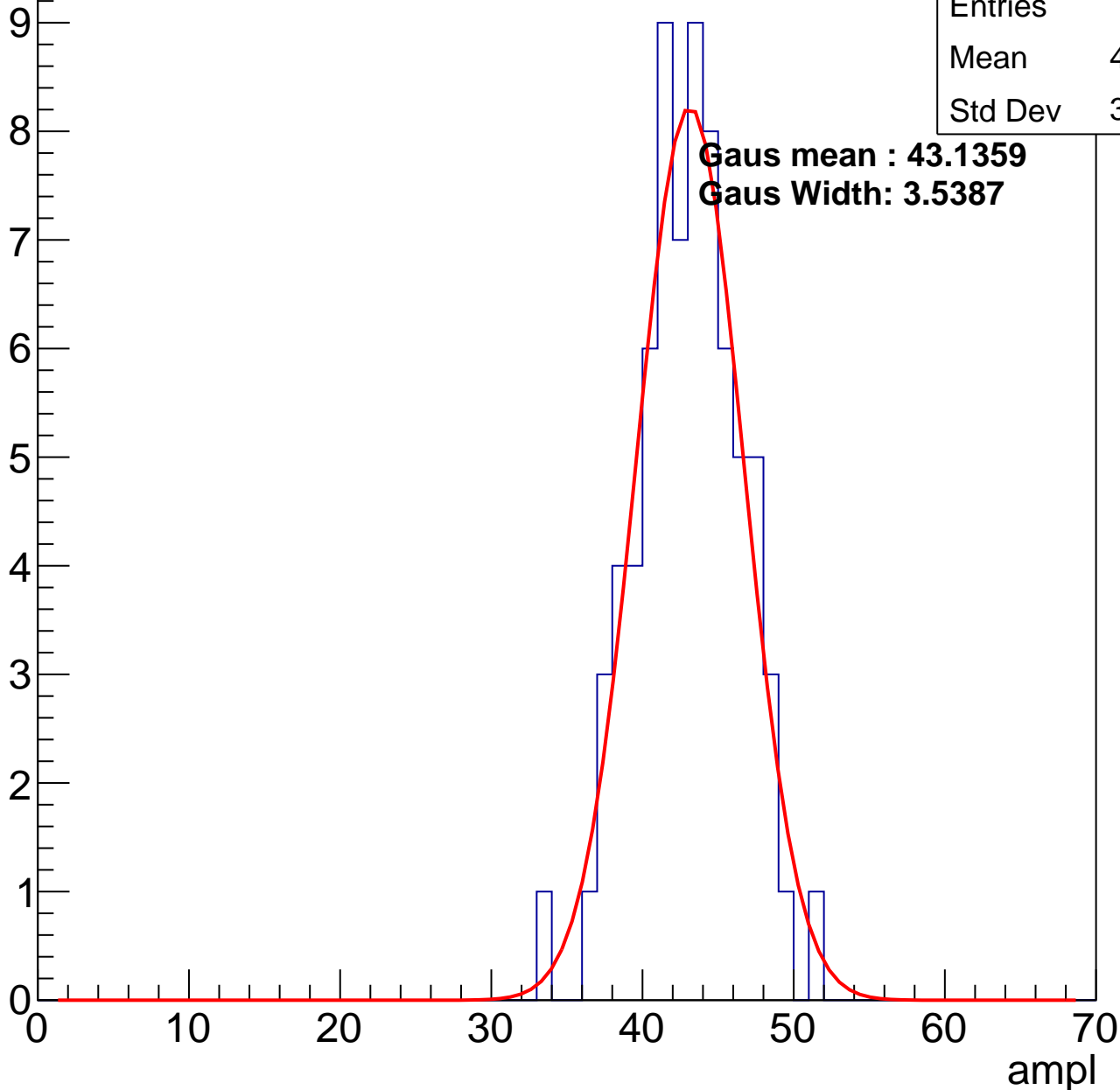
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.59
Std Dev	3.387

Gaus mean : 43.1359

Gaus Width: 3.5387

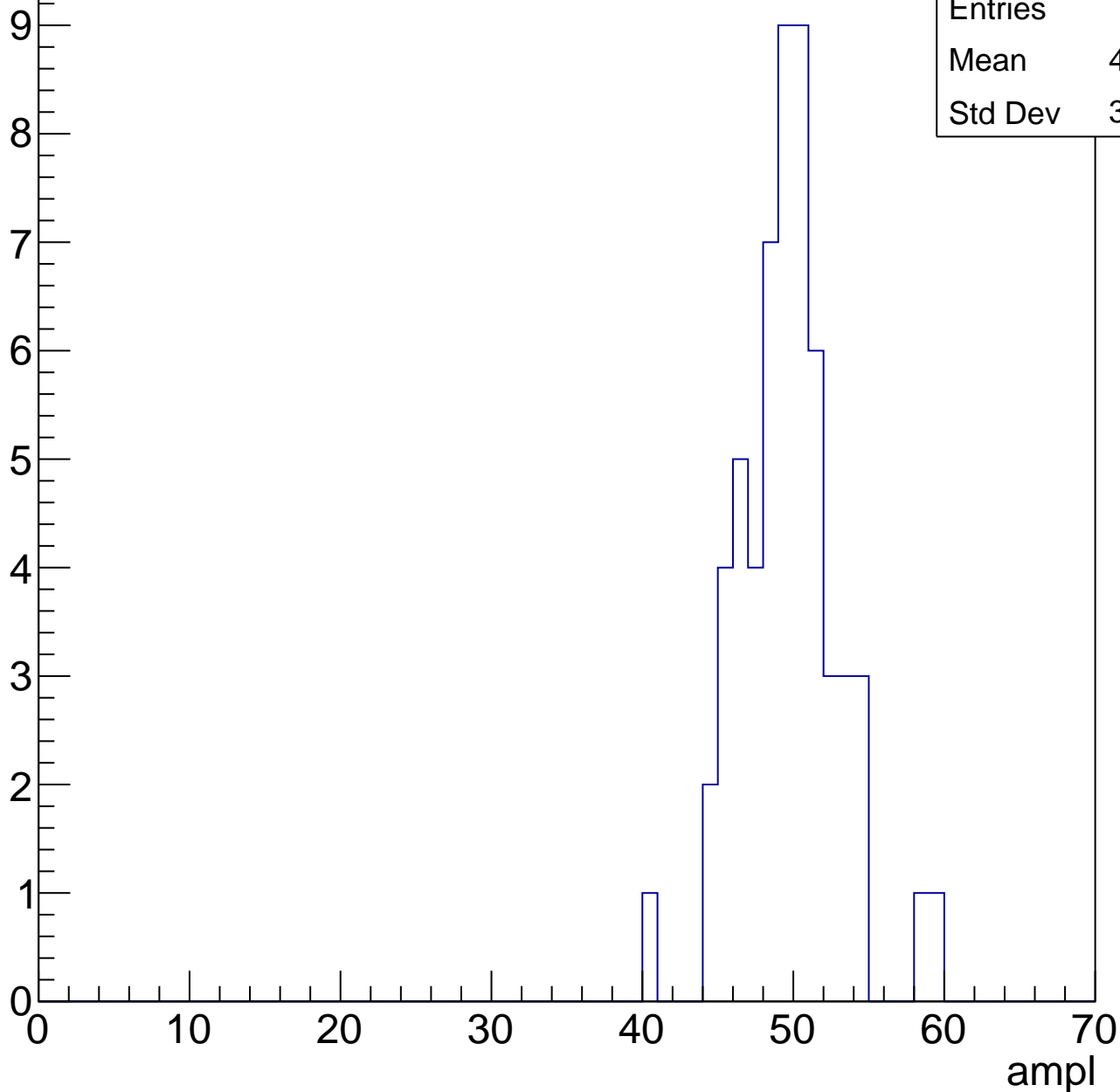


B1L003S, U20-ch56, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	49.19
Std Dev	3.282

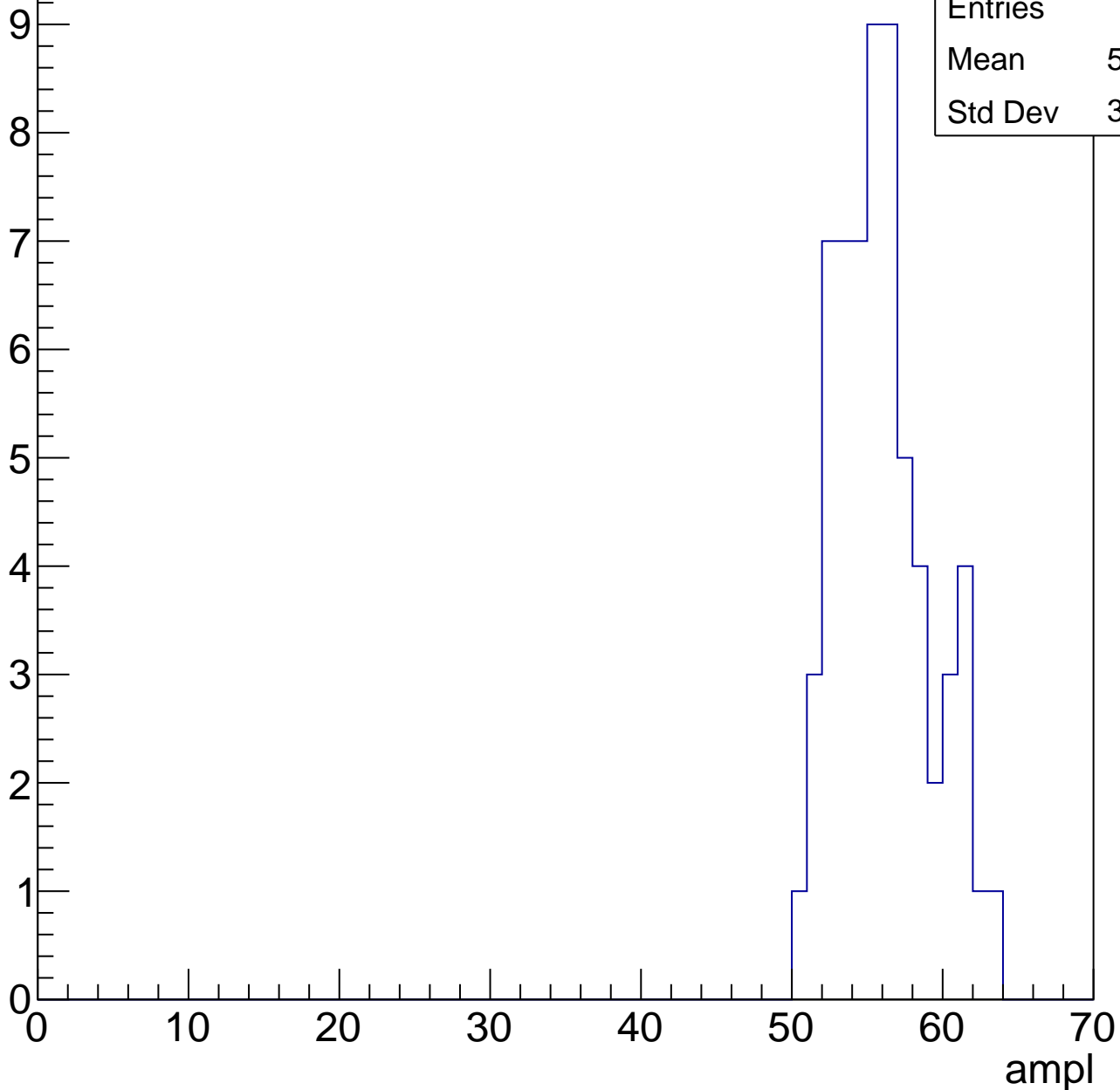


B1L003S, U20-ch56, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	55.54
Std Dev	3.039



B1L003S, U20-ch56, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	58.52
Std Dev	9.174

ampl

0

10

20

30

40

50

60

70

B1L003S, U20-ch56, adc6

calib_packv5_042523_0143.root, FC#13, port D2

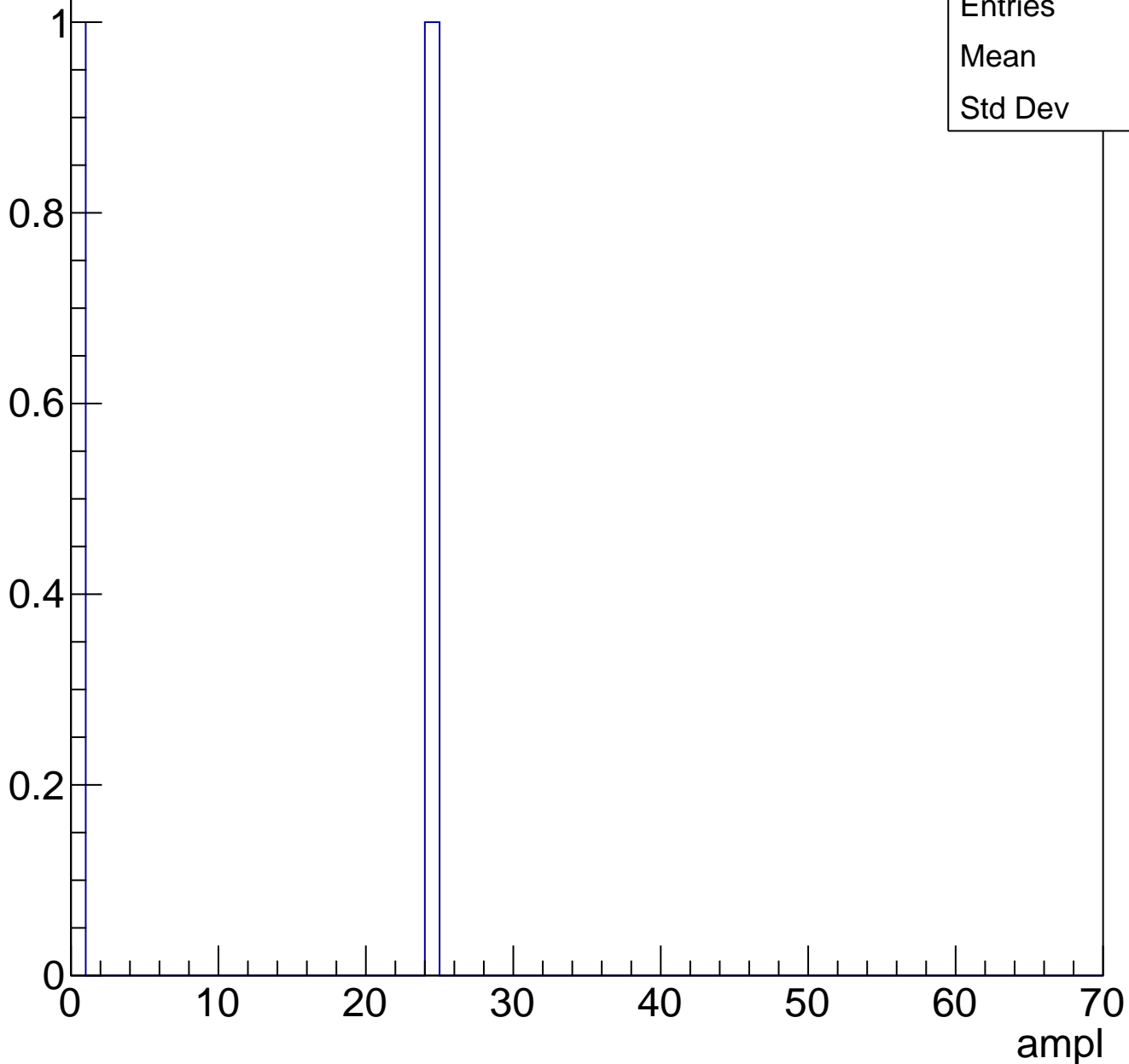
Entry



B1L003S, U20-ch56, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch57, adc0

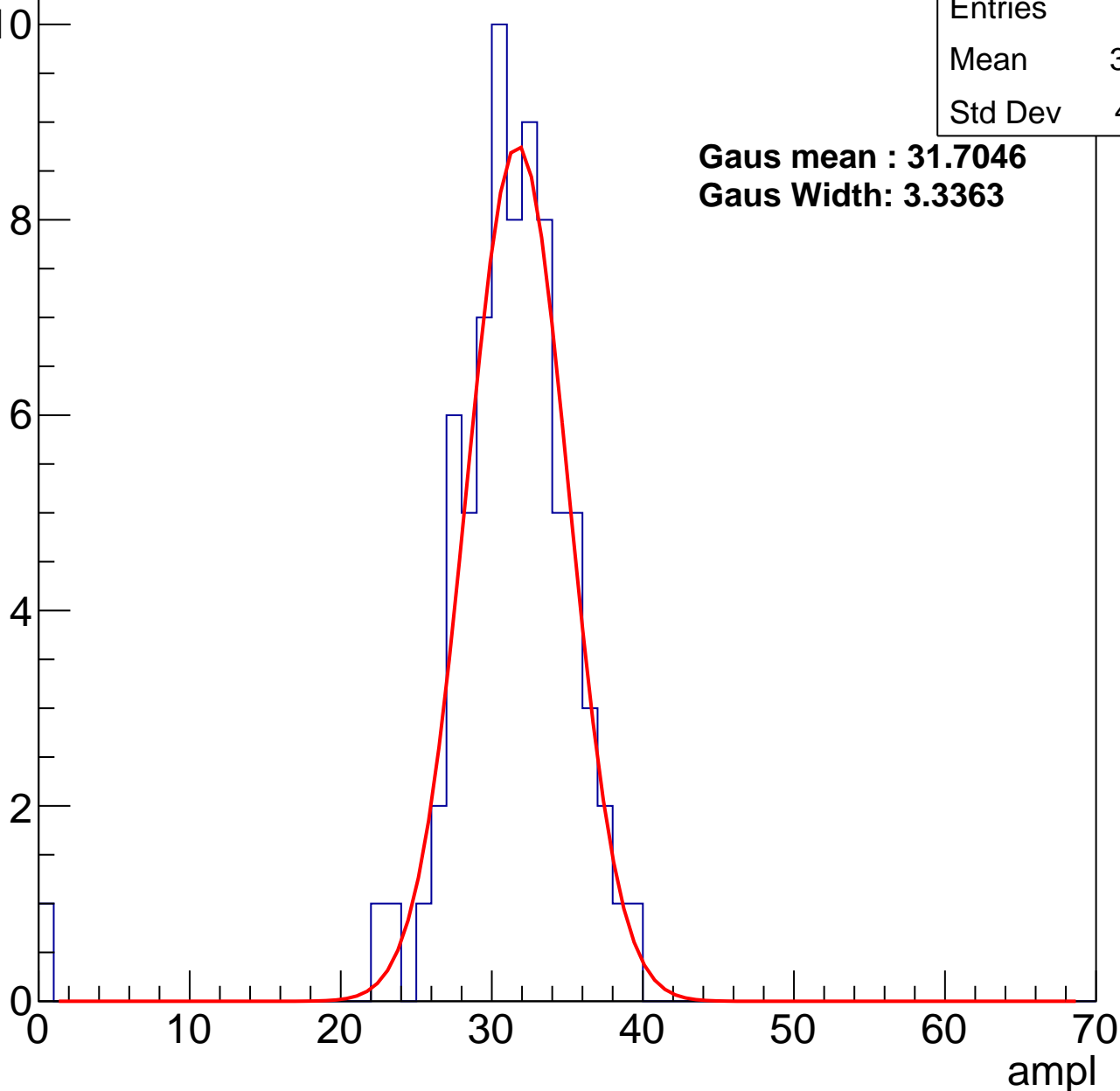
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	30.67
Std Dev	4.851

Gaus mean : 31.7046

Gaus Width: 3.3363



B1L003S, U20-ch57, adc1

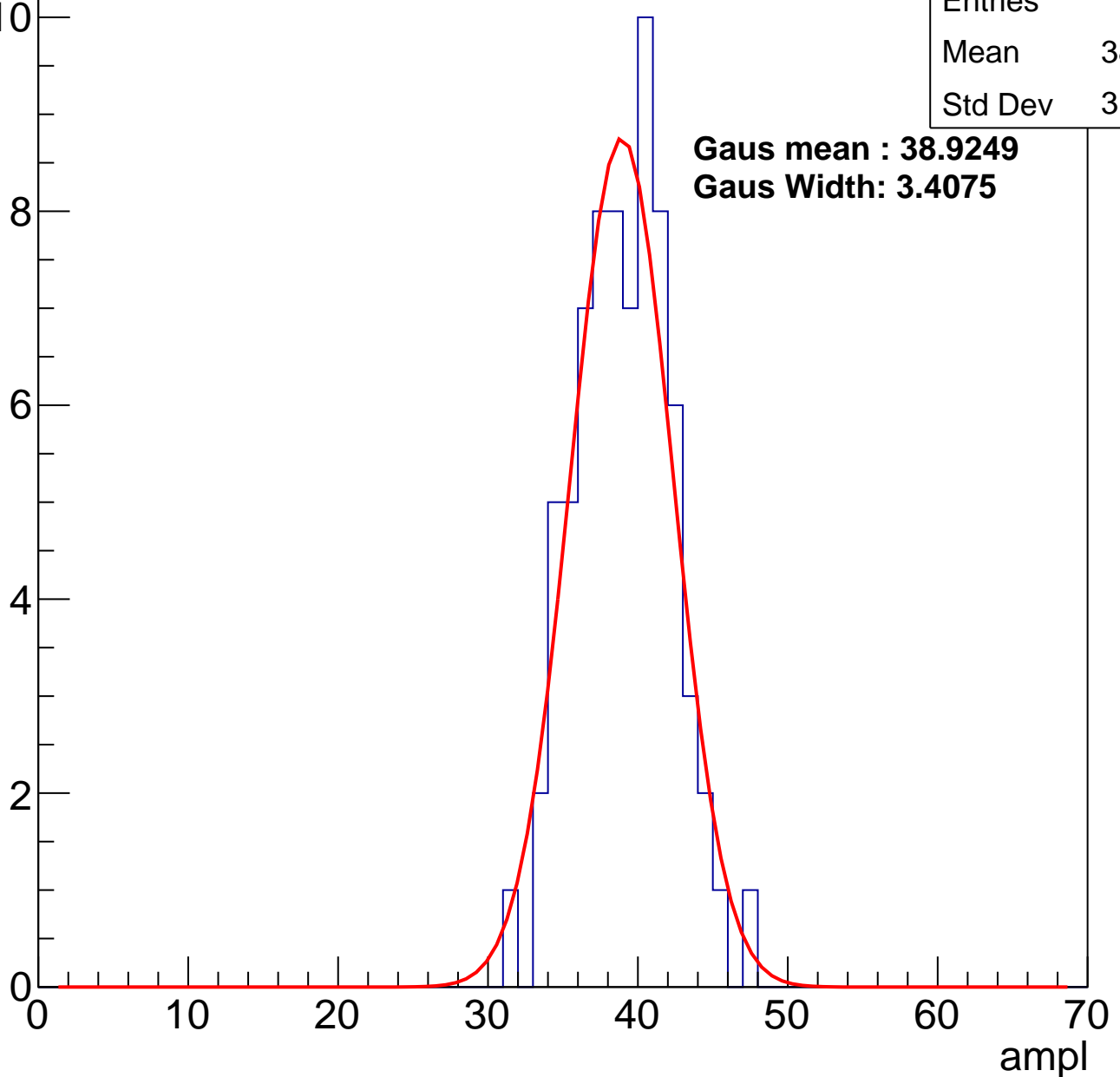
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	38.59
Std Dev	3.132

Gaus mean : 38.9249

Gaus Width: 3.4075



B1L003S, U20-ch57, adc2

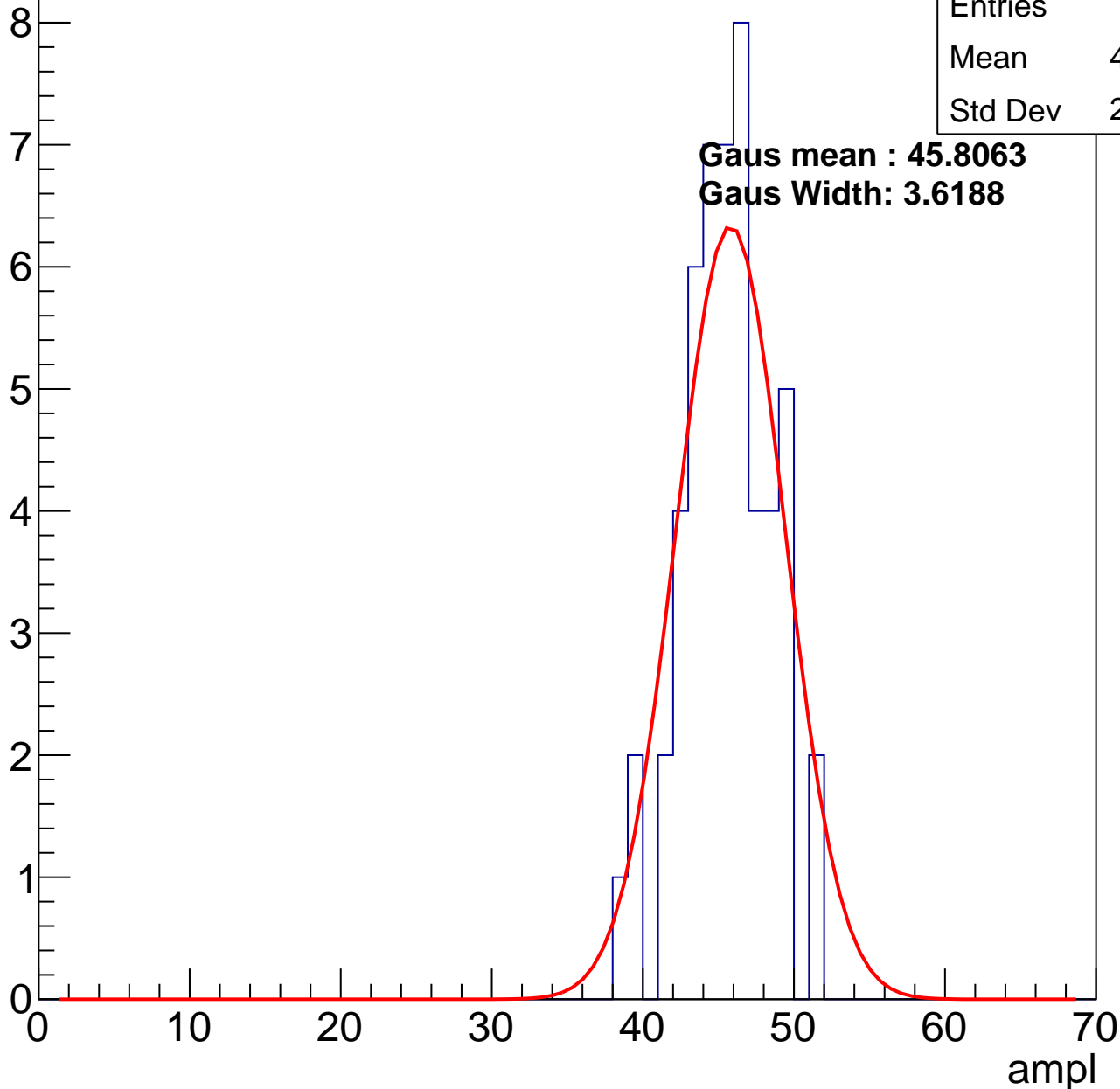
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	45.04
Std Dev	2.882

Gaus mean : 45.8063

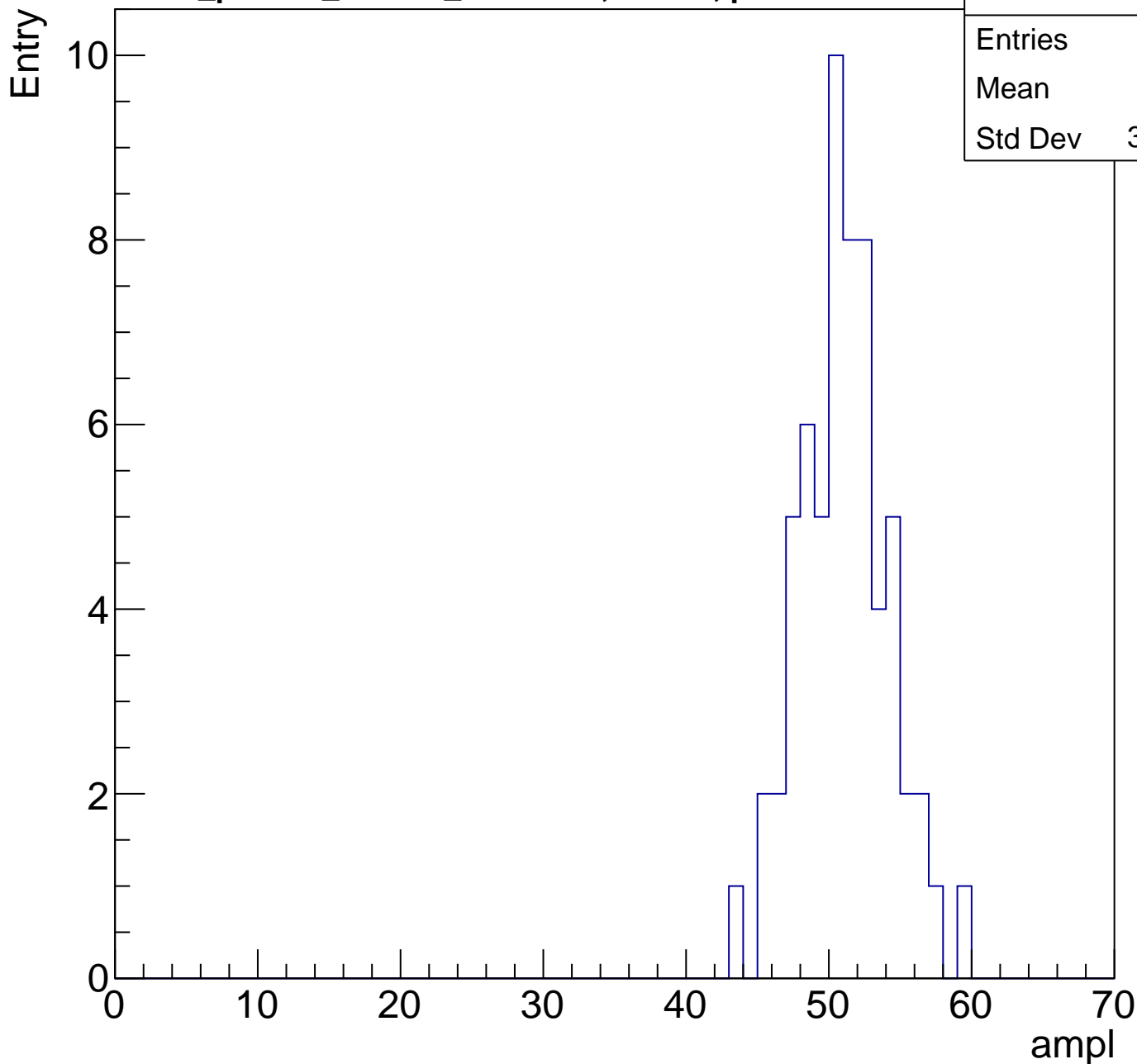
Gaus Width: 3.6188



B1L003S, U20-ch57, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	50.6
Std Dev	3.087

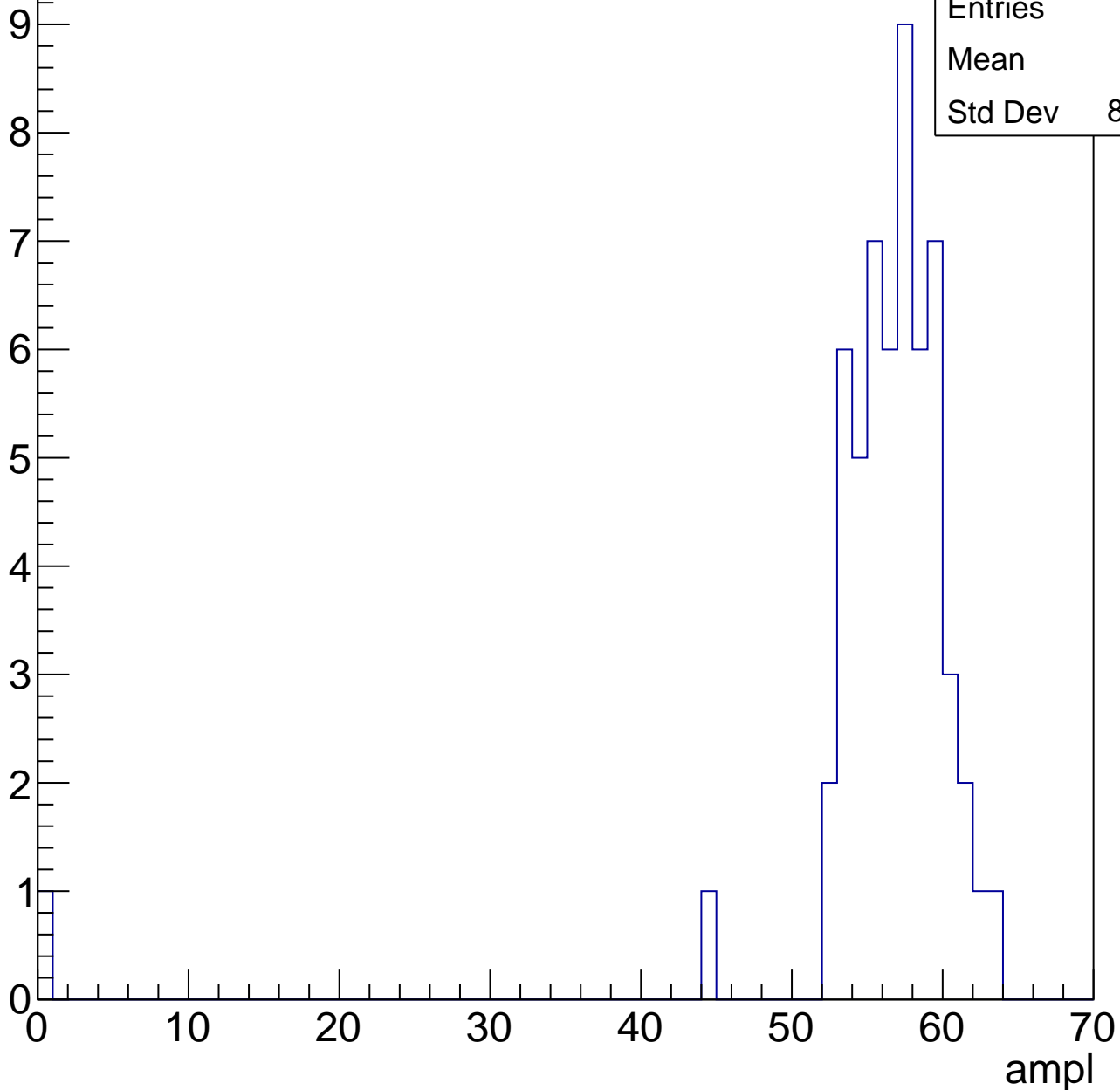


B1L003S, U20-ch57, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

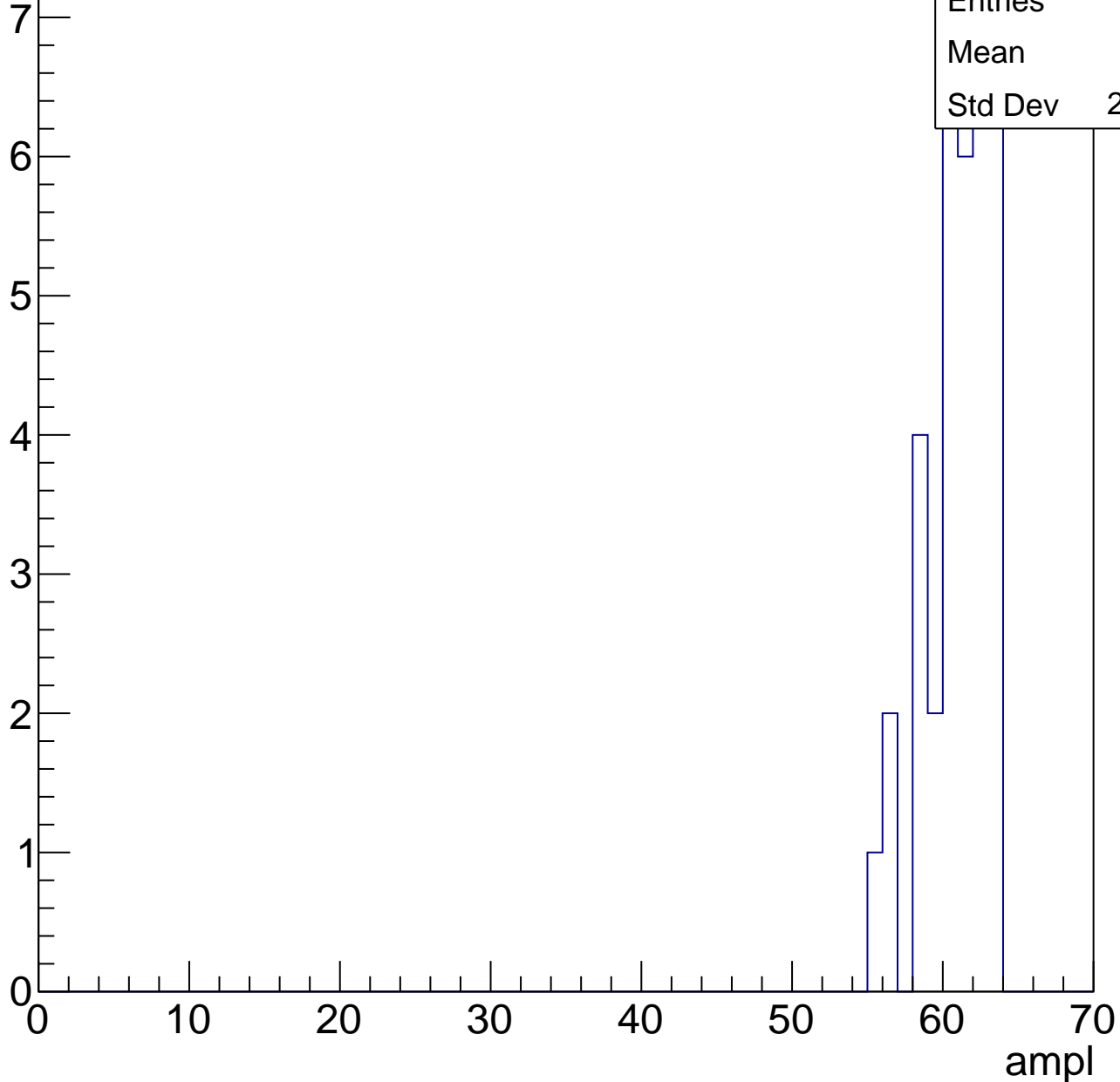
Entries	57
Mean	55.4
Std Dev	8.004



B1L003S, U20-ch57, adc5

calib_packv5_042523_0143.root, FC#13, port D2

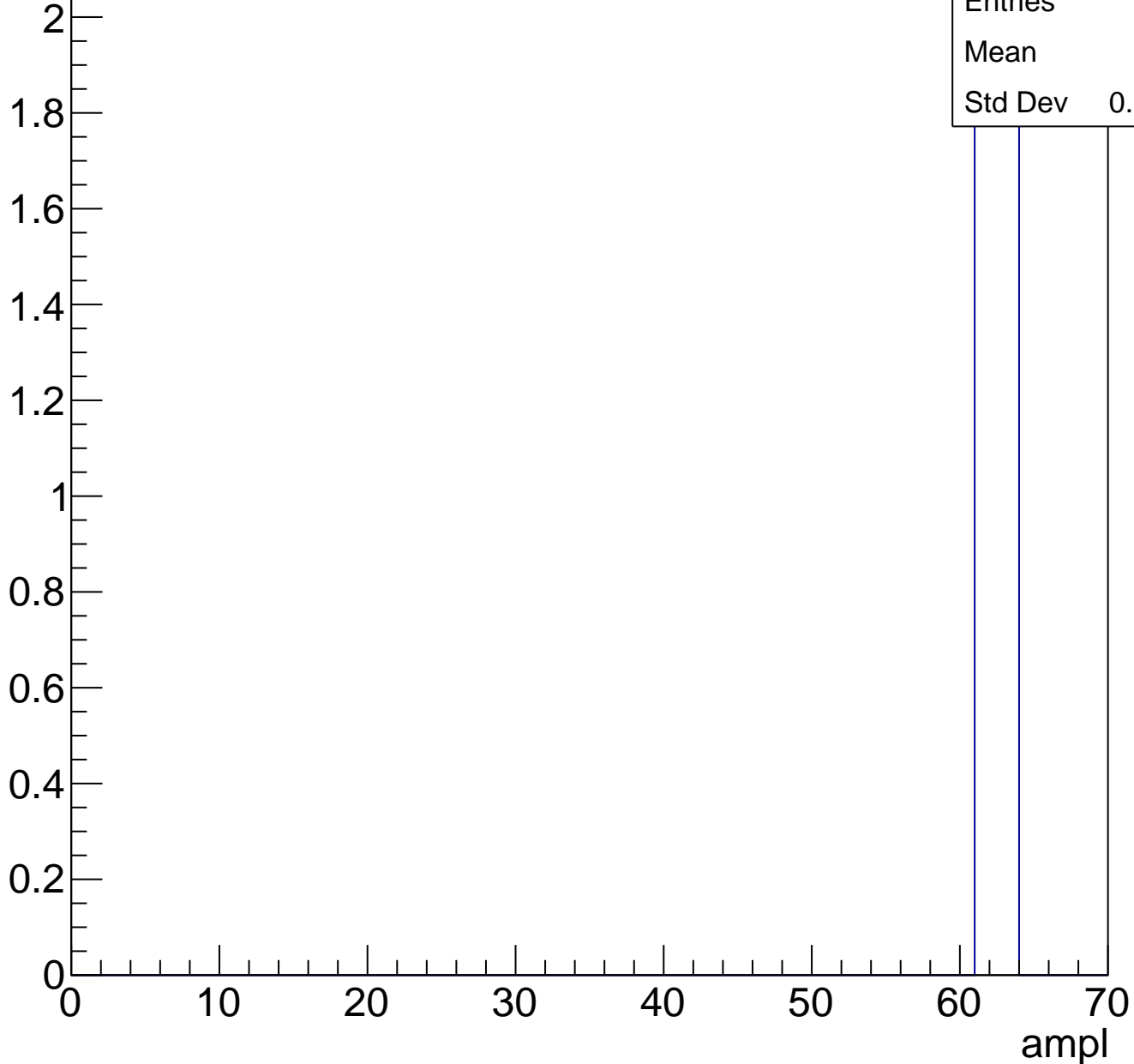
Entry



B1L003S, U20-ch57, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch57, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch58, adc0

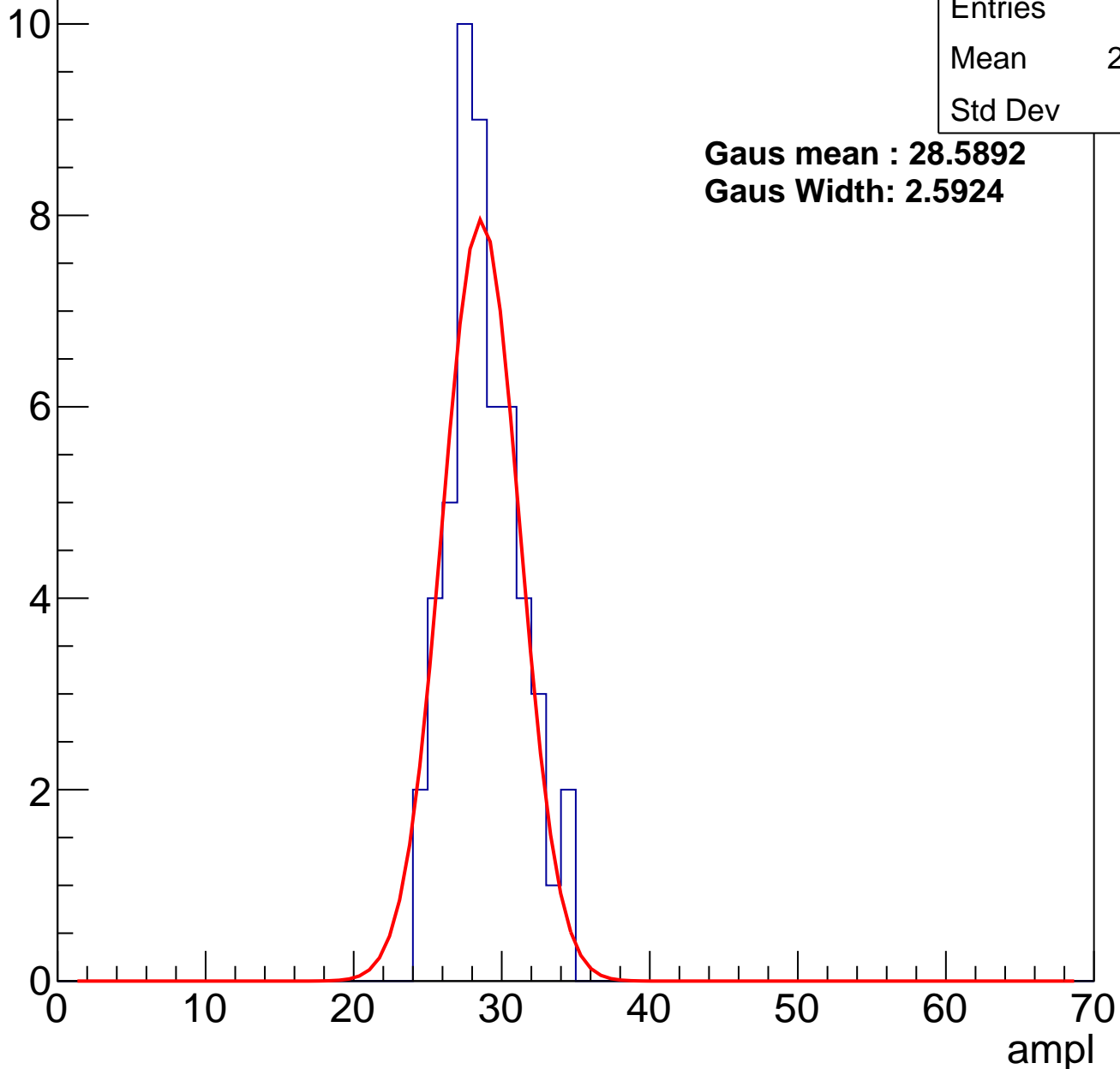
calib_packv5_042523_0143.root, FC#13, port D2

Entries	52
Mean	28.37
Std Dev	2.41

Gaus mean : 28.5892

Gaus Width: 2.5924

Entry



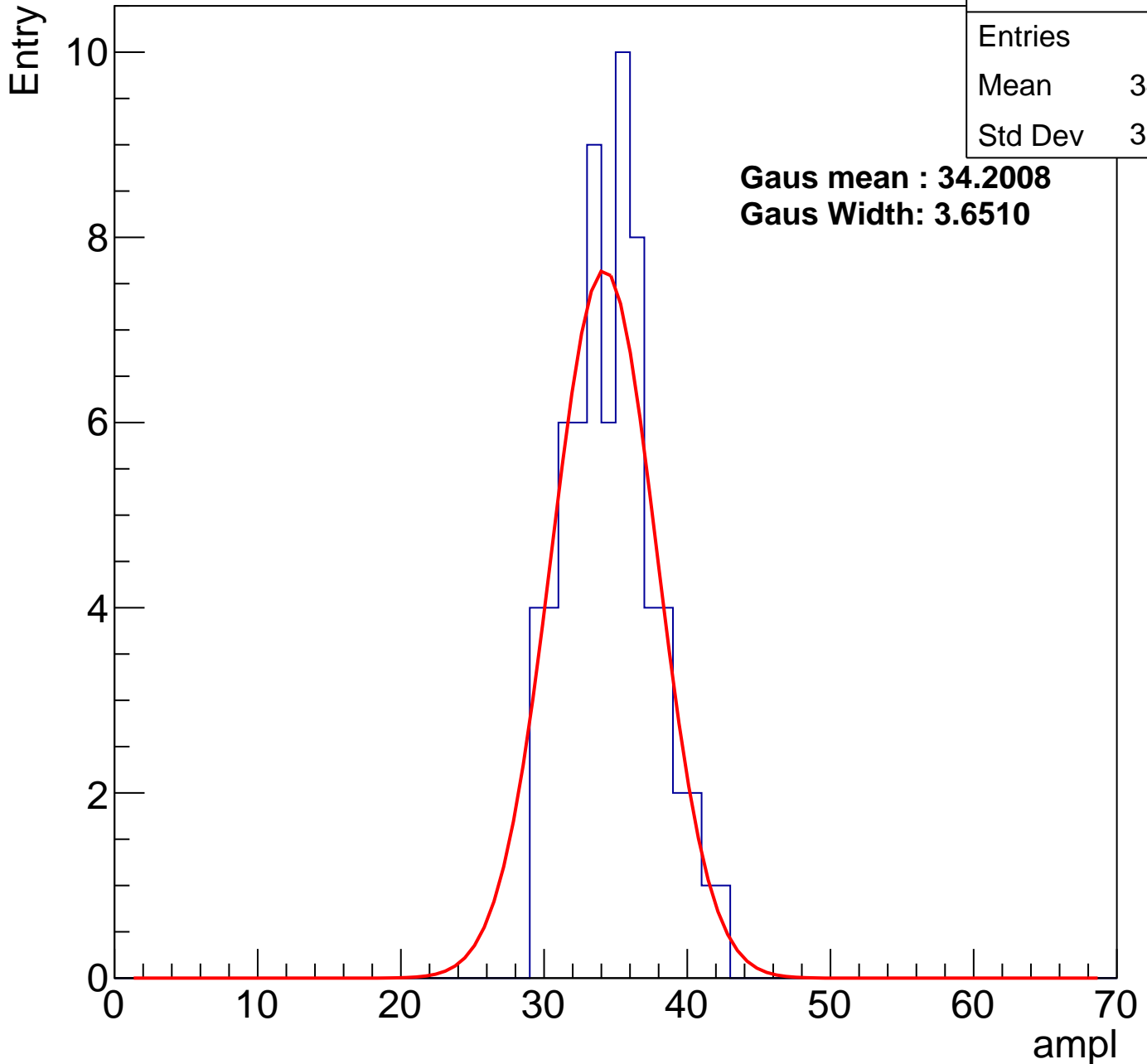
B1L003S, U20-ch58, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	34.24
Std Dev	3.052

Gaus mean : 34.2008

Gaus Width: 3.6510



B1L003S, U20-ch58, adc2

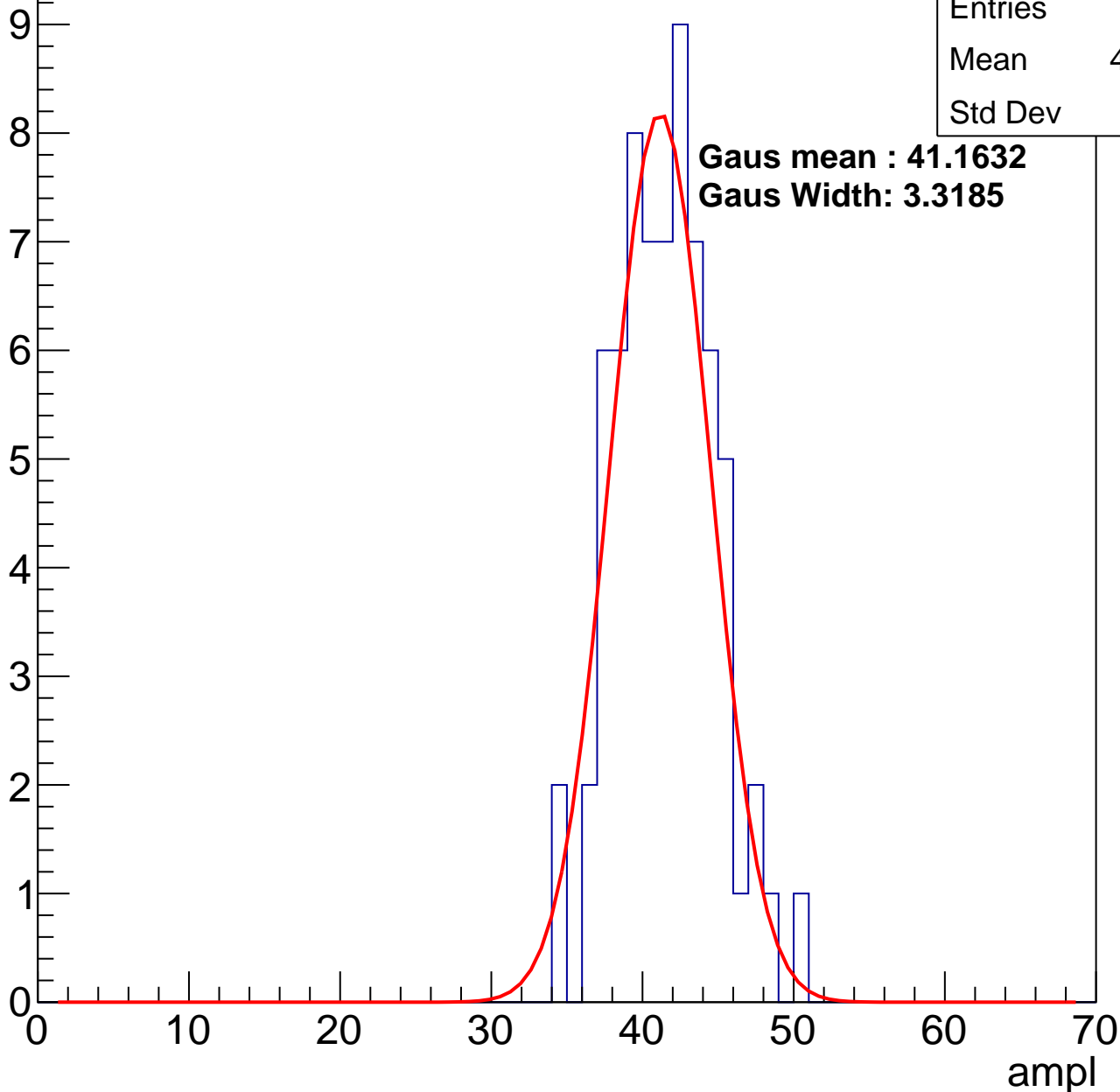
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	41.07
Std Dev	3.24

Gaus mean : 41.1632

Gaus Width: 3.3185



B1L003S, U20-ch58, adc3

calib_packv5_042523_0143.root, FC#13, port D2

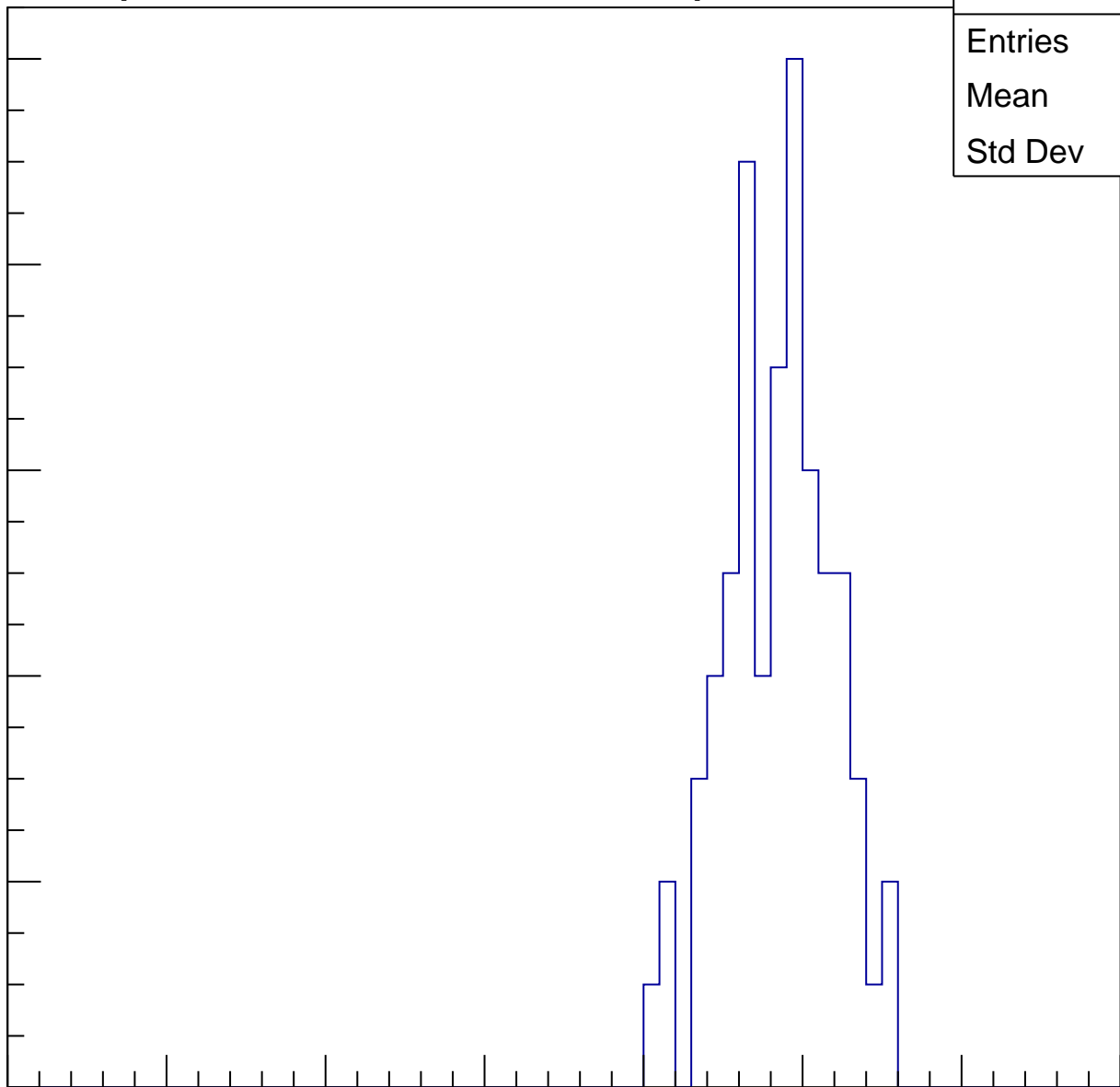
Entries	67
Mean	48.03
Std Dev	3.355

Entry

10
8
6
4
2
0

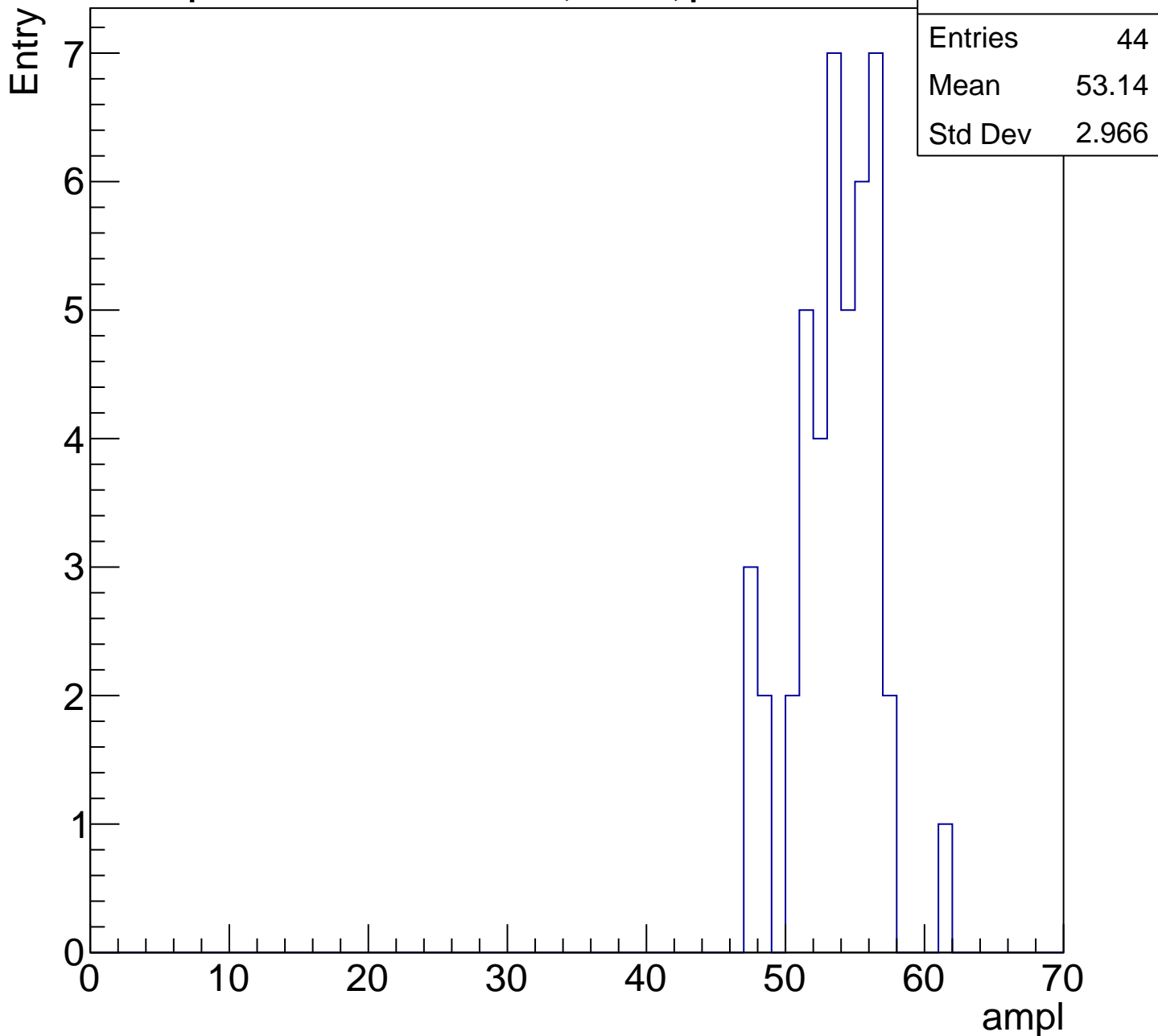
0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch58, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch58, adc5

calib_packv5_042523_0143.root, FC#13, port D2

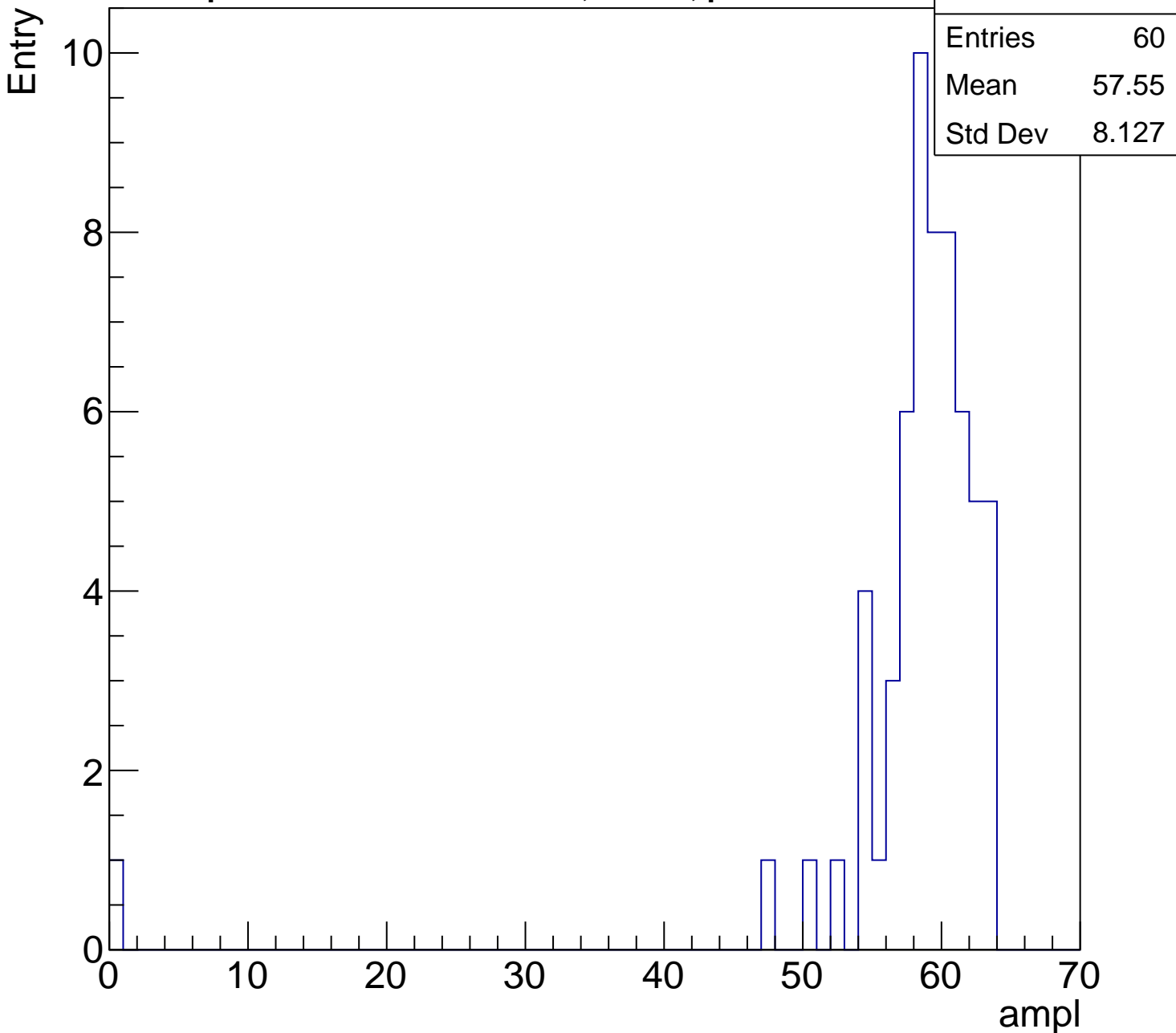
Entries	60
Mean	57.55
Std Dev	8.127

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

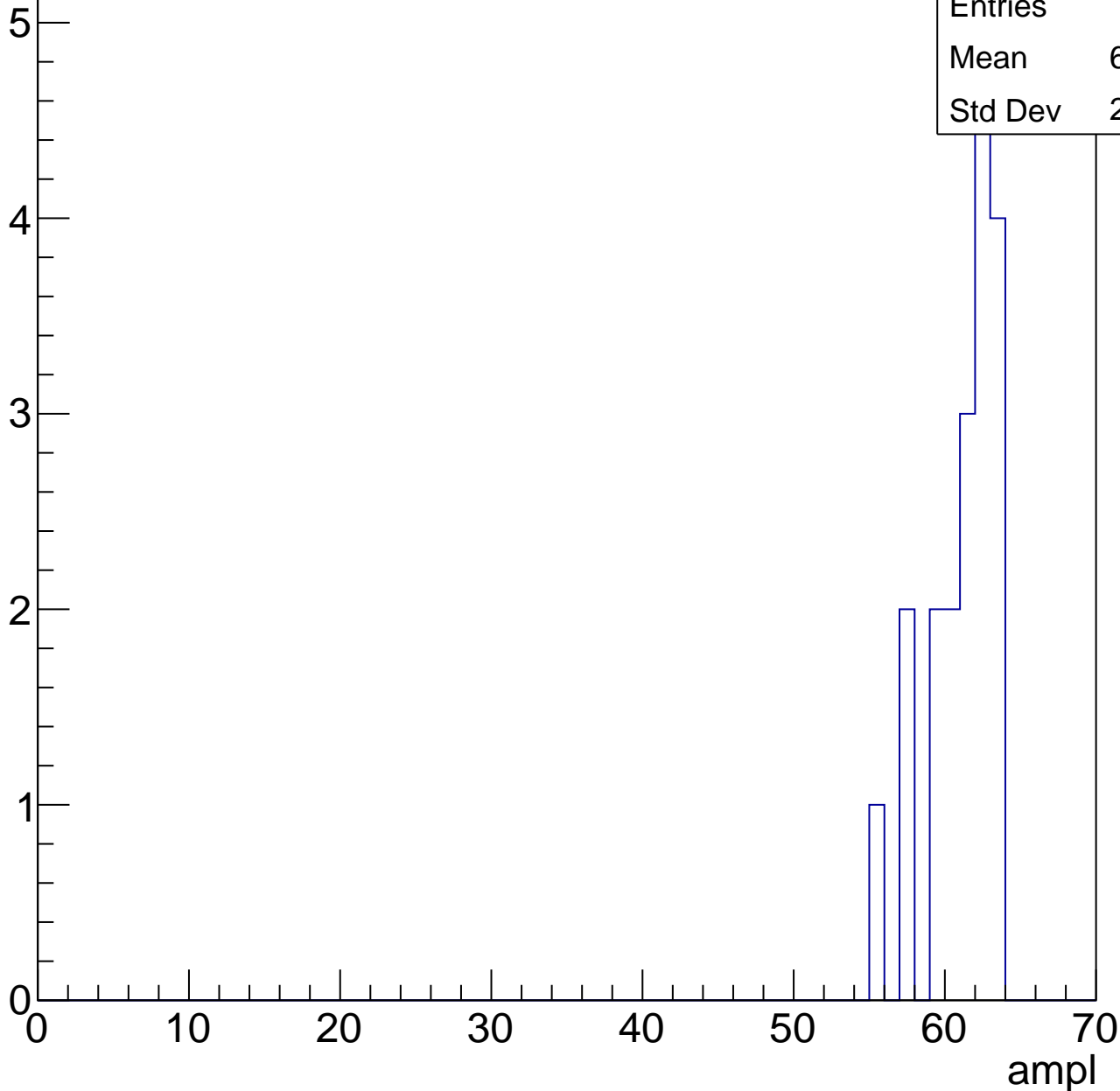


B1L003S, U20-ch58, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

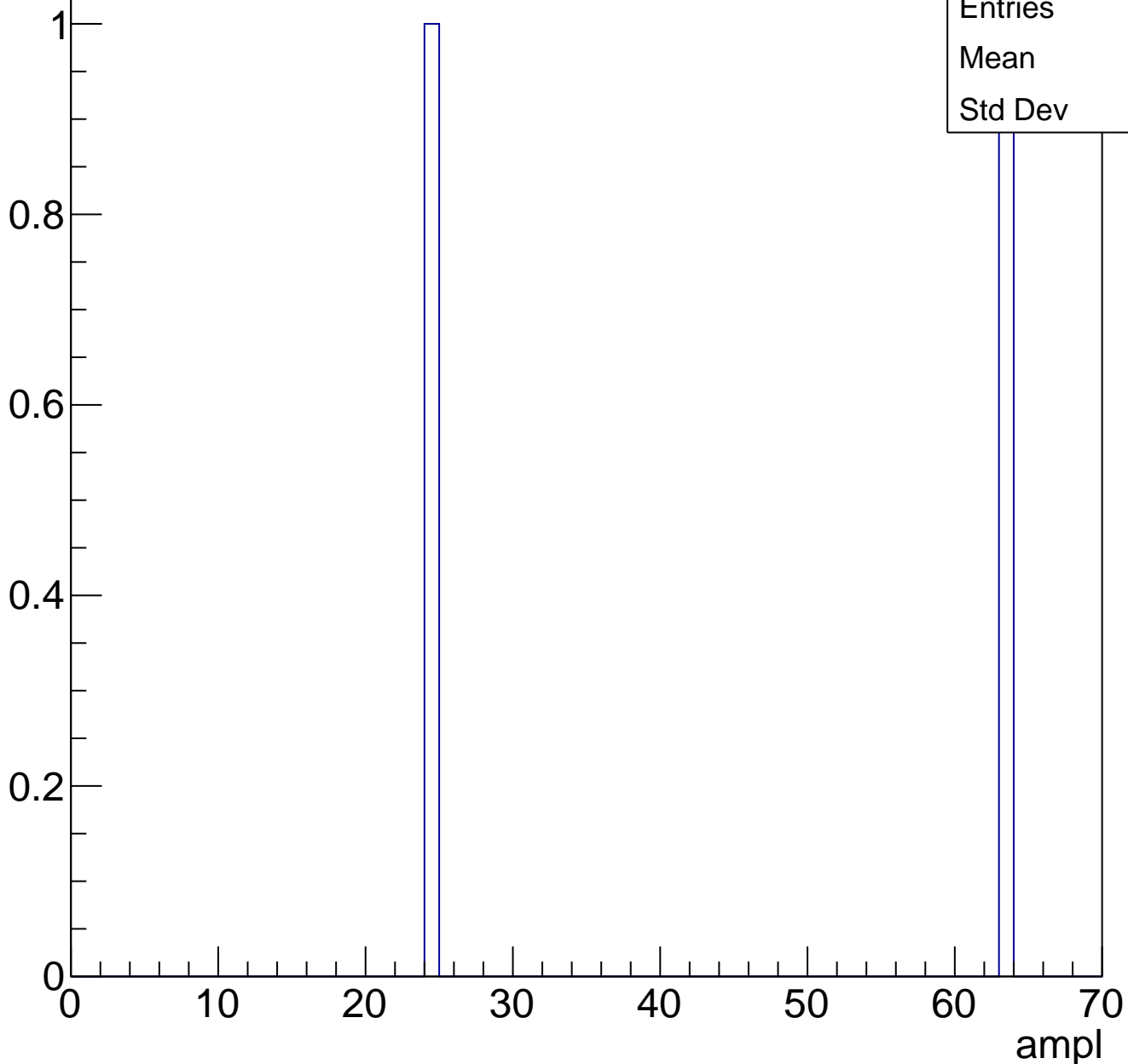
Entries	19
Mean	60.63
Std Dev	2.253



B1L003S, U20-ch58, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	43.5
Std Dev	19.5

B1L003S, U20-ch59, adc0

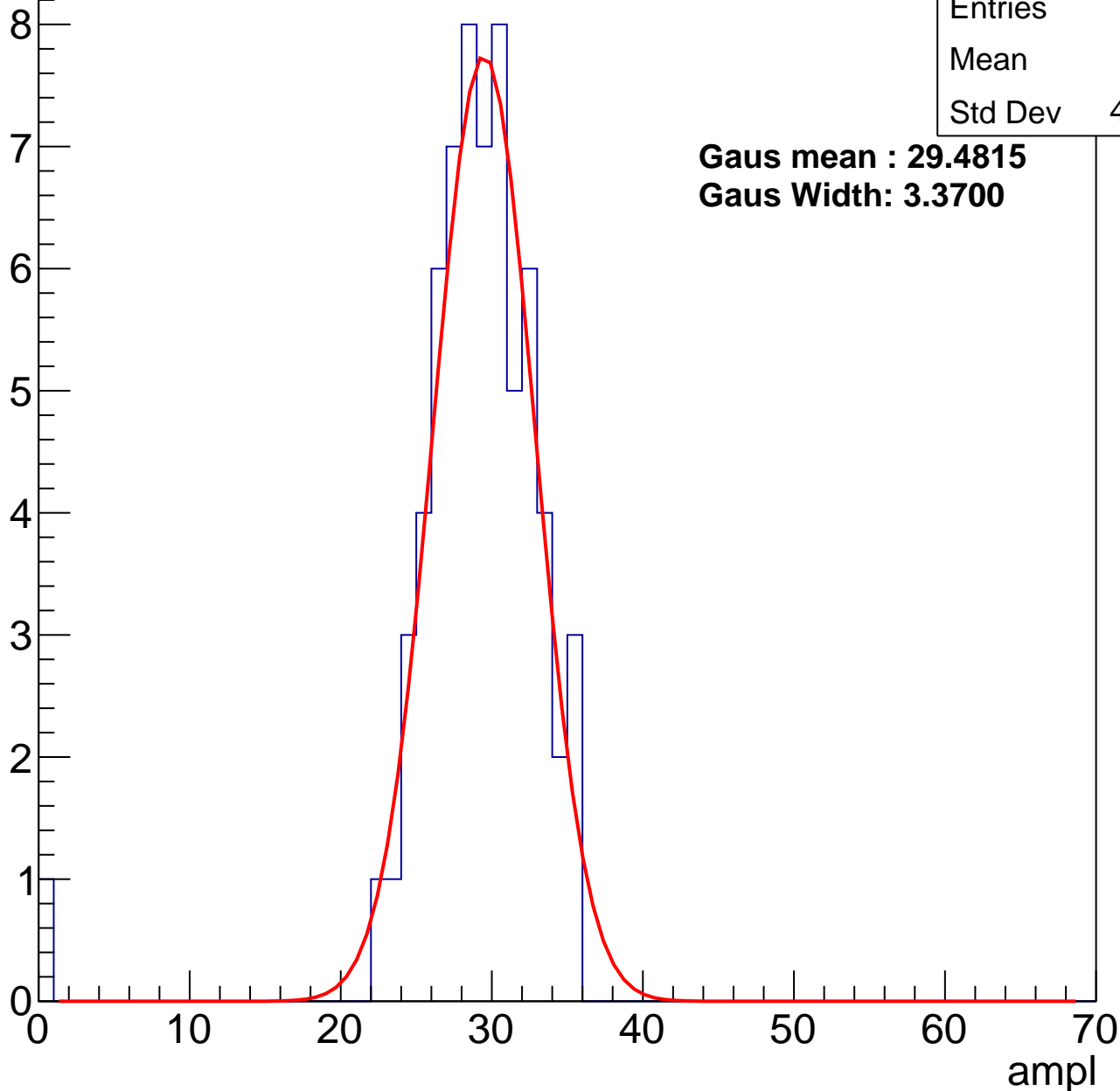
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.5
Std Dev	4.675

Gaus mean : 29.4815

Gaus Width: 3.3700



B1L003S, U20-ch59, adc1

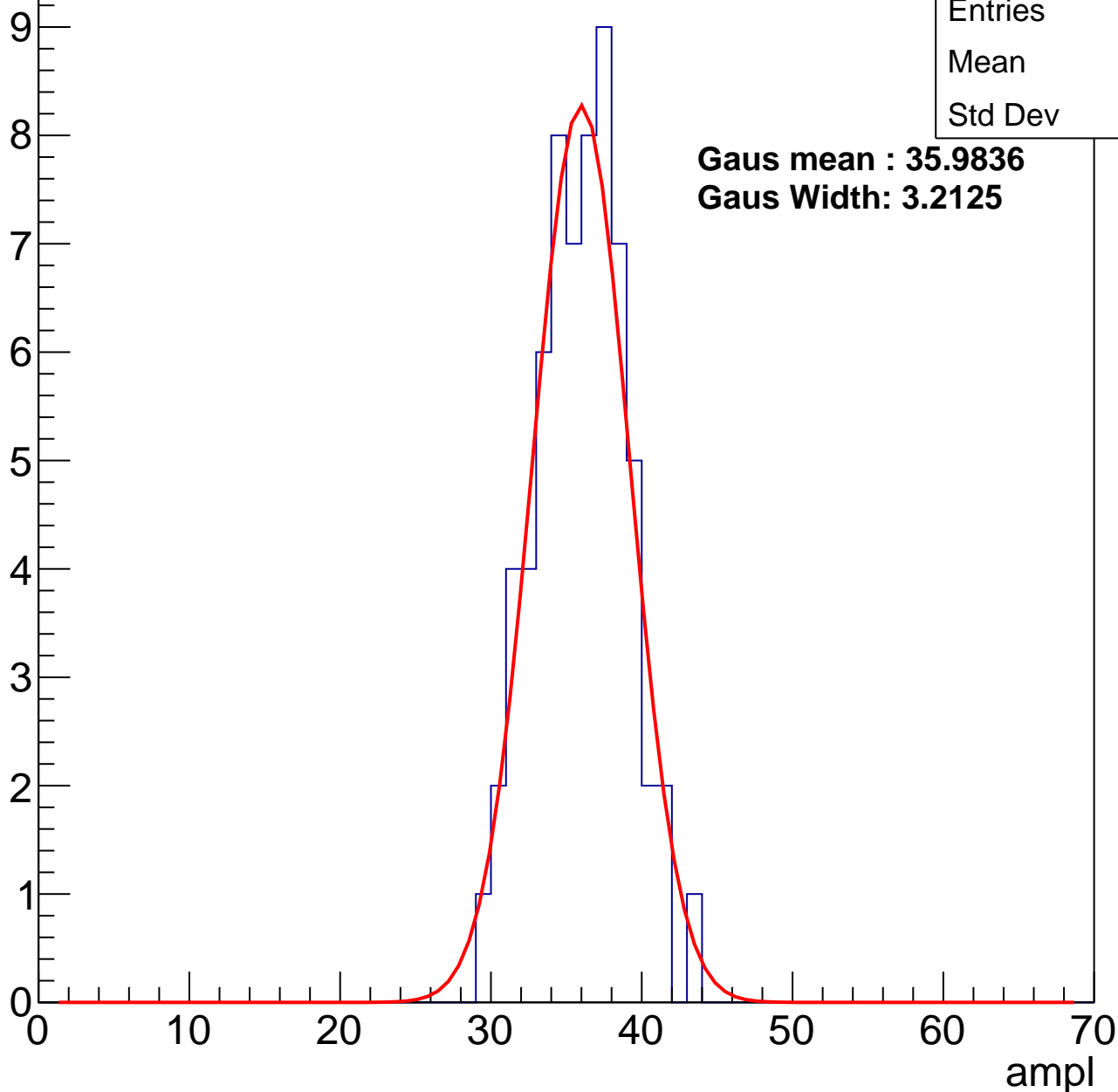
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.5
Std Dev	2.95

Gaus mean : 35.9836

Gaus Width: 3.2125



B1L003S, U20-ch59, adc2

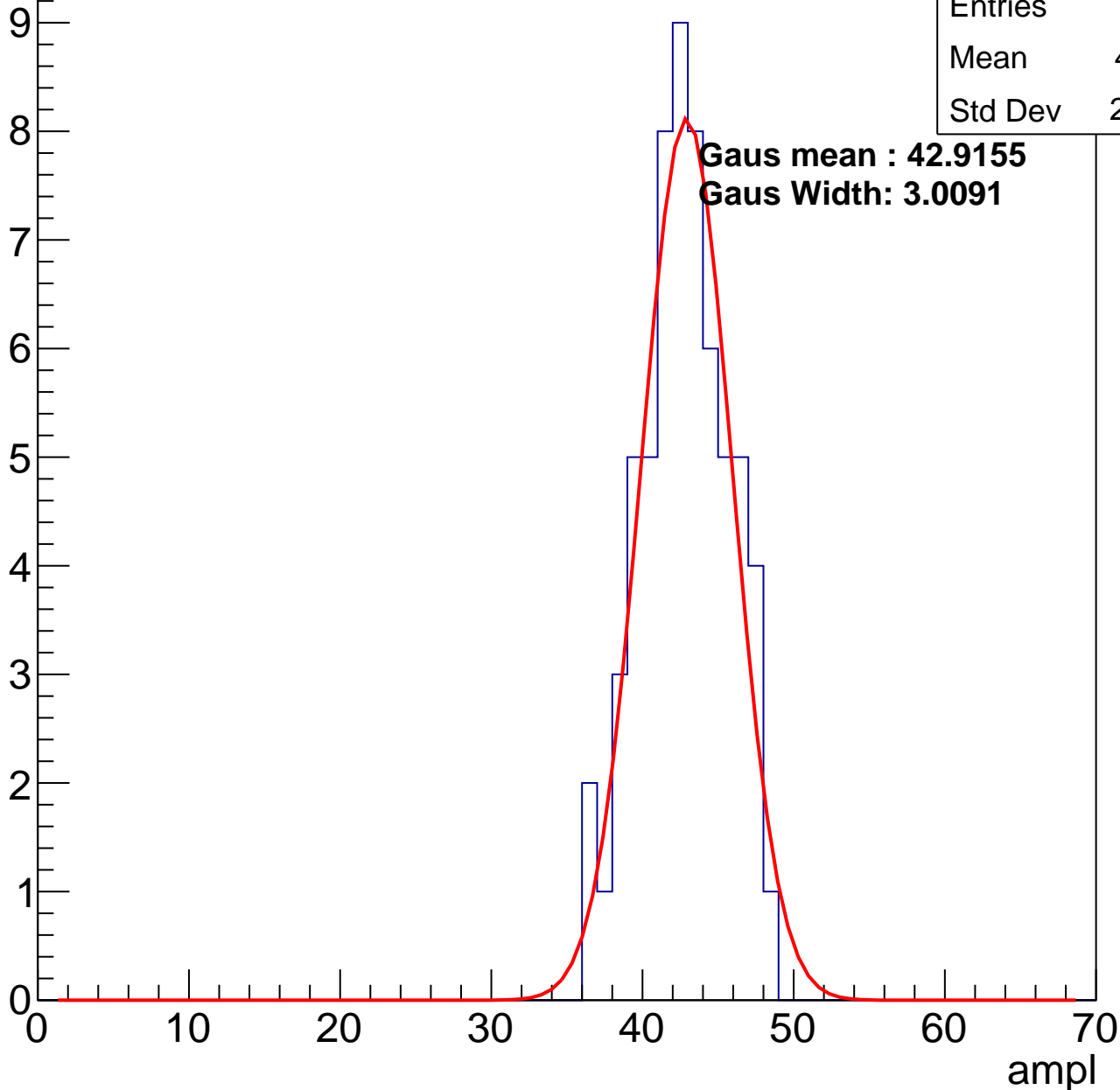
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.31
Std Dev	2.854

Gaus mean : 42.9155

Gaus Width: 3.0091

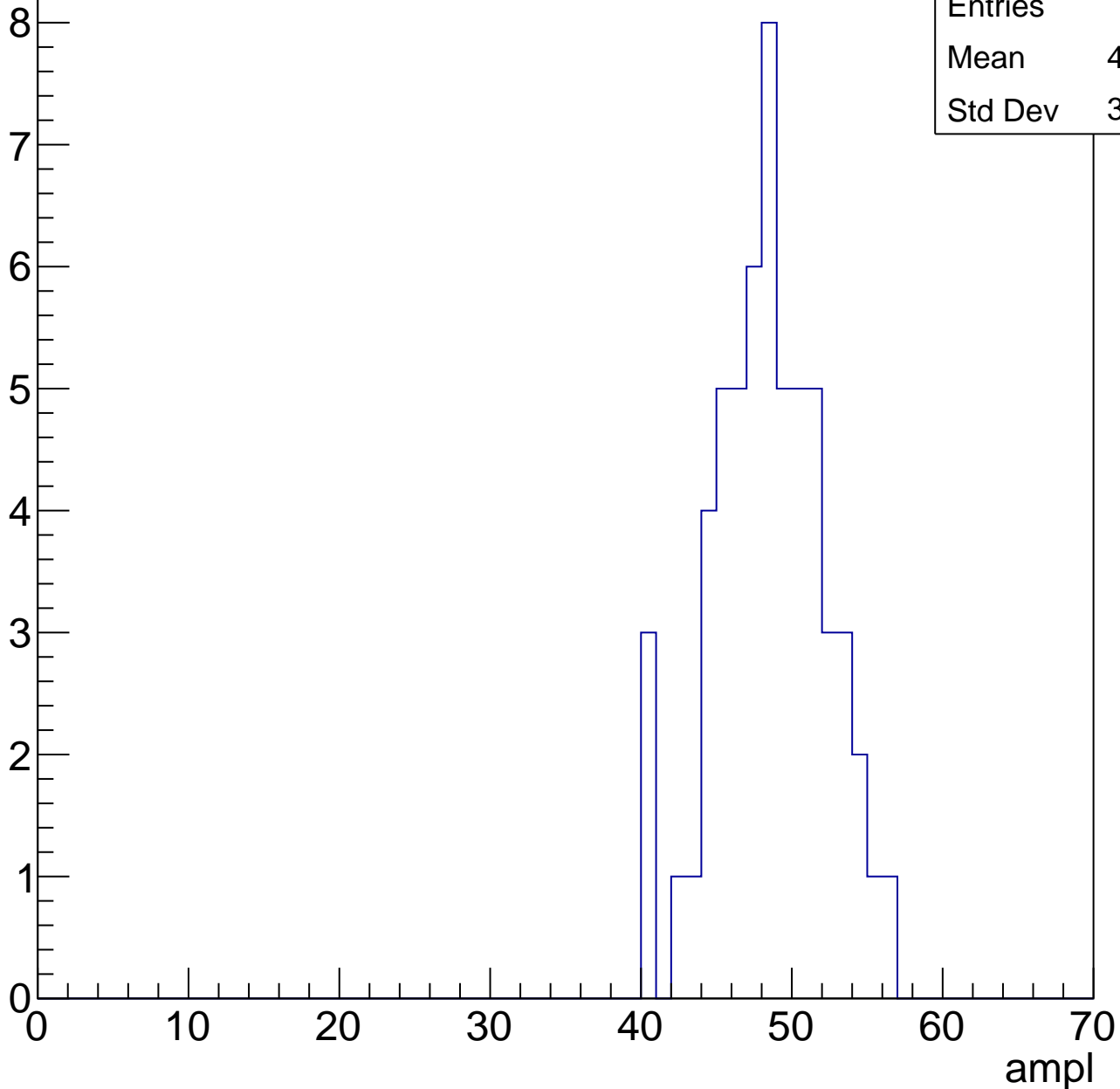


B1L003S, U20-ch59, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	48.03
Std Dev	3.634

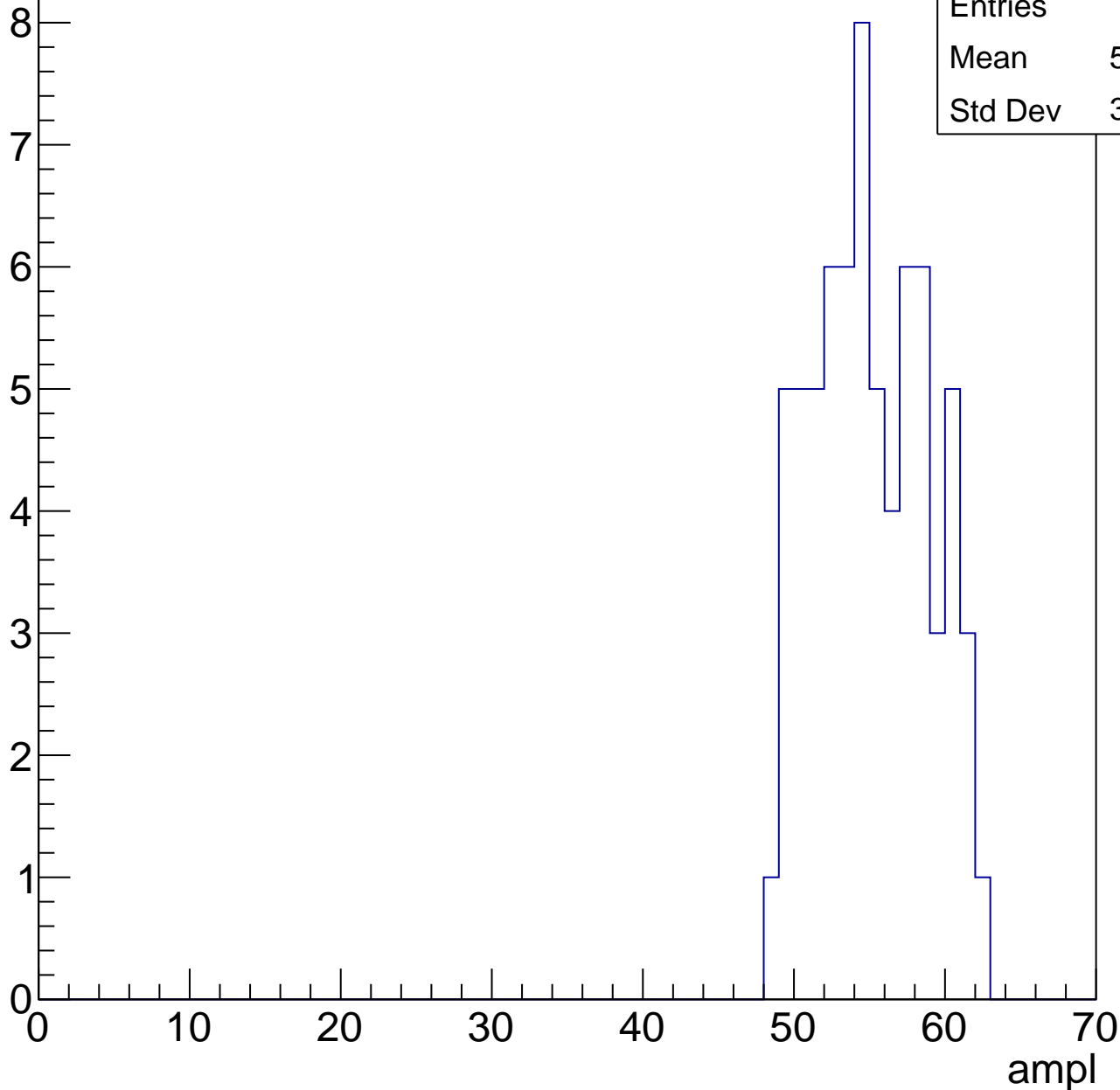


B1L003S, U20-ch59, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	54.65
Std Dev	3.659

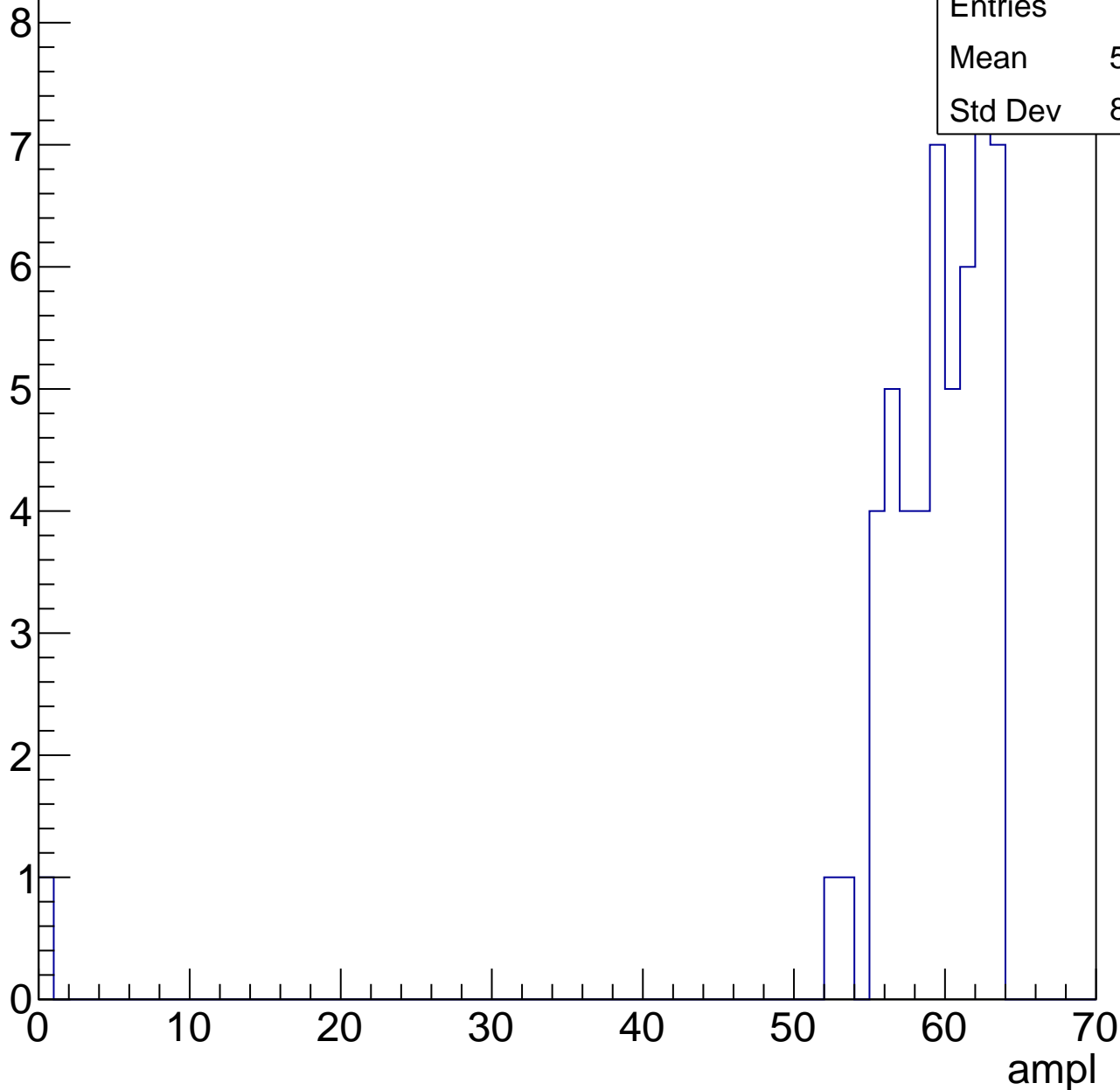


B1L003S, U20-ch59, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.13
Std Dev	8.543



B1L003S, U20-ch59, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch59, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch60, adc0

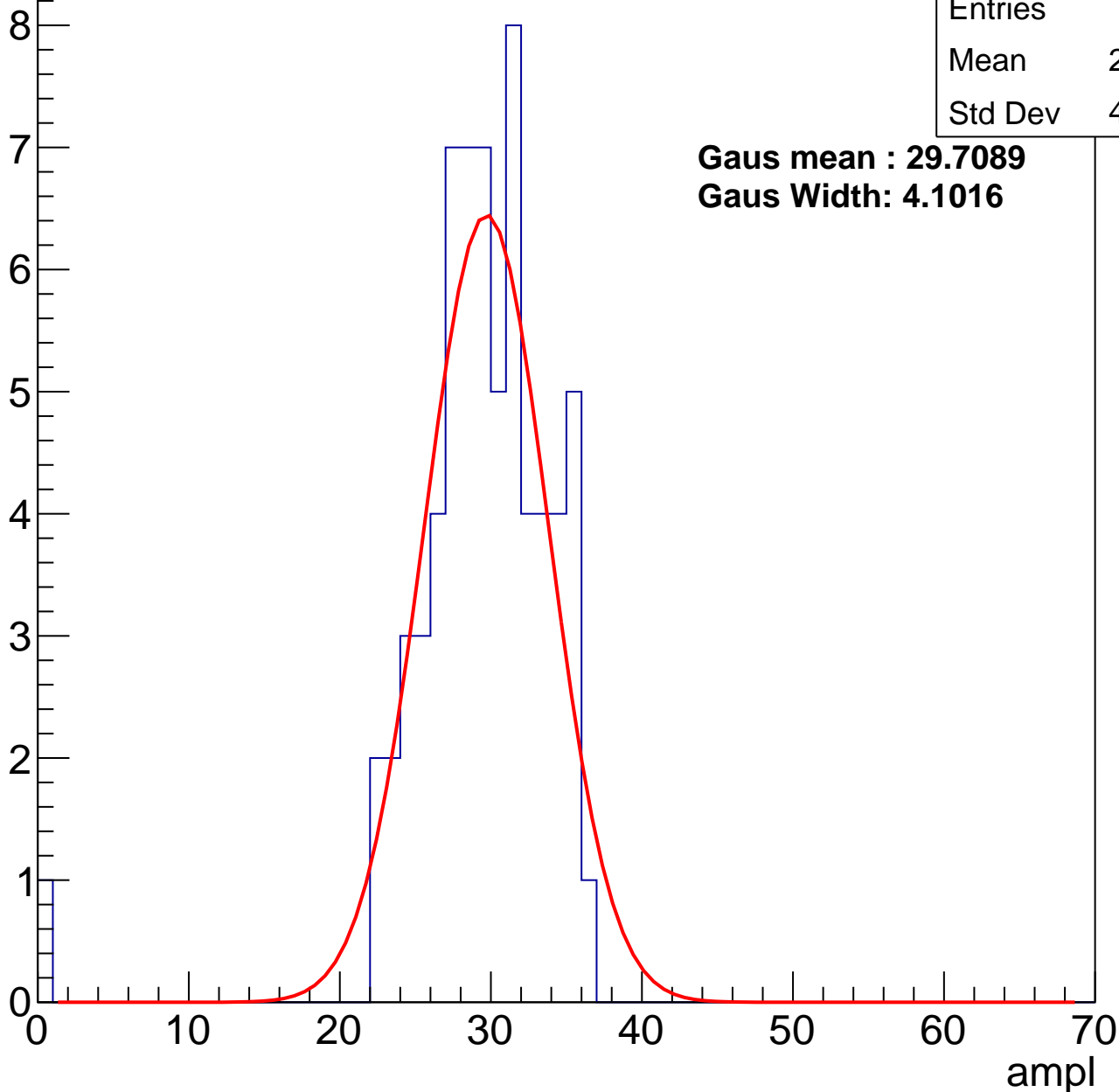
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.87
Std Dev	4.992

Gaus mean : 29.7089

Gaus Width: 4.1016



B1L003S, U20-ch60, adc1

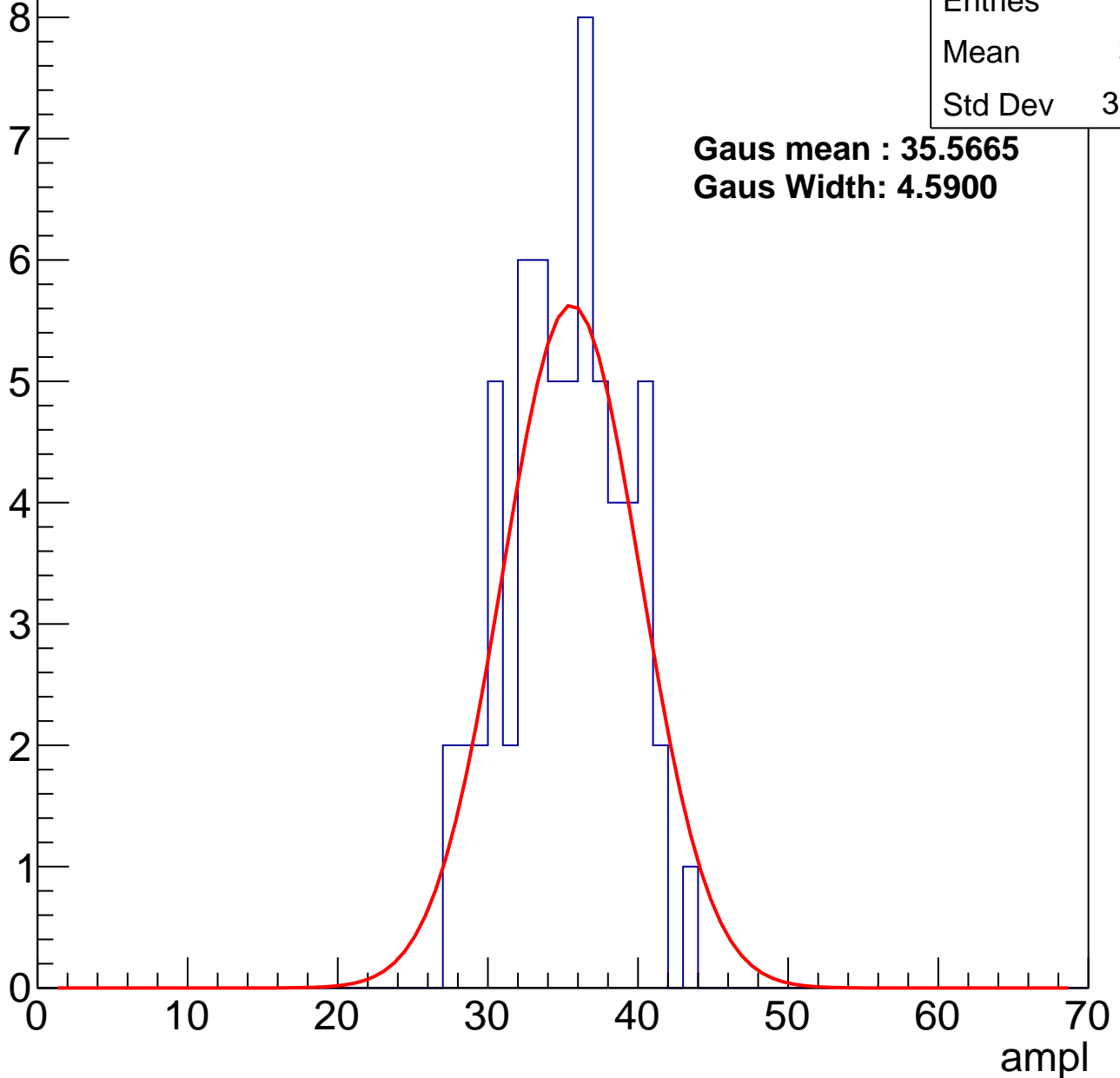
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	34.7
Std Dev	3.794

Gaus mean : 35.5665

Gaus Width: 4.5900



B1L003S, U20-ch60, adc2

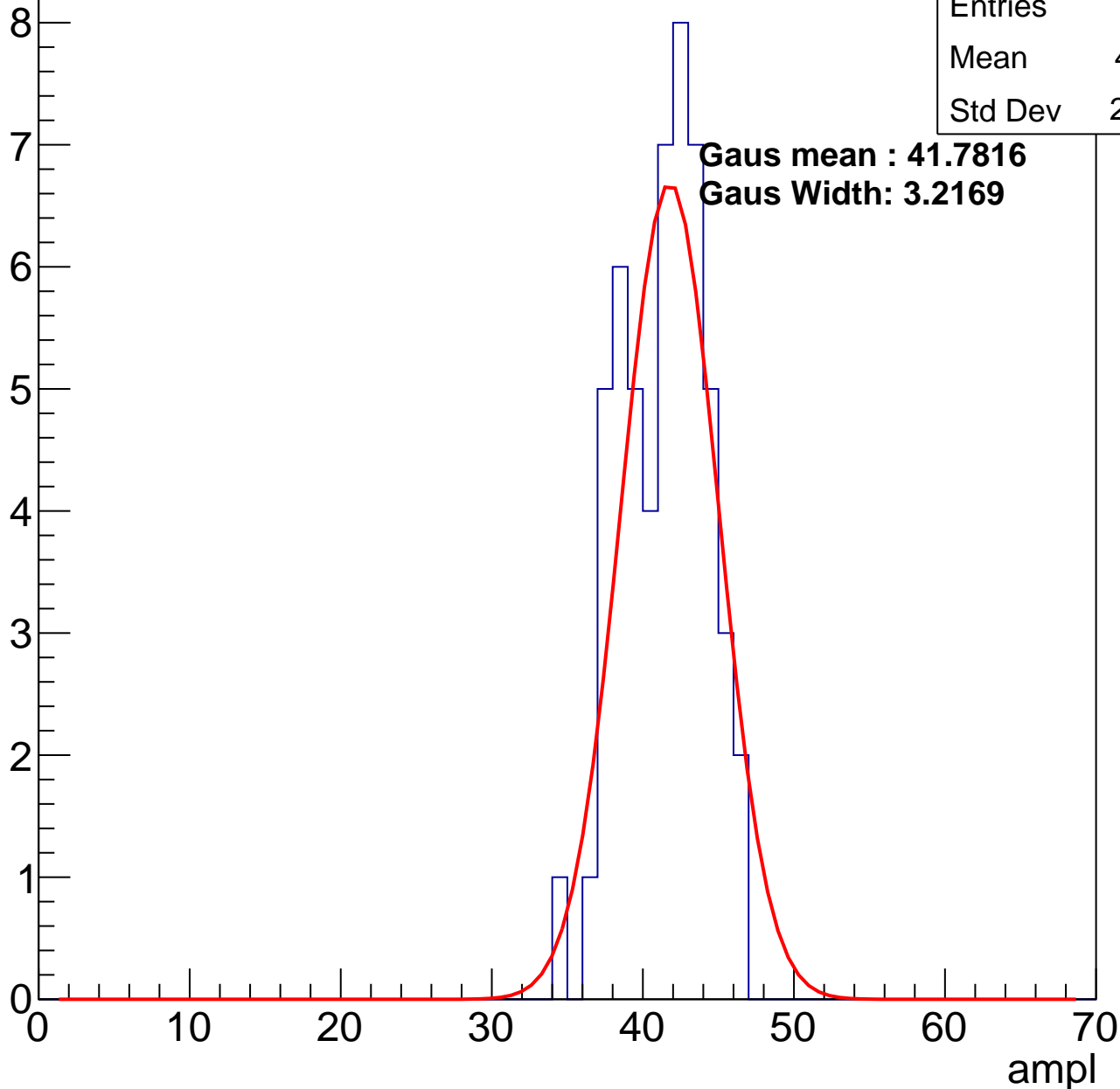
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	40.91
Std Dev	2.757

Gaus mean : 41.7816

Gaus Width: 3.2169



B1L003S, U20-ch60, adc3

calib_packv5_042523_0143.root, FC#13, port D2

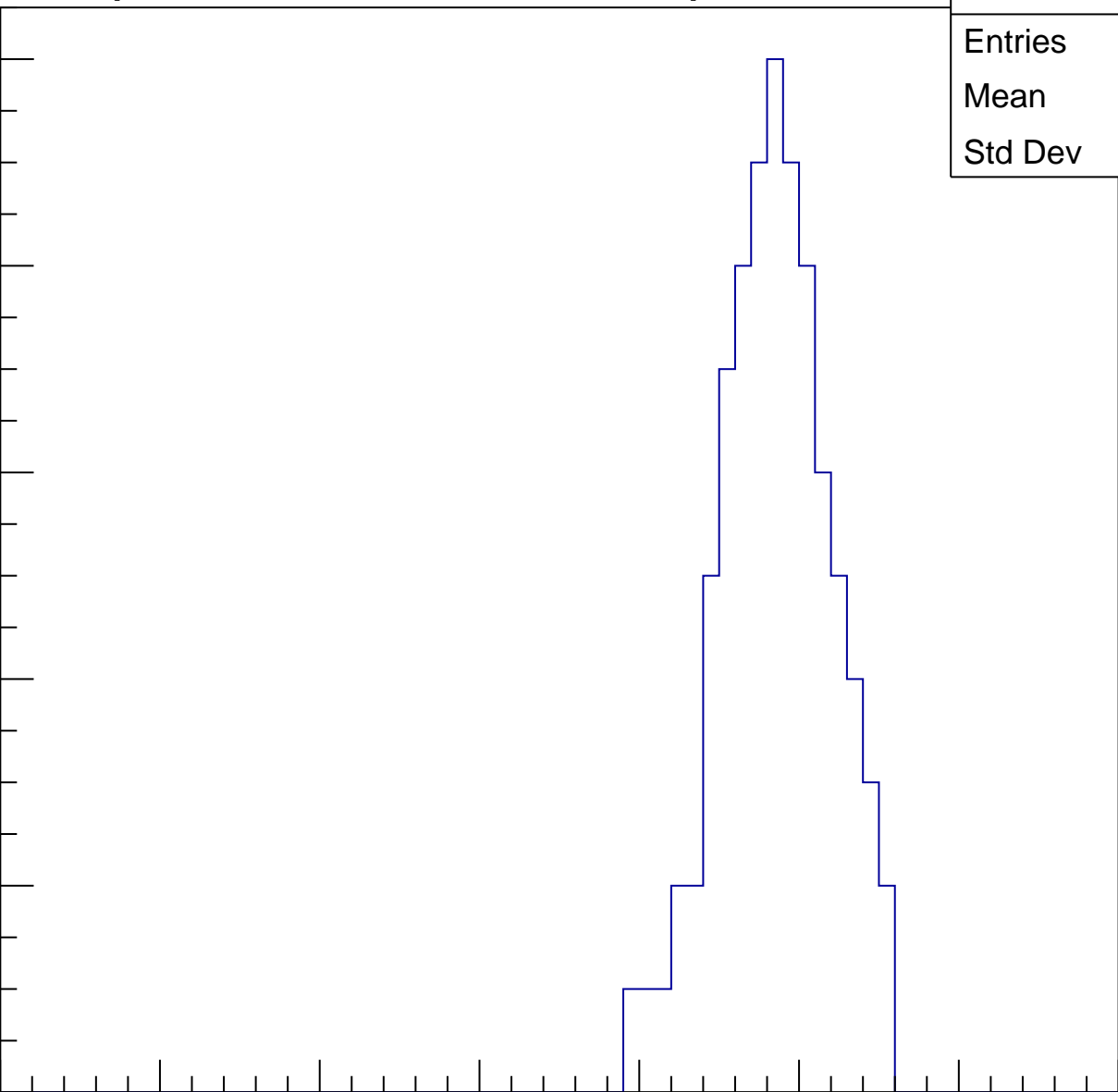
Entries	83
Mean	48.04
Std Dev	3.438

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

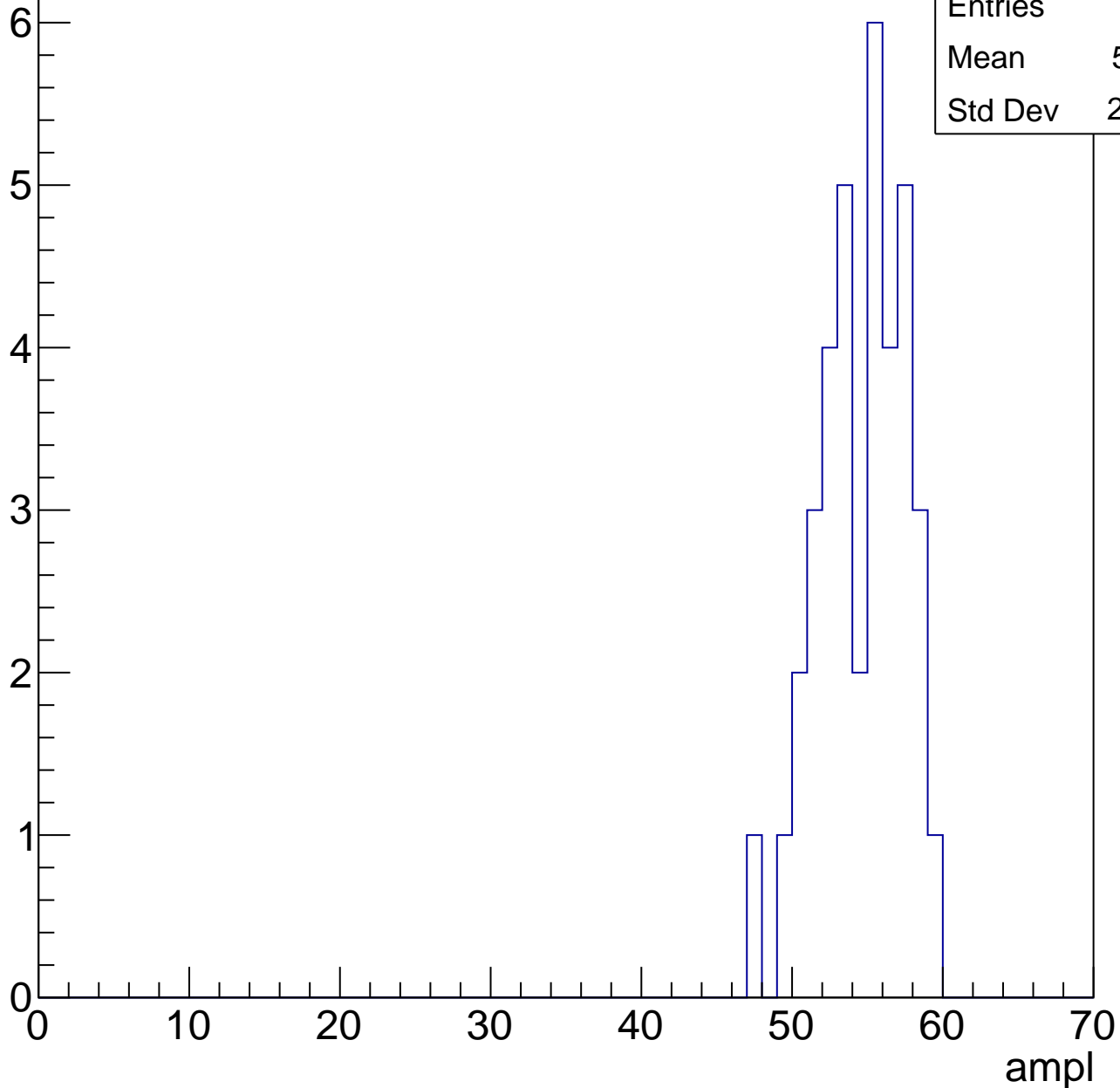


B1L003S, U20-ch60, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	54.11
Std Dev	2.817



B1L003S, U20-ch60, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

8

6

4

2

0

0

10

20

30

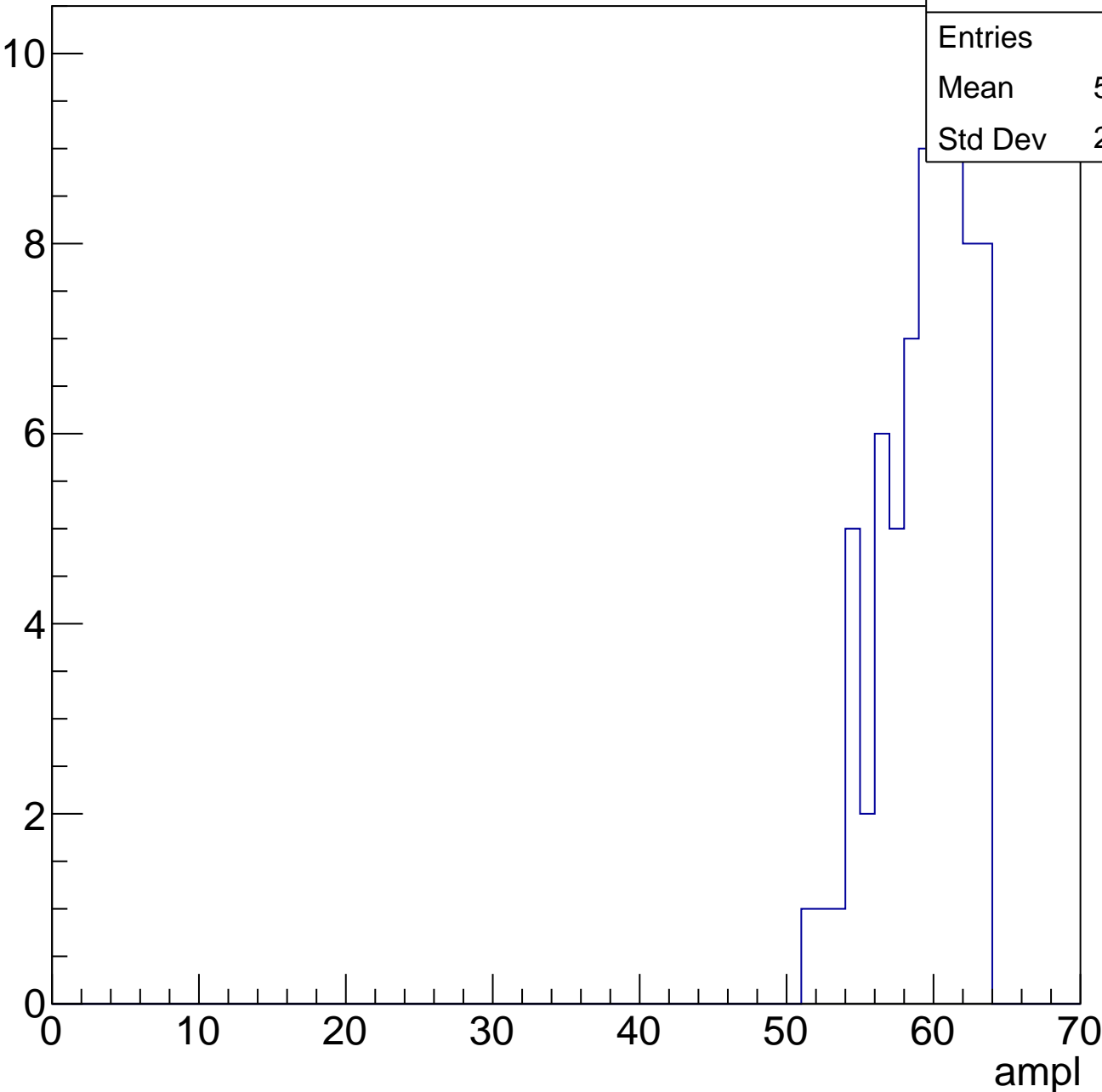
40

50

60

ampl

Entries	73
Mean	58.96
Std Dev	2.954



B1L003S, U20-ch60, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch60, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch61, adc0

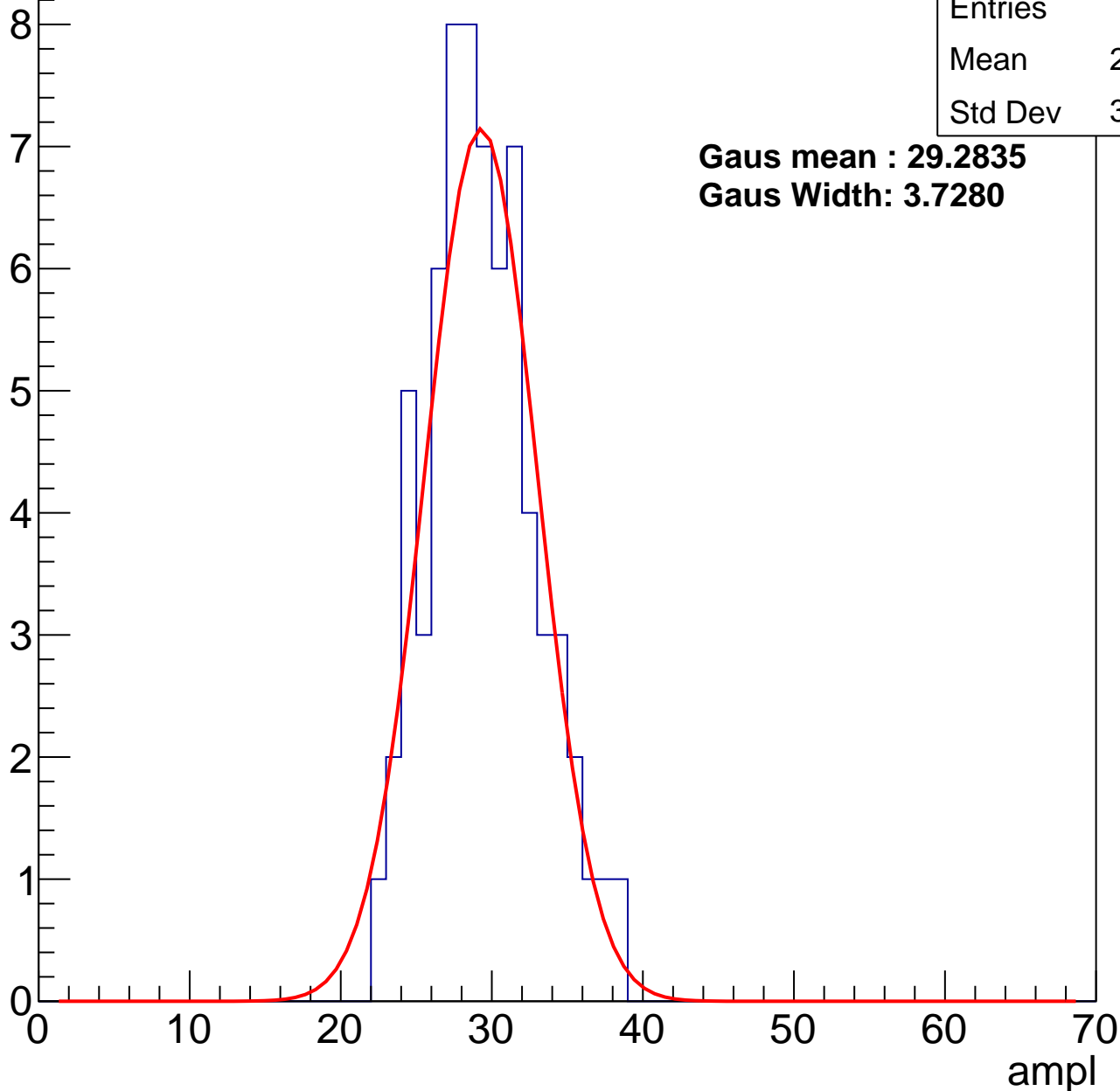
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.96
Std Dev	3.529

Gaus mean : 29.2835

Gaus Width: 3.7280



B1L003S, U20-ch61, adc1

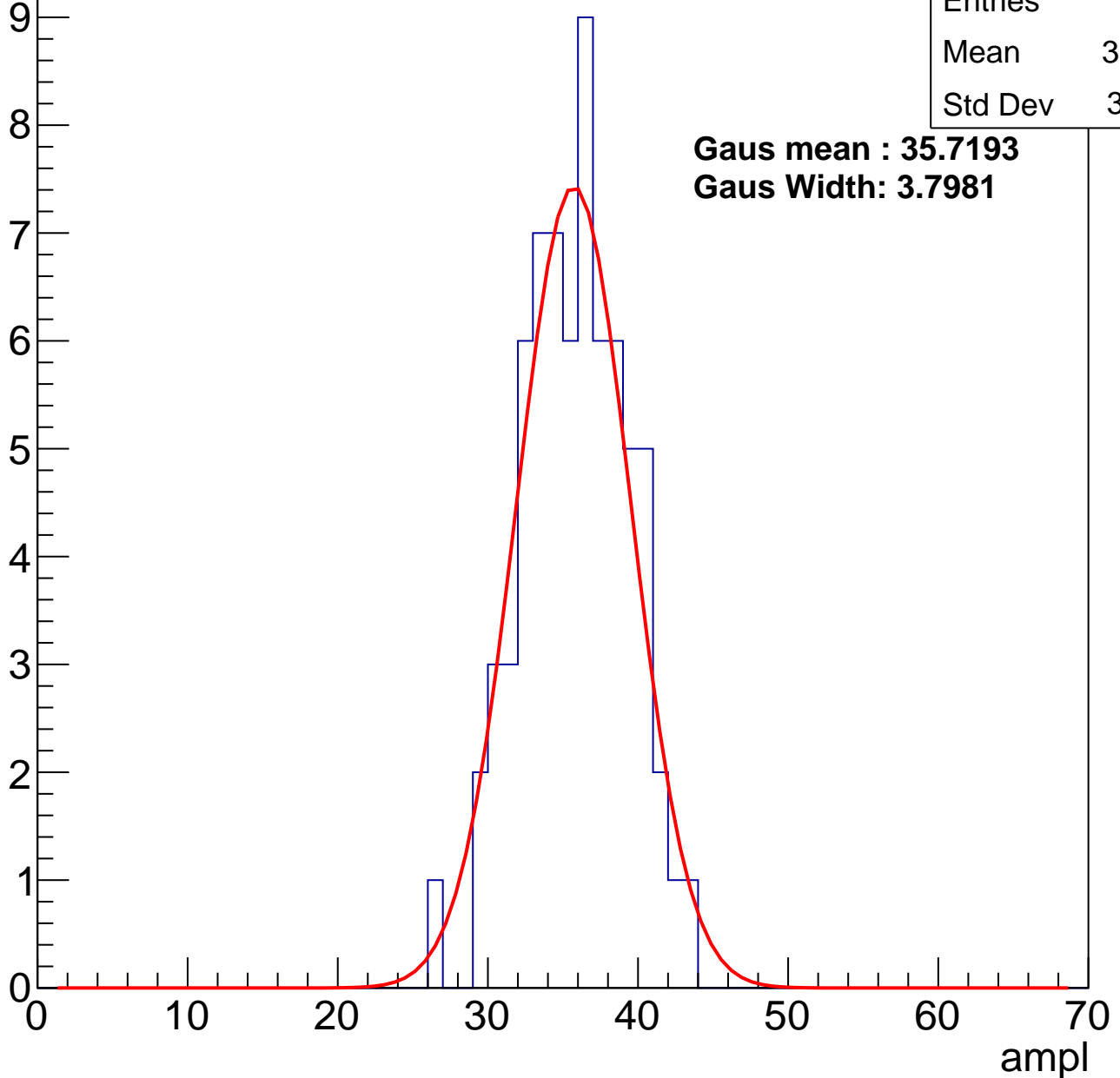
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.34
Std Dev	3.451

Gaus mean : 35.7193

Gaus Width: 3.7981



B1L003S, U20-ch61, adc2

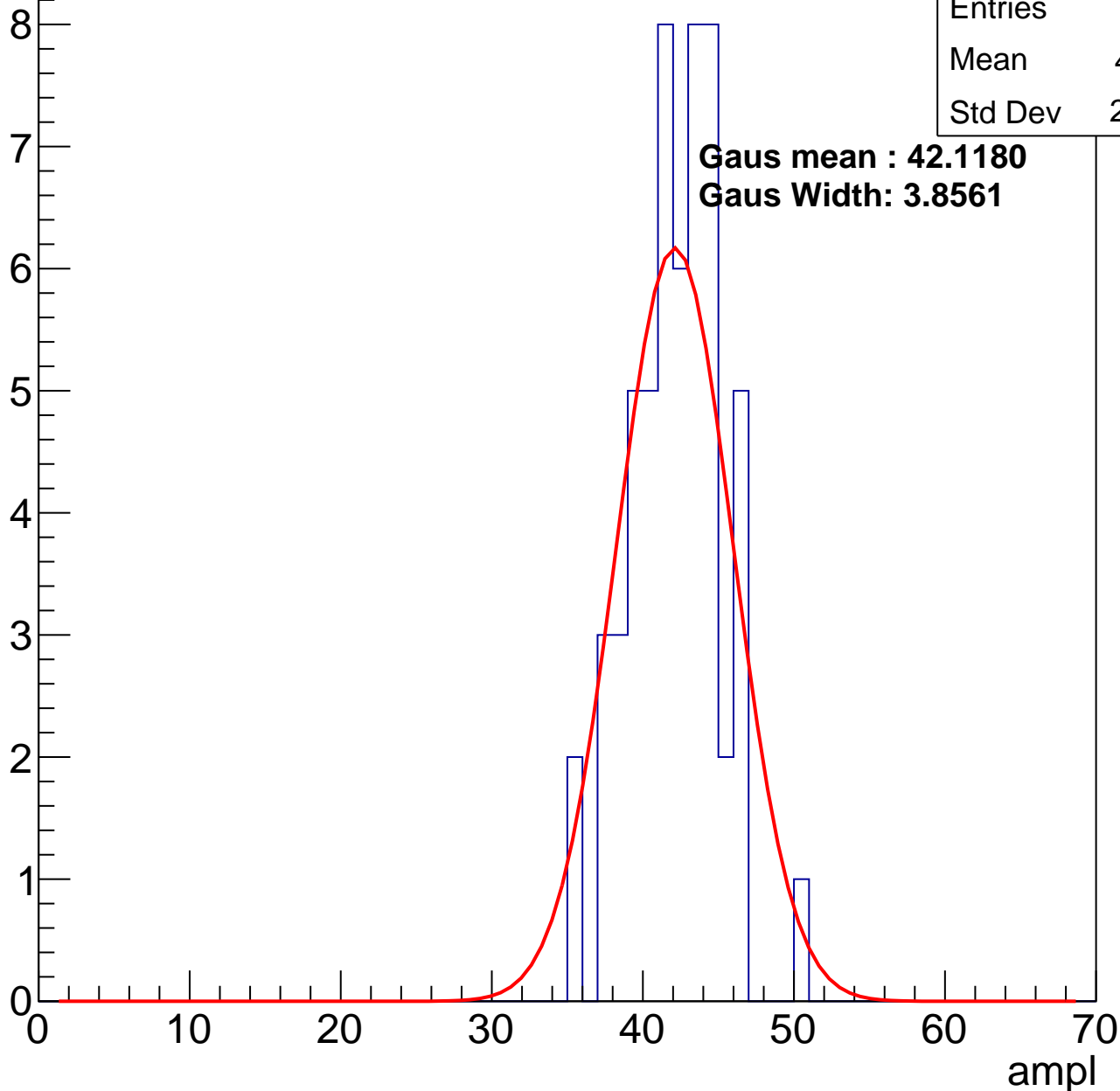
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.71
Std Dev	2.962

Gaus mean : 42.1180

Gaus Width: 3.8561

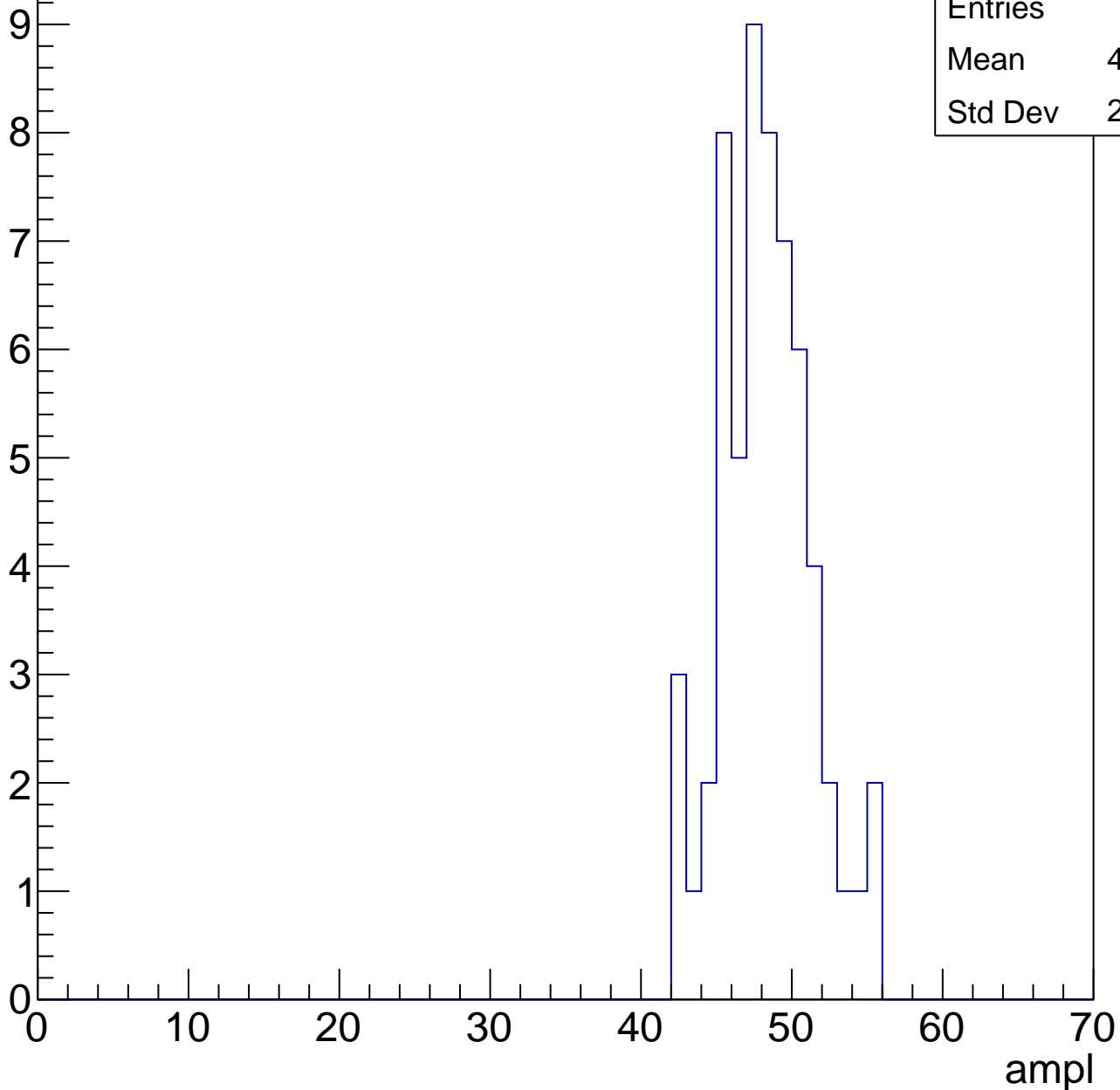


B1L003S, U20-ch61, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	47.83
Std Dev	2.975

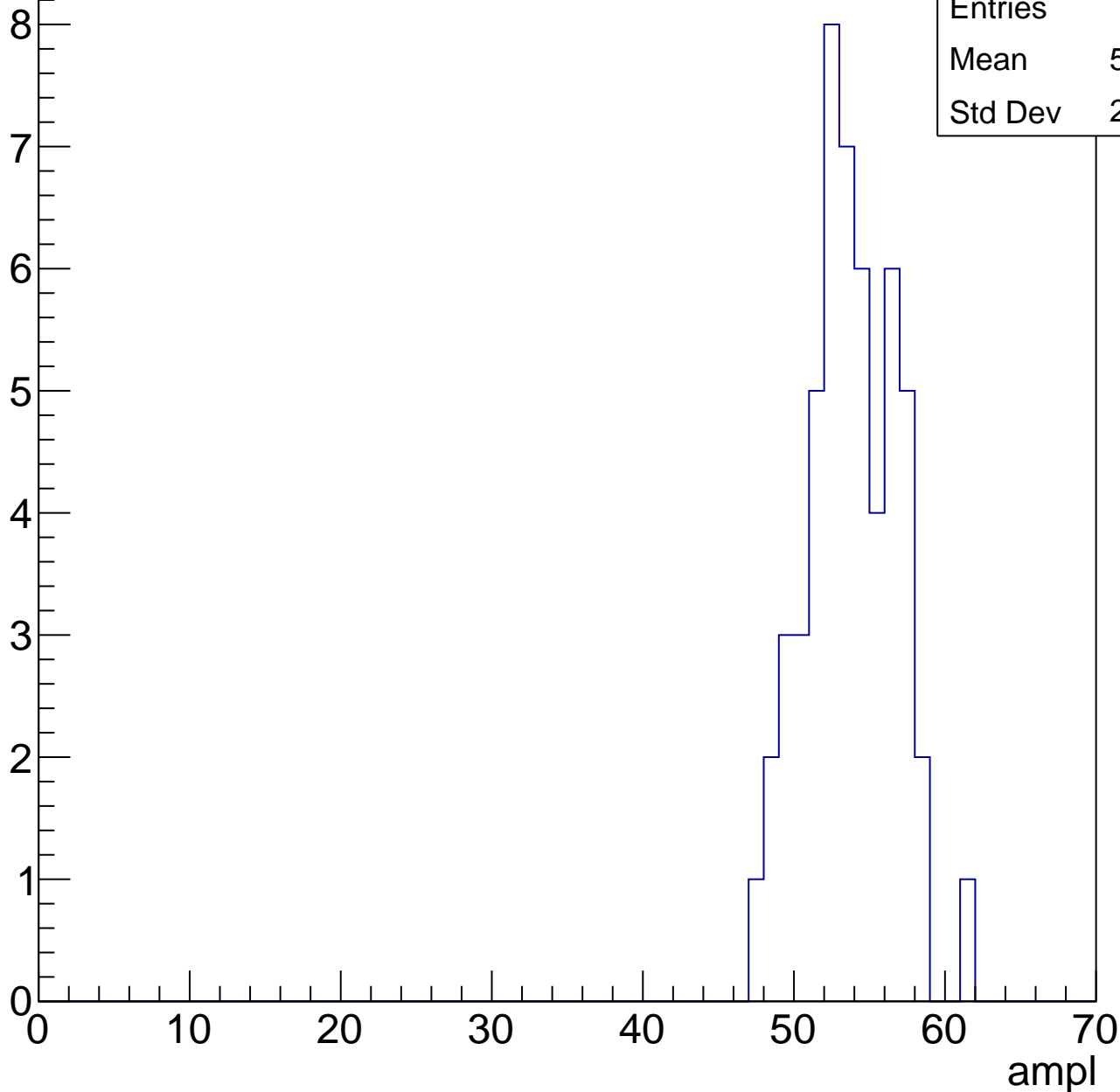


B1L003S, U20-ch61, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	53.28
Std Dev	2.929

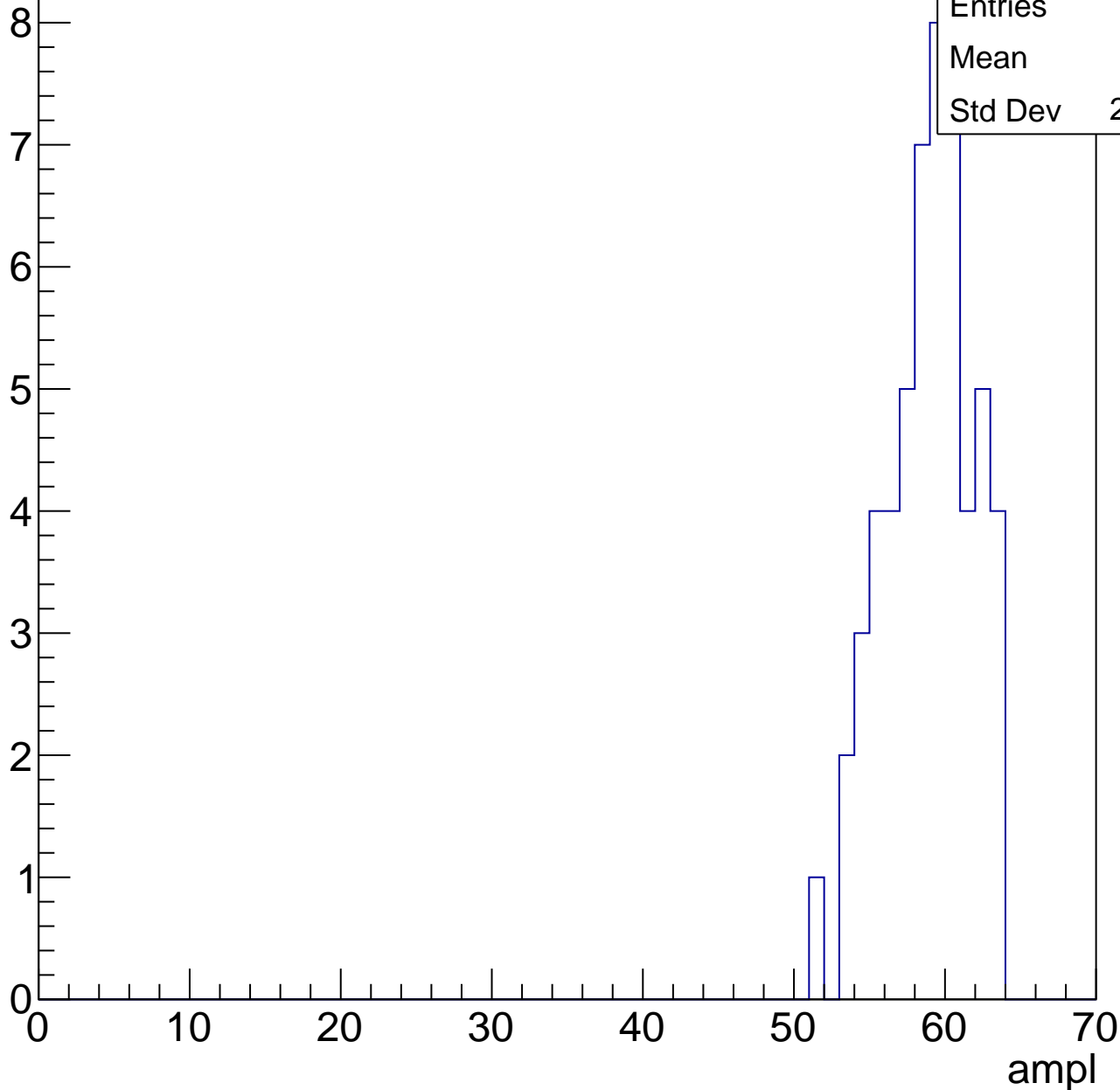


B1L003S, U20-ch61, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	58.4
Std Dev	2.864

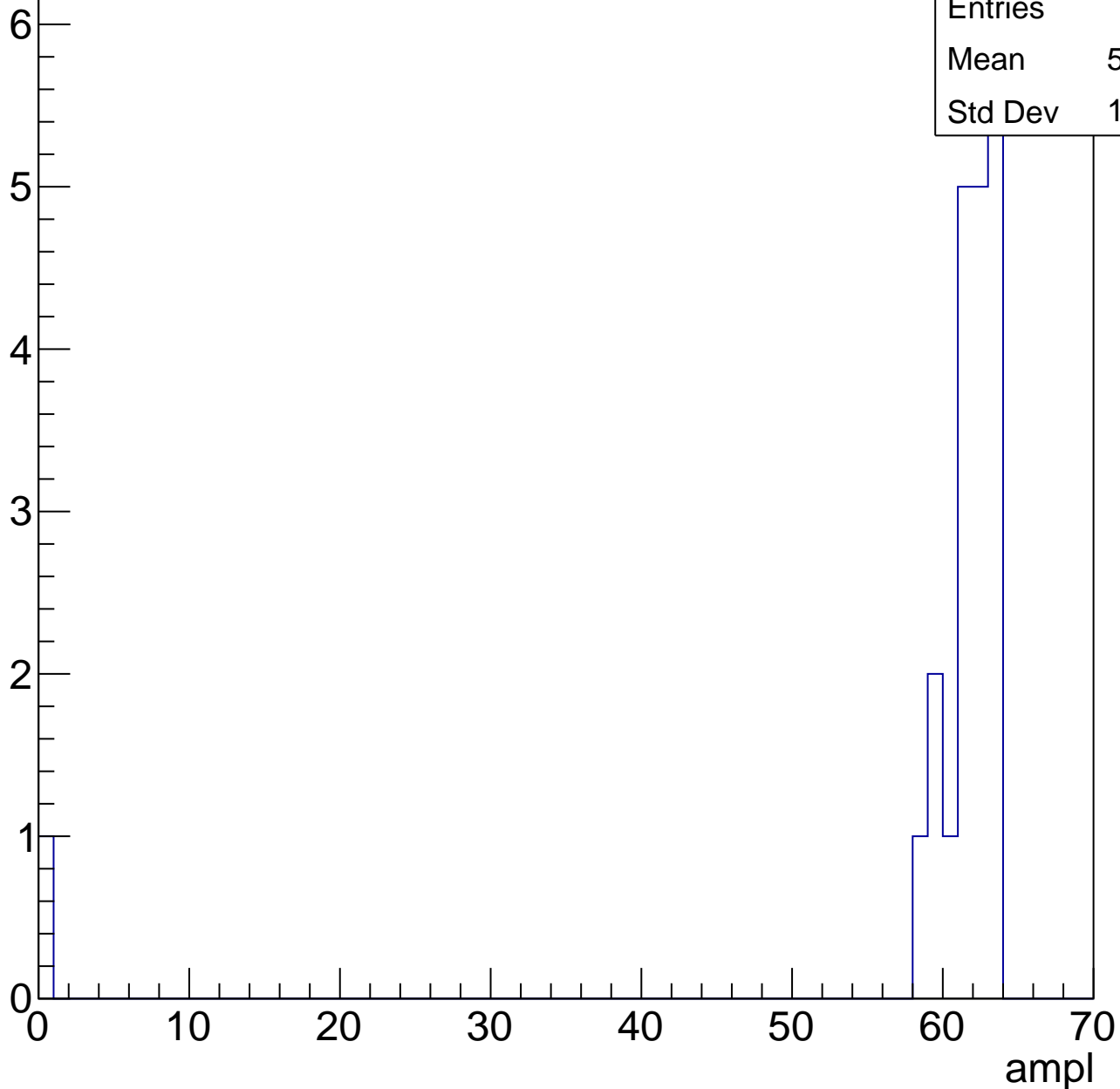


B1L003S, U20-ch61, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.52
Std Dev	13.16



B1L003S, U20-ch61, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch62, adc0

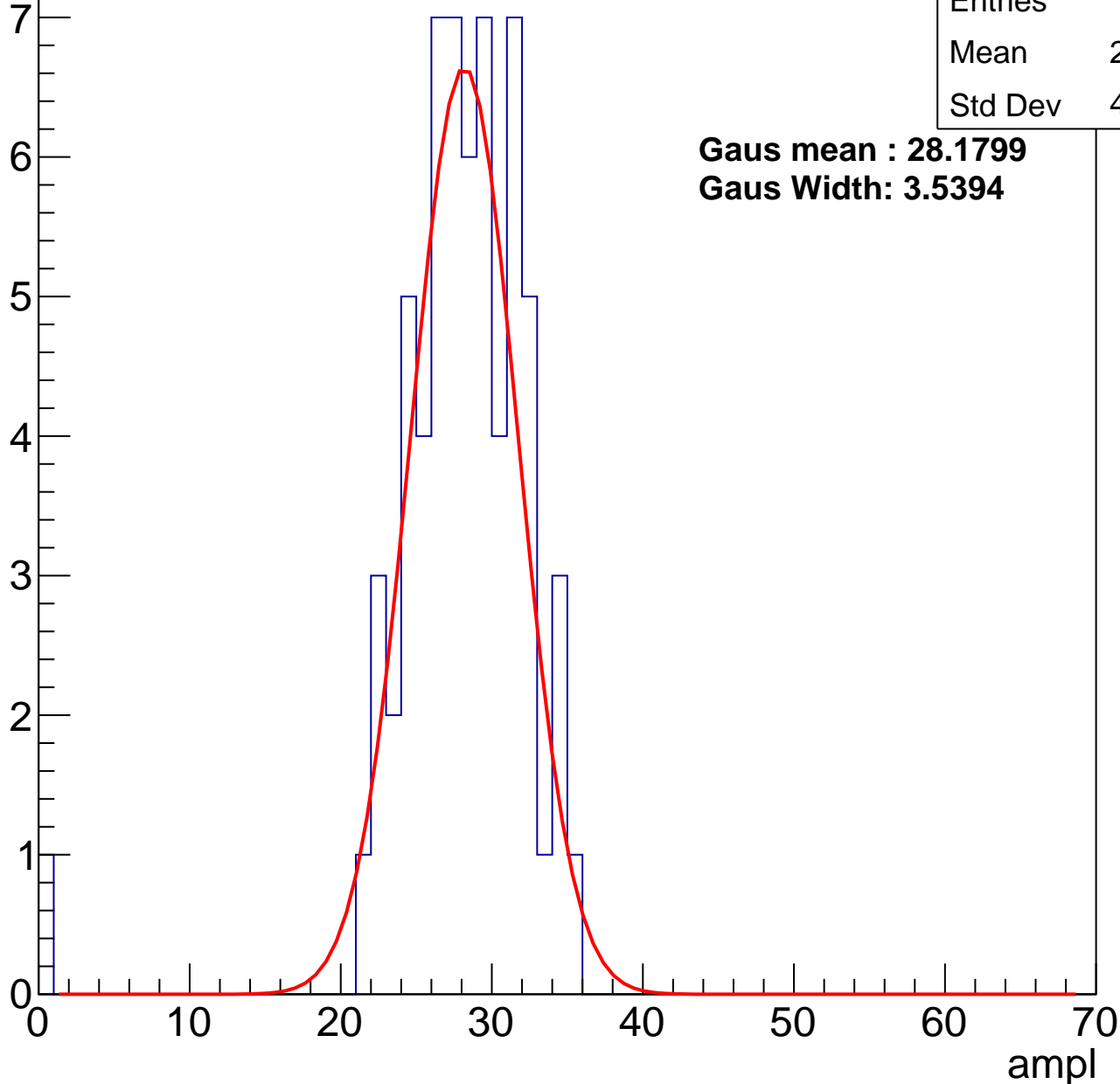
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	27.53
Std Dev	4.802

Gaus mean : 28.1799

Gaus Width: 3.5394



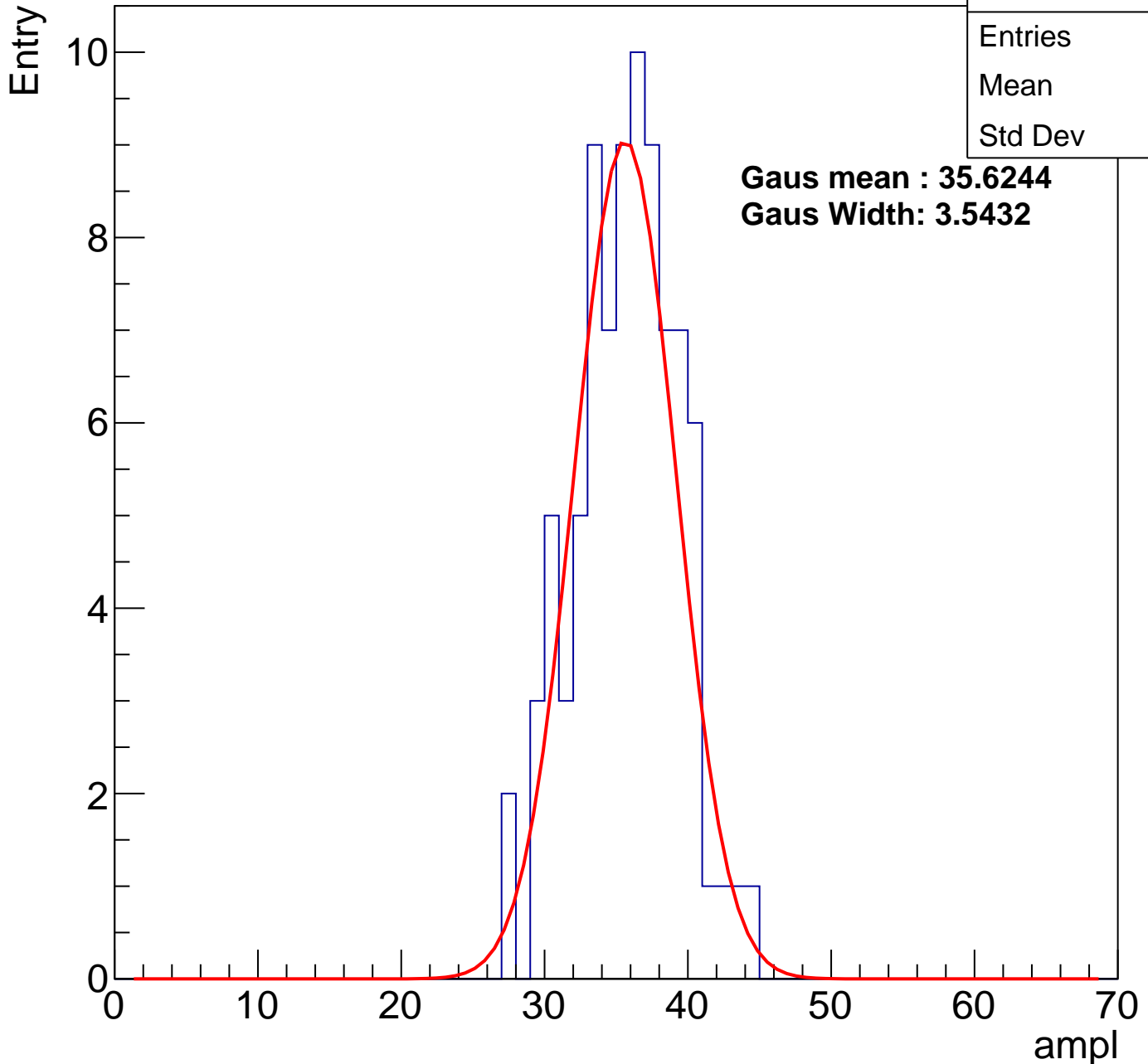
B1L003S, U20-ch62, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	35.3
Std Dev	3.56

Gaus mean : 35.6244

Gaus Width: 3.5432



B1L003S, U20-ch62, adc2

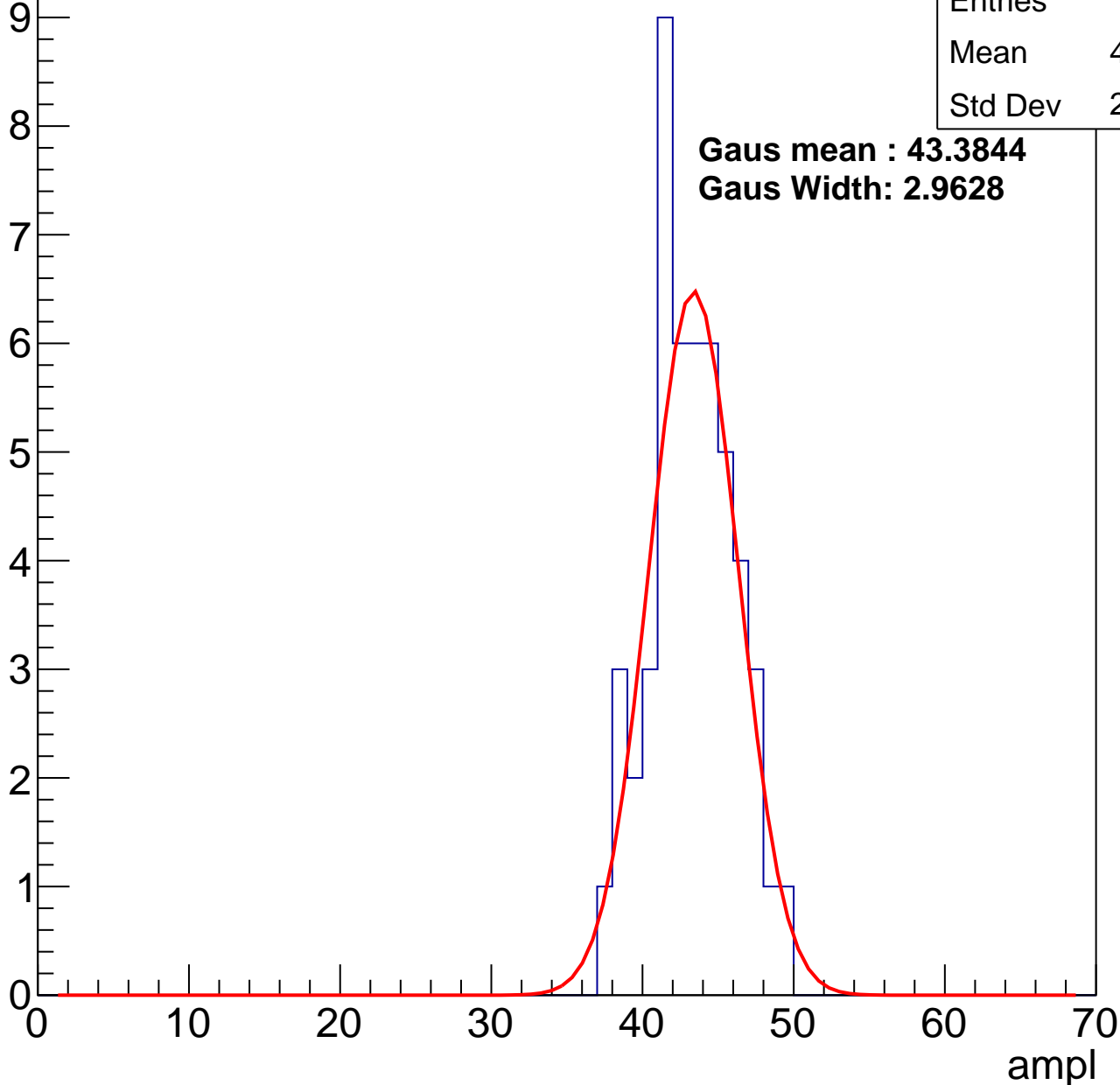
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	42.78
Std Dev	2.759

Gaus mean : 43.3844

Gaus Width: 2.9628



B1L003S, U20-ch62, adc3

calib_packv5_042523_0143.root, FC#13, port D2

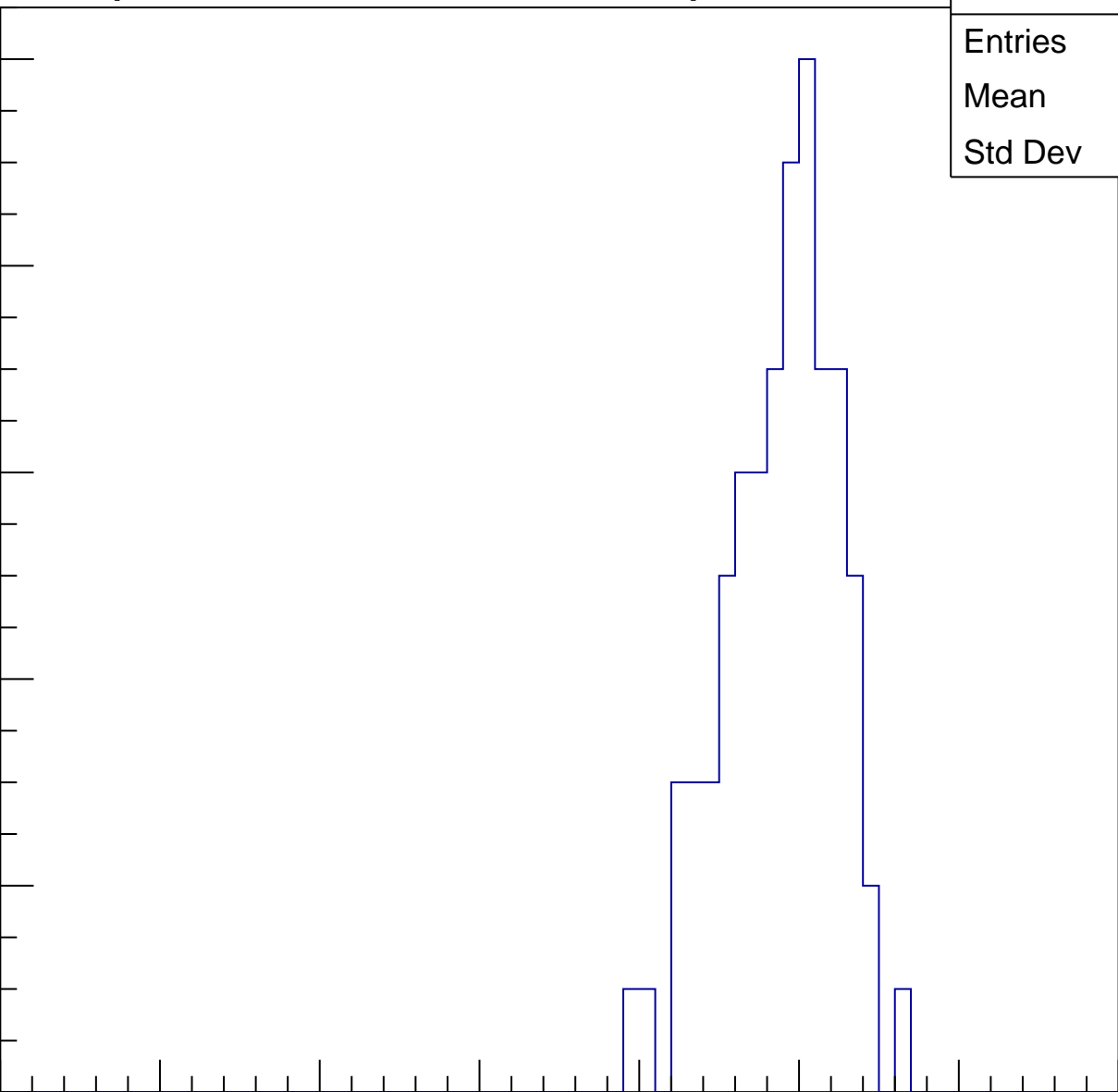
Entries	76
Mean	48.37
Std Dev	3.49

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

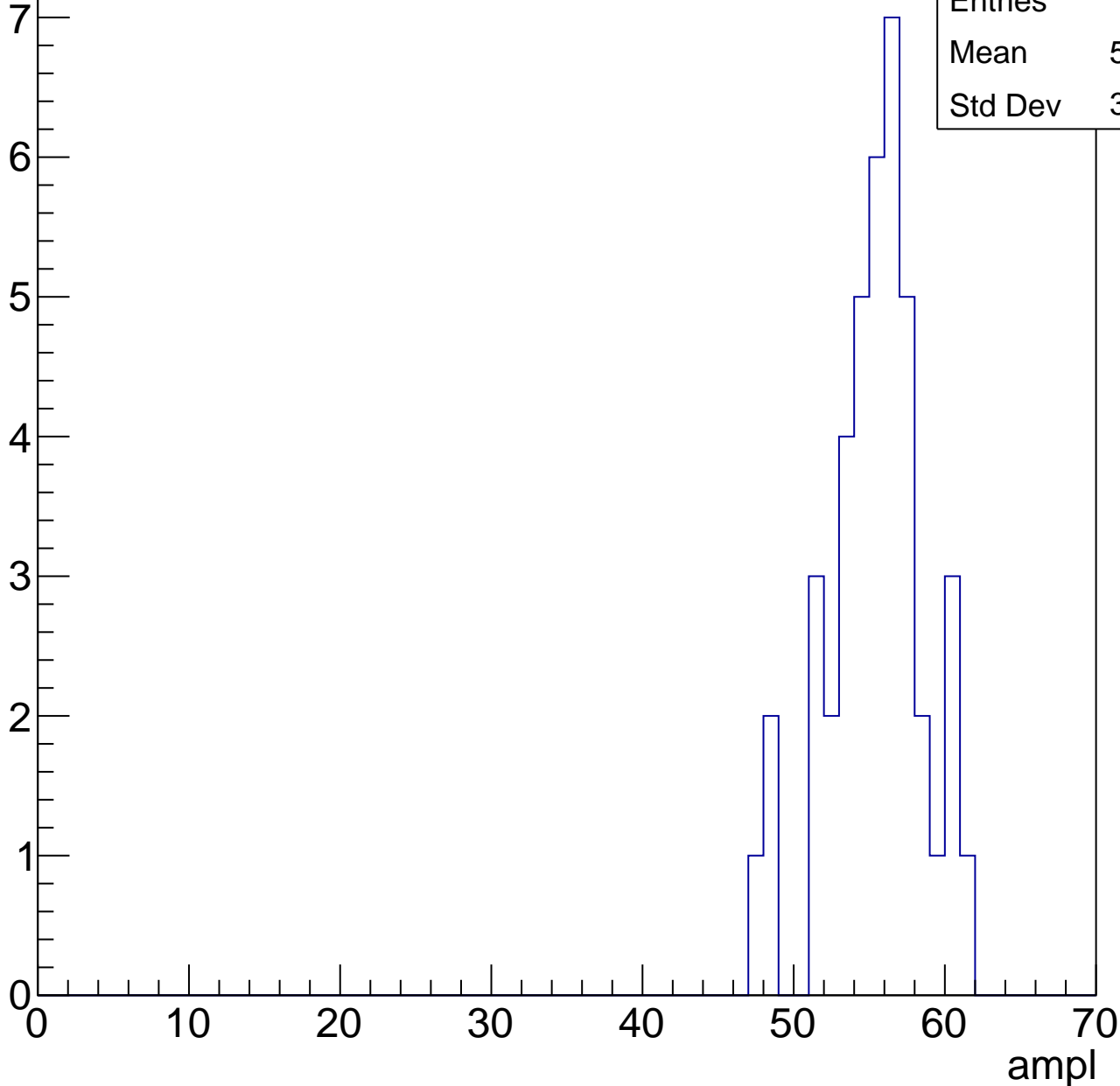


B1L003S, U20-ch62, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

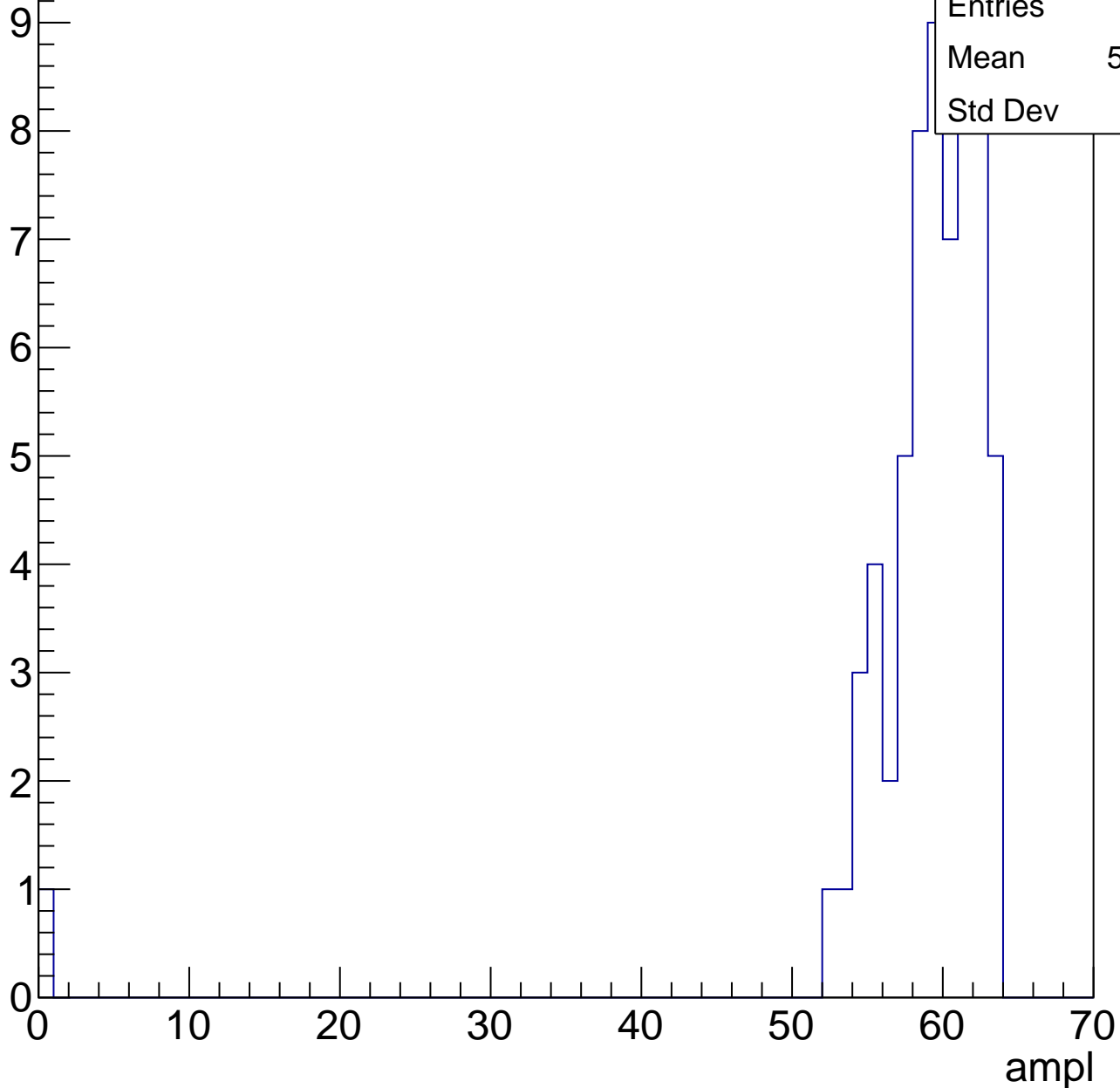
Entries	42
Mean	54.88
Std Dev	3.164



B1L003S, U20-ch62, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch62, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch62, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U20-ch63, adc0

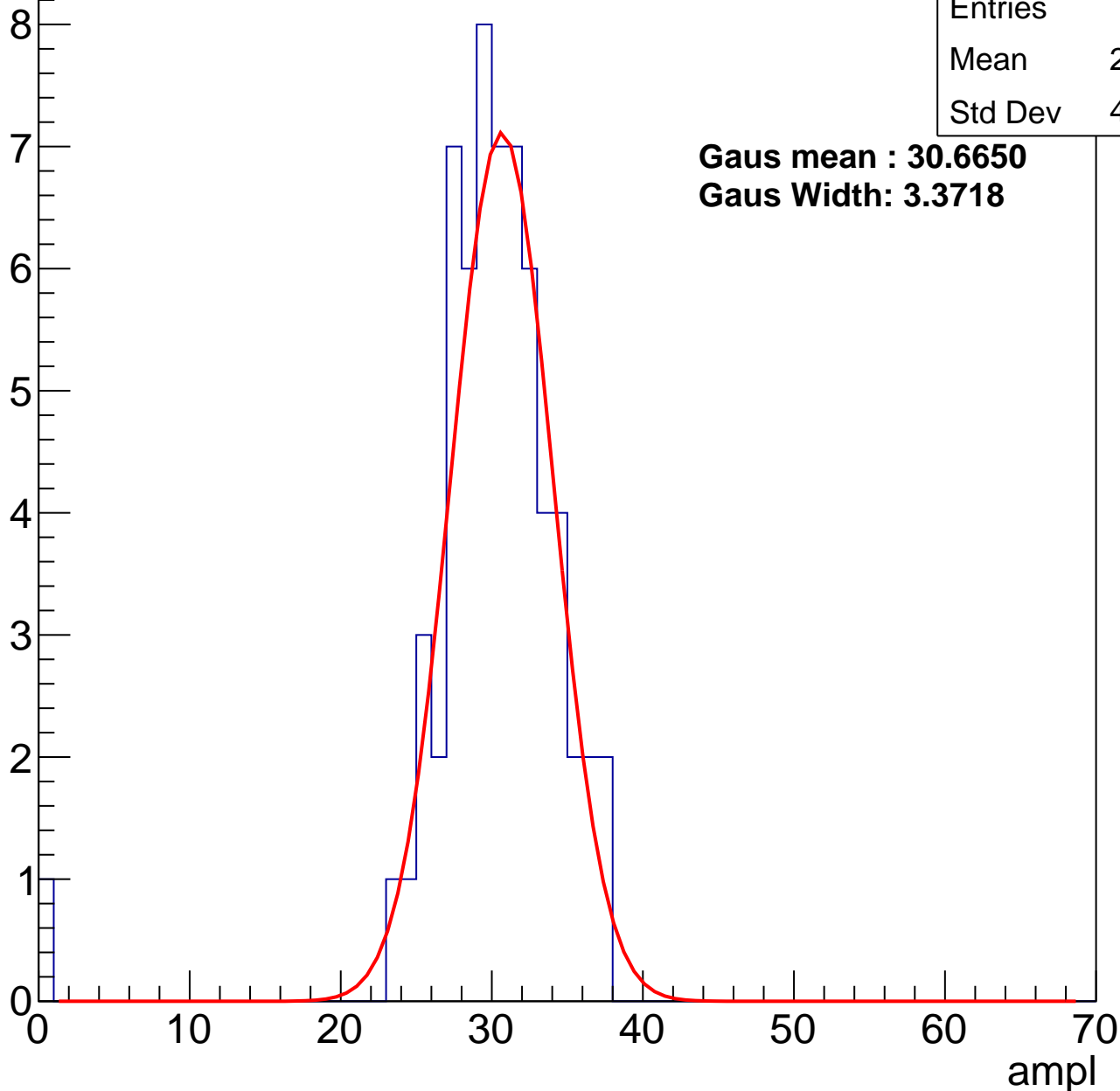
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.62
Std Dev	4.926

Gaus mean : 30.6650

Gaus Width: 3.3718



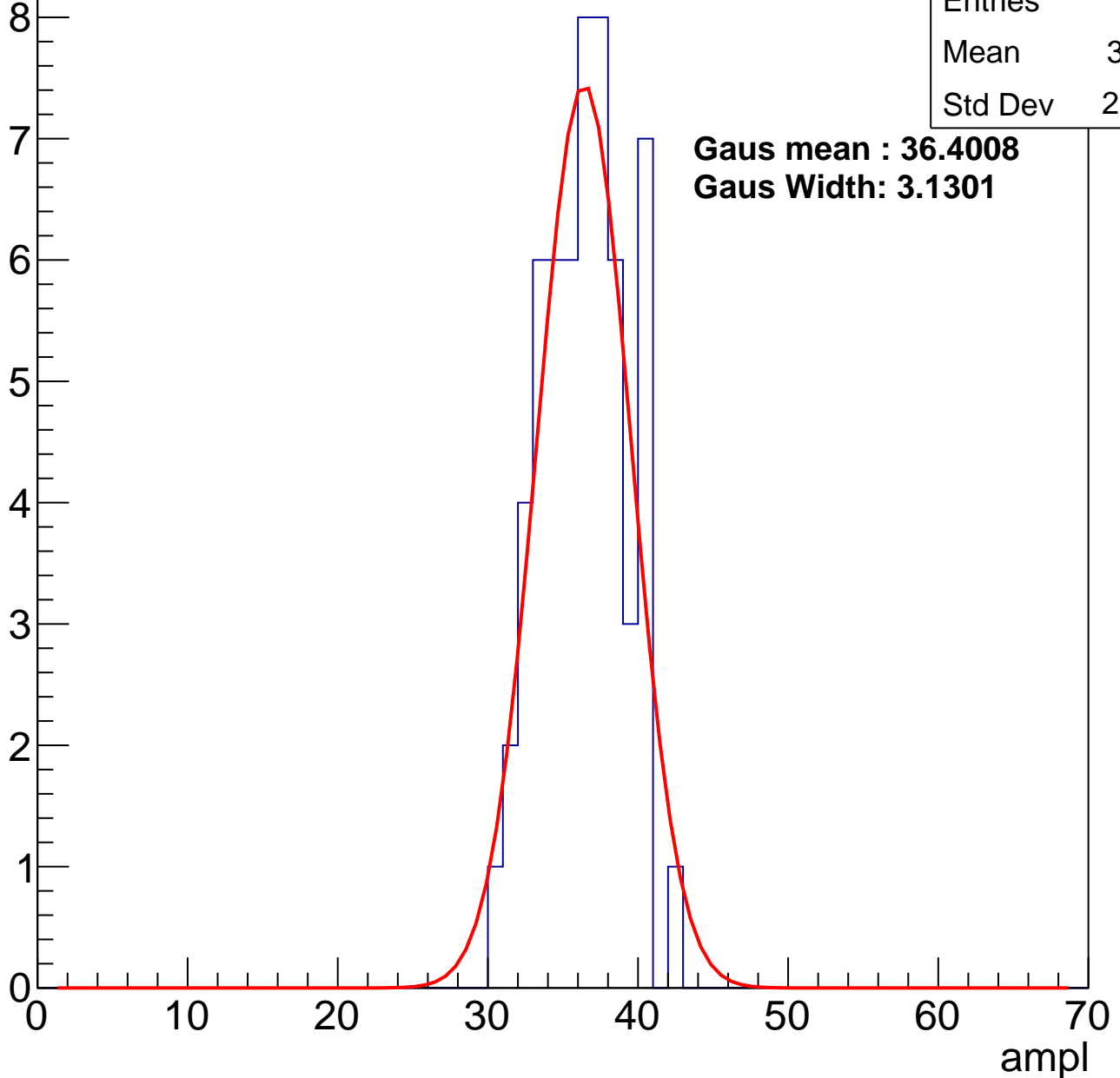
B1L003S, U20-ch63, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	35.91
Std Dev	2.756

Gaus mean : 36.4008
Gaus Width: 3.1301



B1L003S, U20-ch63, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	43.25
Std Dev	3.459

Gaus mean : 43.3904

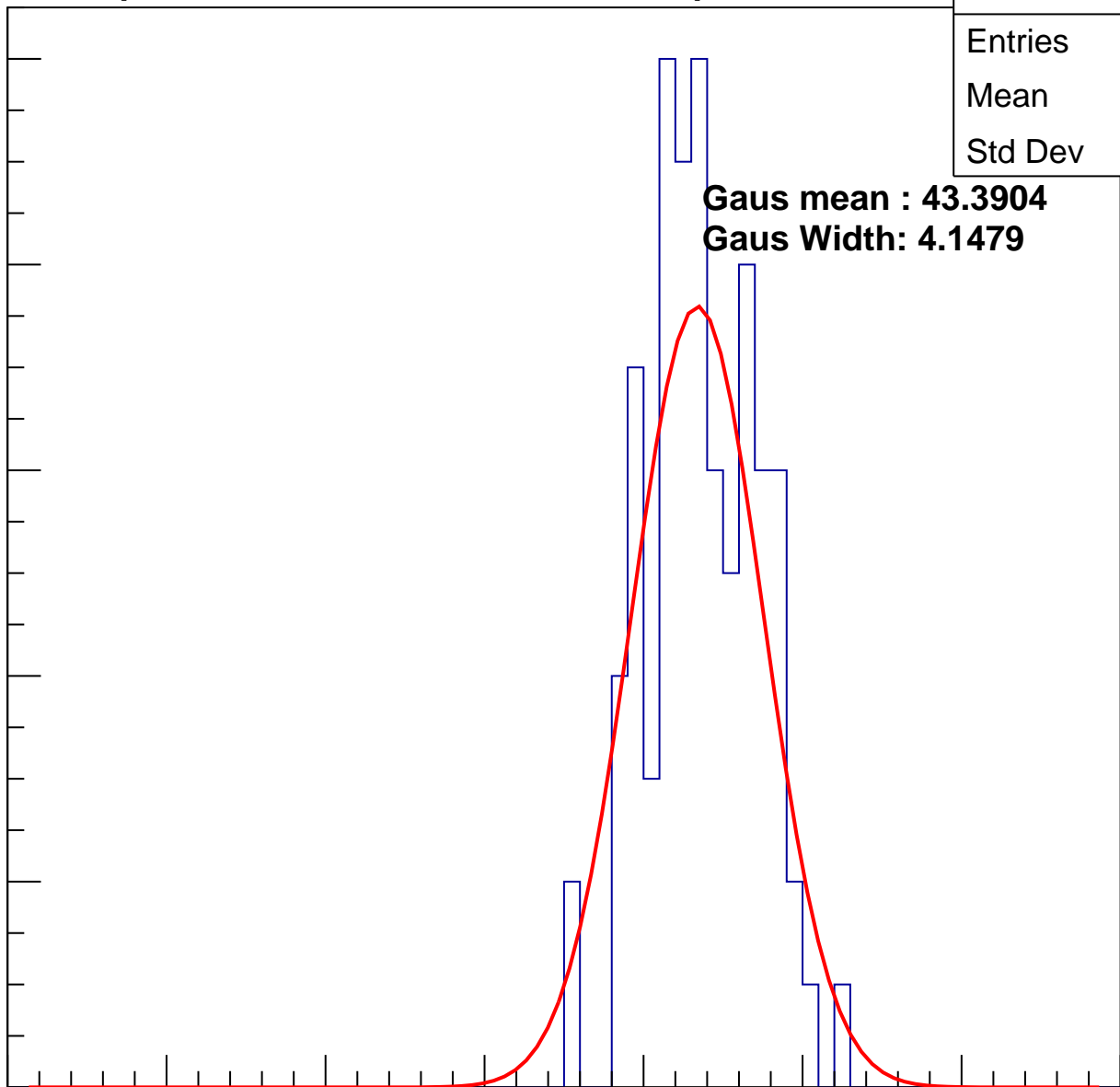
Gaus Width: 4.1479

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

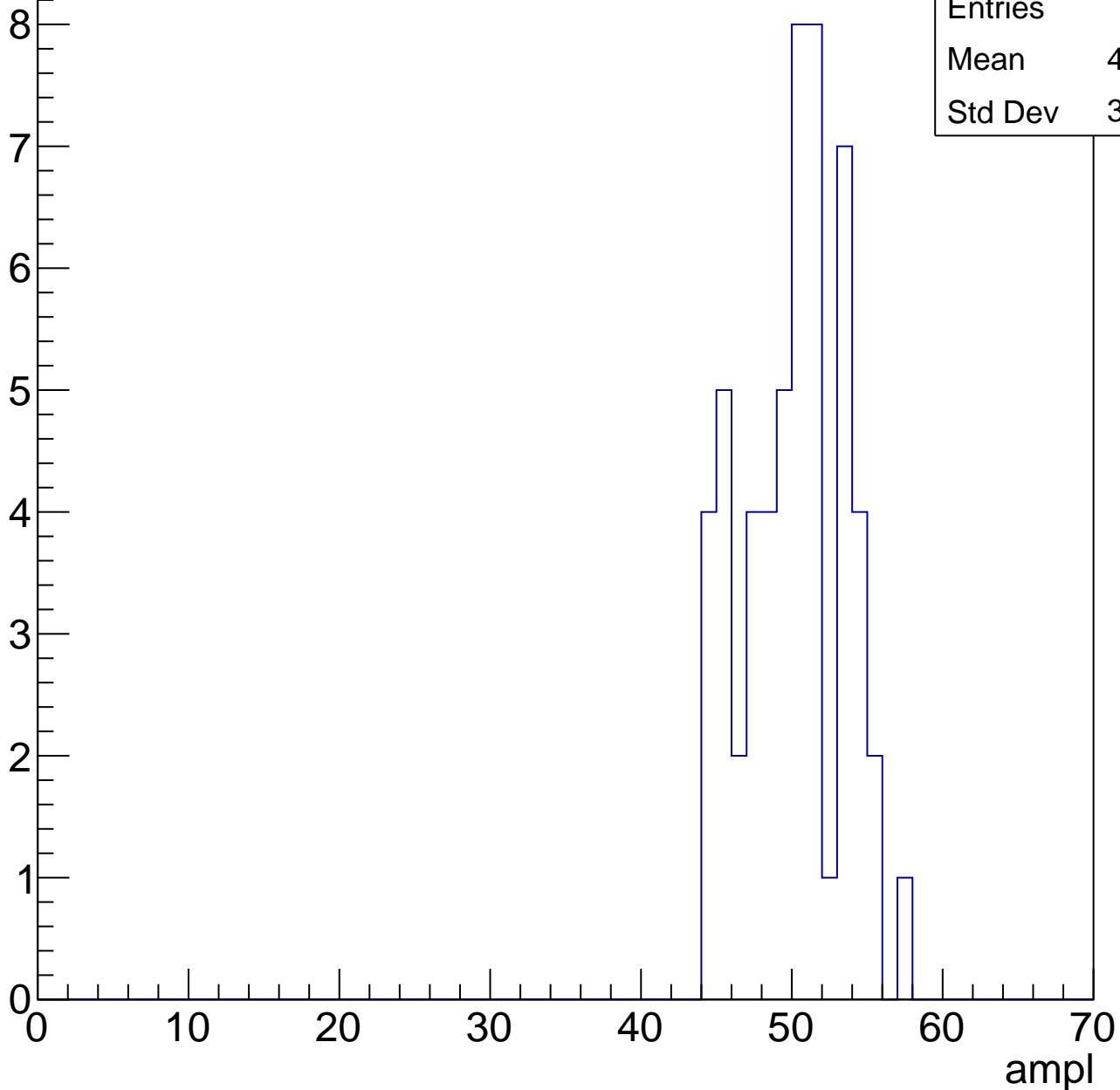


B1L003S, U20-ch63, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

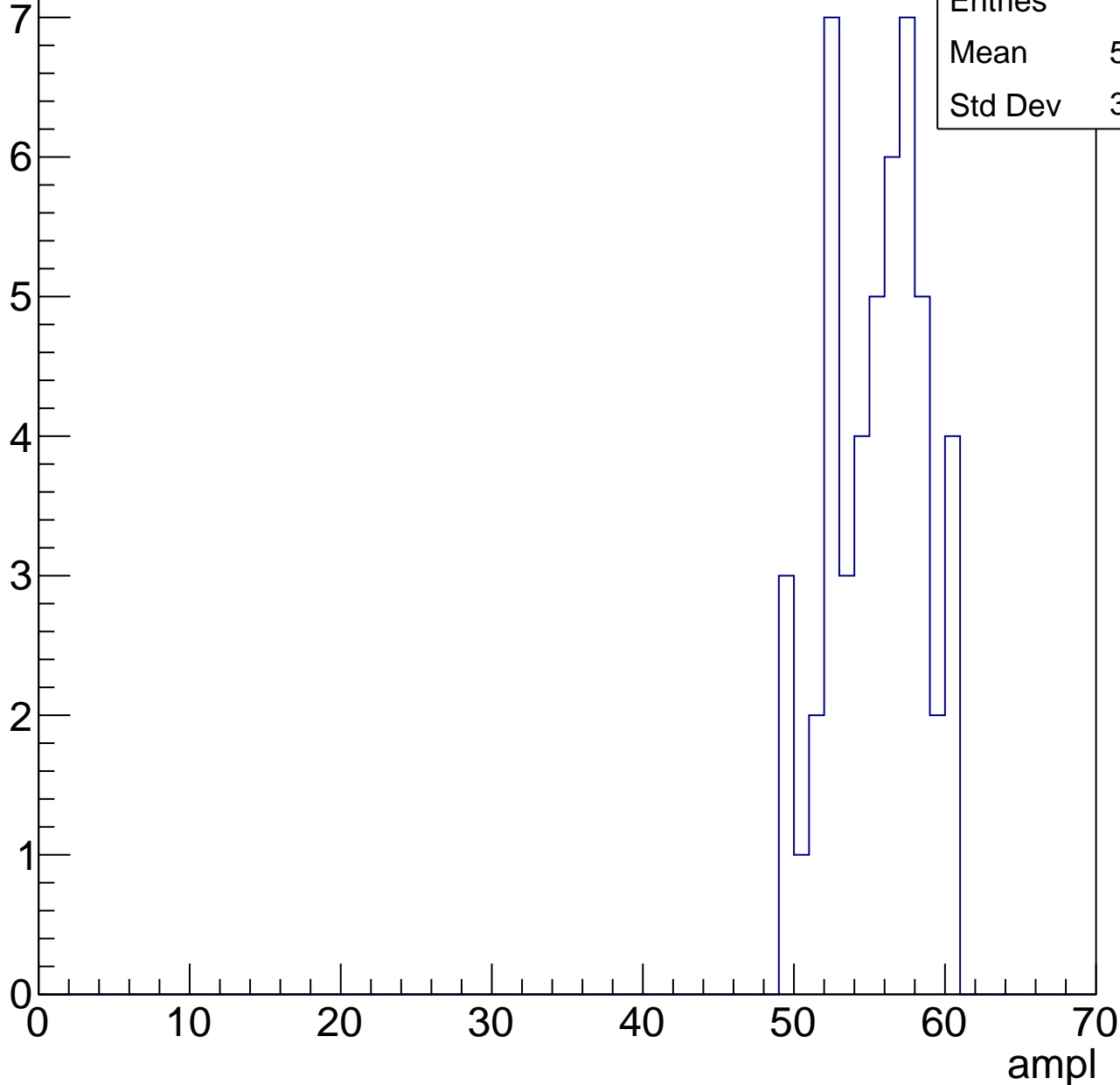
Entries	55
Mean	49.67
Std Dev	3.276



B1L003S, U20-ch63, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch63, adc5

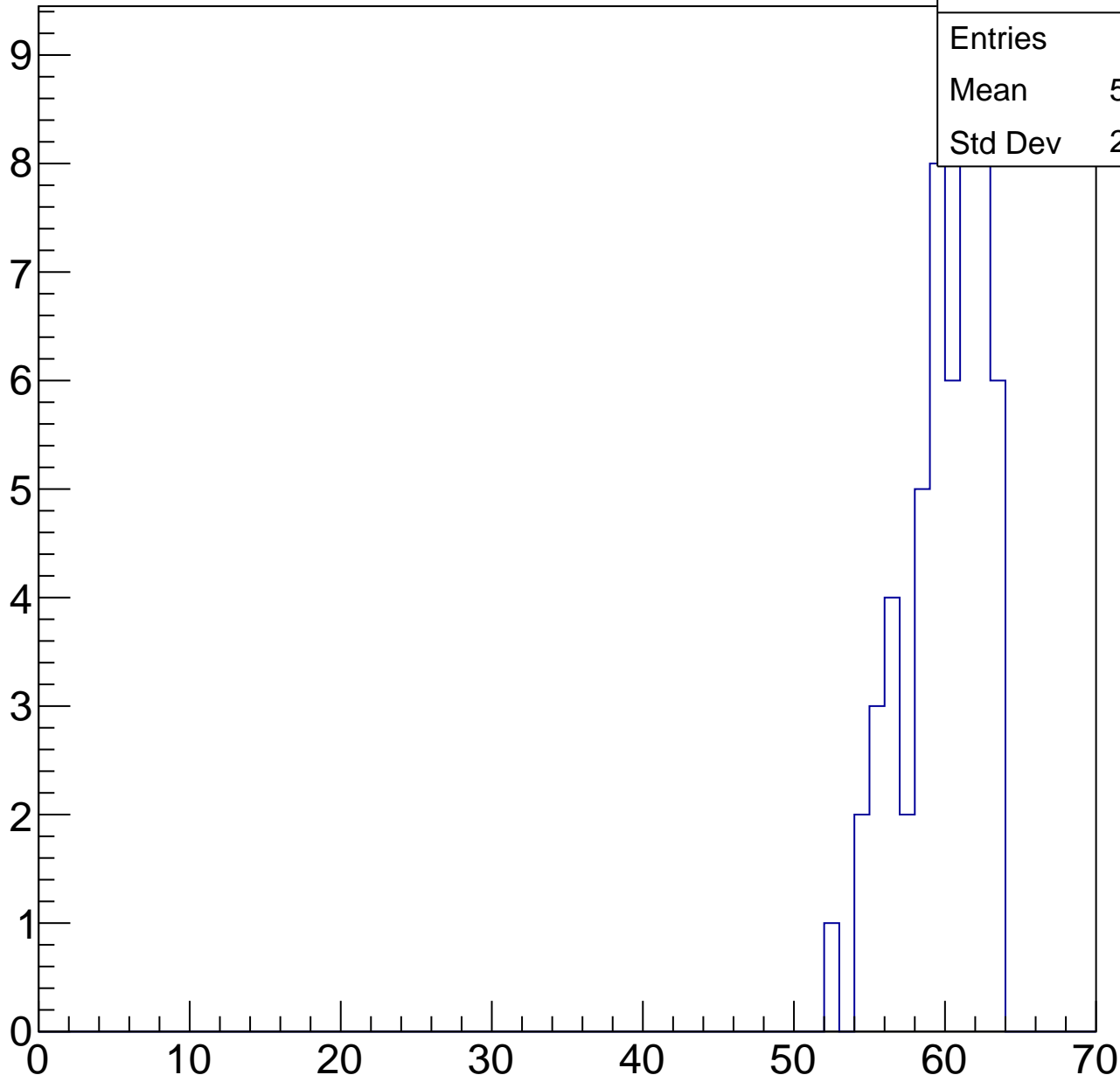
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	59.45
Std Dev	2.709

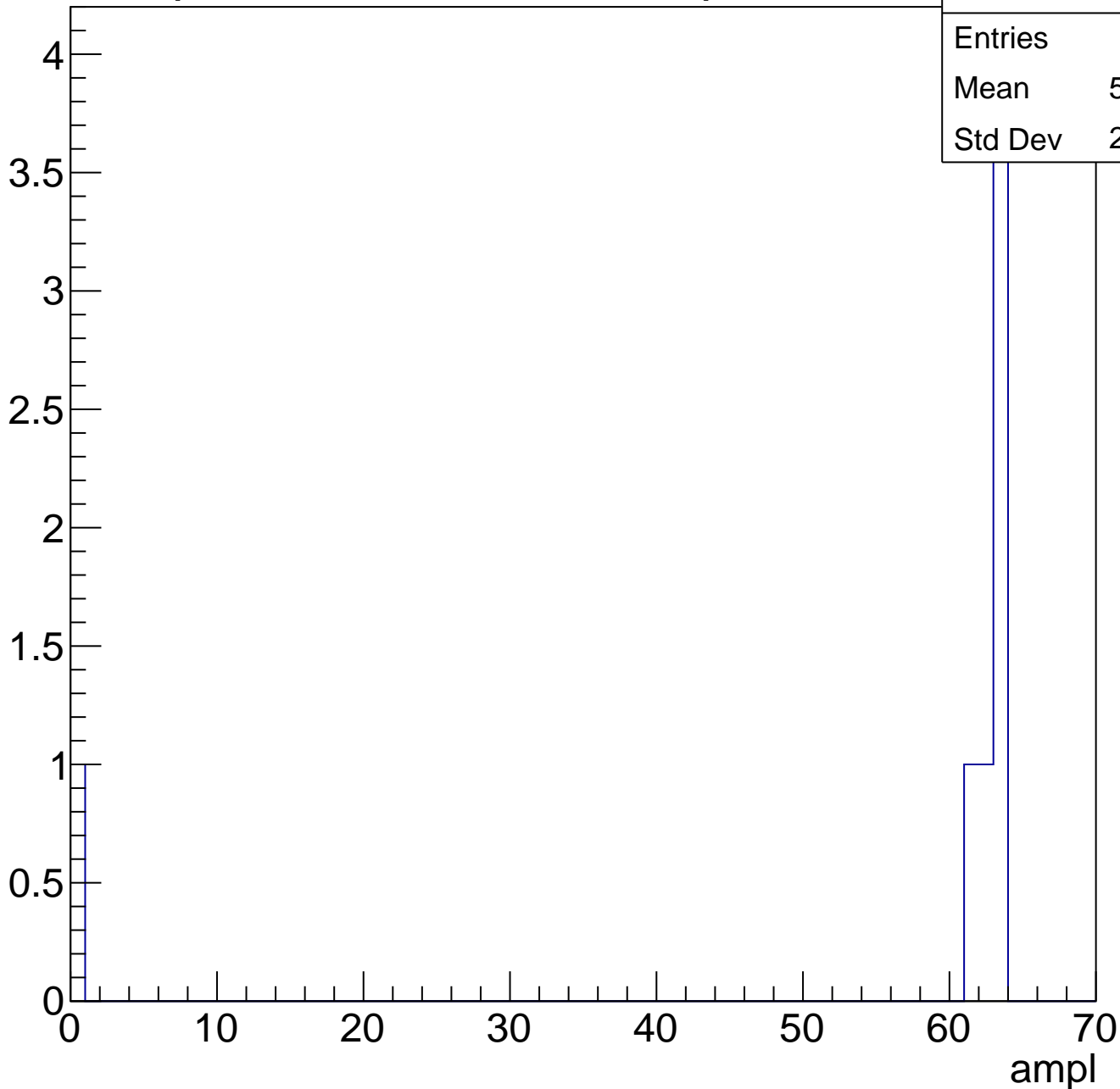
ampl



B1L003S, U20-ch63, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch63, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L003S, U20-ch64, adc0

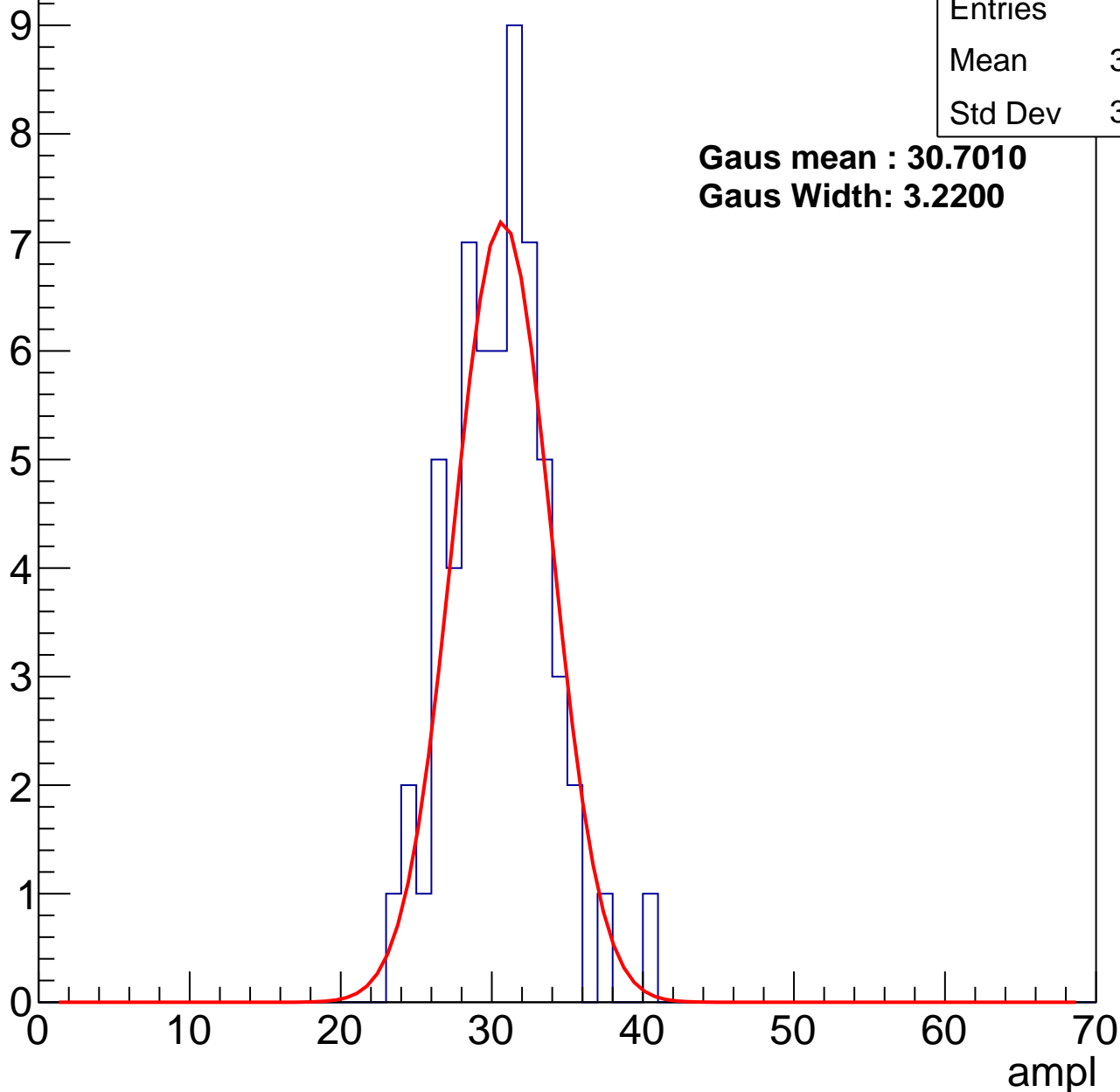
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	30.02
Std Dev	3.227

Gaus mean : 30.7010

Gaus Width: 3.2200



B1L003S, U20-ch64, adc1

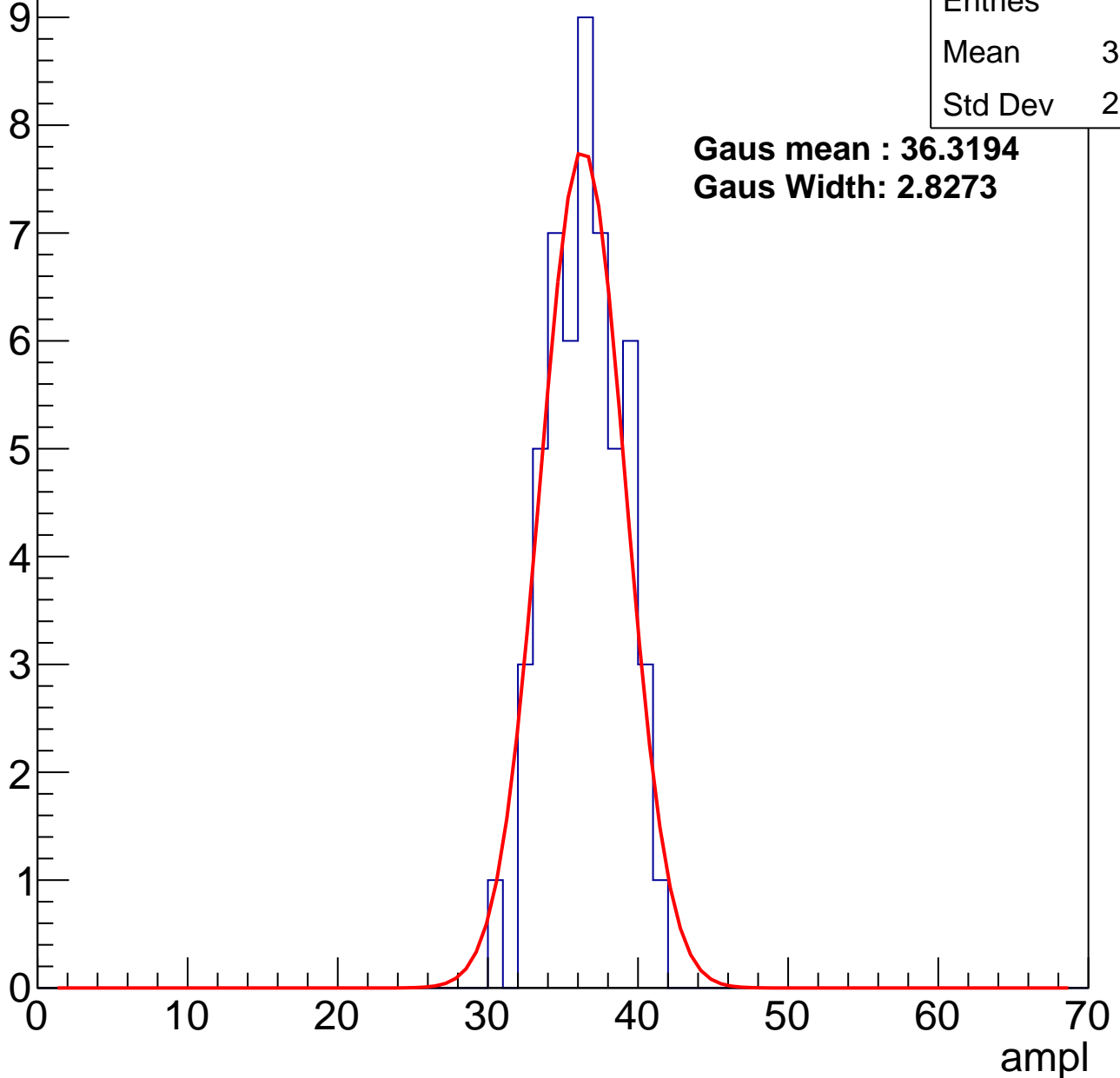
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.98
Std Dev	2.446

Gaus mean : 36.3194

Gaus Width: 2.8273



B1L003S, U20-ch64, adc2

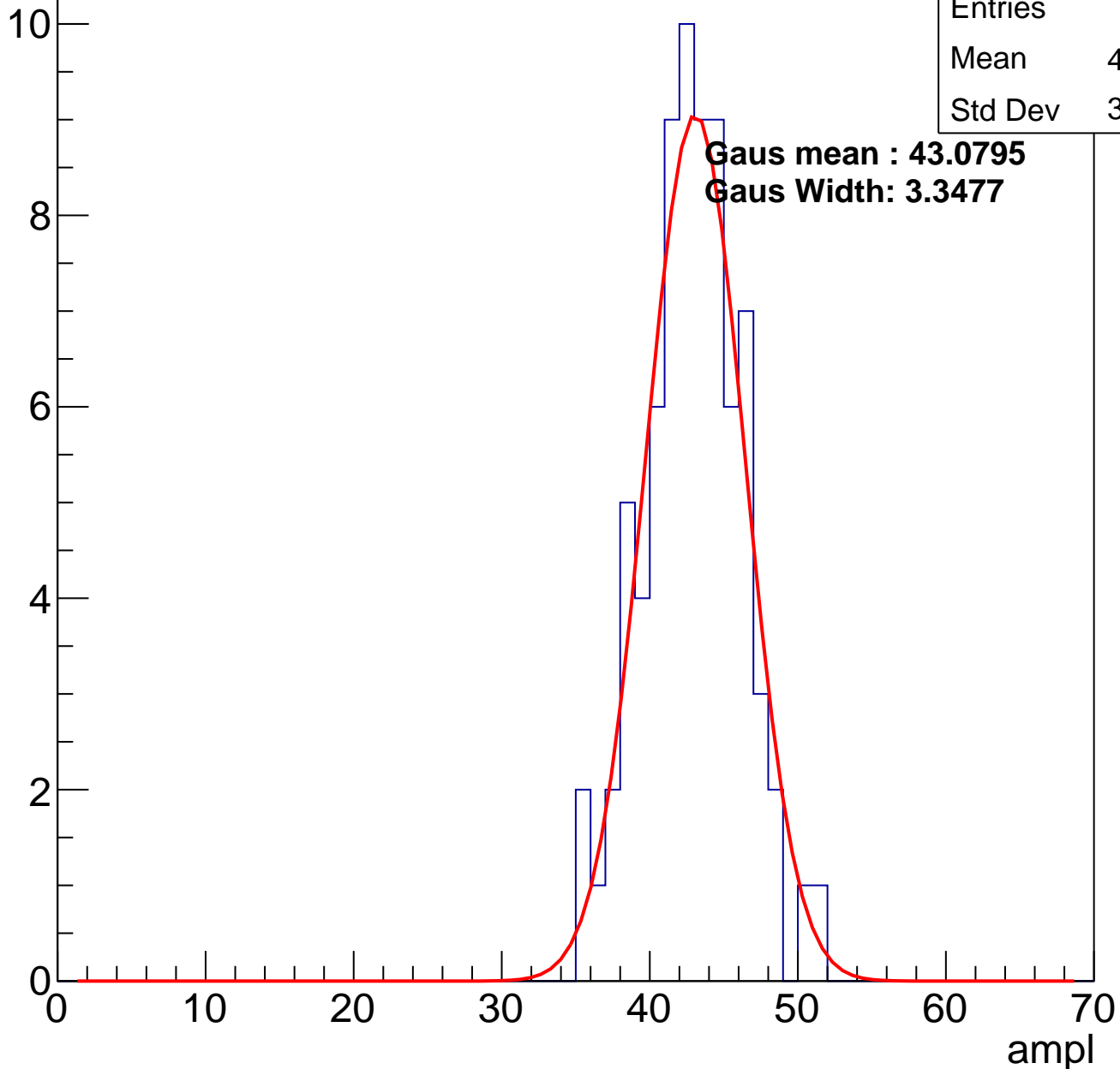
calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	42.44
Std Dev	3.265

Gaus mean : 43.0795

Gaus Width: 3.3477

Entry

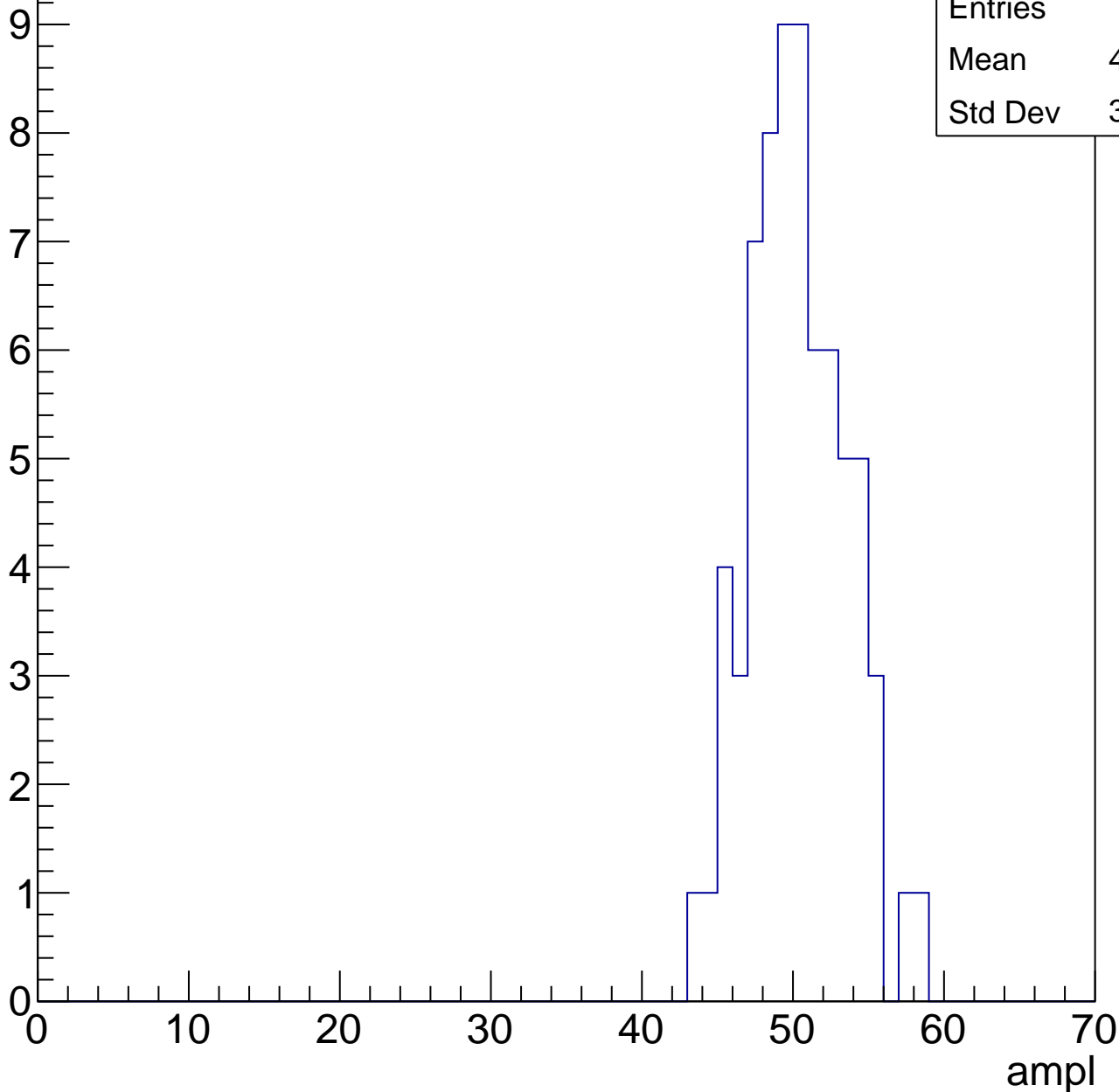


B1L003S, U20-ch64, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.88
Std Dev	3.137

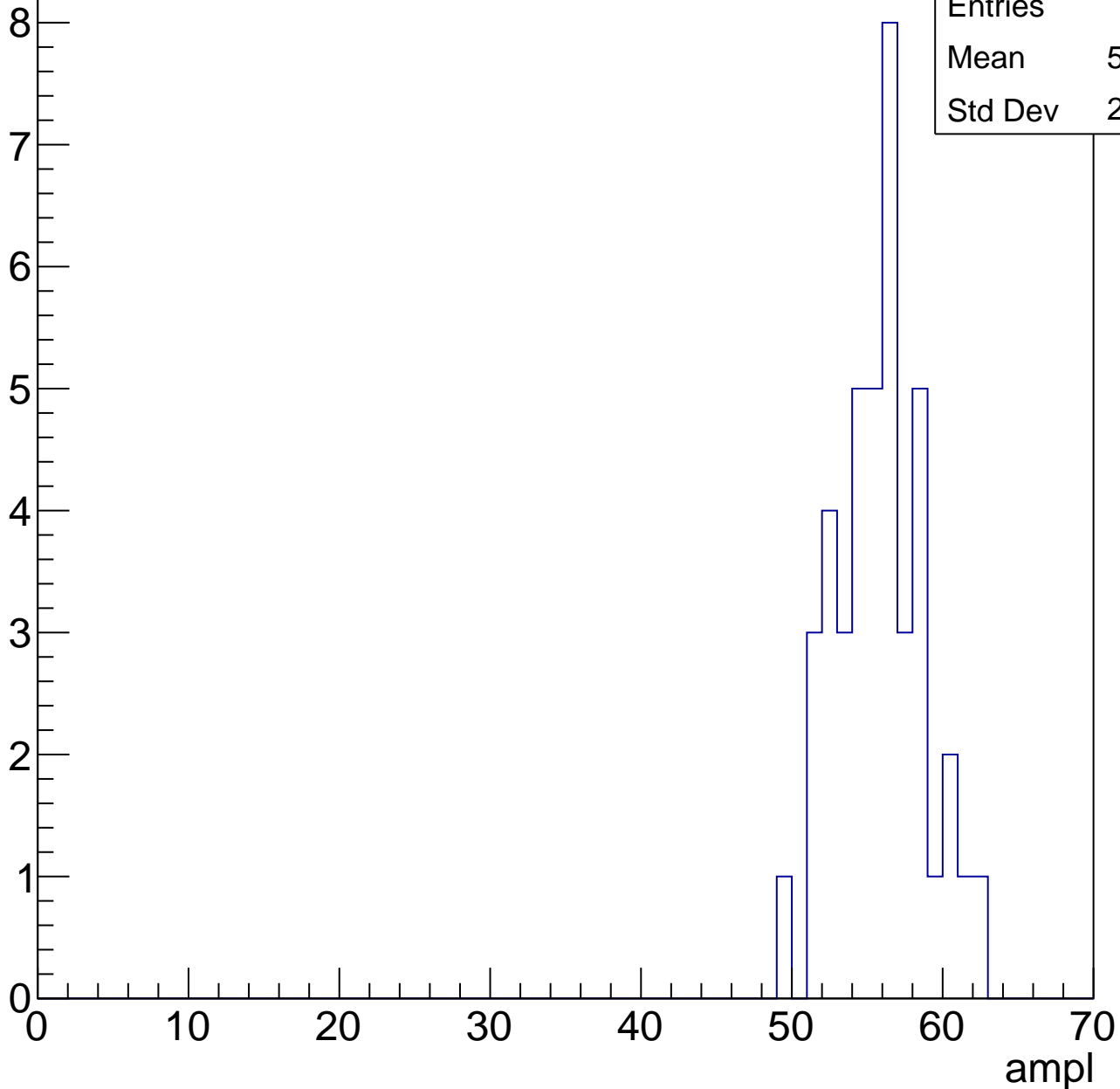


B1L003S, U20-ch64, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

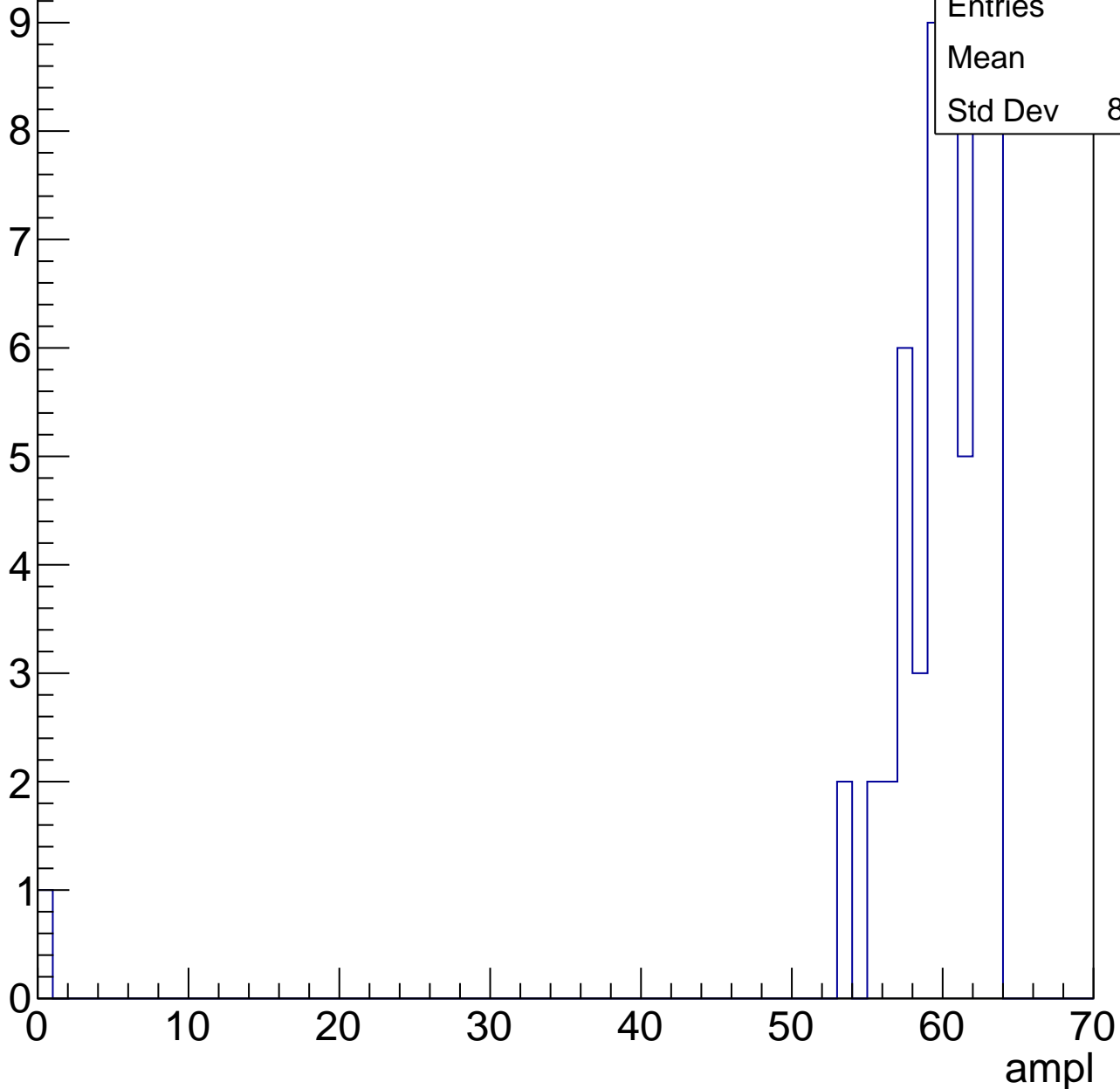
Entries	42
Mean	55.36
Std Dev	2.877



B1L003S, U20-ch64, adc5

calib_packv5_042523_0143.root, FC#13, port D2

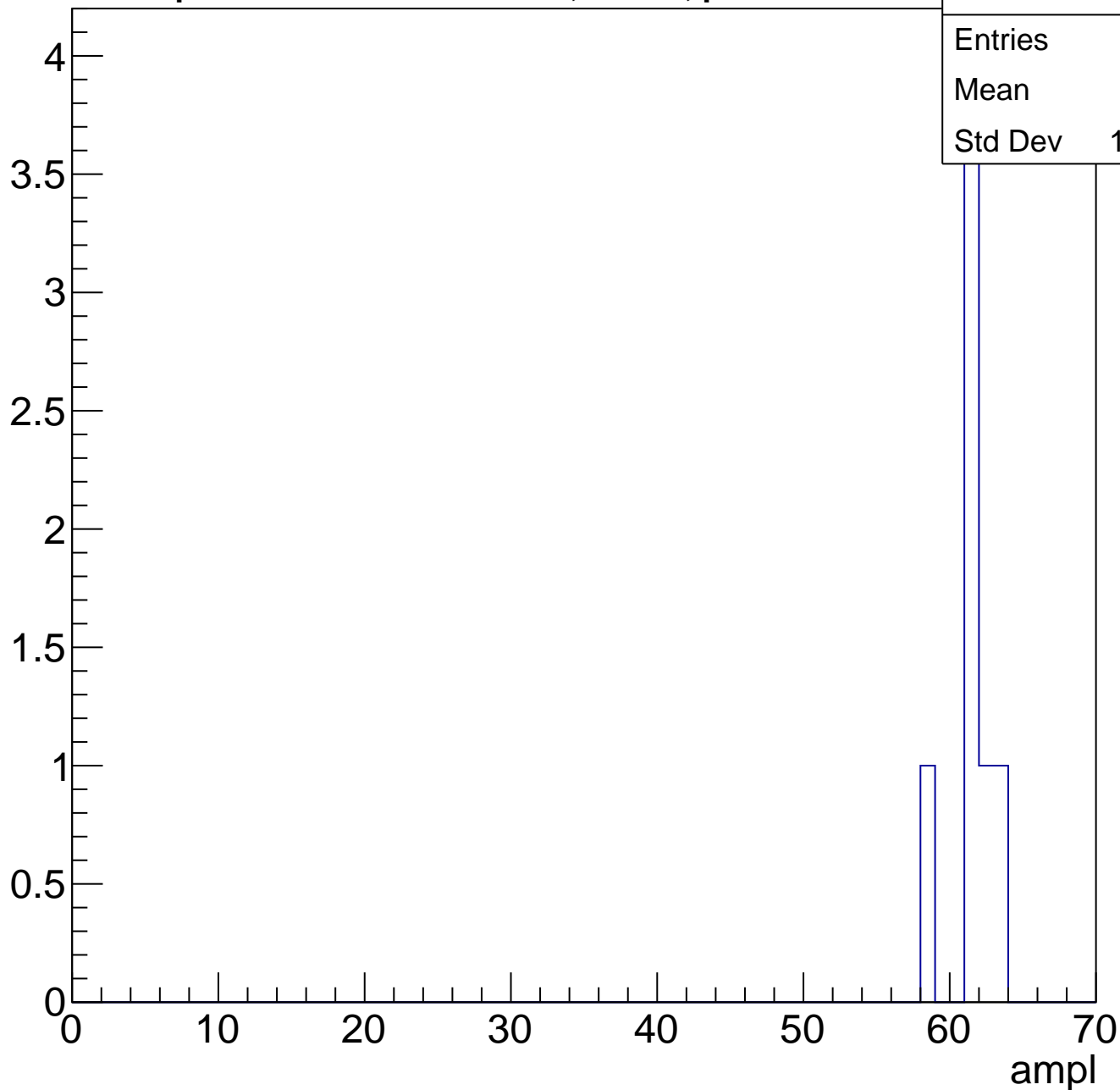
Entry



B1L003S, U20-ch64, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch64, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch65, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	30.19
Std Dev	5.103

Gaus mean : 31.3899

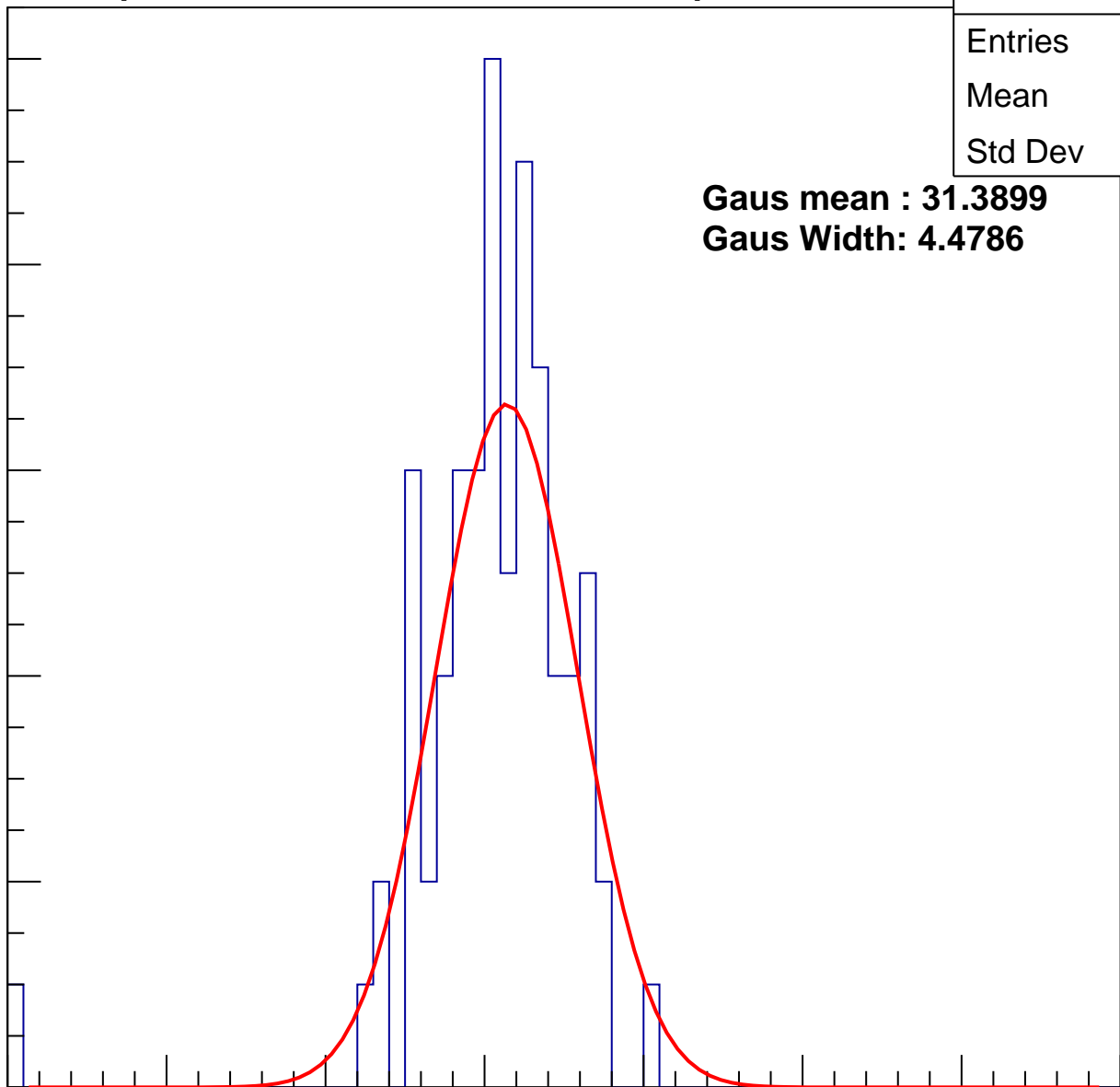
Gaus Width: 4.4786

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



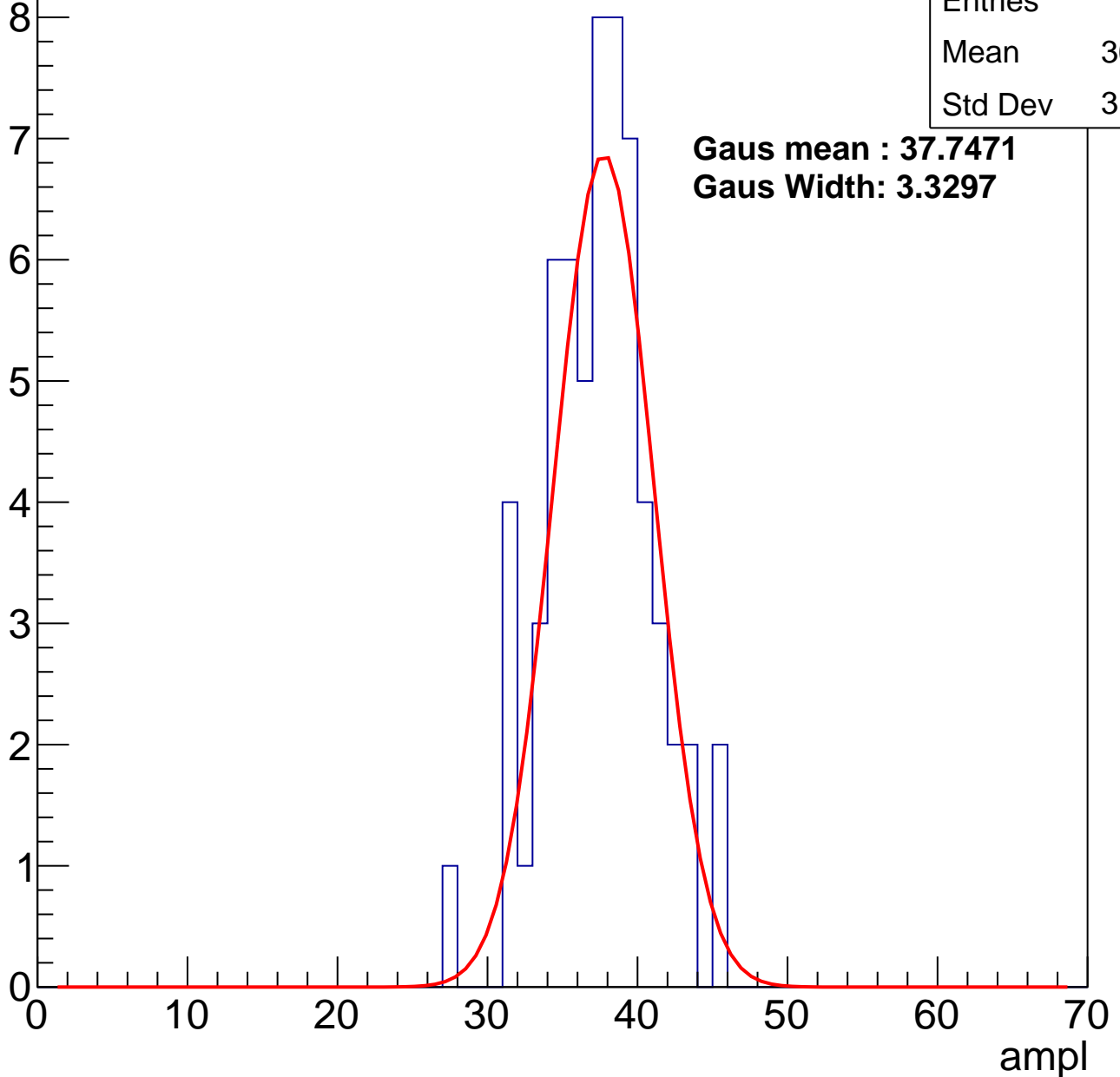
B1L003S, U20-ch65, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.97
Std Dev	3.524

Gaus mean : 37.7471
Gaus Width: 3.3297



B1L003S, U20-ch65, adc2

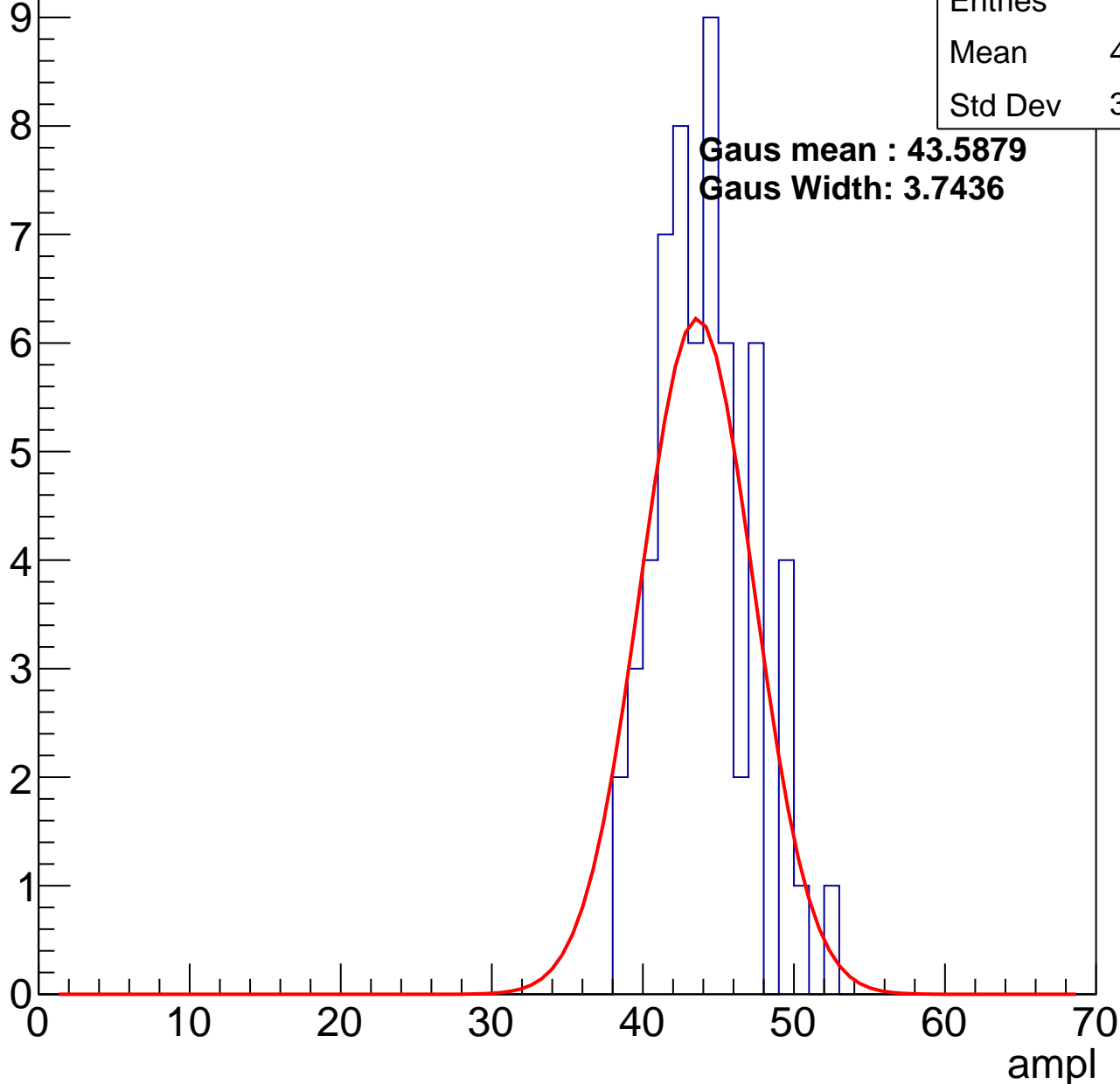
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.59
Std Dev	3.109

Gaus mean : 43.5879

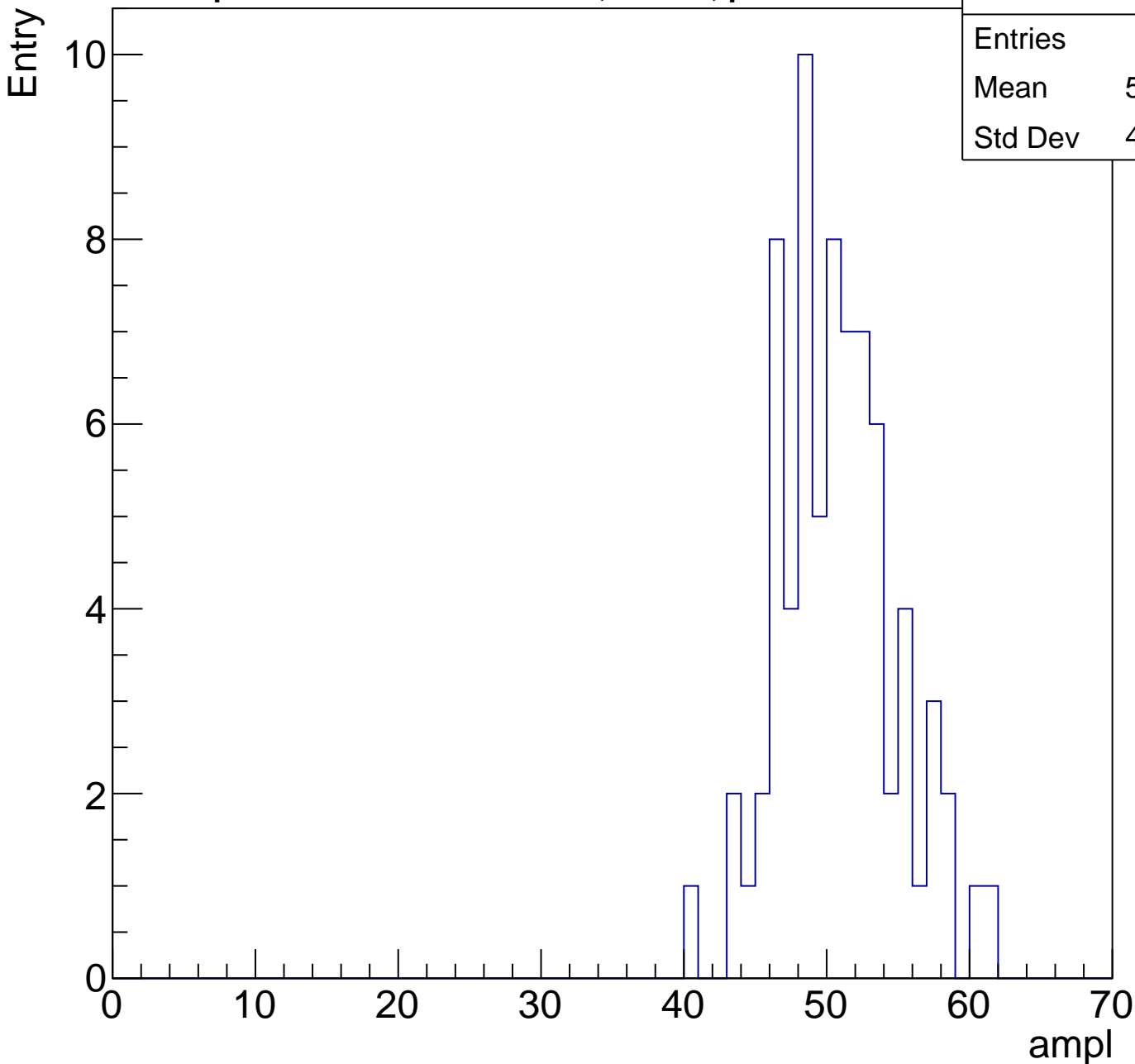
Gaus Width: 3.7436



B1L003S, U20-ch65, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	50.29
Std Dev	4.065

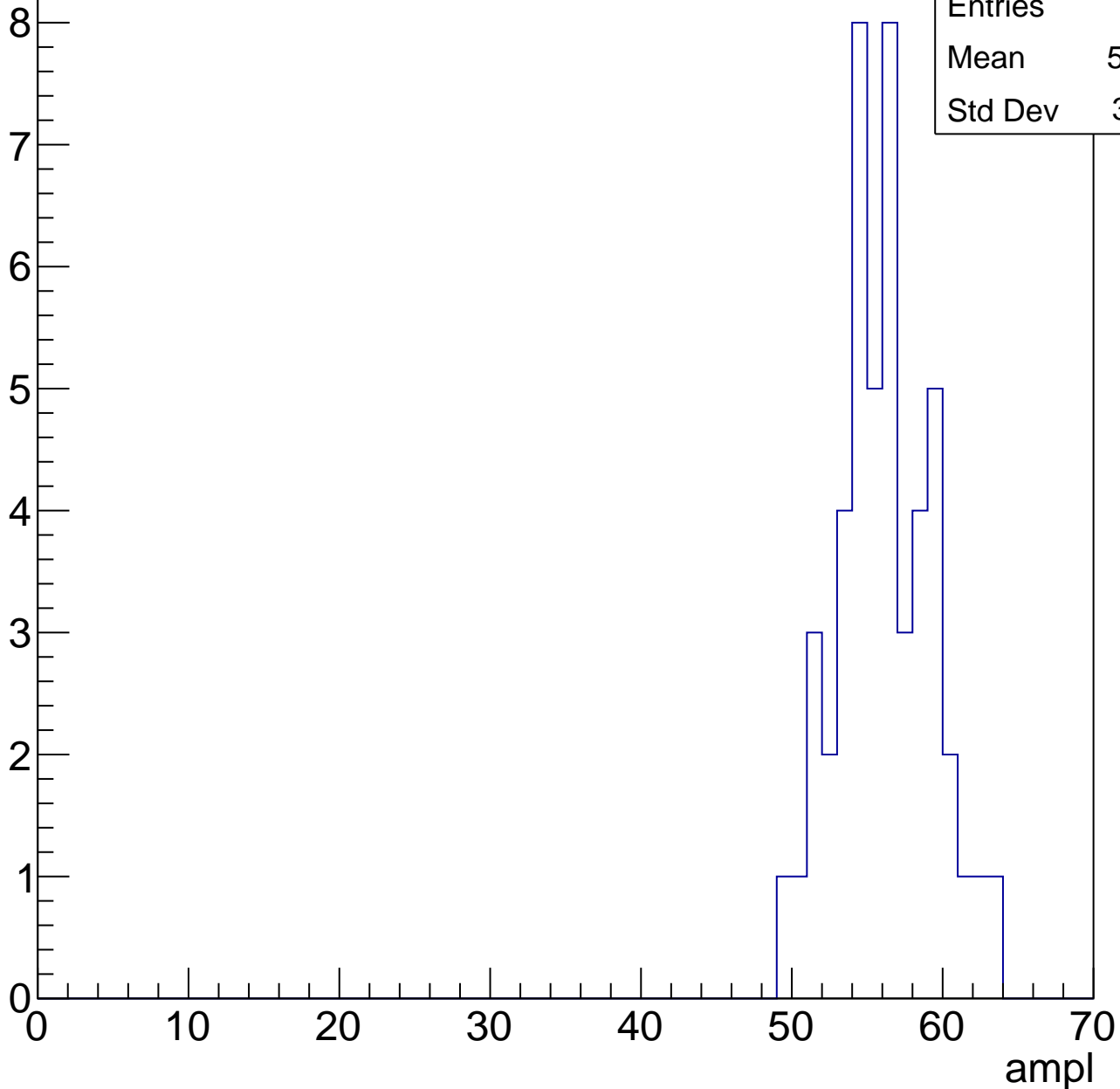


B1L003S, U20-ch65, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.65
Std Dev	3.081

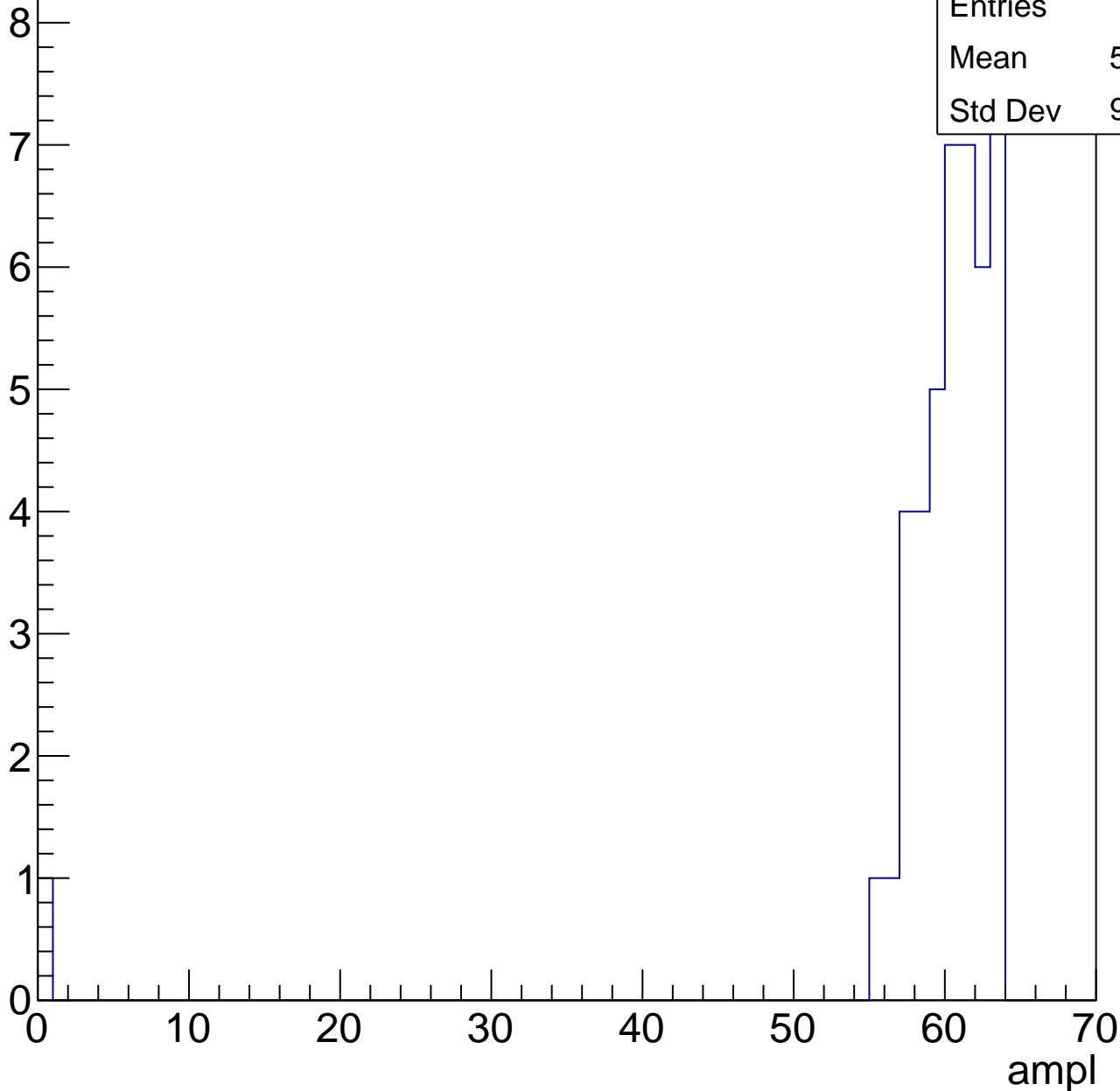


B1L003S, U20-ch65, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	58.84
Std Dev	9.222



B1L003S, U20-ch65, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch65, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch66, adc0

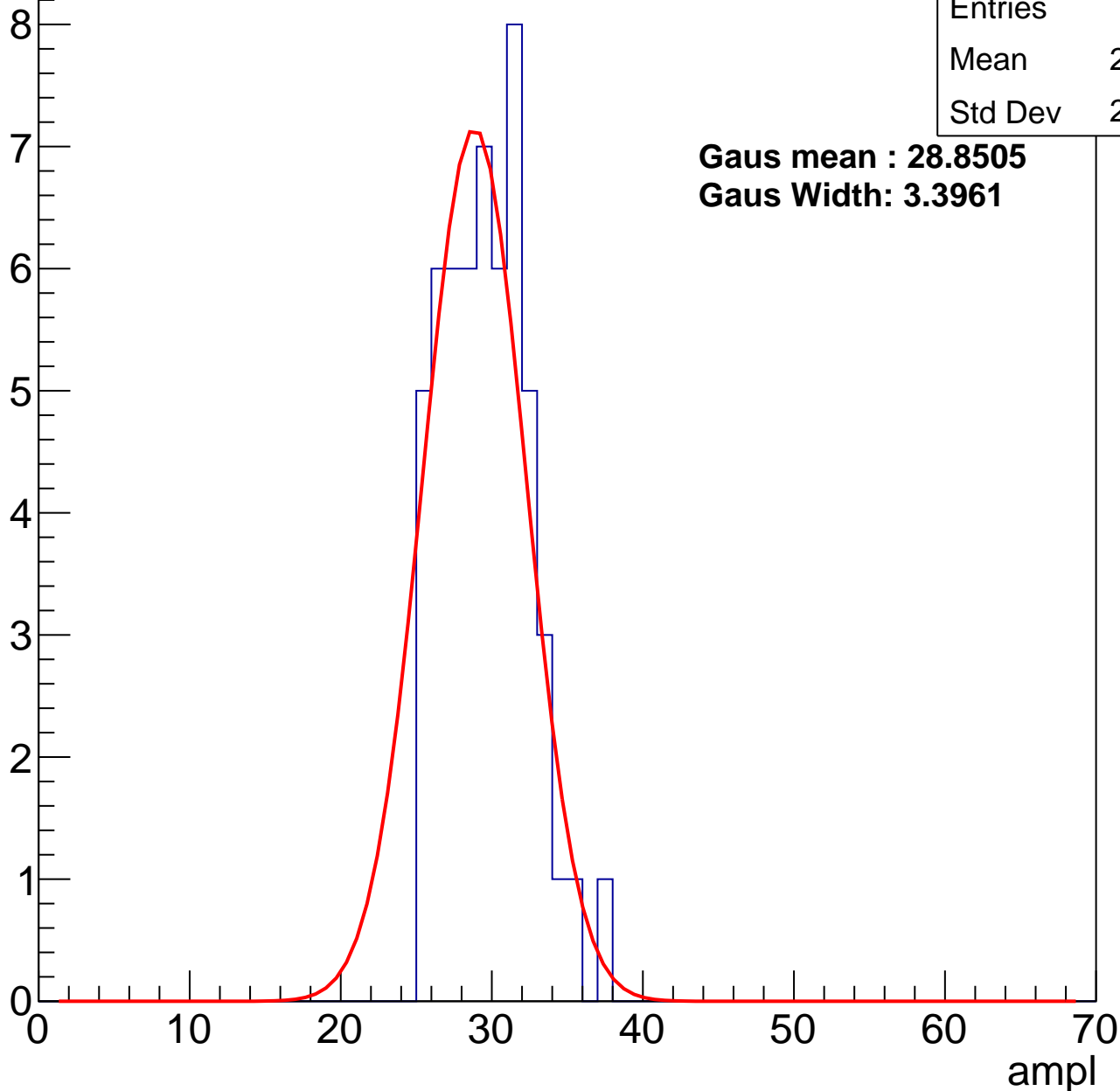
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.22
Std Dev	2.755

Gaus mean : 28.8505

Gaus Width: 3.3961



B1L003S, U20-ch66, adc1

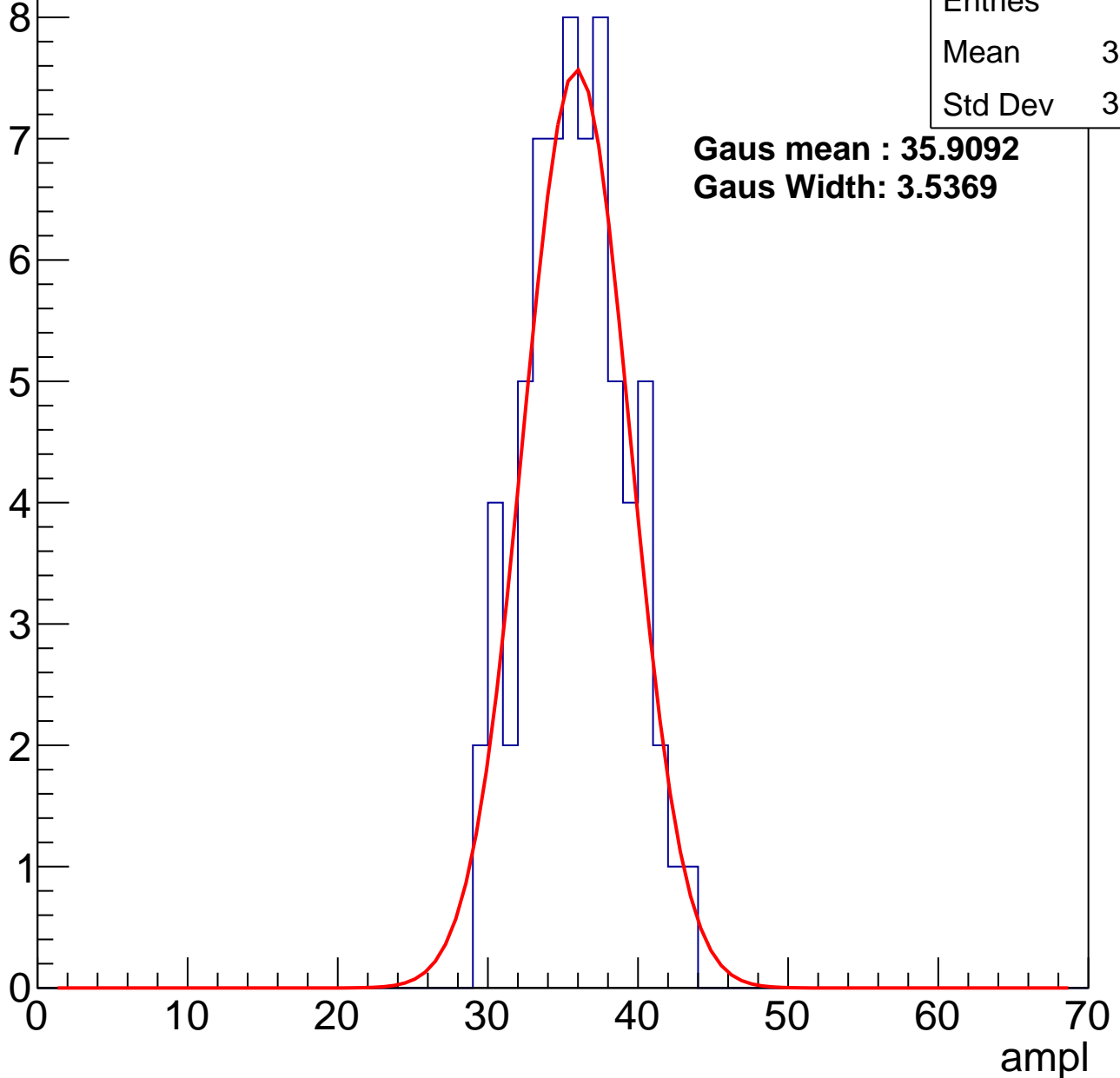
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.44
Std Dev	3.274

Gaus mean : 35.9092

Gaus Width: 3.5369



B1L003S, U20-ch66, adc2

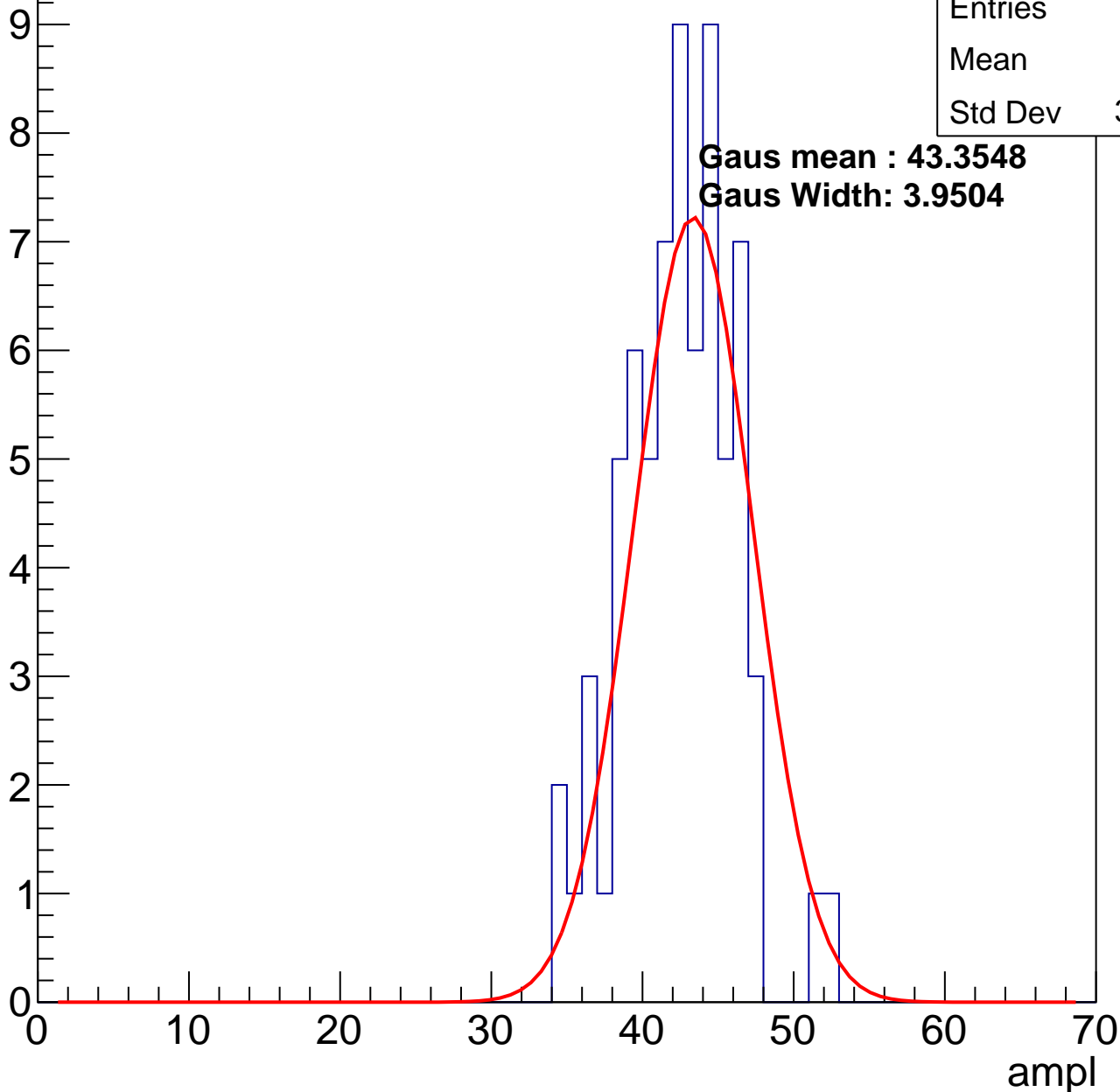
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42
Std Dev	3.611

Gaus mean : 43.3548

Gaus Width: 3.9504

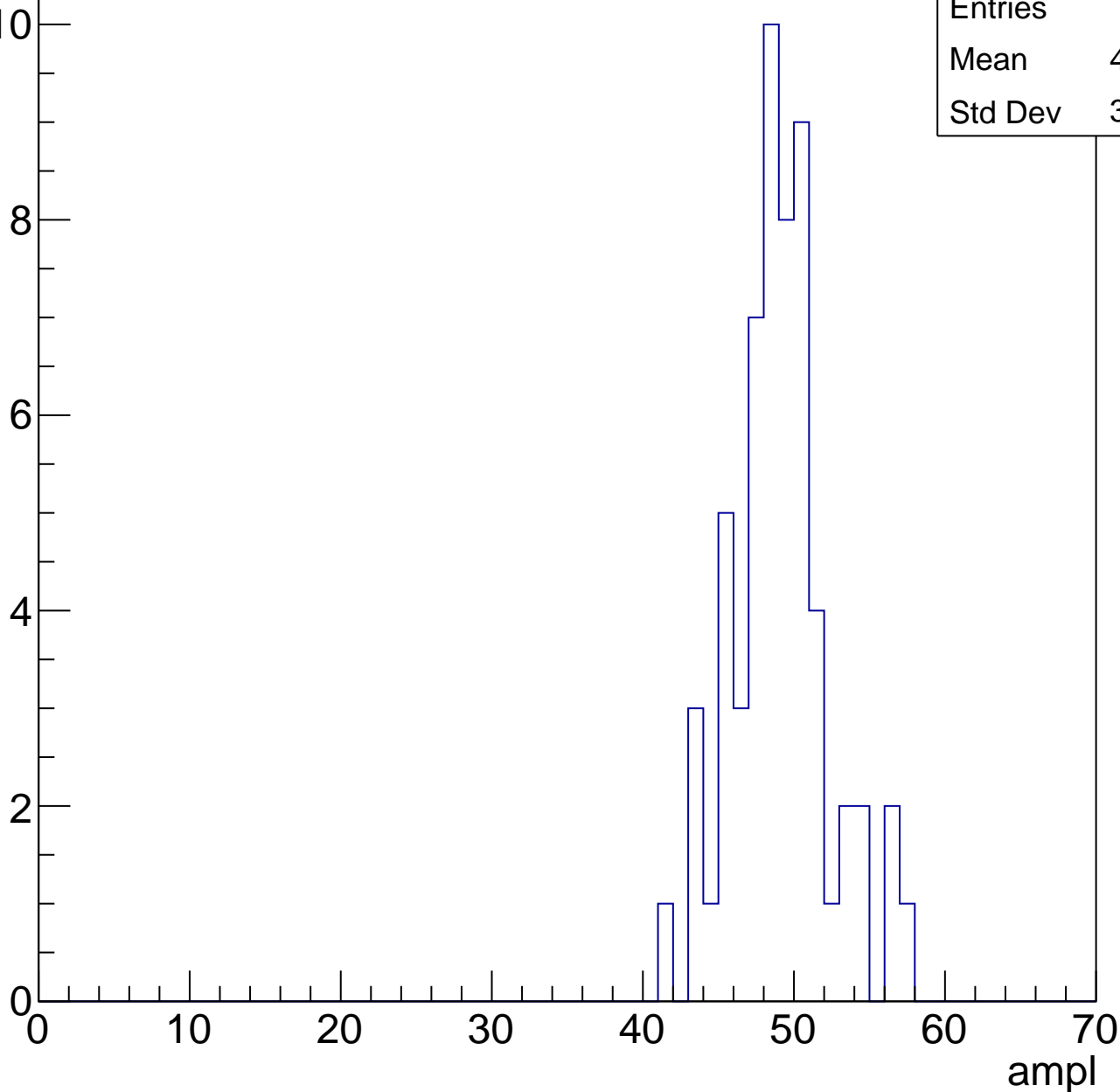


B1L003S, U20-ch66, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.59
Std Dev	3.216

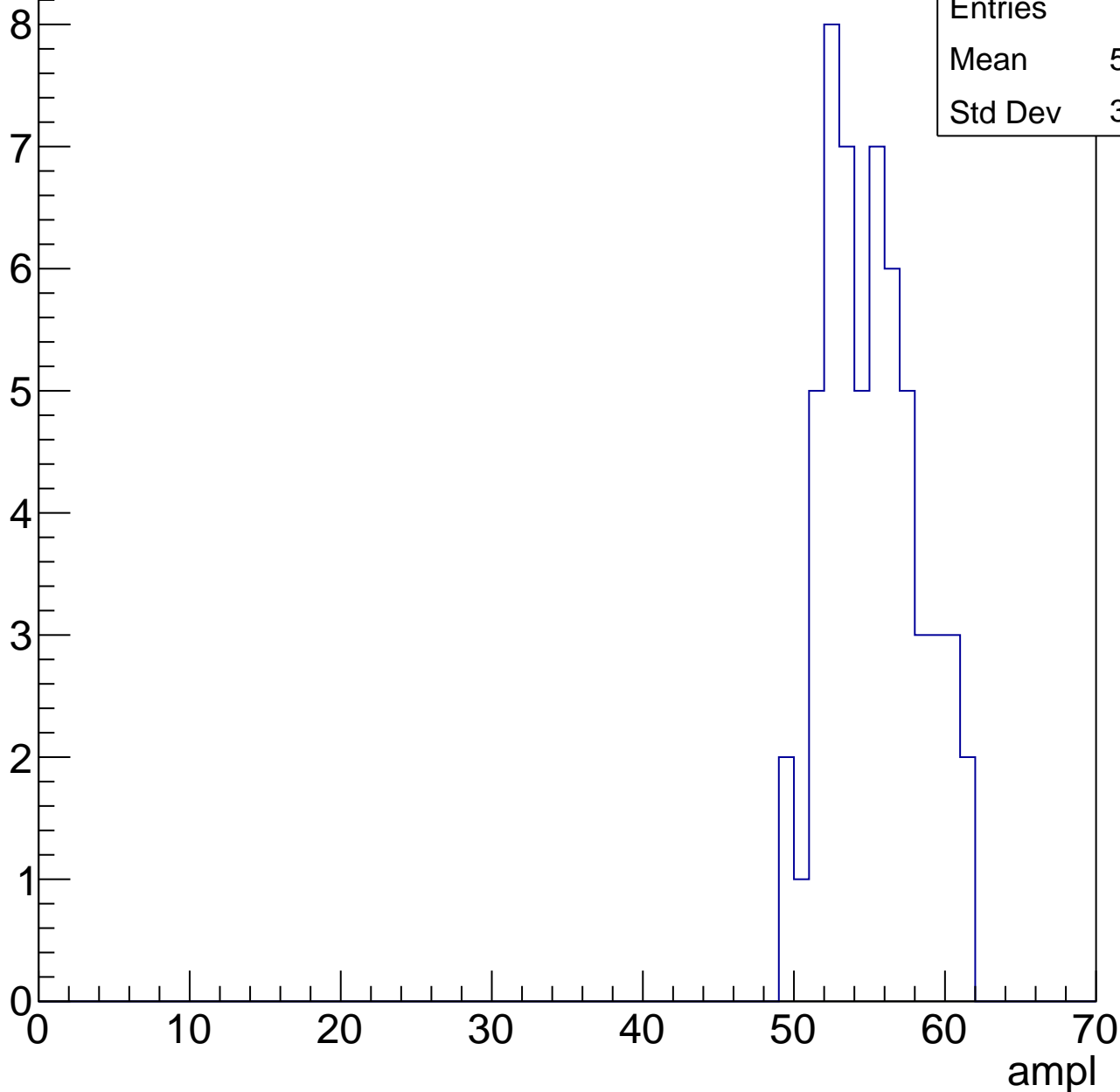


B1L003S, U20-ch66, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.72
Std Dev	3.036

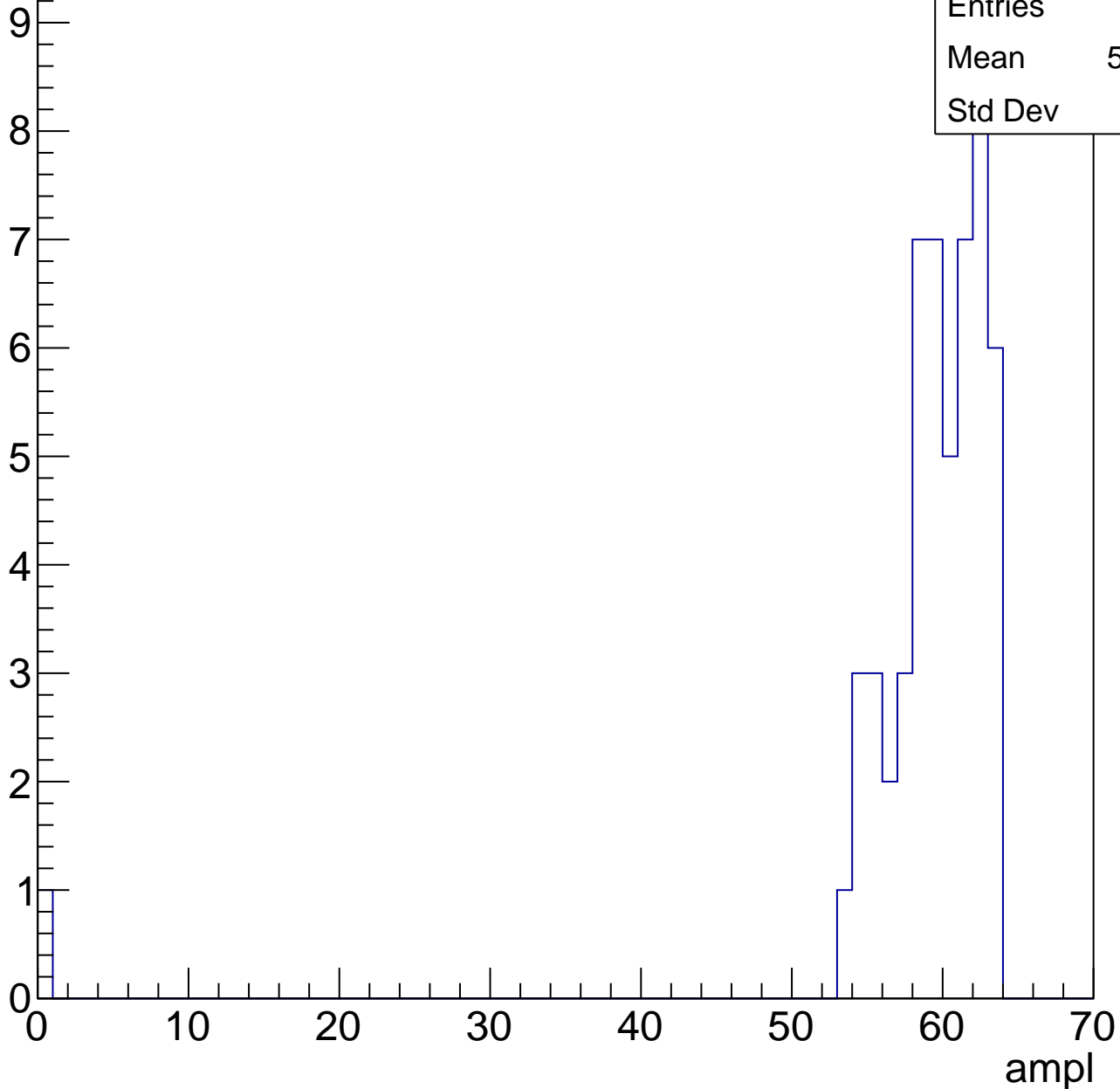


B1L003S, U20-ch66, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.24
Std Dev	8.45



B1L003S, U20-ch66, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

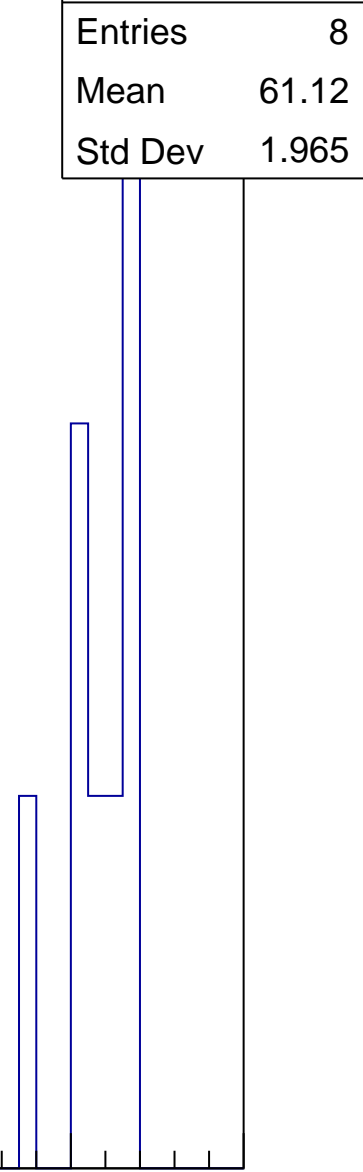
8

Mean

61.12

Std Dev

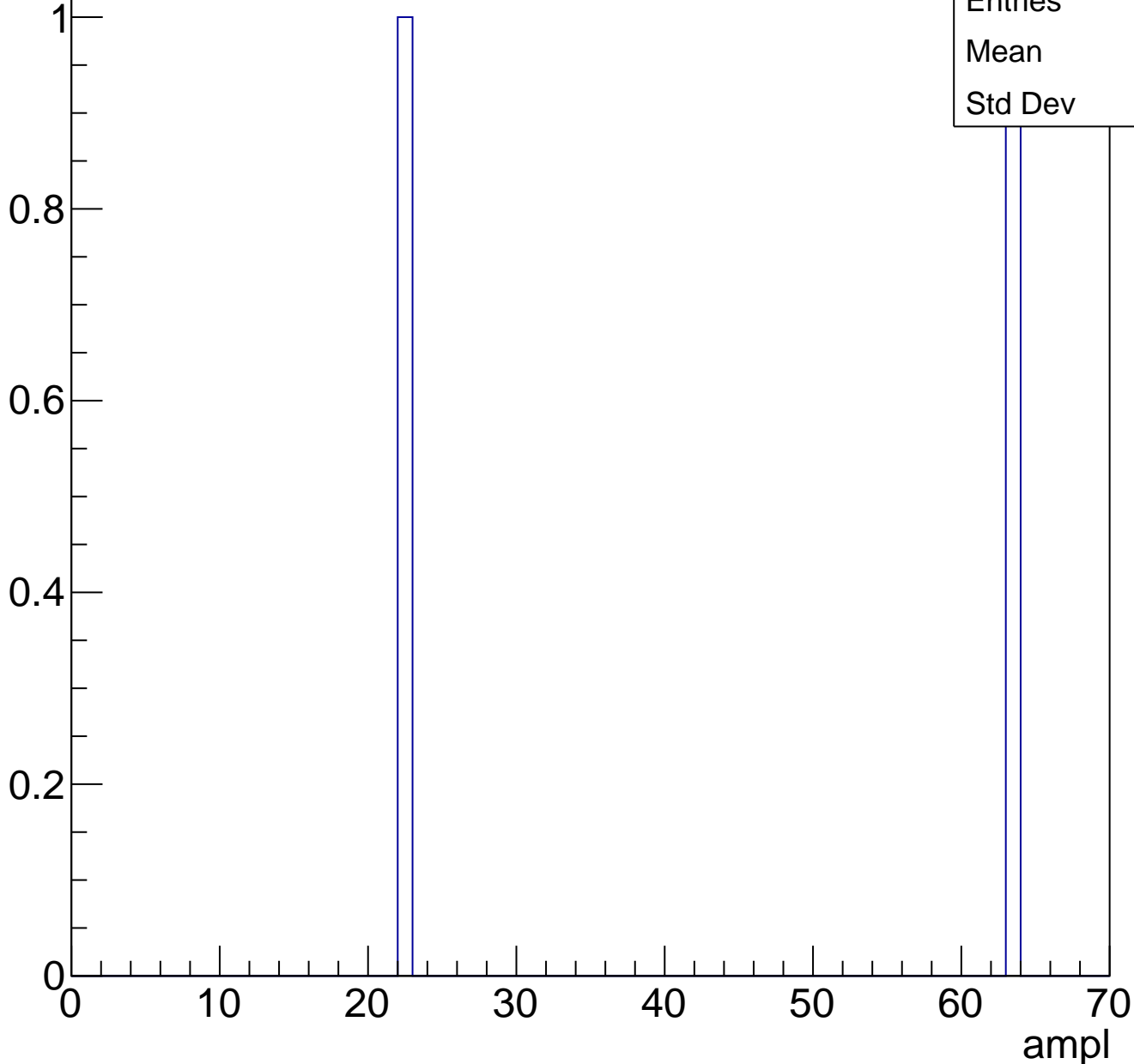
1.965



B1L003S, U20-ch66, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	42.5
Std Dev	20.5

B1L003S, U20-ch67, adc0

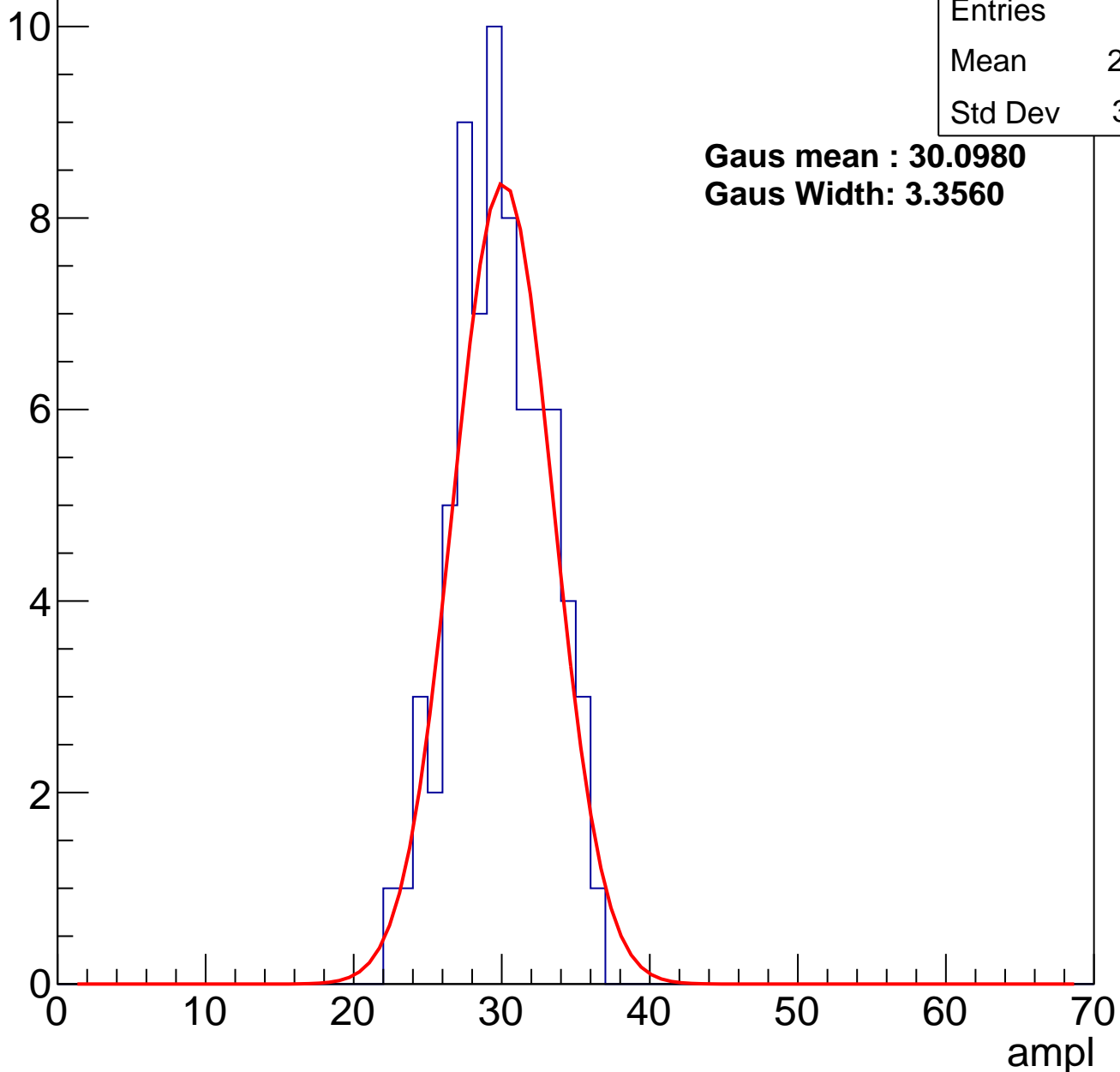
calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	29.43
Std Dev	3.131

Gaus mean : 30.0980

Gaus Width: 3.3560

Entry



B1L003S, U20-ch67, adc1

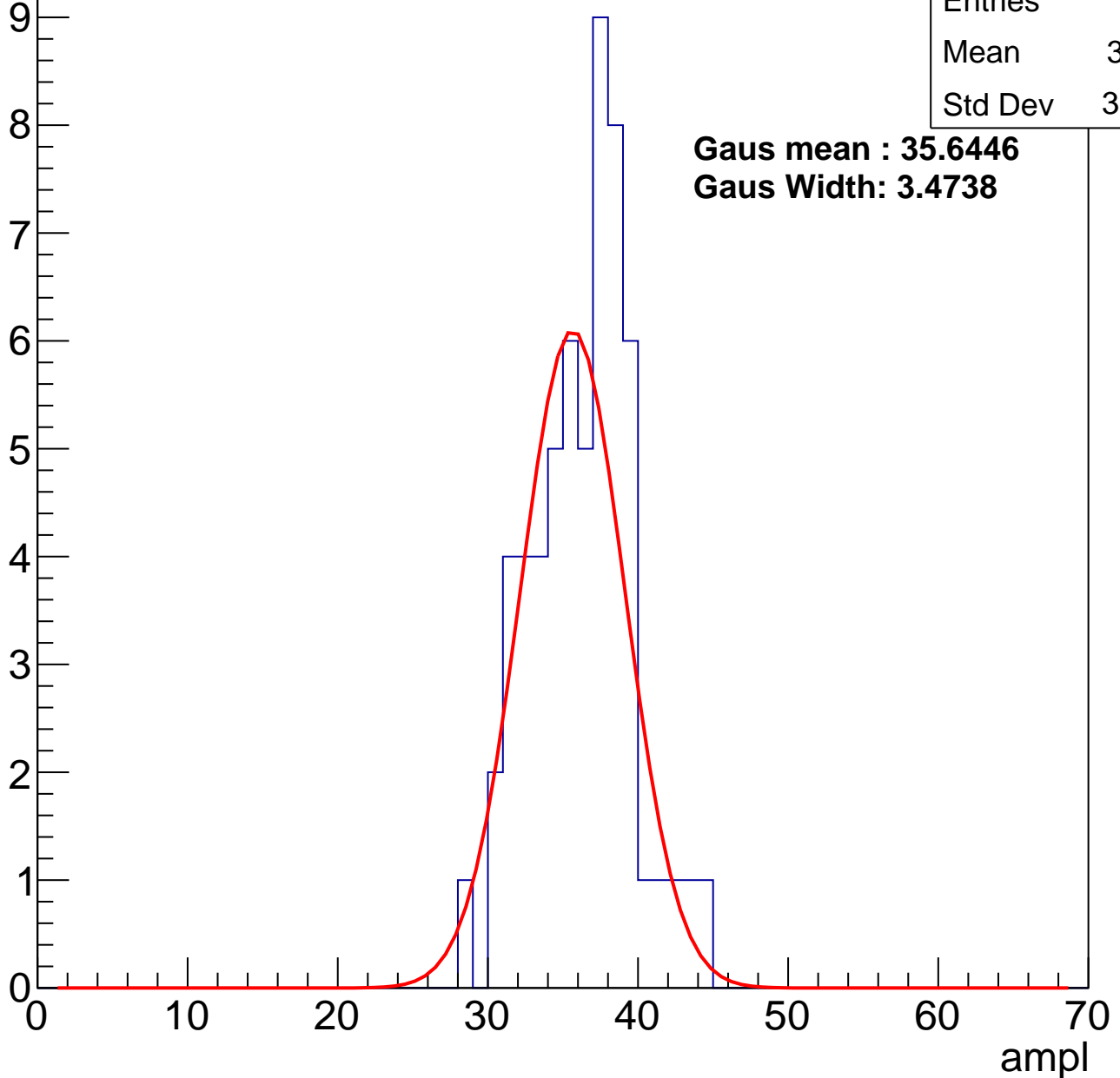
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.81
Std Dev	3.306

Gaus mean : 35.6446

Gaus Width: 3.4738



B1L003S, U20-ch67, adc2

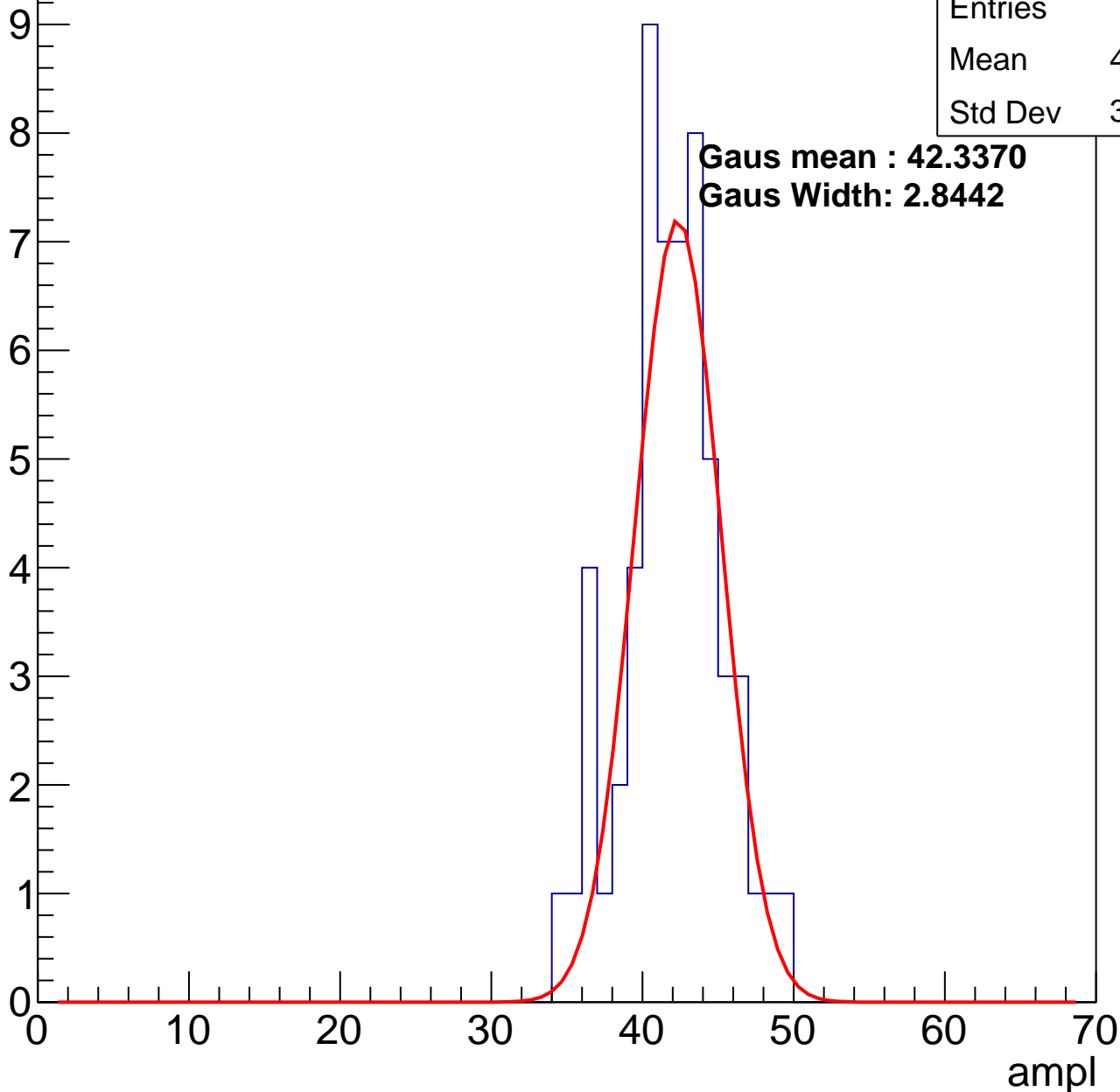
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	41.45
Std Dev	3.185

Gaus mean : 42.3370

Gaus Width: 2.8442

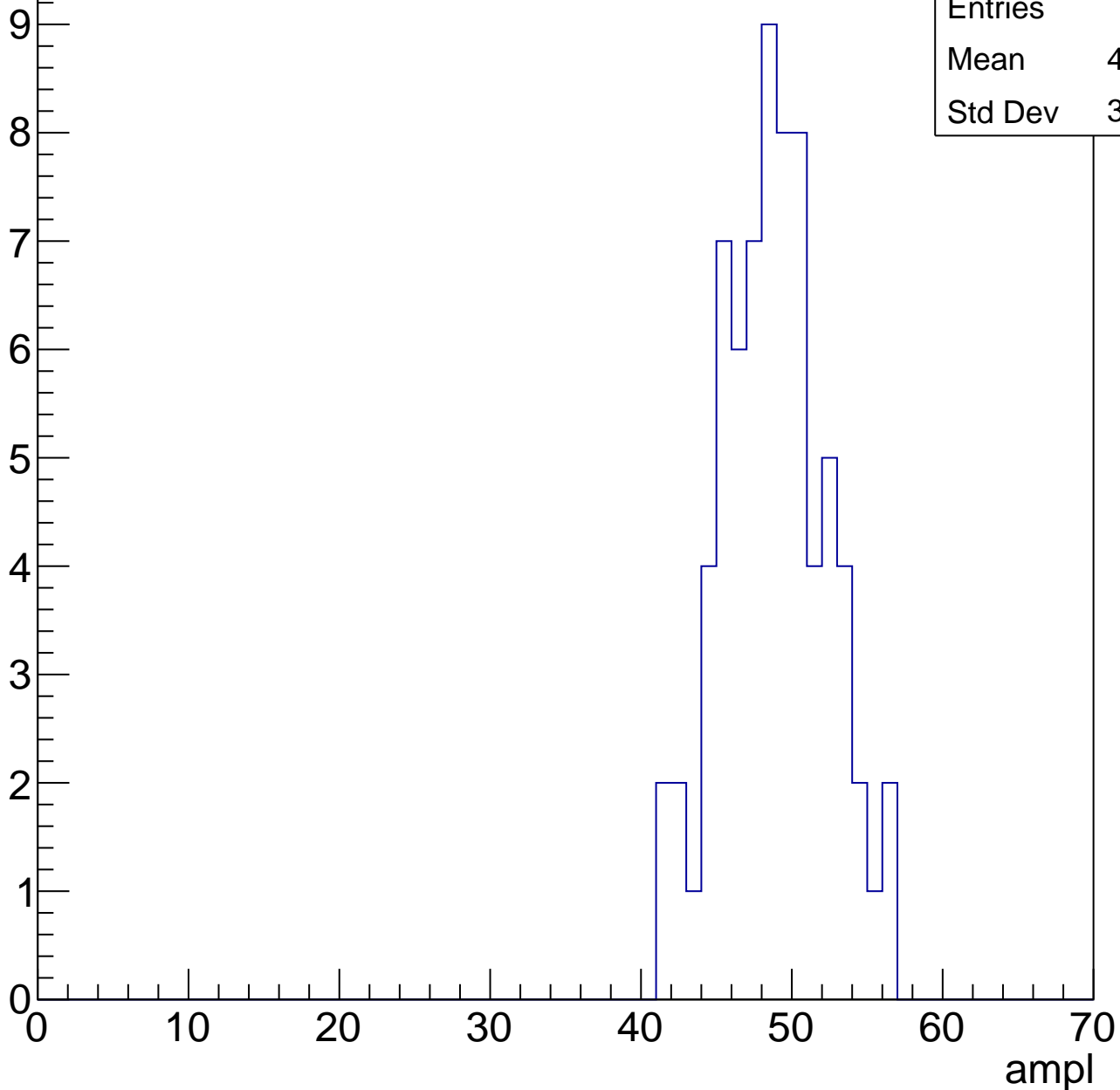


B1L003S, U20-ch67, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.33
Std Dev	3.436

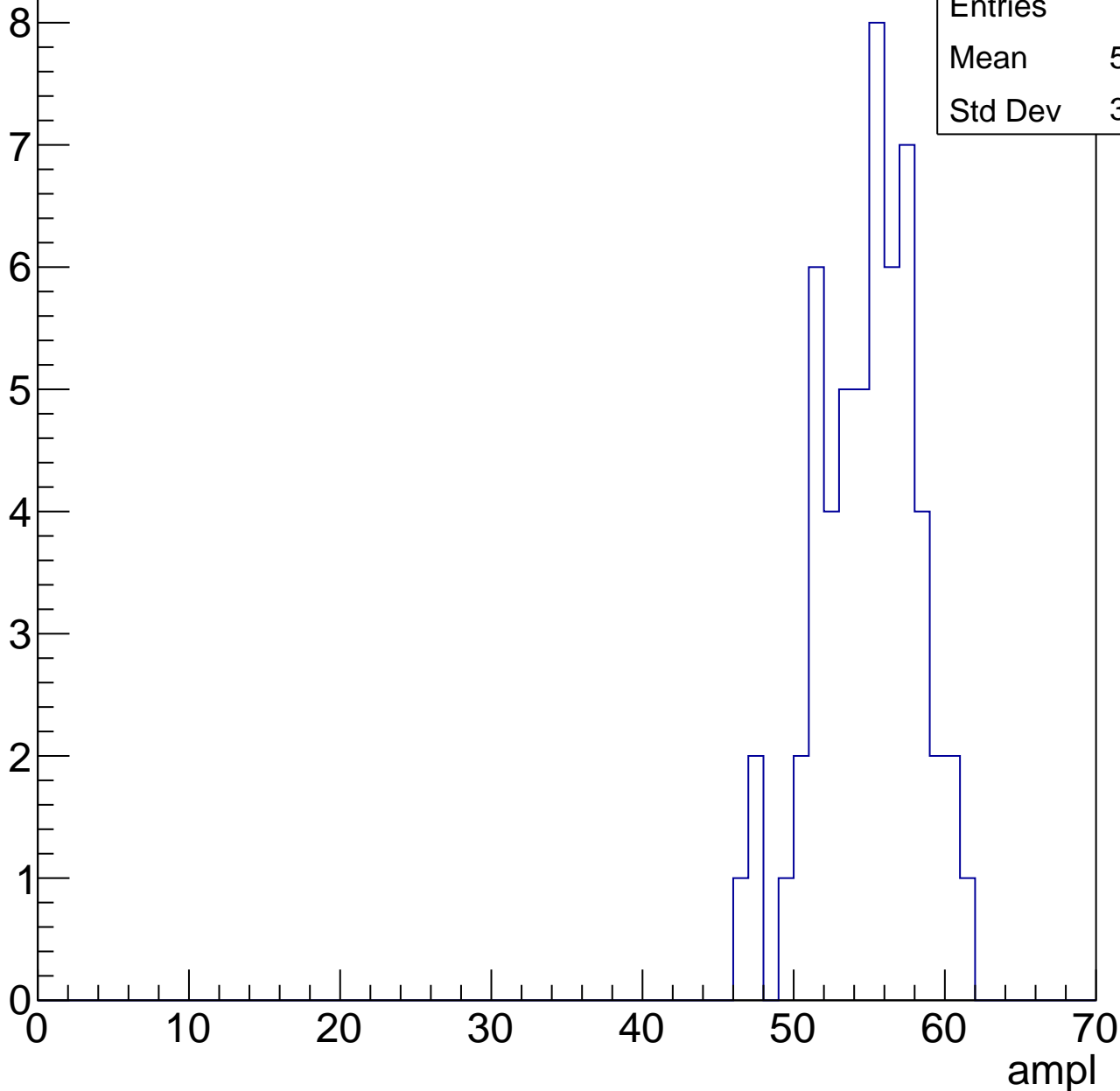


B1L003S, U20-ch67, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.36
Std Dev	3.319

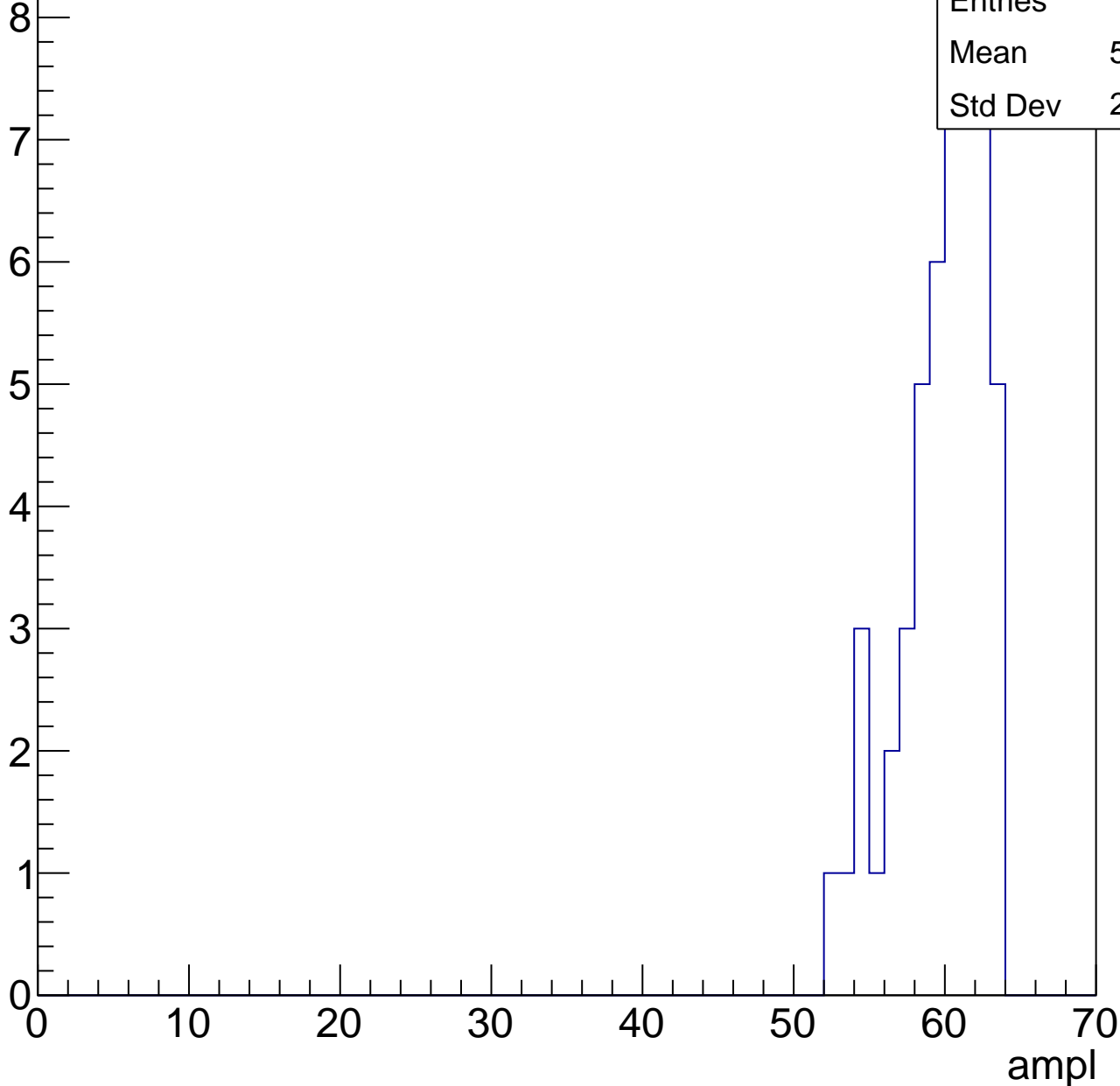


B1L003S, U20-ch67, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.37
Std Dev	2.786

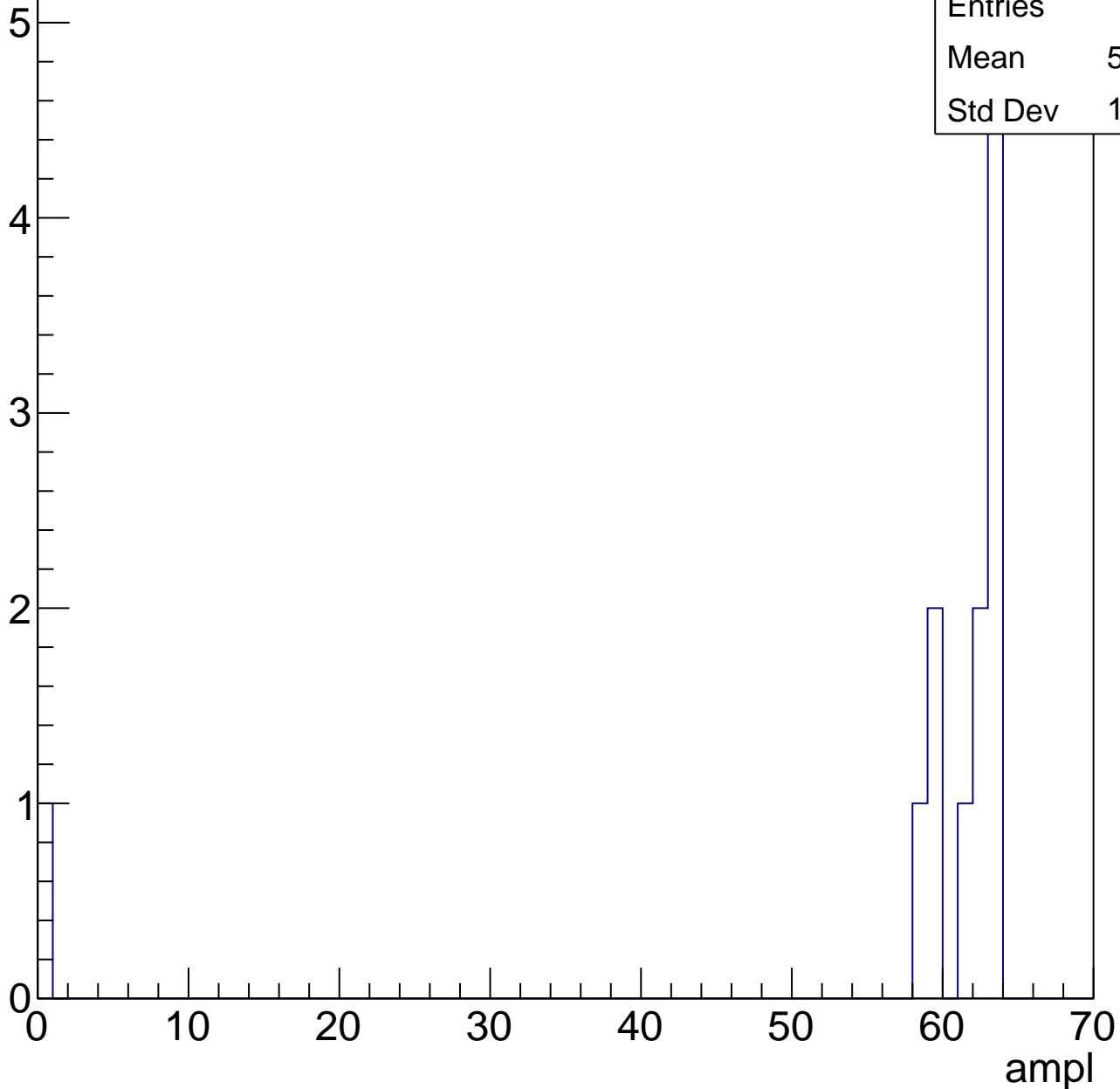


B1L003S, U20-ch67, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	56.33
Std Dev	17.07



B1L003S, U20-ch67, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch68, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	28.23
Std Dev	4.757

Gaus mean : 29.1840

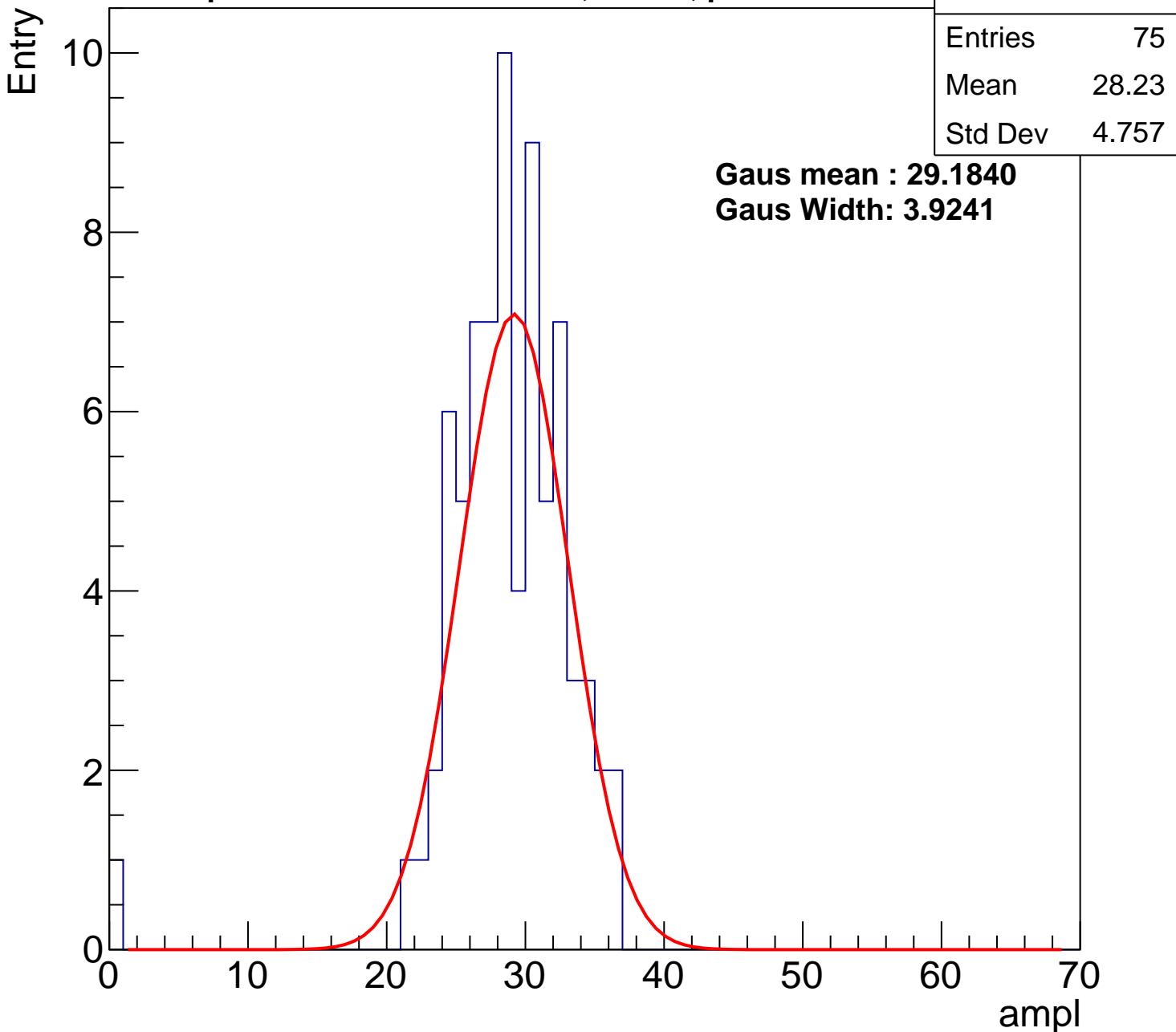
Gaus Width: 3.9241

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



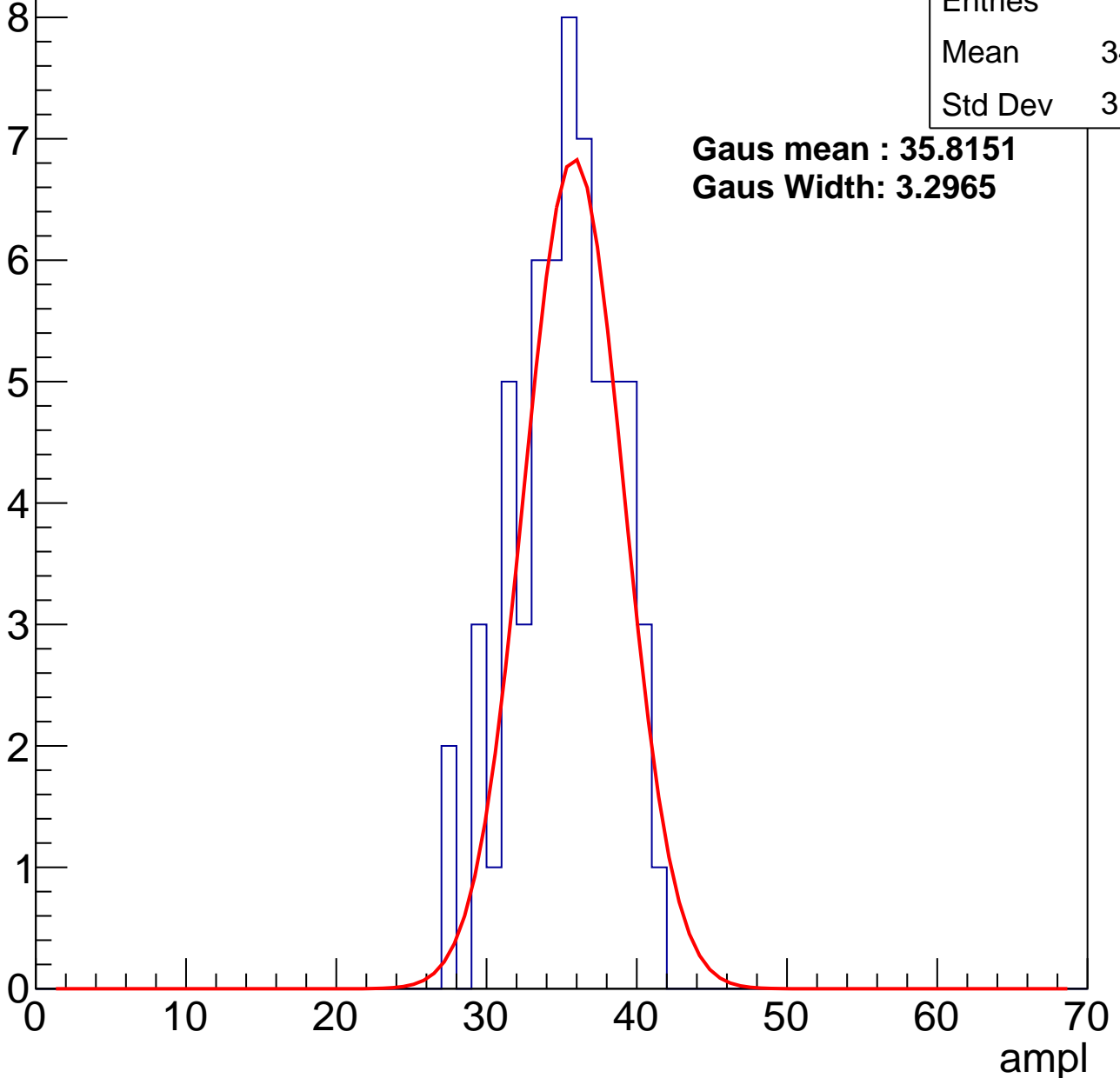
B1L003S, U20-ch68, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	34.78
Std Dev	3.312

Gaus mean : 35.8151
Gaus Width: 3.2965



B1L003S, U20-ch68, adc2

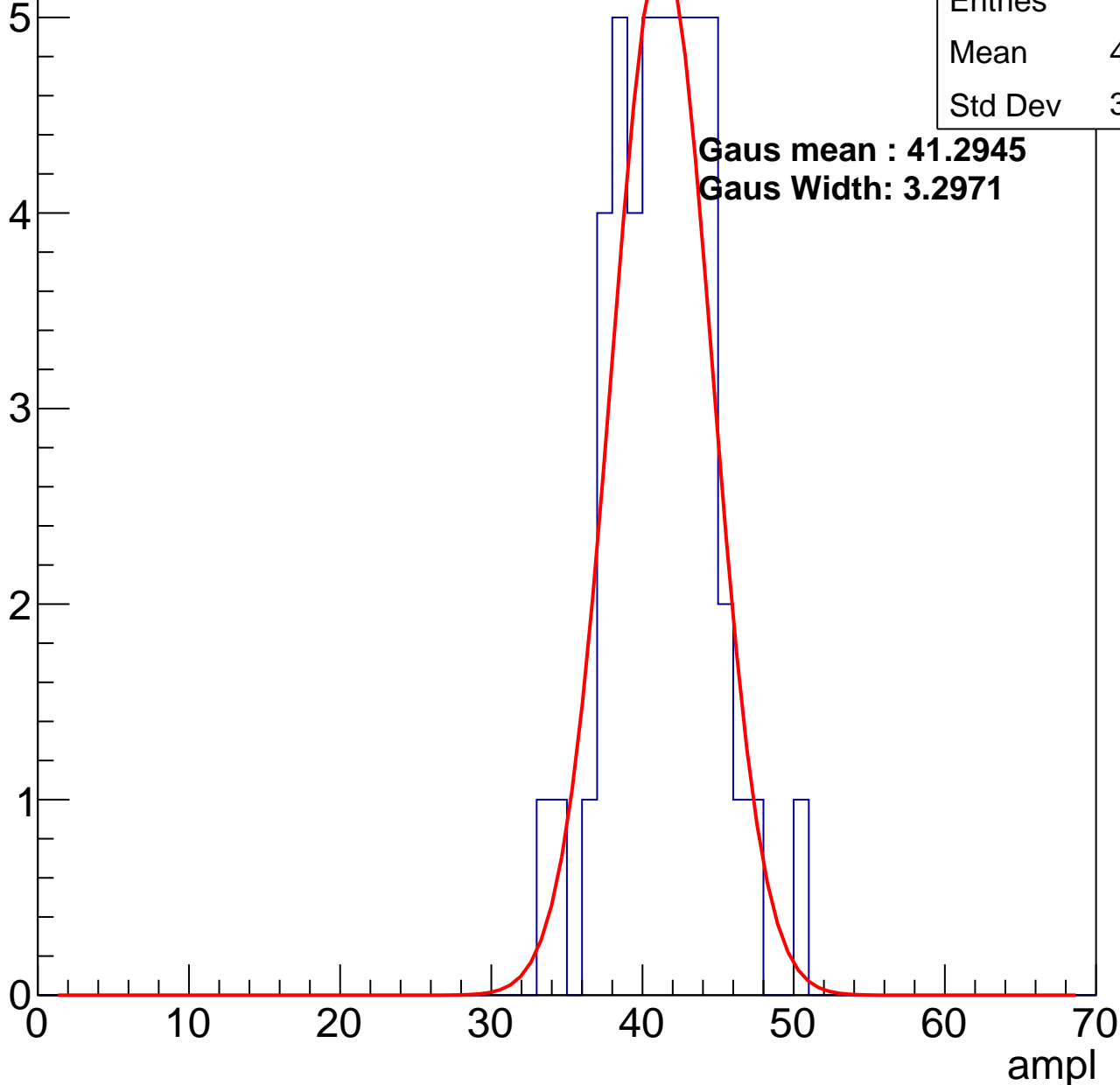
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	40.87
Std Dev	3.334

Gaus mean : 41.2945

Gaus Width: 3.2971

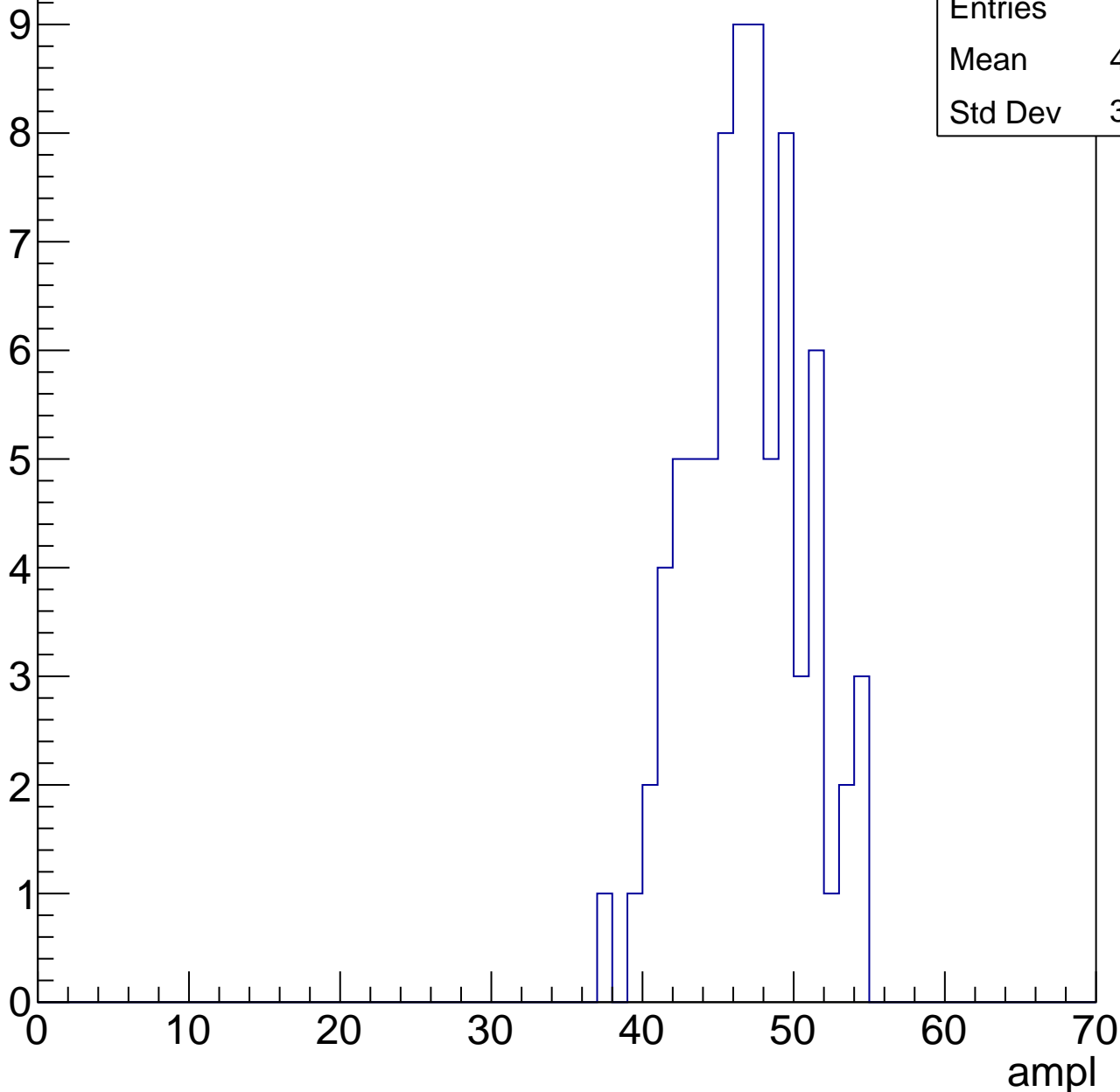


B1L003S, U20-ch68, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	46.36
Std Dev	3.727

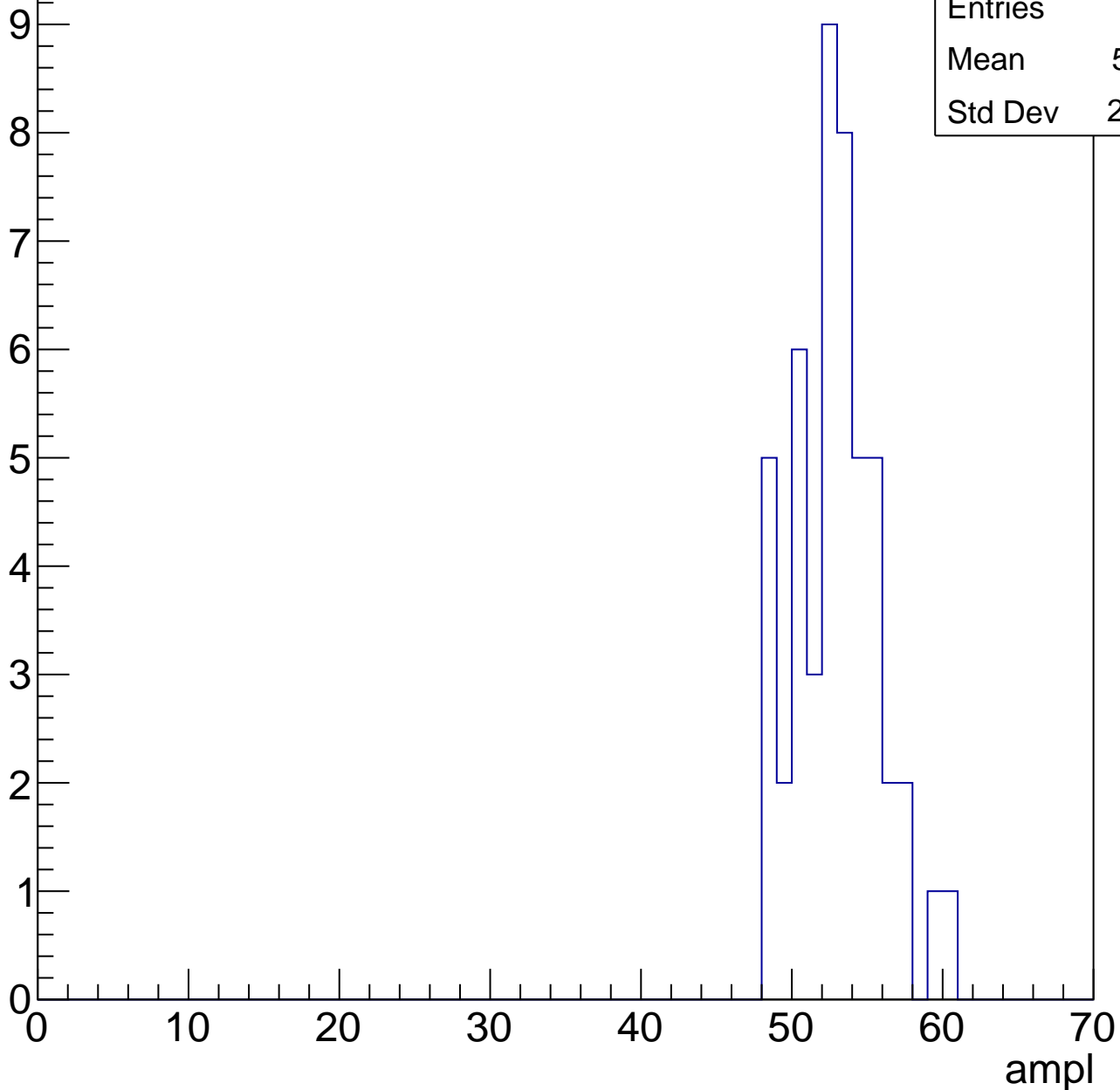


B1L003S, U20-ch68, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	52.51
Std Dev	2.786

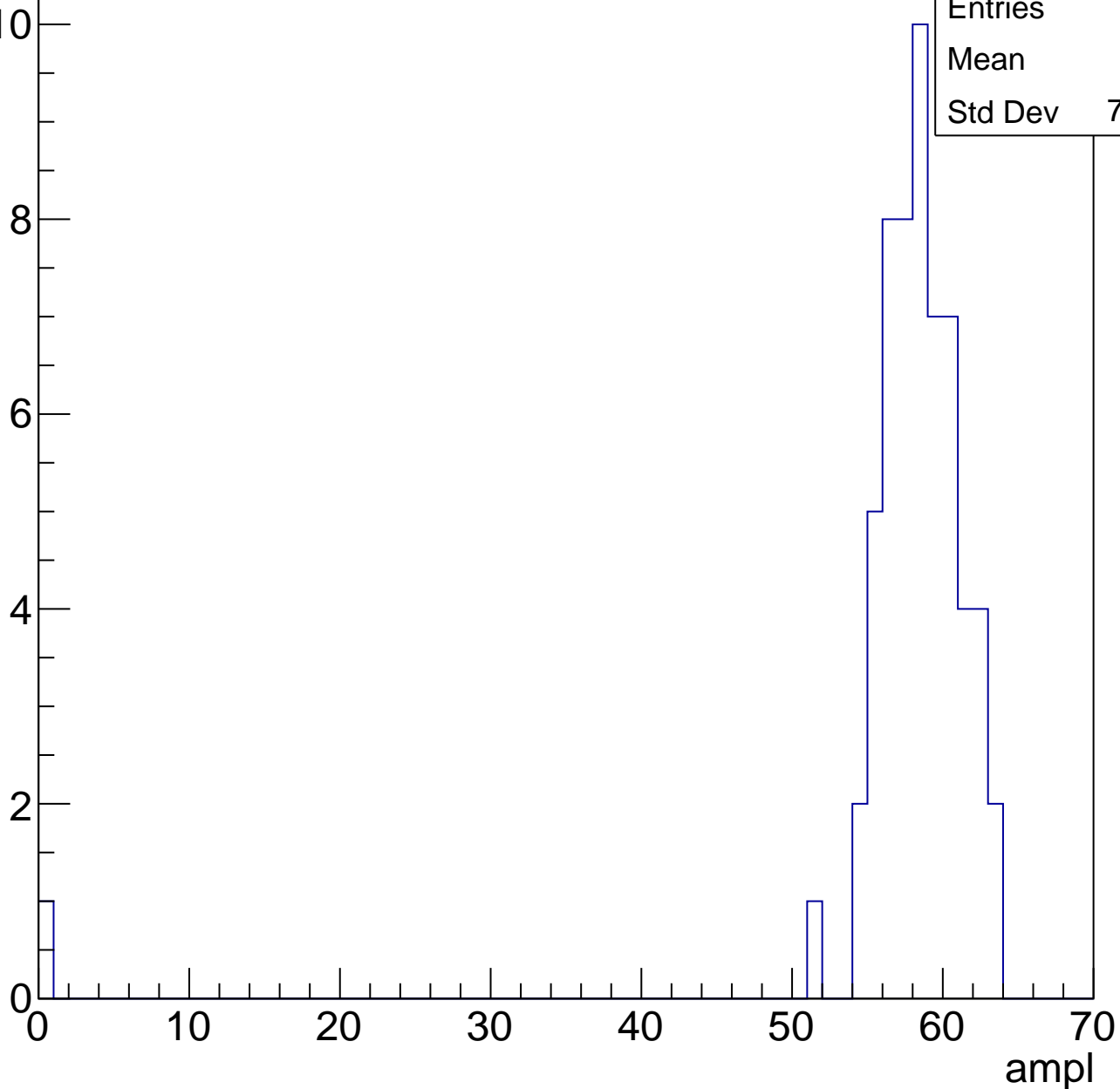


B1L003S, U20-ch68, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.1
Std Dev	7.884

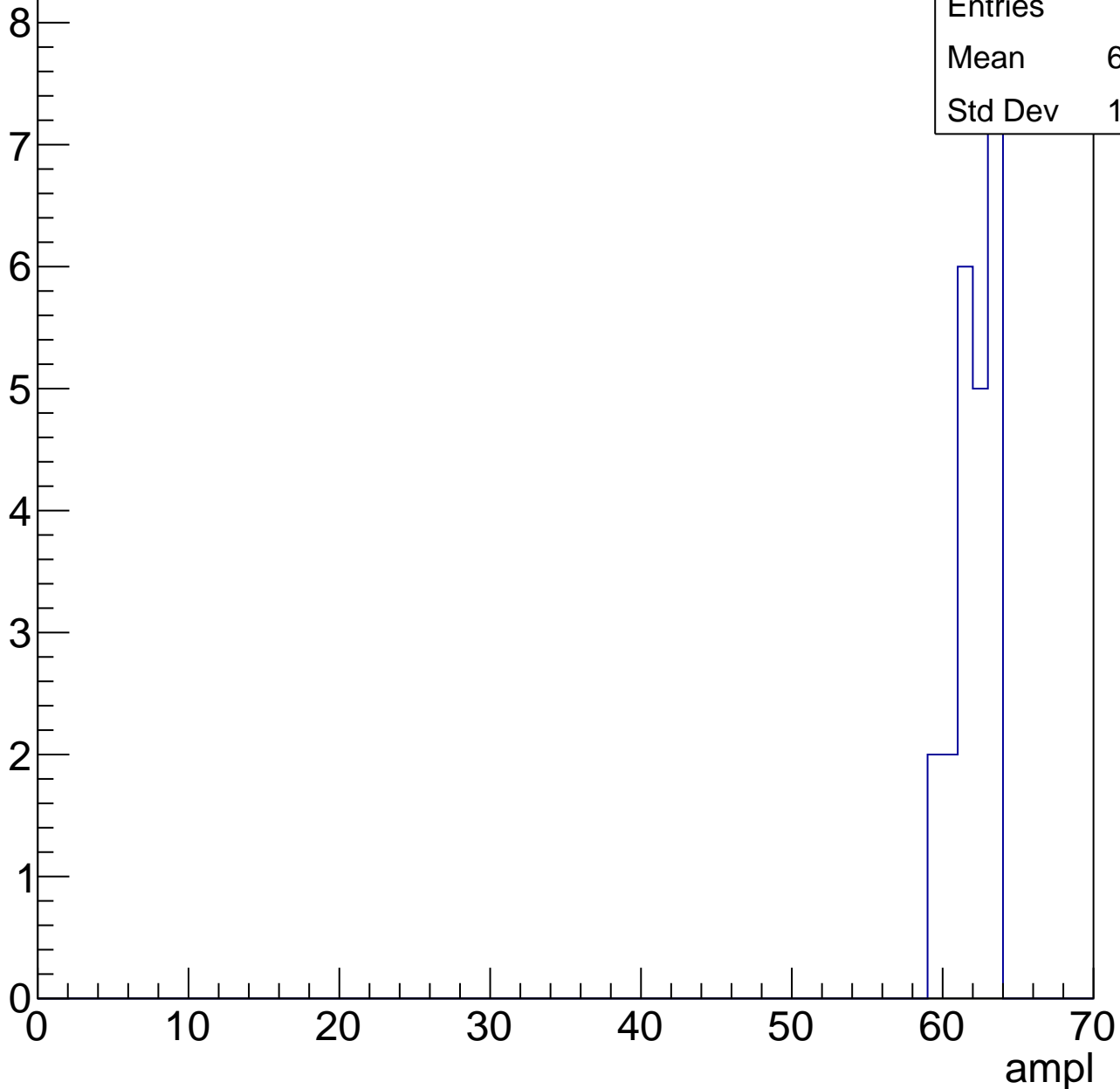


B1L003S, U20-ch68, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	61.65
Std Dev	1.272



B1L003S, U20-ch68, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch69, adc0

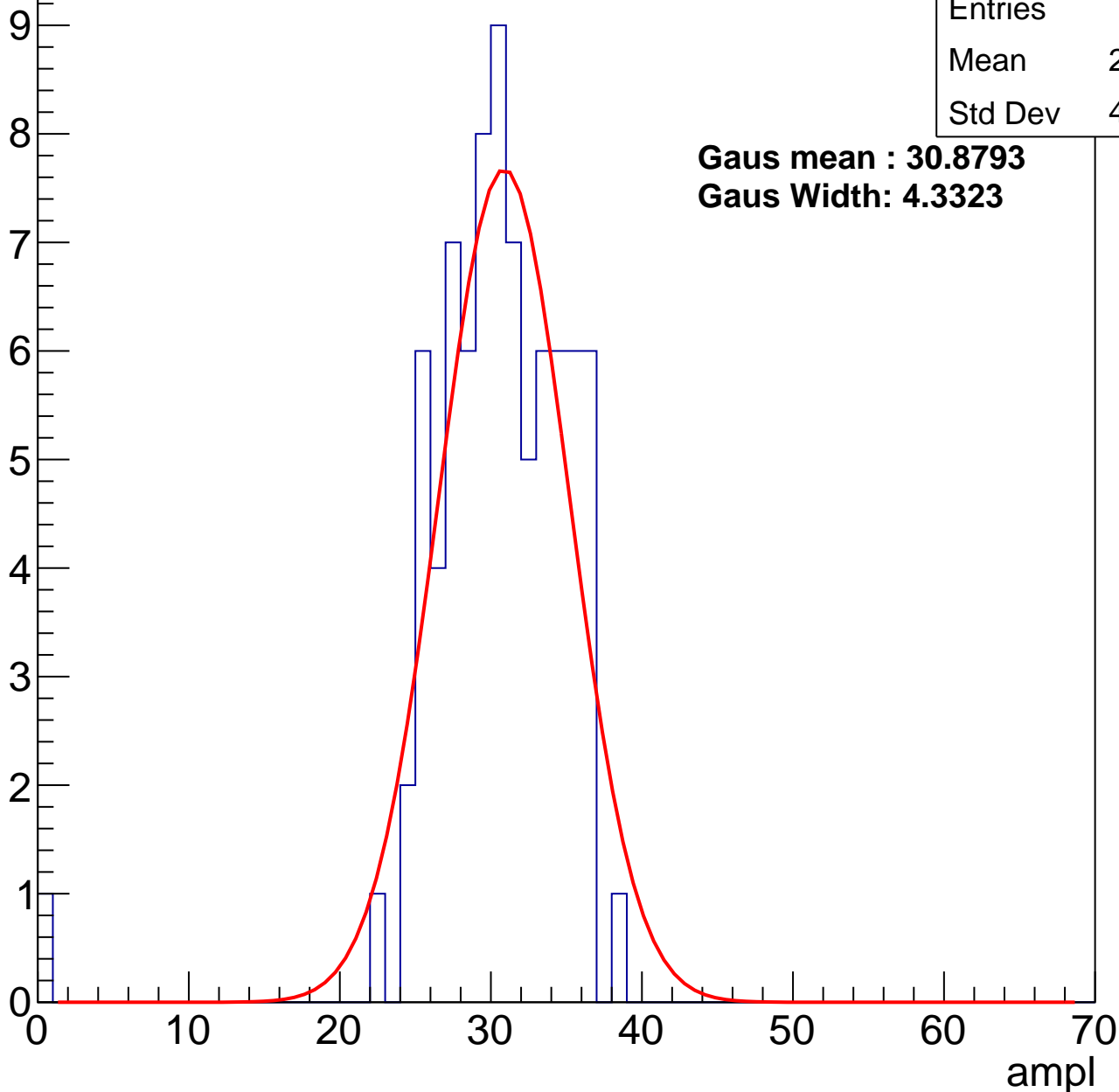
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	29.95
Std Dev	4.909

Gaus mean : 30.8793

Gaus Width: 4.3323



B1L003S, U20-ch69, adc1

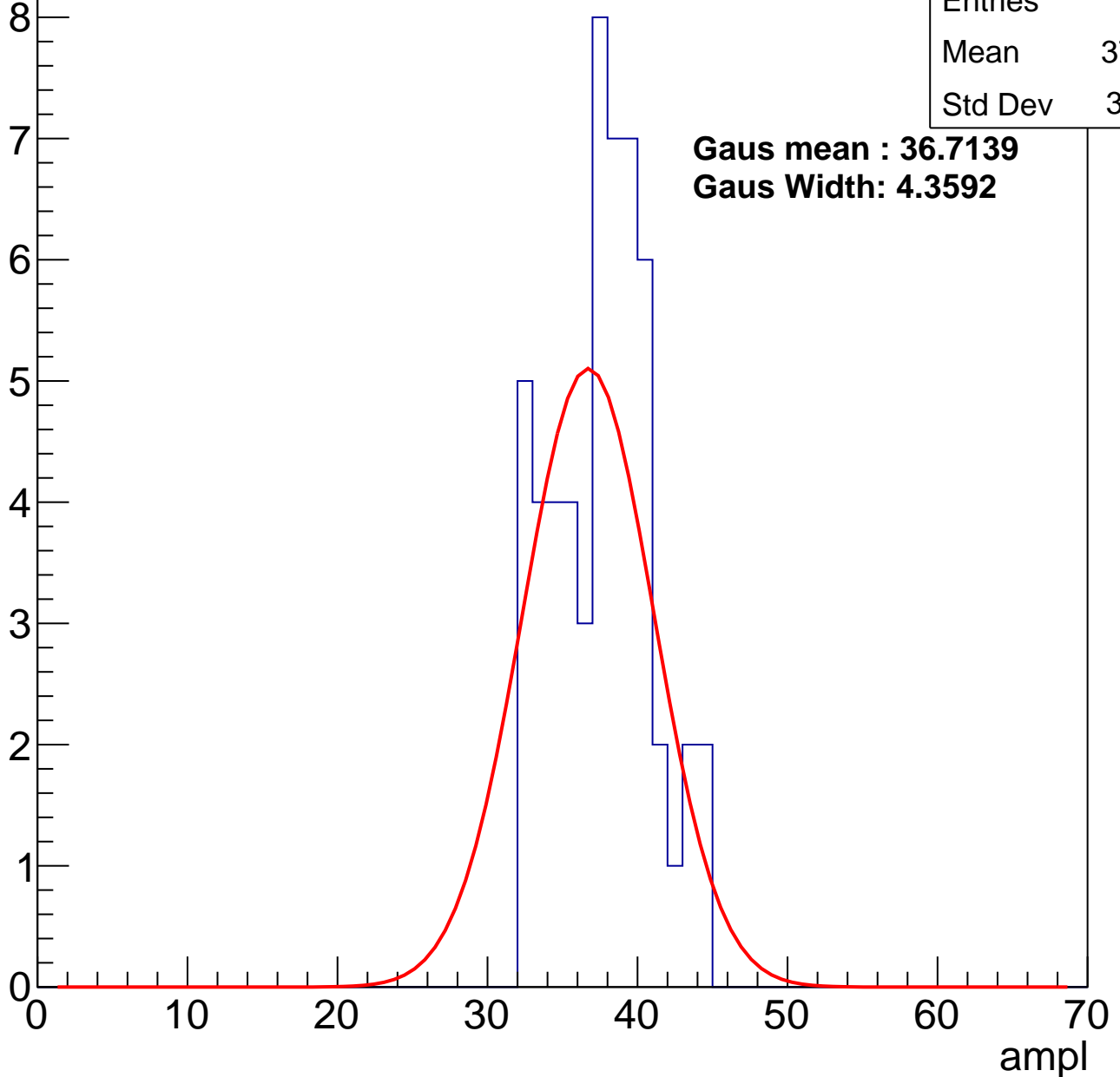
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	37.25
Std Dev	3.181

Gaus mean : 36.7139

Gaus Width: 4.3592



B1L003S, U20-ch69, adc2

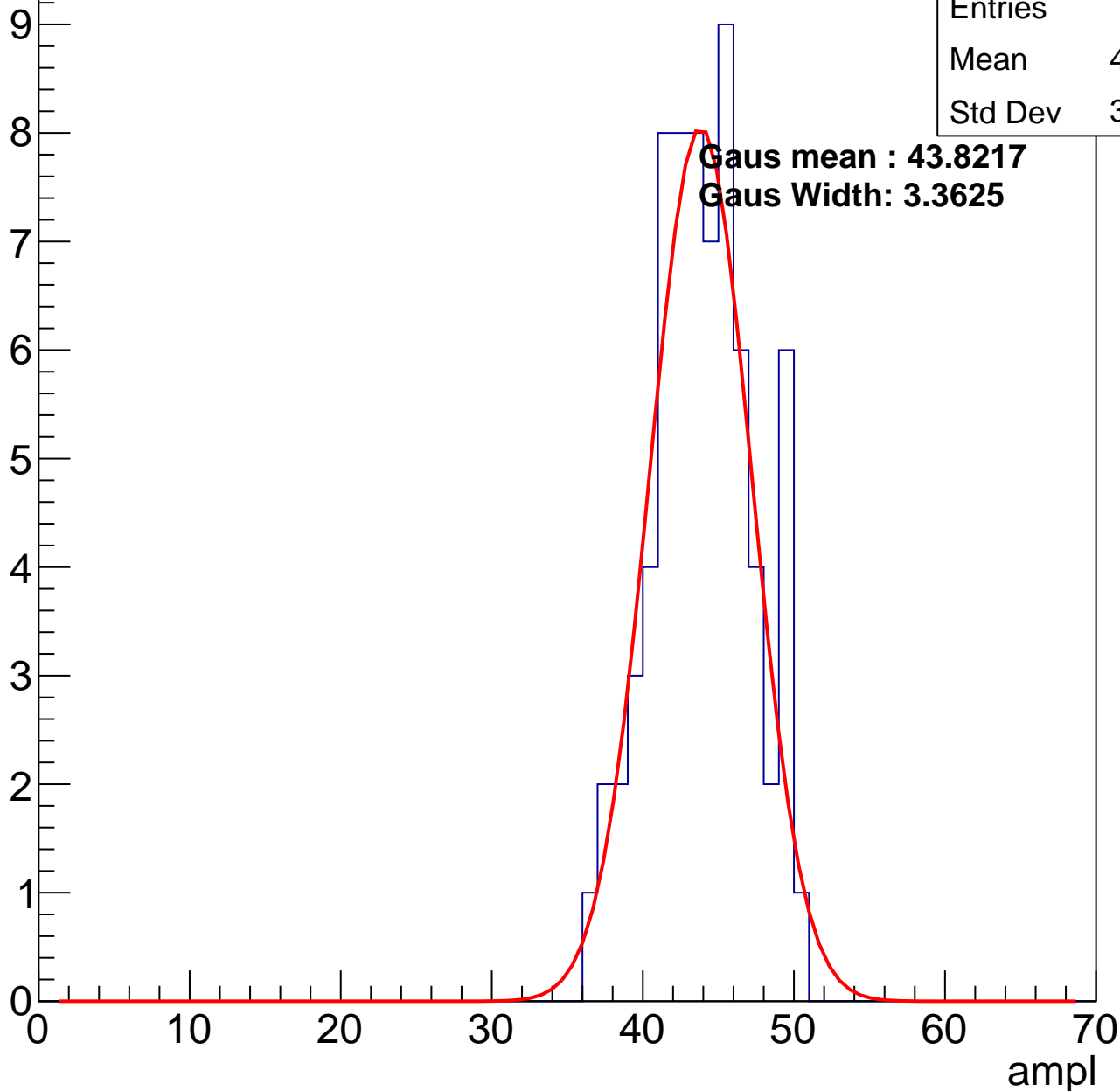
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.49
Std Dev	3.258

Gaus mean : 43.8217

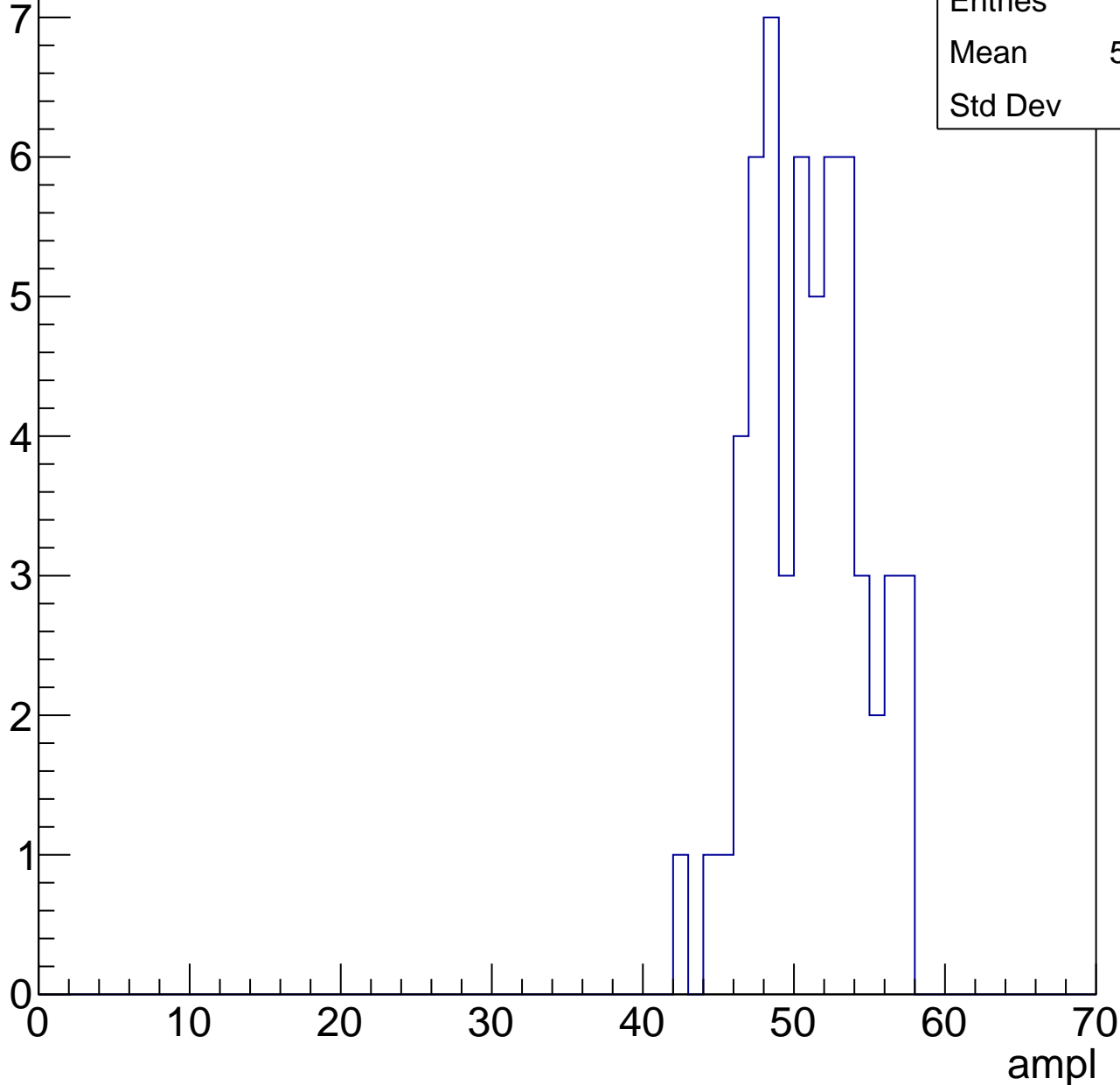
Gaus Width: 3.3625



B1L003S, U20-ch69, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



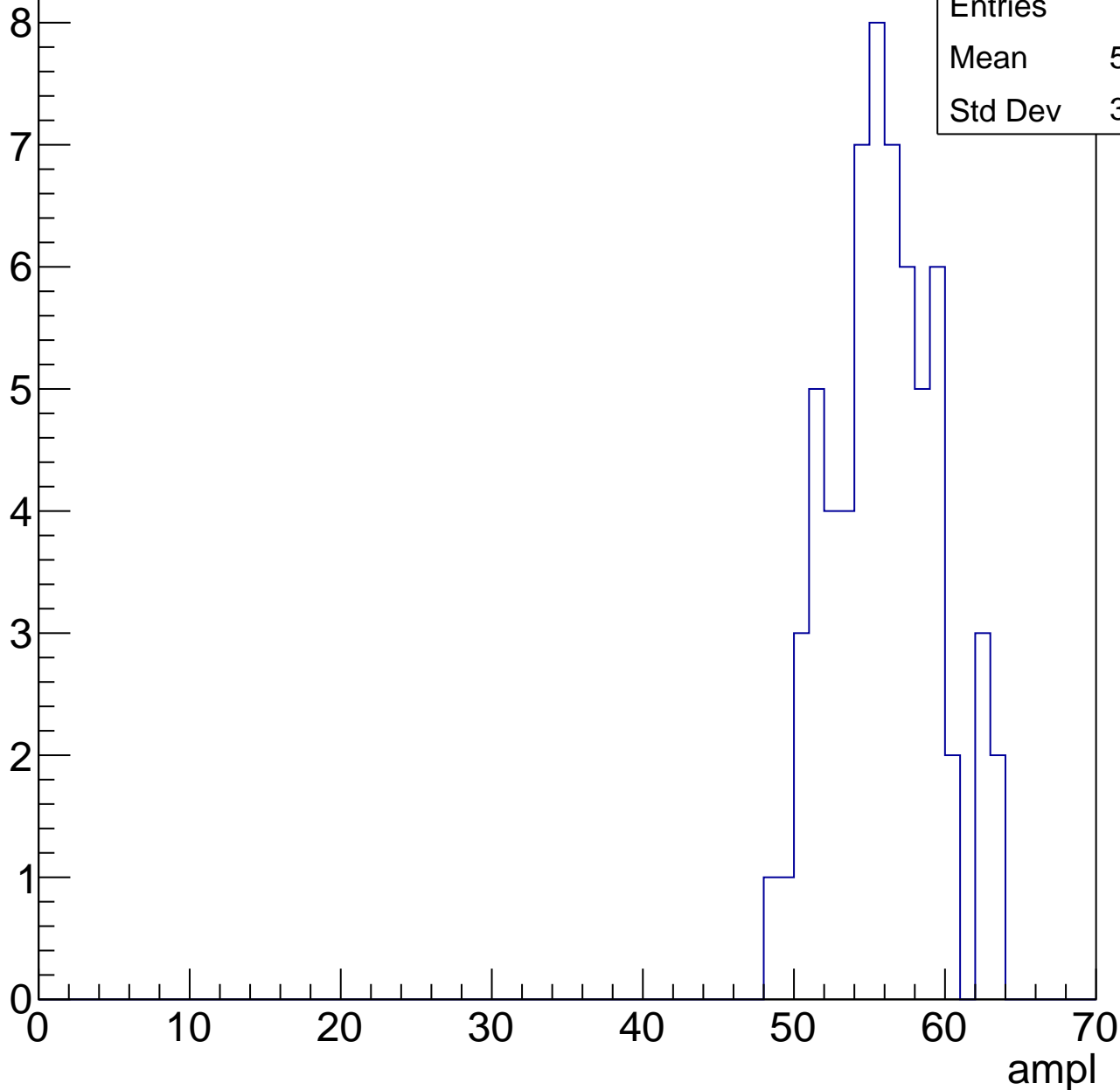
Entries	57
Mean	50.46
Std Dev	3.5

B1L003S, U20-ch69, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.47
Std Dev	3.495

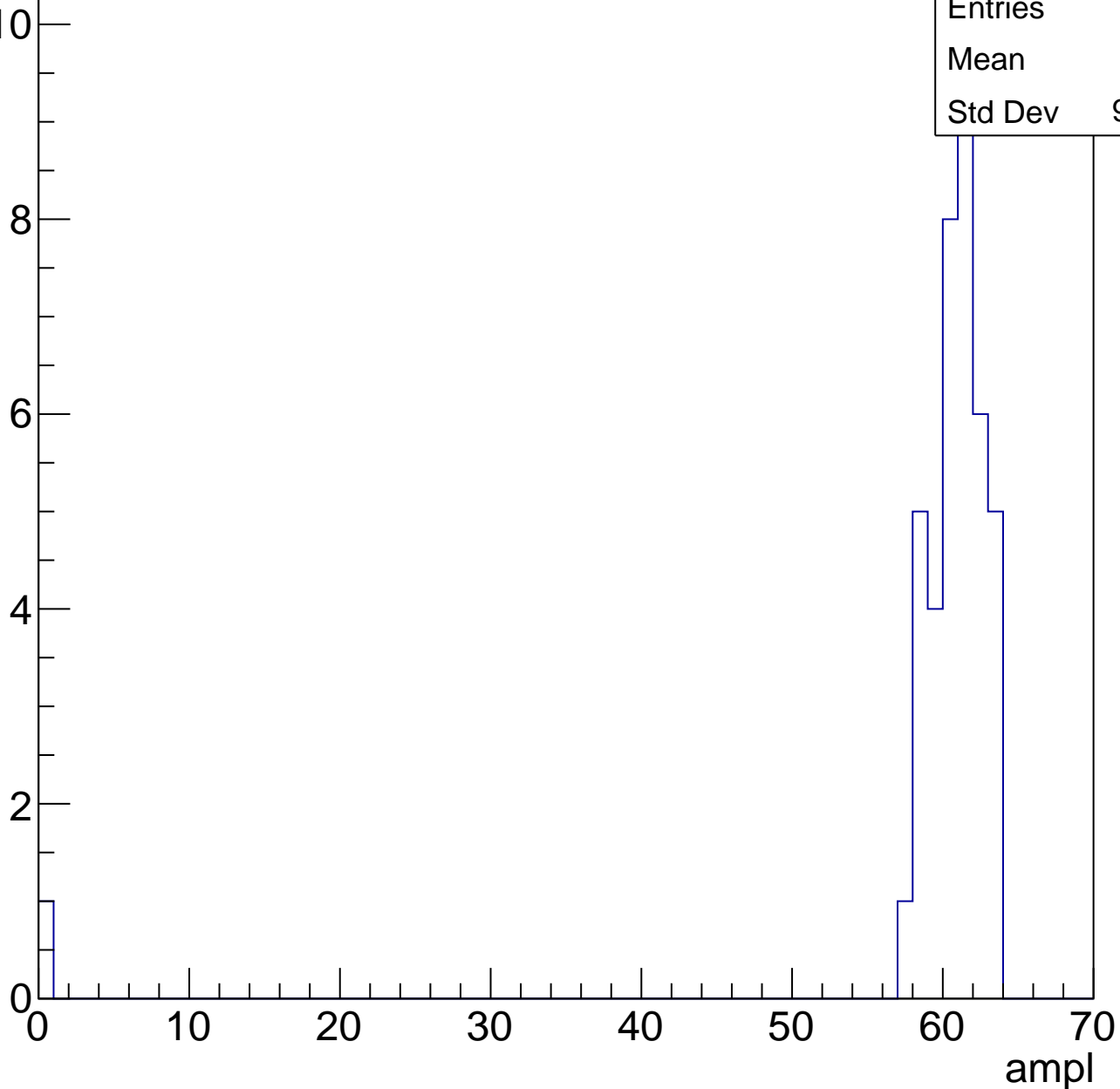


B1L003S, U20-ch69, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	59
Std Dev	9.581



B1L003S, U20-ch69, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch69, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch70, adc0

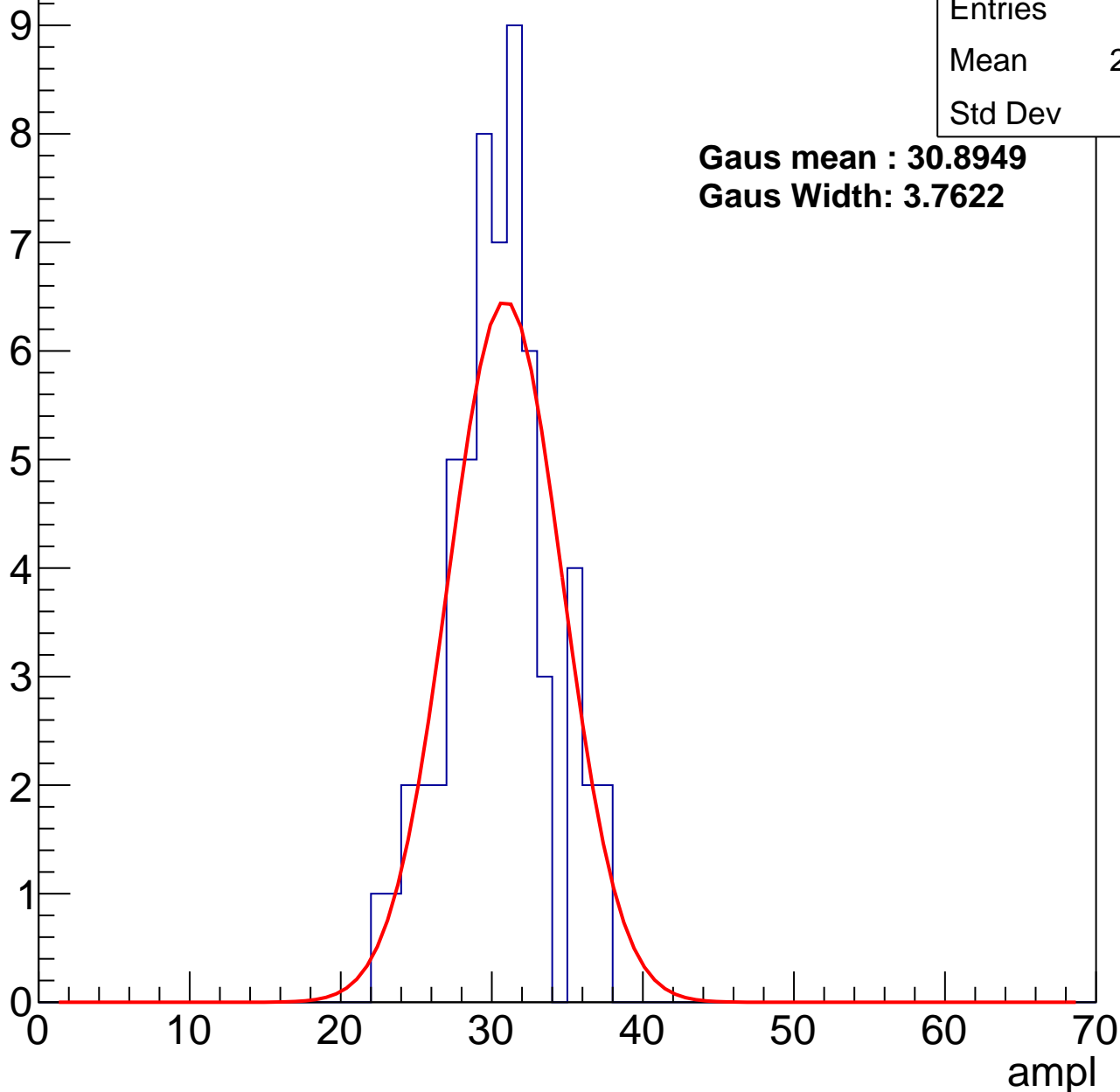
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.97
Std Dev	3.37

Gaus mean : 30.8949

Gaus Width: 3.7622



B1L003S, U20-ch70, adc1

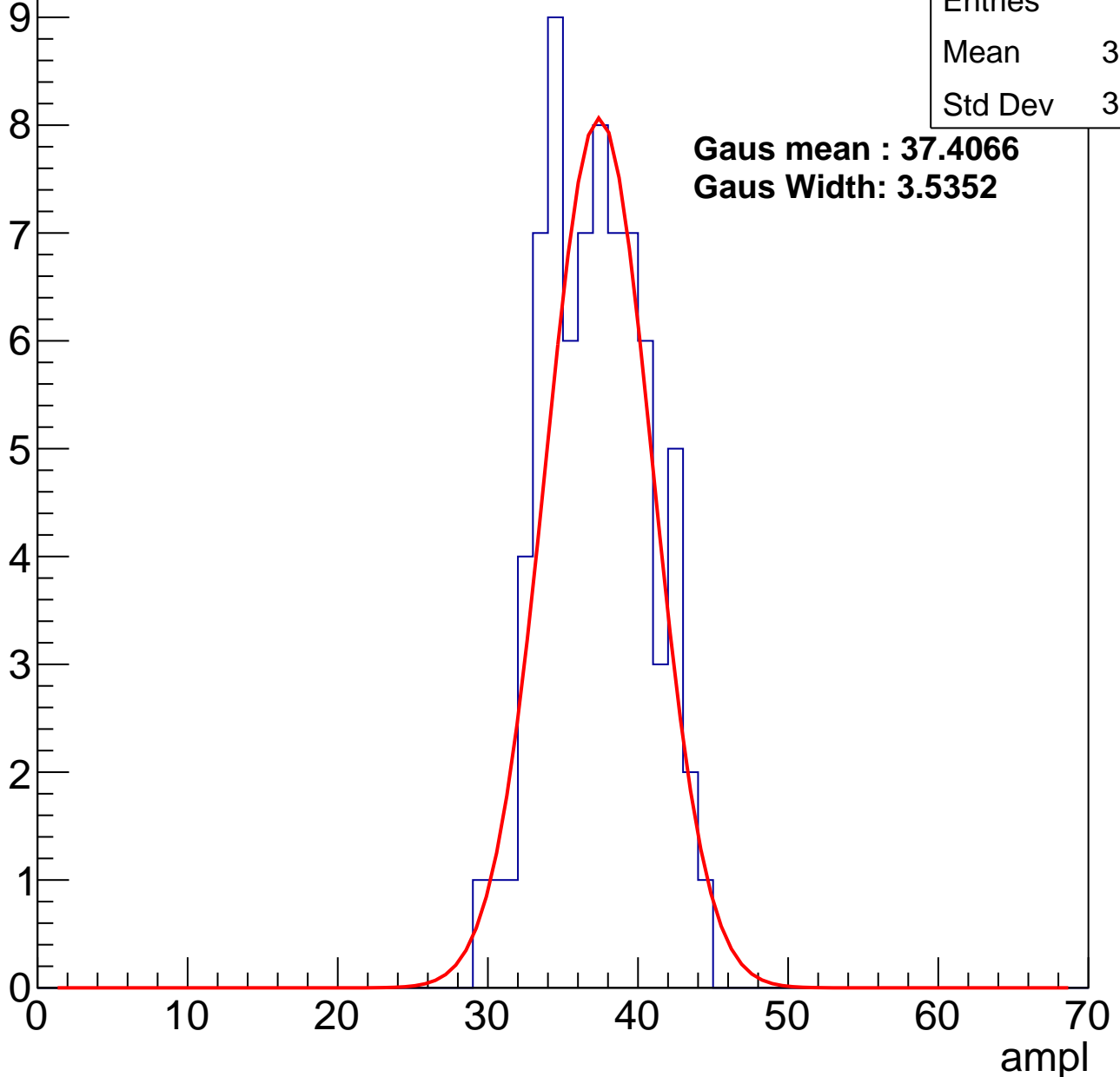
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.73
Std Dev	3.368

Gaus mean : 37.4066

Gaus Width: 3.5352



B1L003S, U20-ch70, adc2

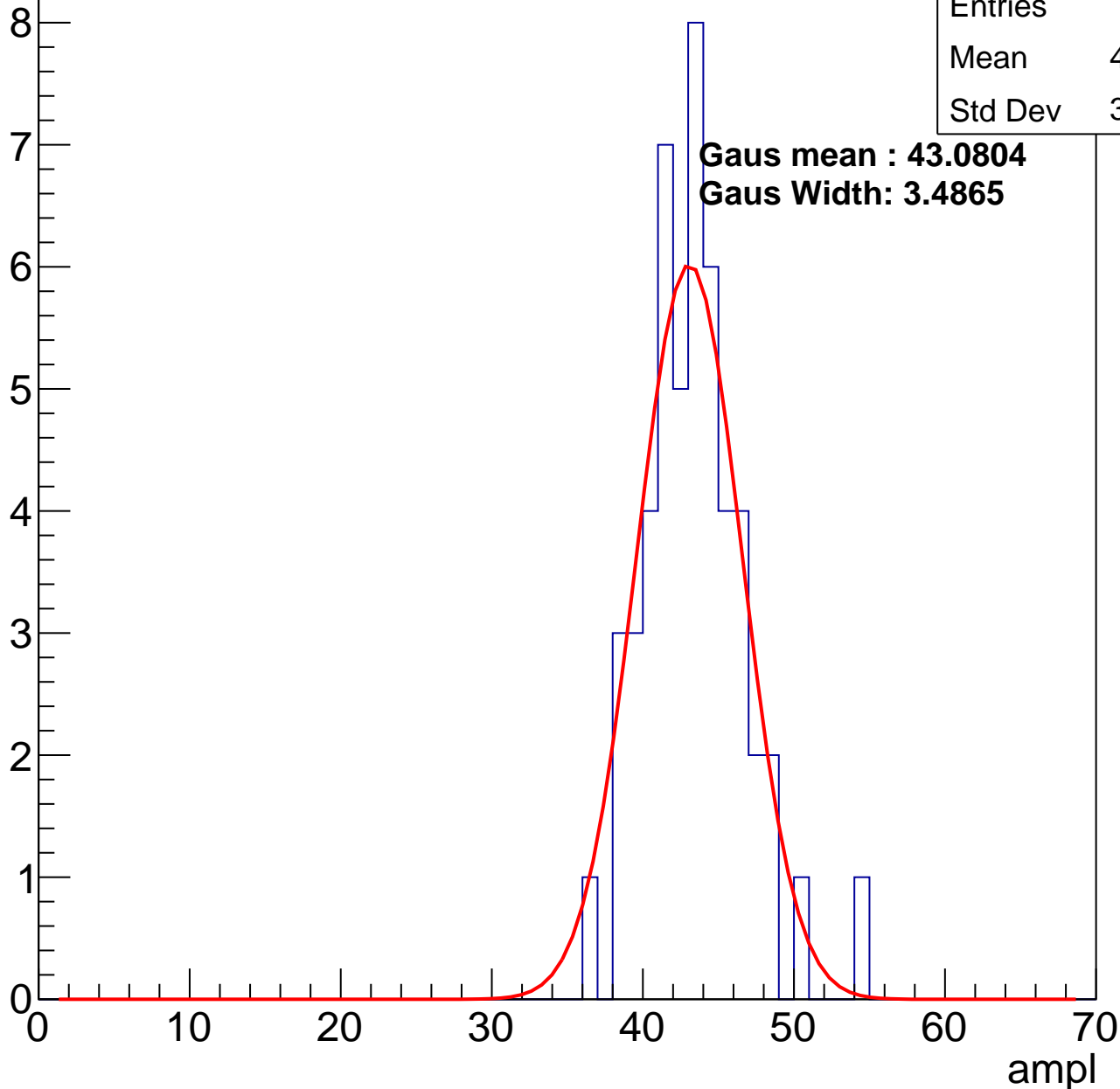
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	42.94
Std Dev	3.286

Gaus mean : 43.0804

Gaus Width: 3.4865

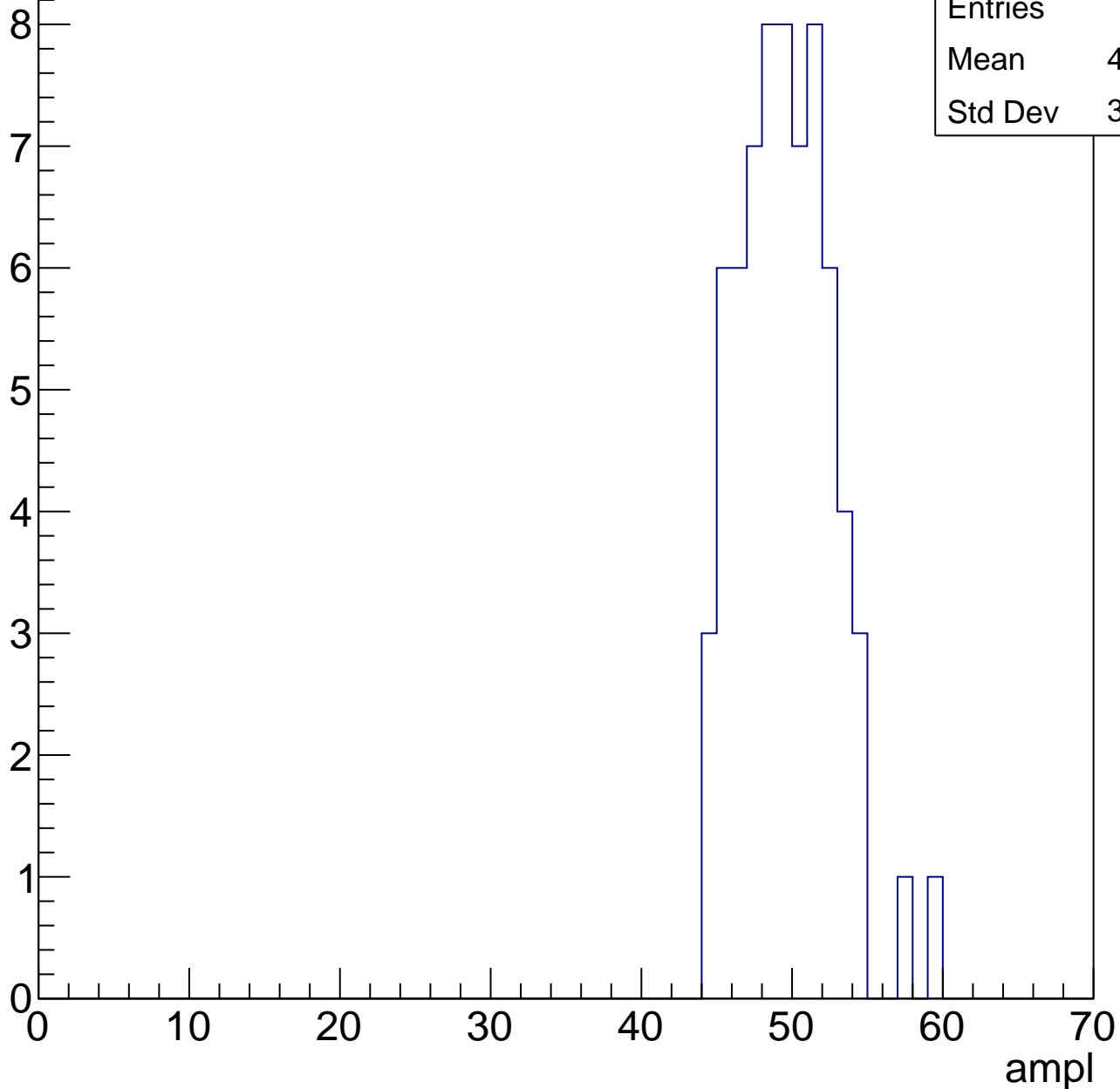


B1L003S, U20-ch70, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.16
Std Dev	3.104

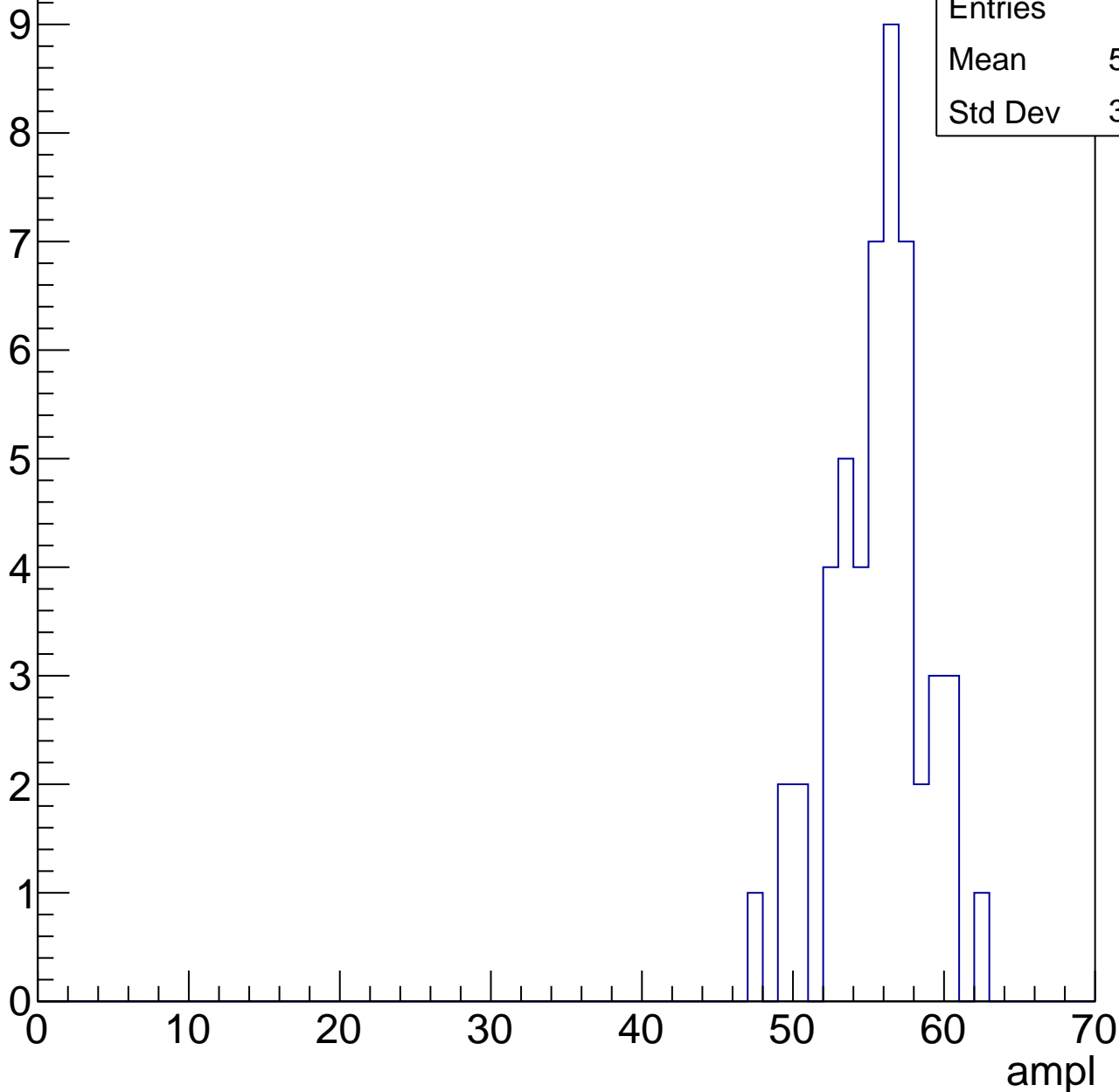


B1L003S, U20-ch70, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.14
Std Dev	3.073

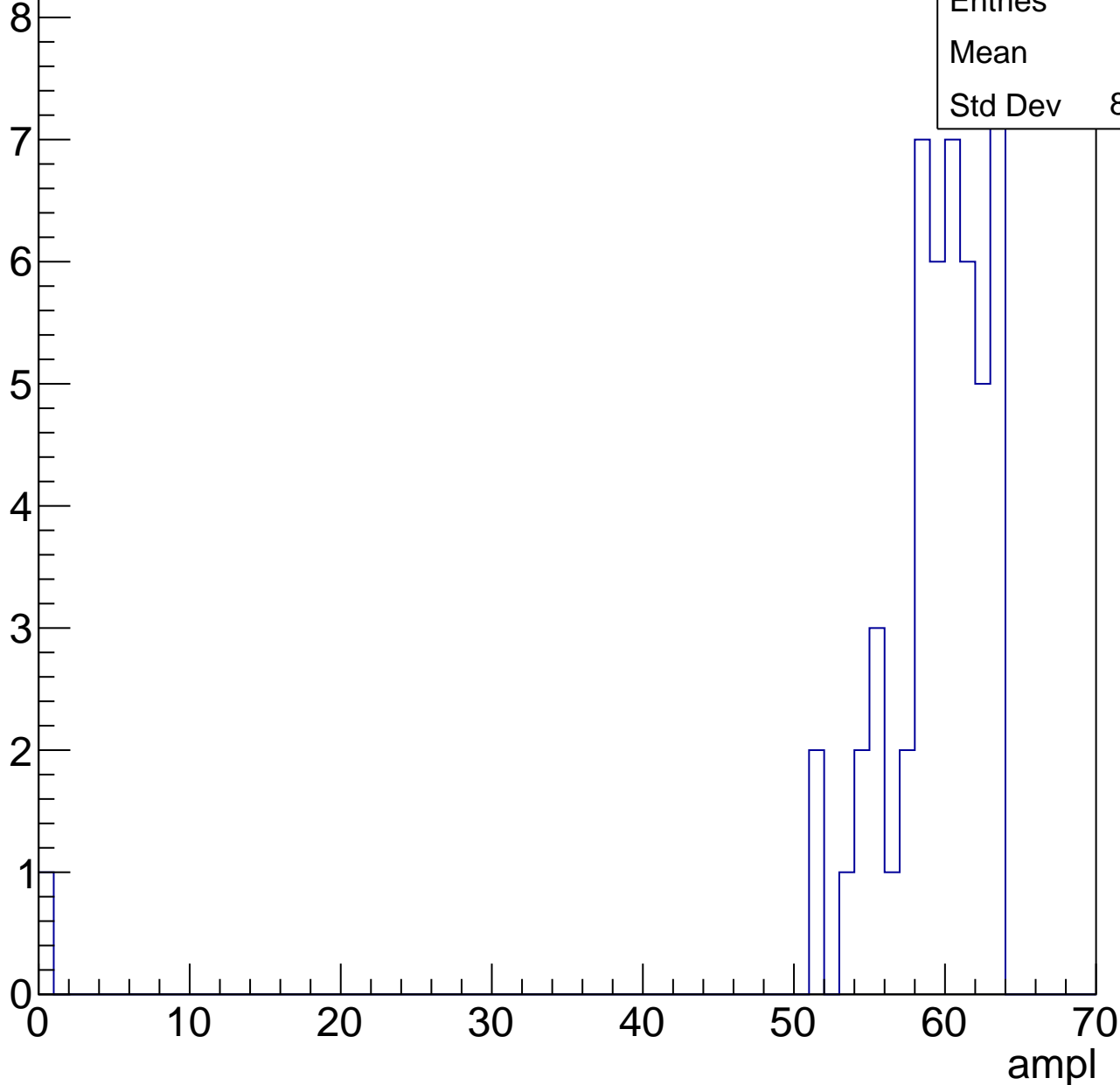


B1L003S, U20-ch70, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

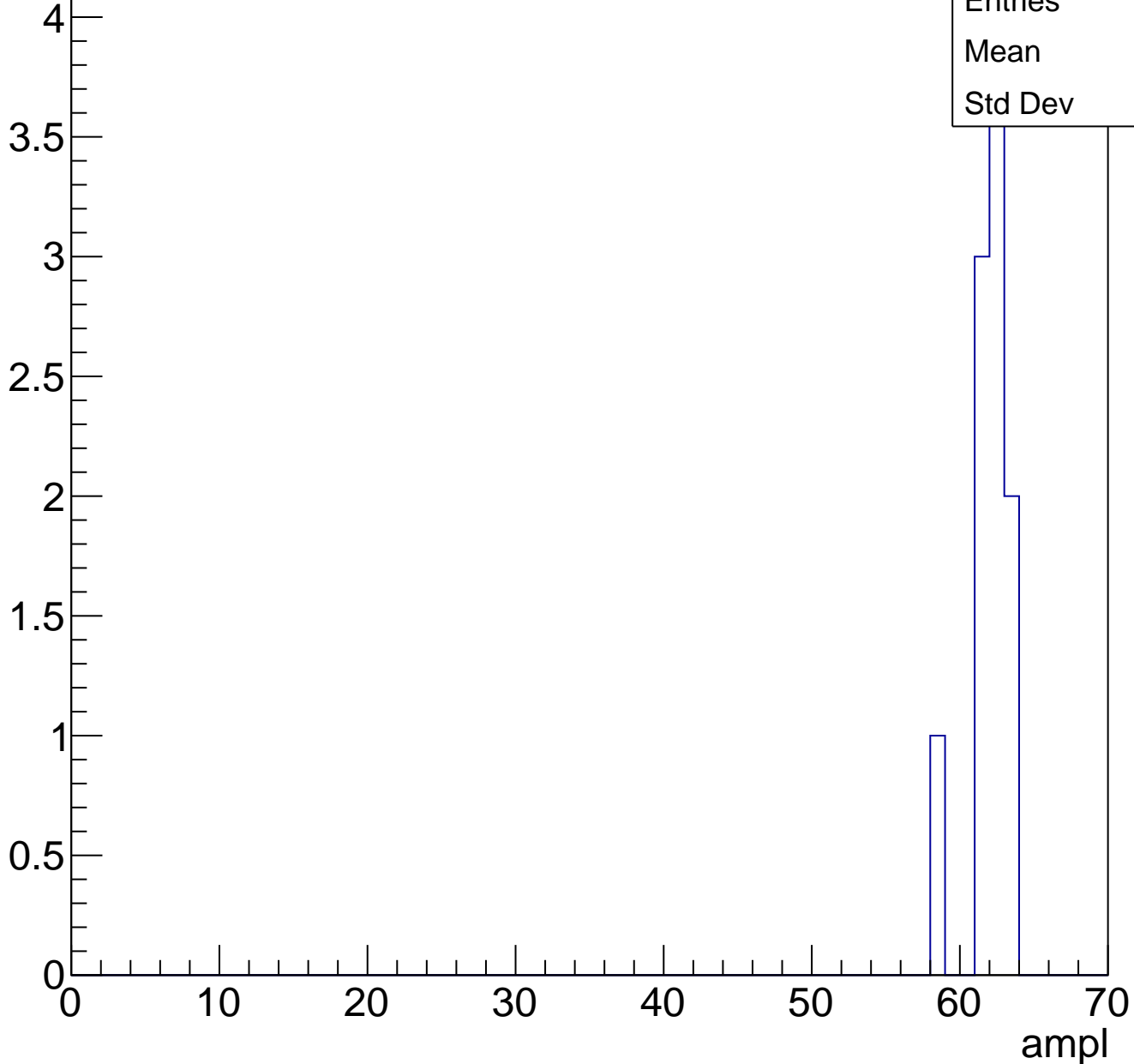
Entries	51
Mean	58
Std Dev	8.767



B1L003S, U20-ch70, adc6

calib_packv5_042523_0143.root, FC#13, port D2

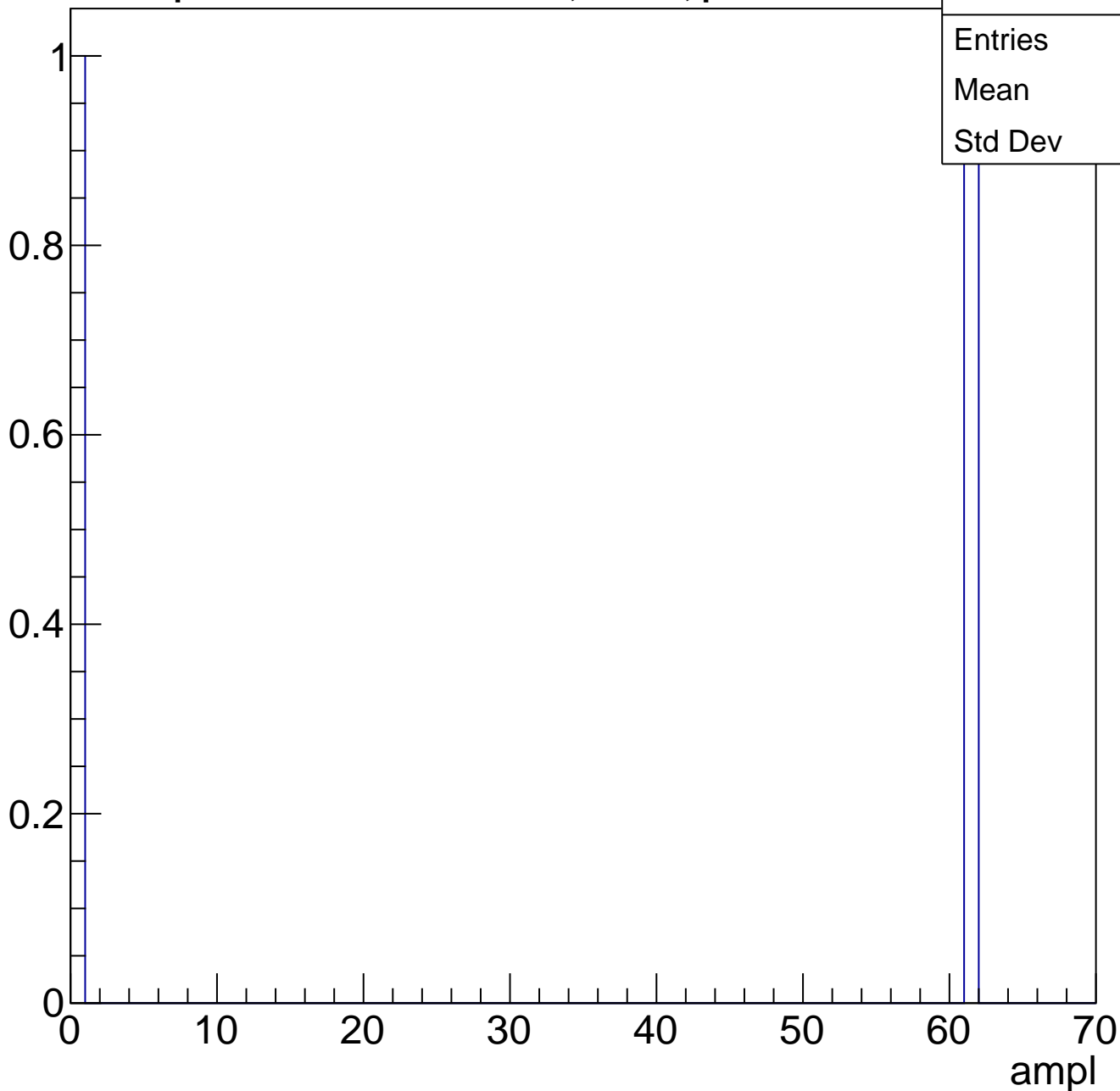
Entry



B1L003S, U20-ch70, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch71, adc0

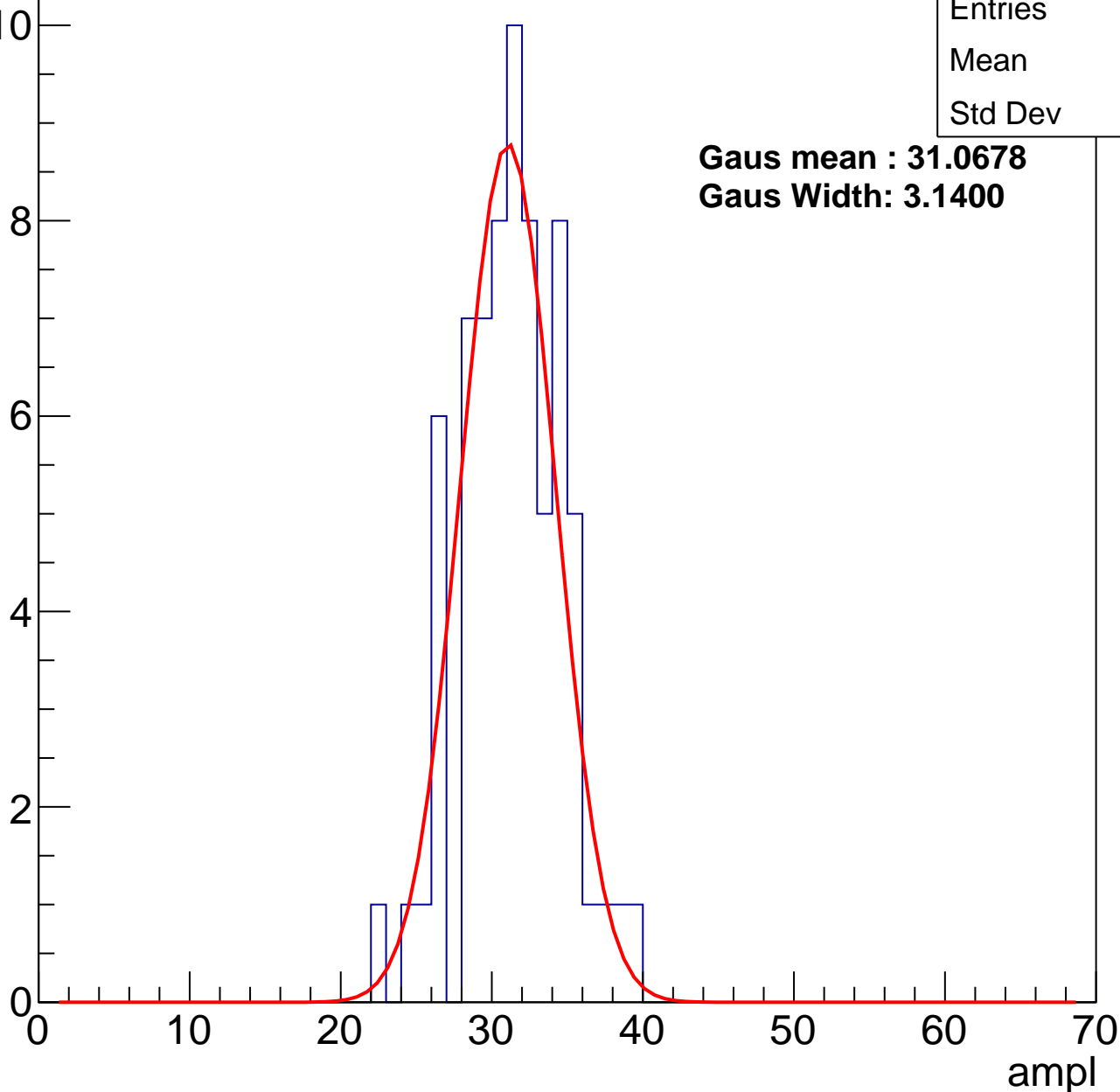
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.9
Std Dev	3.29

Gaus mean : 31.0678

Gaus Width: 3.1400



B1L003S, U20-ch71, adc1

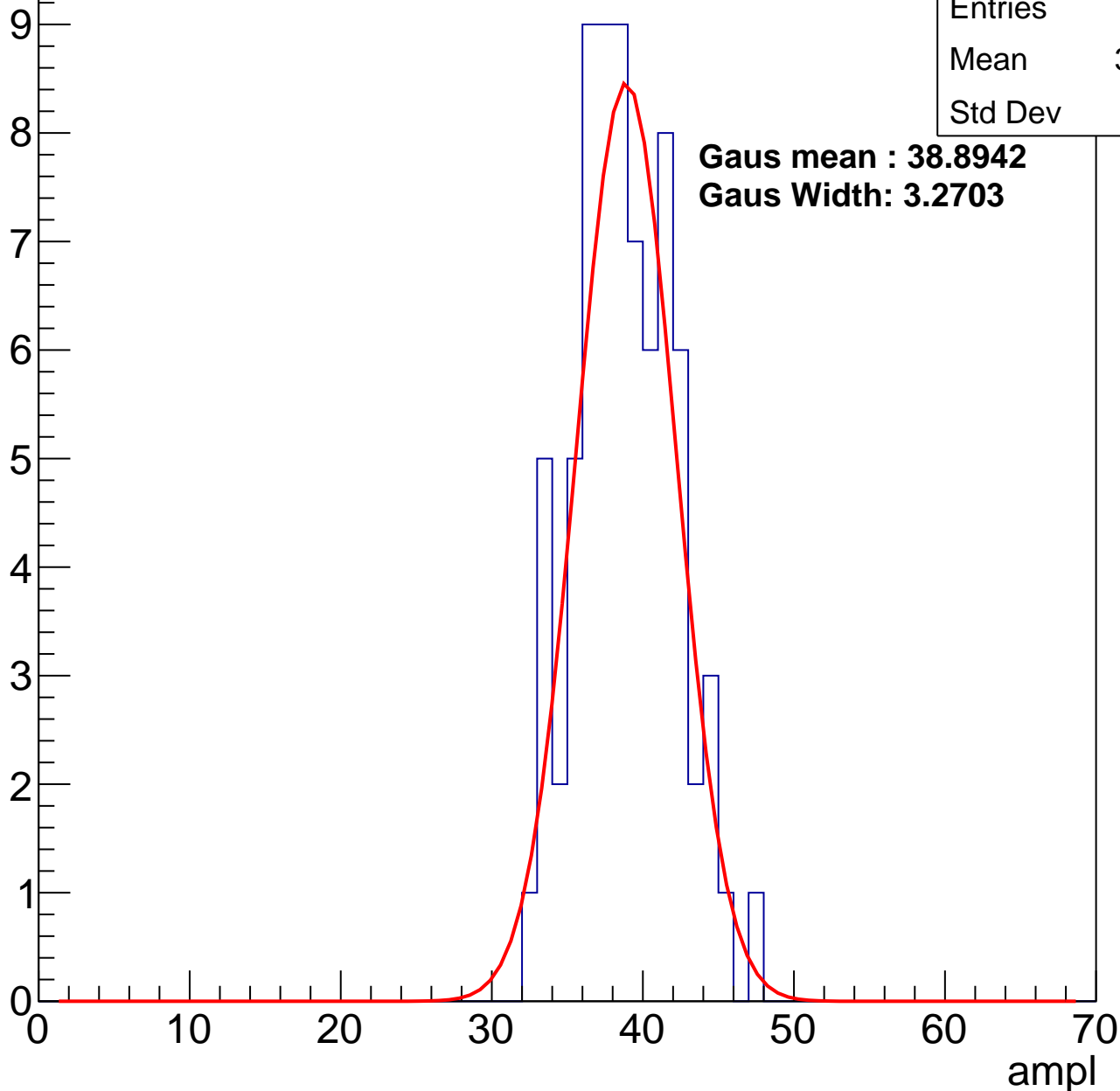
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	38.41
Std Dev	3.2

Gaus mean : 38.8942

Gaus Width: 3.2703



B1L003S, U20-ch71, adc2

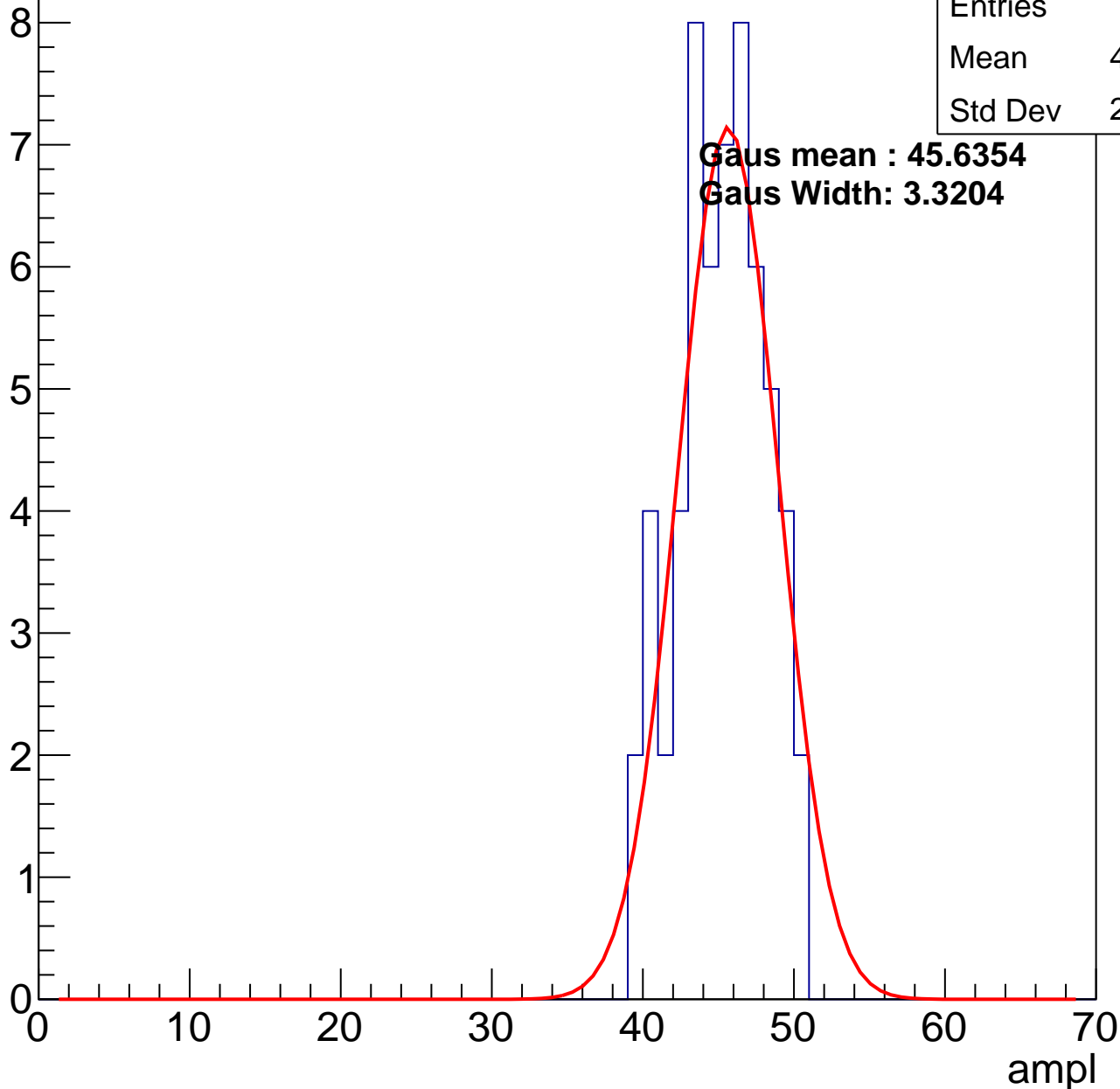
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.78
Std Dev	2.835

Gaus mean : 45.6354

Gaus Width: 3.3204

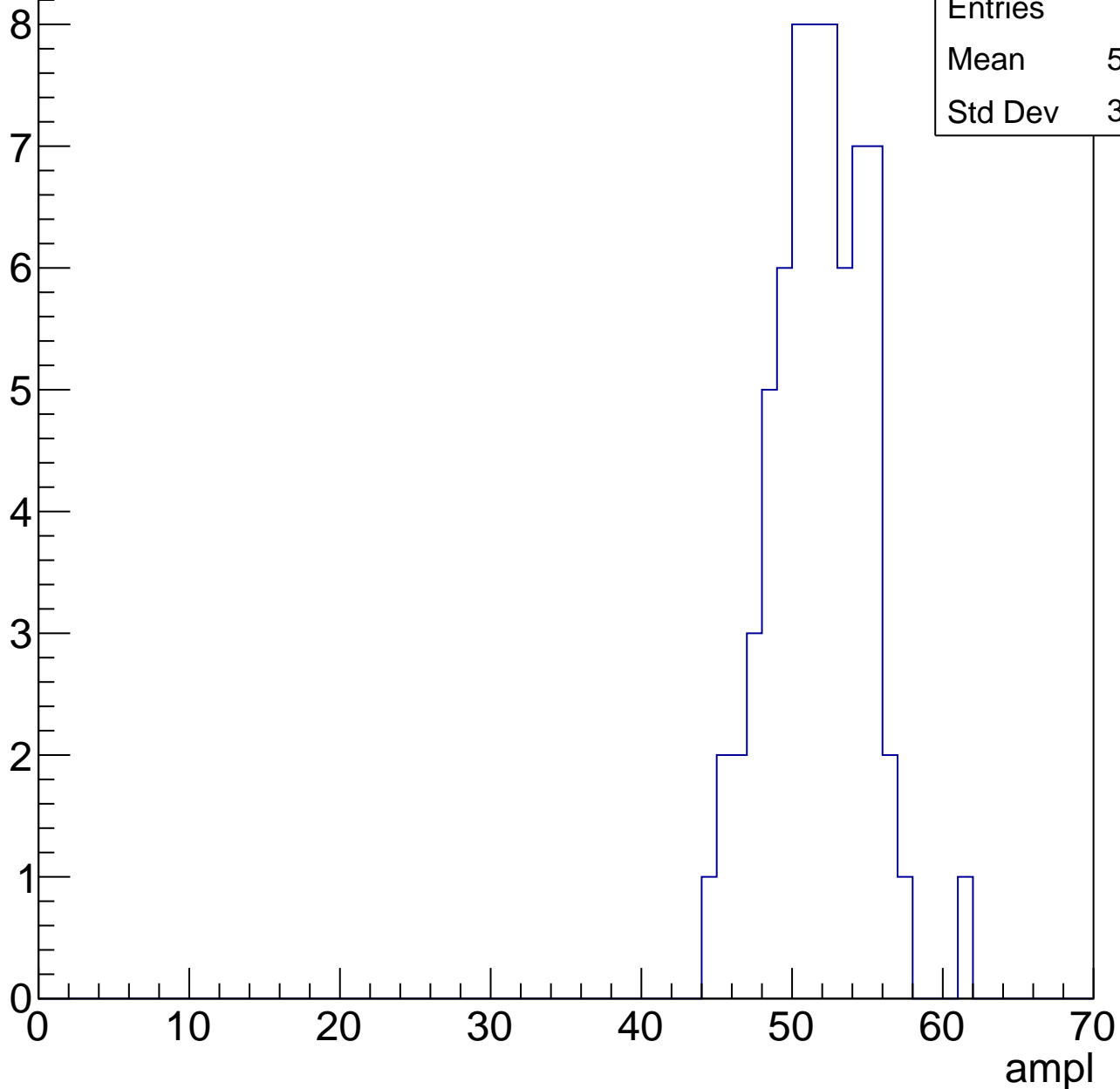


B1L003S, U20-ch71, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	51.28
Std Dev	3.194

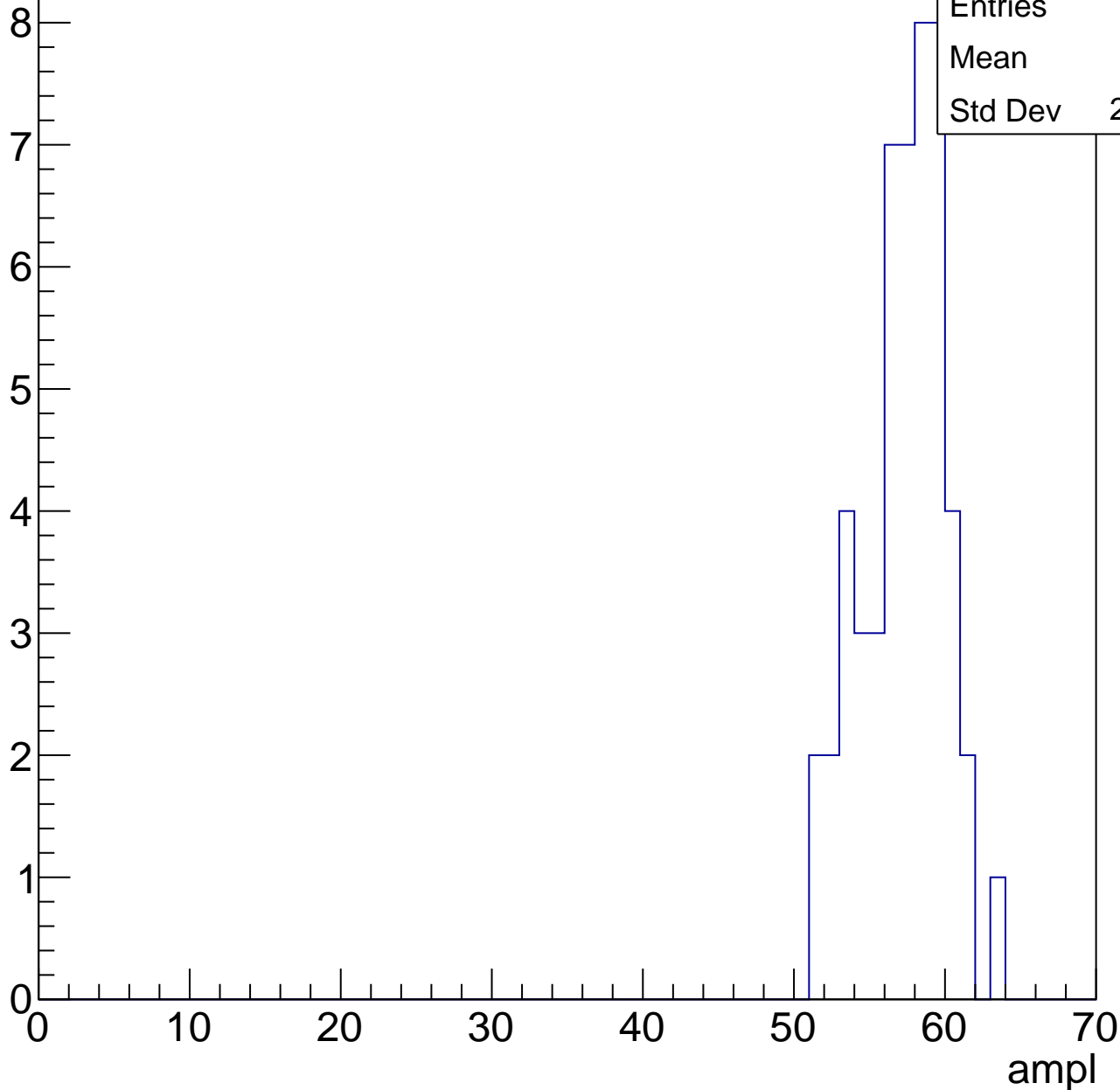


B1L003S, U20-ch71, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.8
Std Dev	2.708

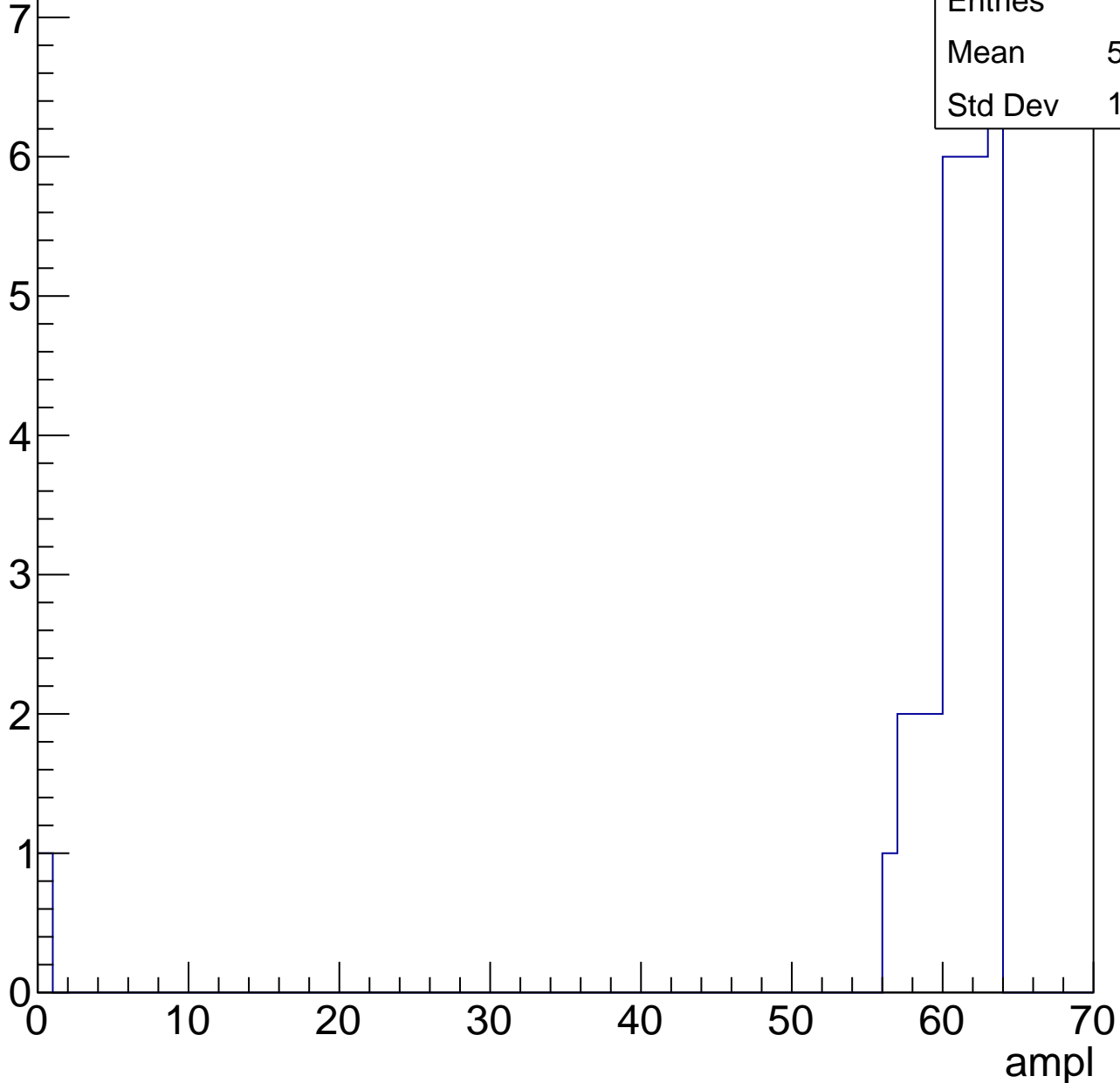


B1L003S, U20-ch71, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.88
Std Dev	10.58



B1L003S, U20-ch71, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch71, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch72, adc0

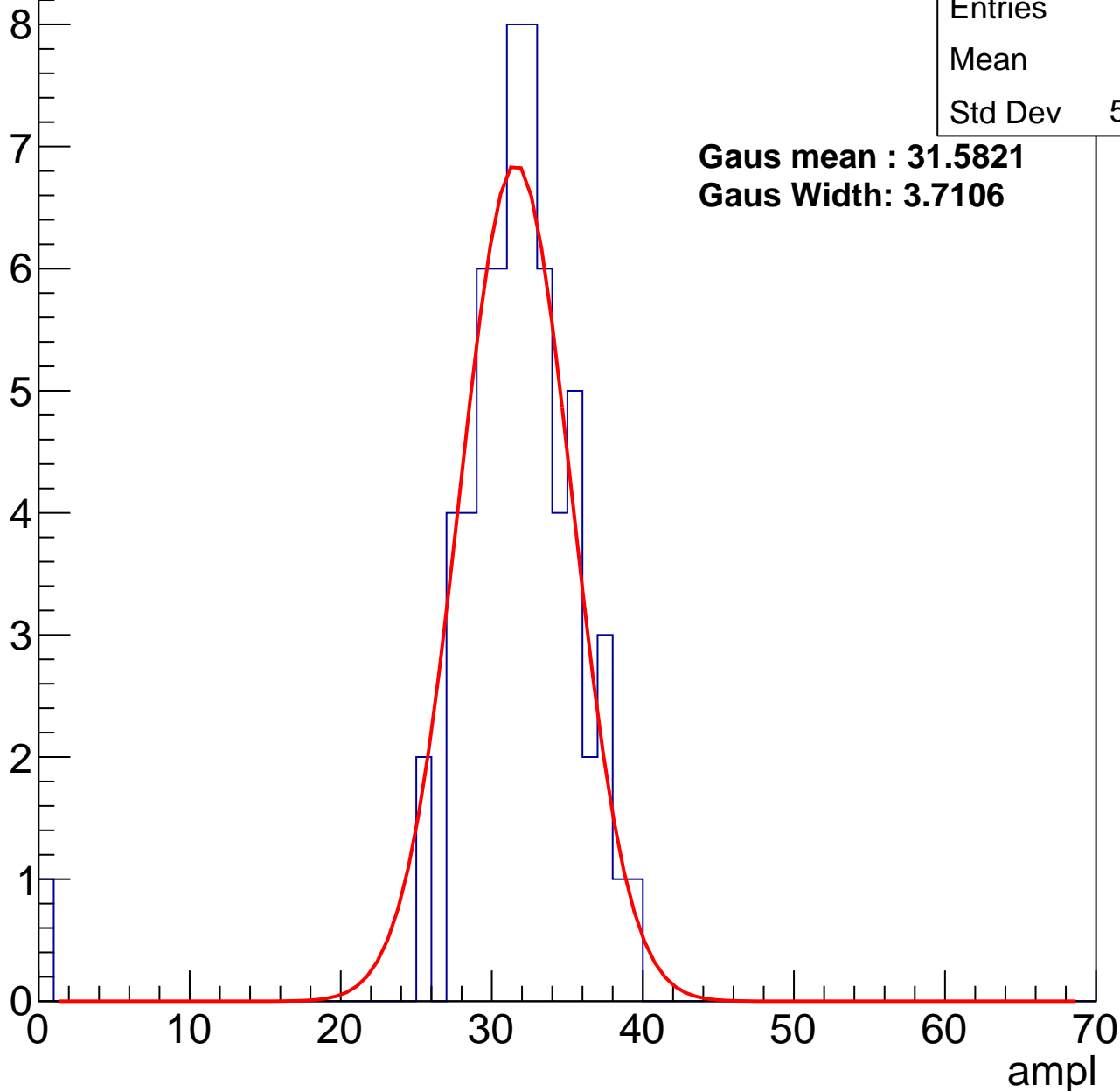
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	31.1
Std Dev	5.092

Gaus mean : 31.5821

Gaus Width: 3.7106



B1L003S, U20-ch72, adc1

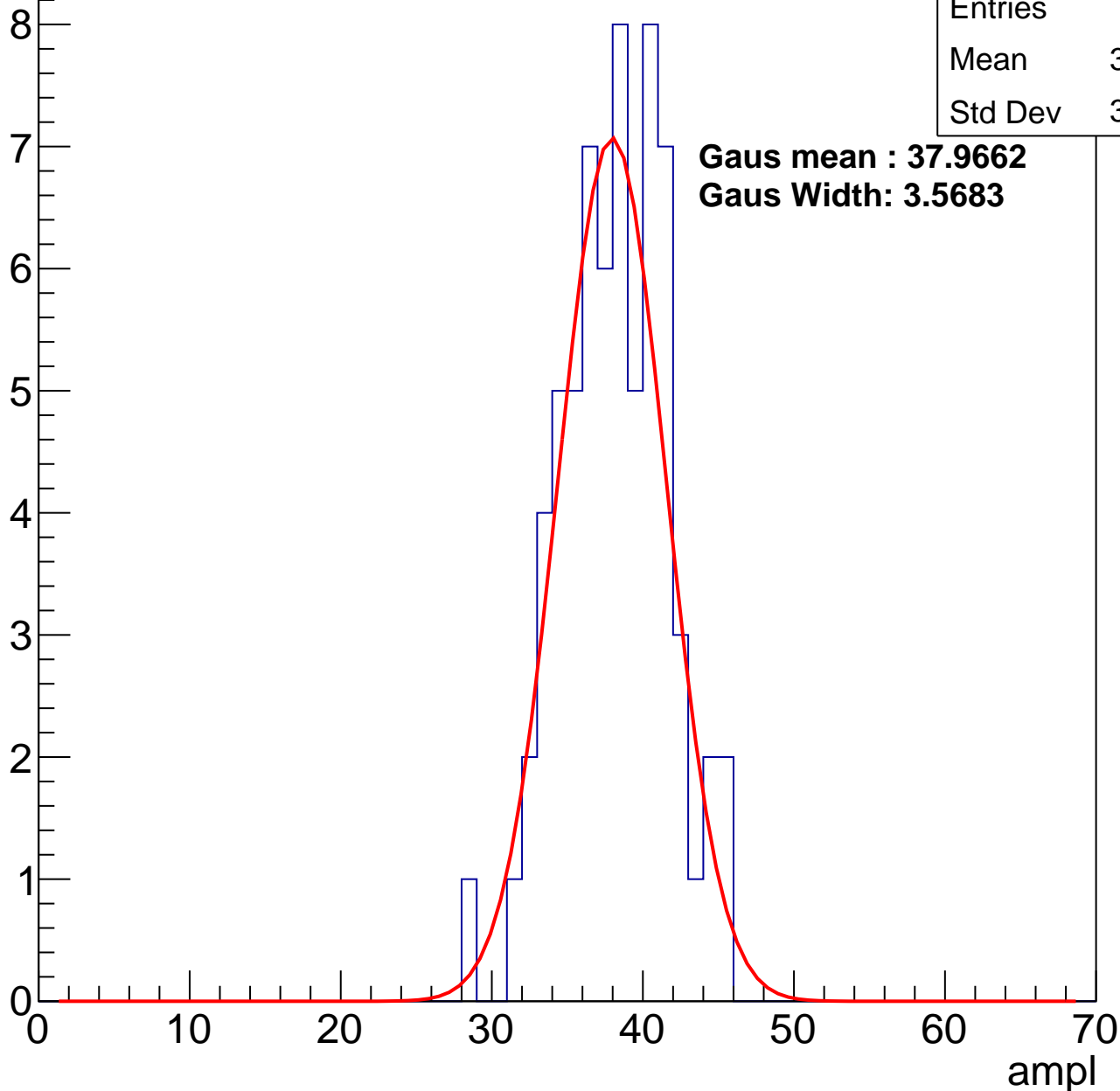
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.72
Std Dev	3.493

Gaus mean : 37.9662

Gaus Width: 3.5683

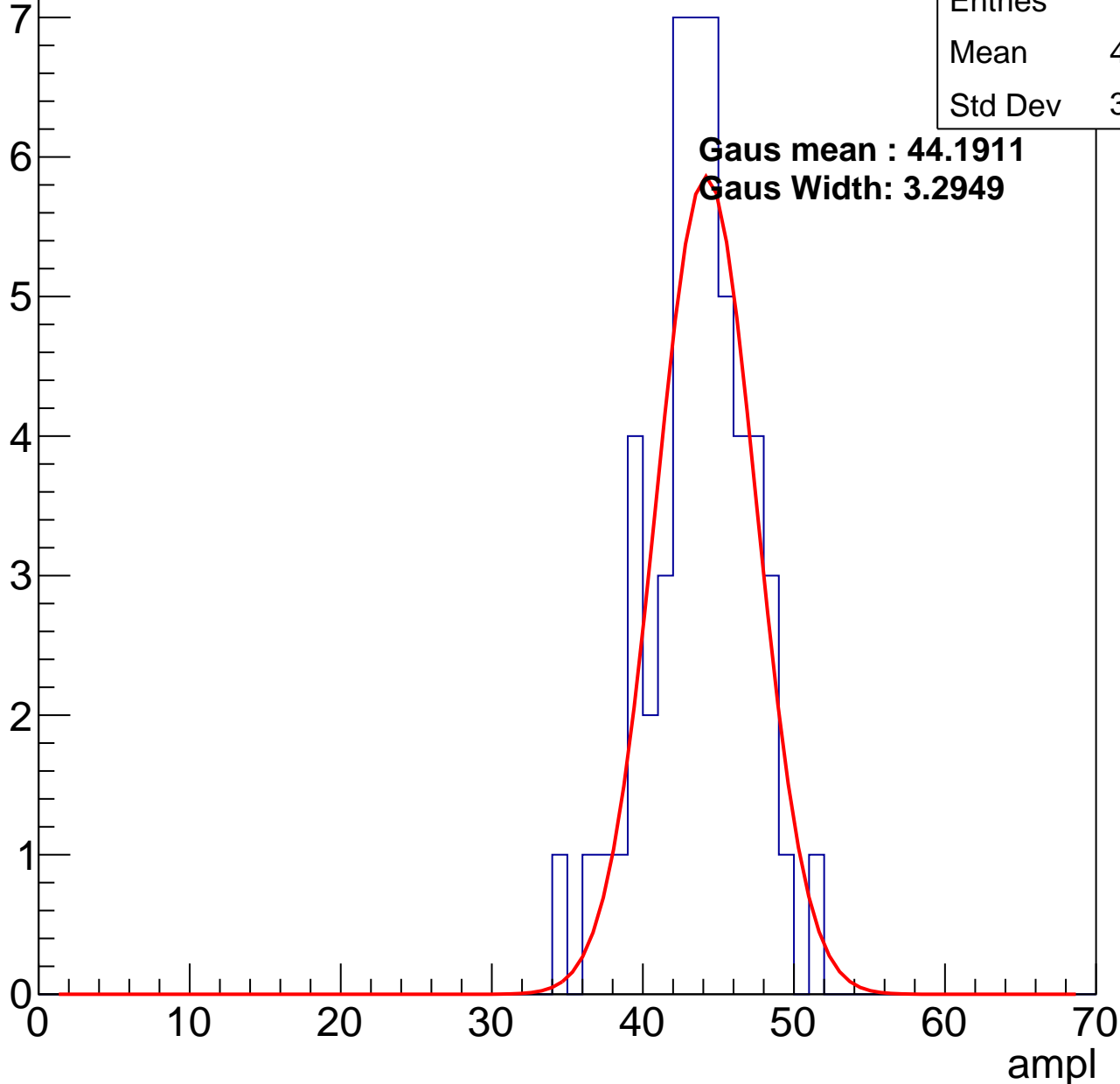


B1L003S, U20-ch72, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.23
Std Dev	3.372

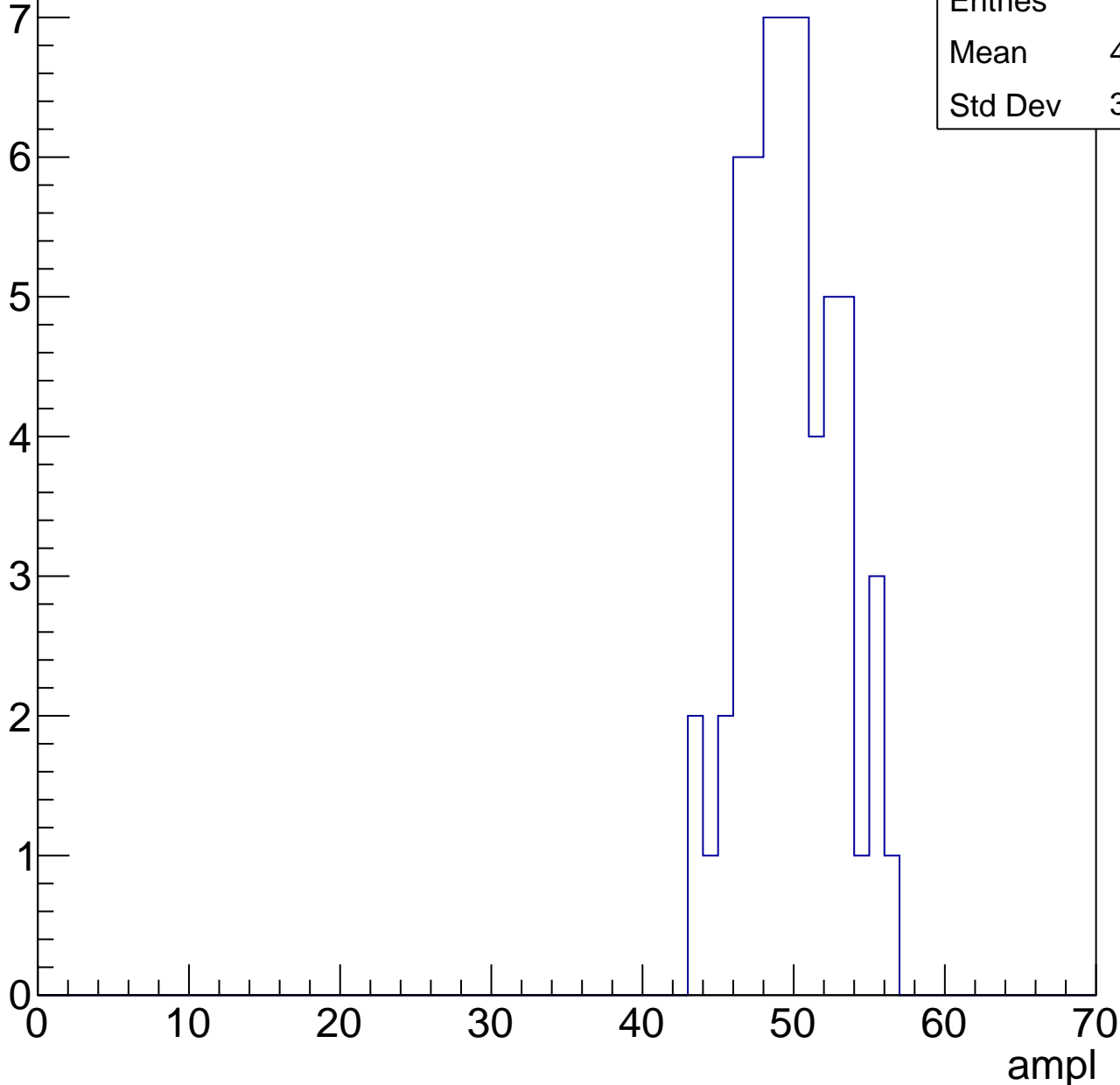


B1L003S, U20-ch72, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	49.32
Std Dev	3.073

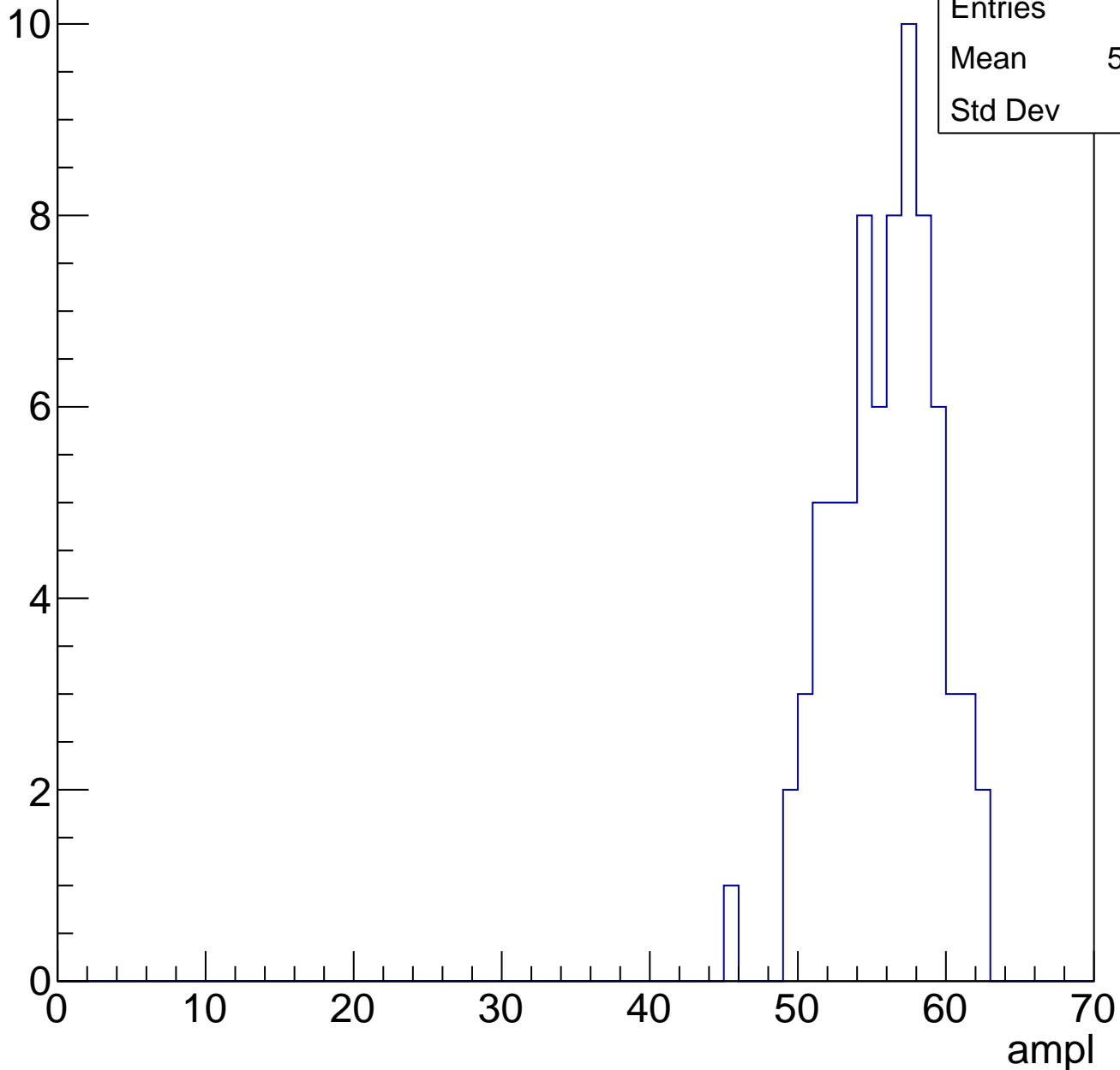


B1L003S, U20-ch72, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	55.44
Std Dev	3.43

Entry

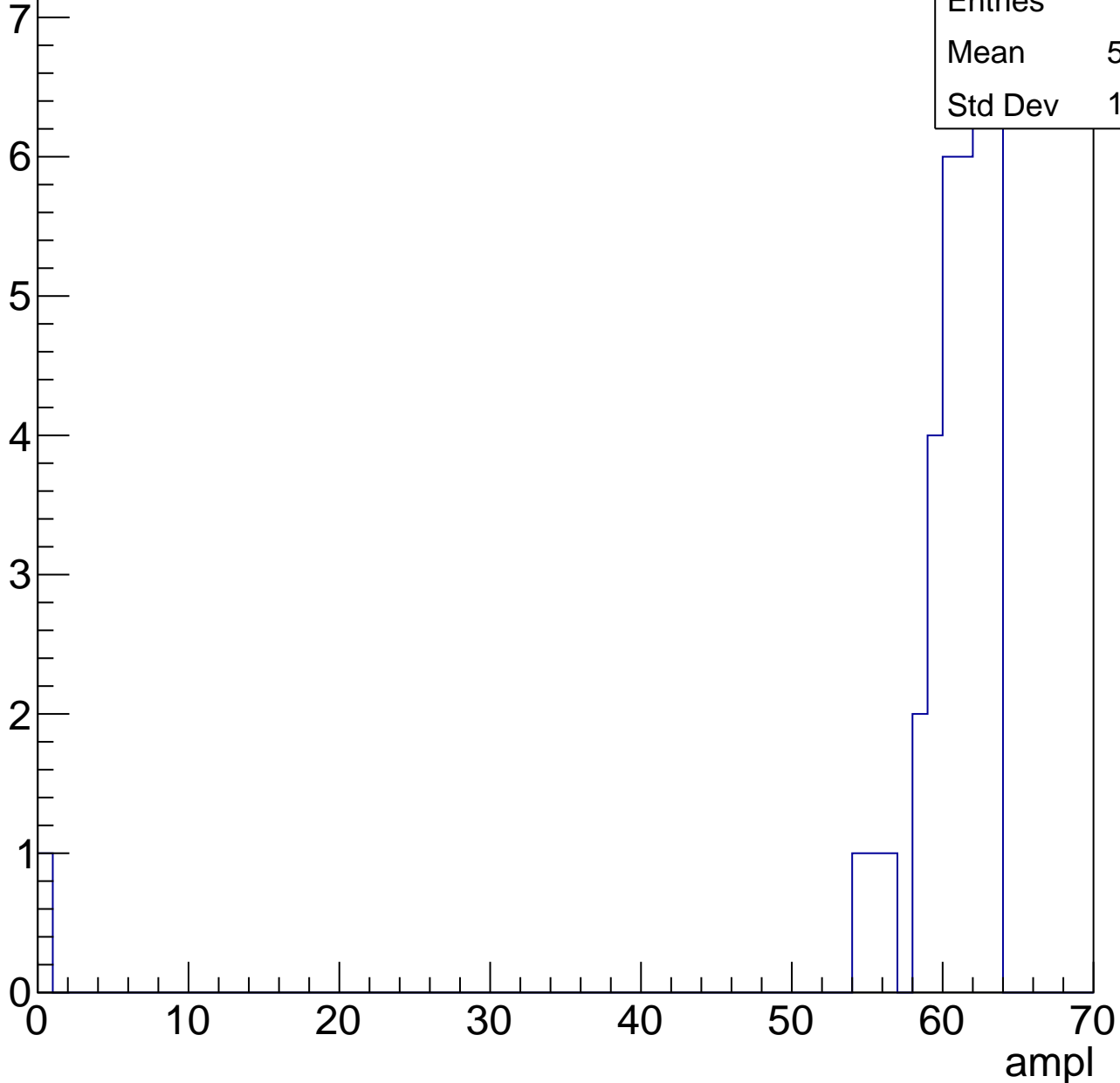


B1L003S, U20-ch72, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	58.83
Std Dev	10.19



B1L003S, U20-ch72, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch72, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch73, adc0

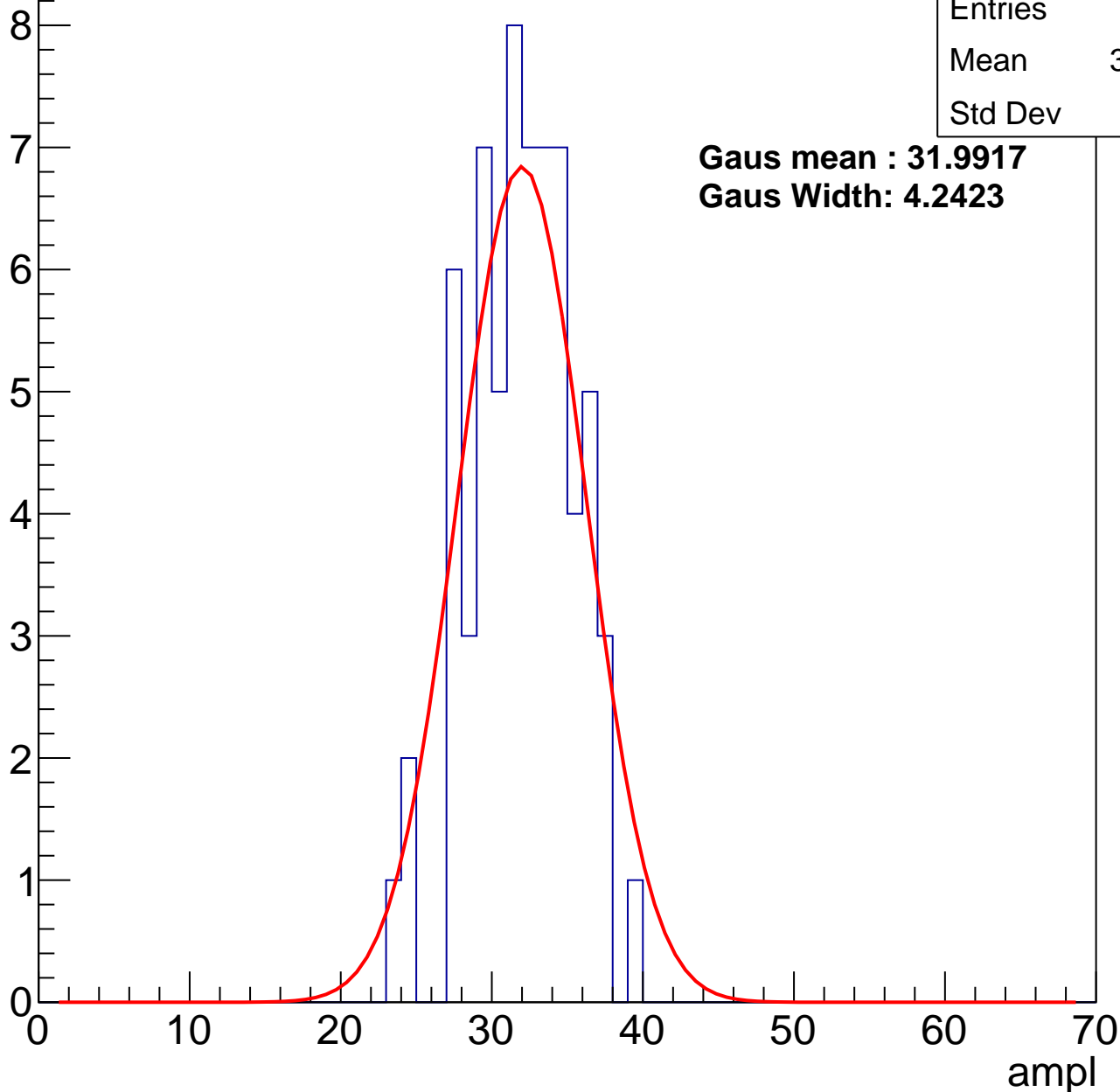
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	31.53
Std Dev	3.39

Gaus mean : 31.9917

Gaus Width: 4.2423



B1L003S, U20-ch73, adc1

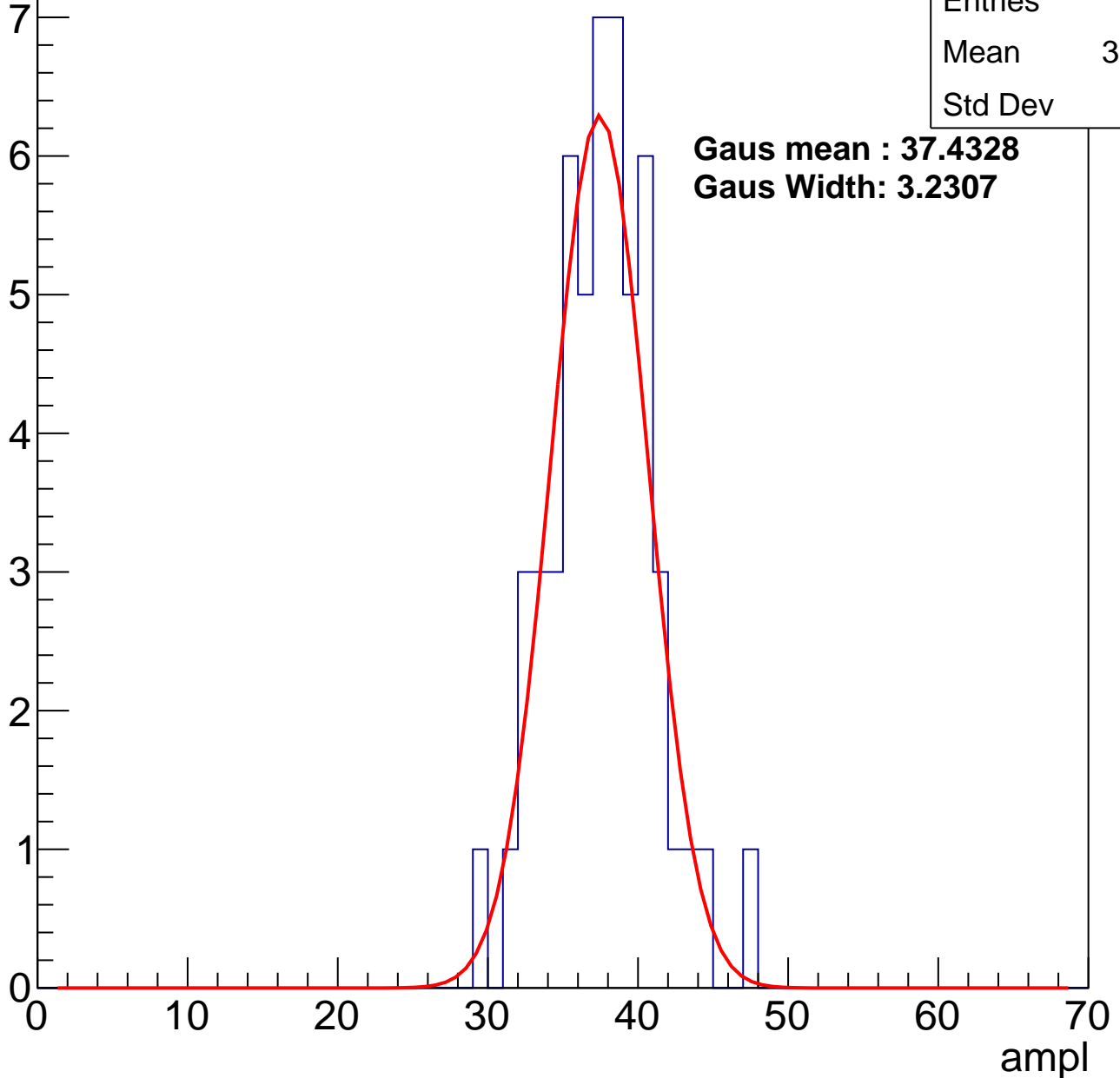
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	37.15
Std Dev	3.38

Gaus mean : 37.4328

Gaus Width: 3.2307



B1L003S, U20-ch73, adc2

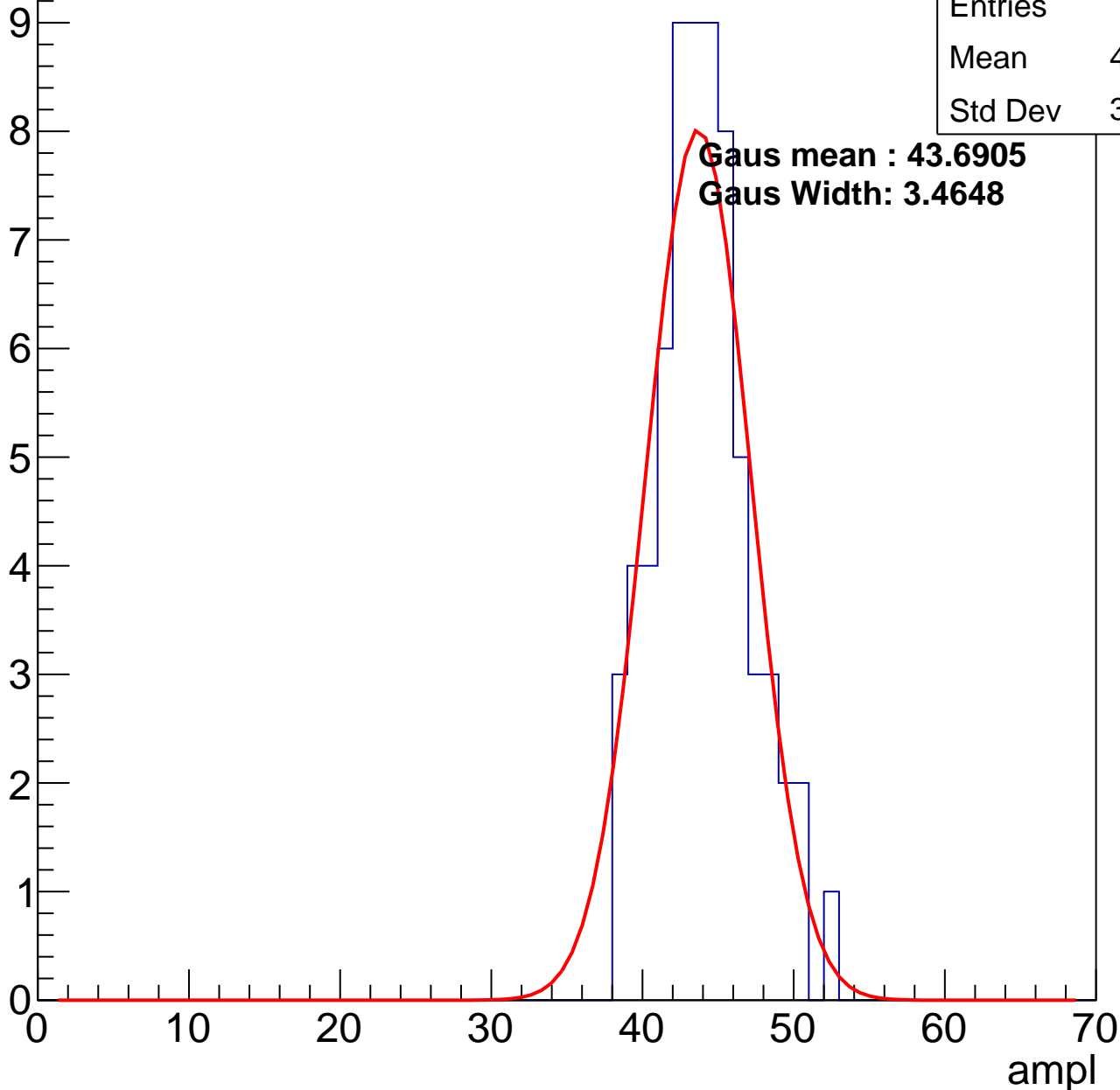
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	43.56
Std Dev	3.084

Gaus mean : 43.6905

Gaus Width: 3.4648



B1L003S, U20-ch73, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	50.59
Std Dev	3.56

Entry

10

8

6

4

2

0

0

10

20

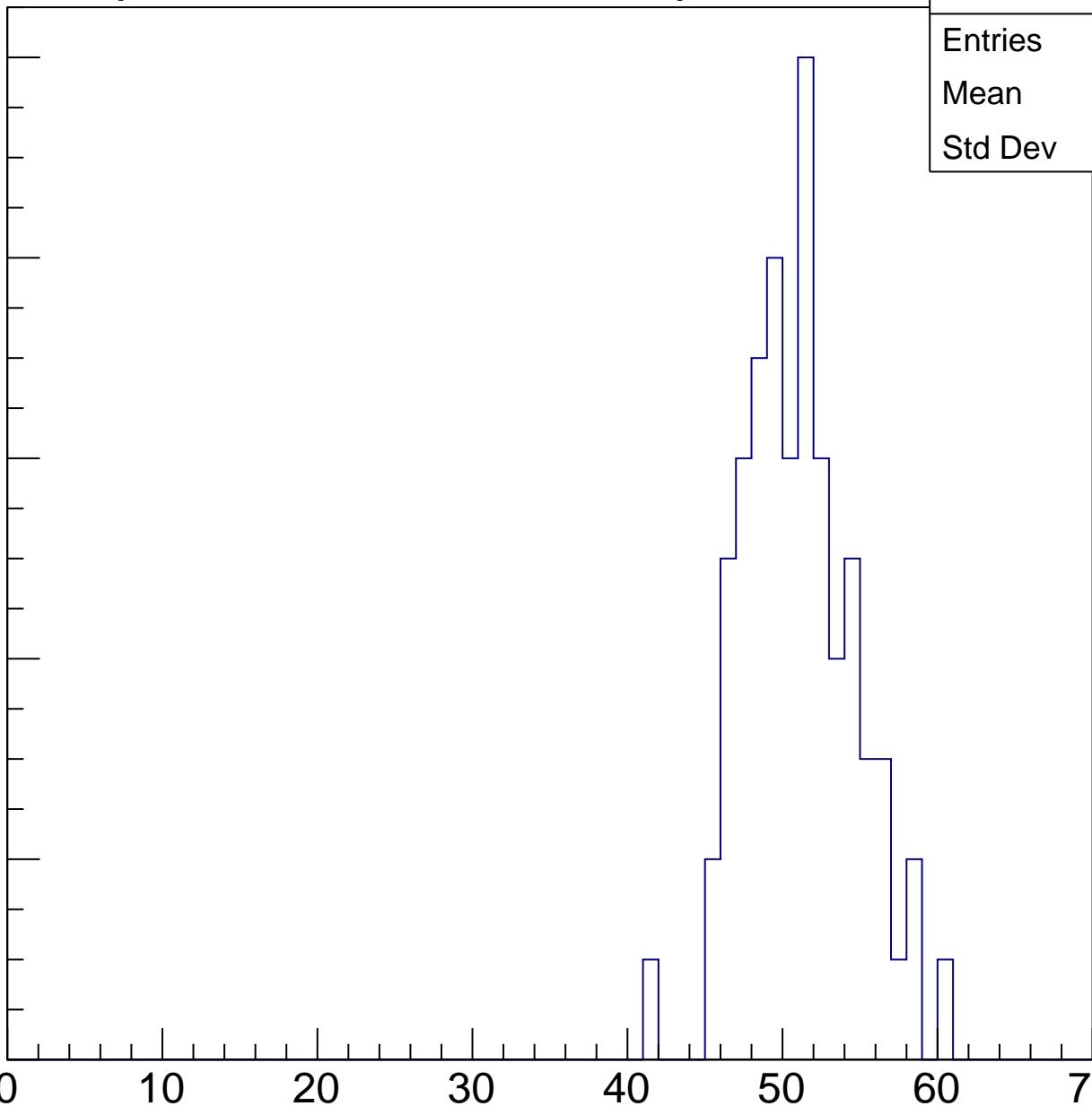
30

40

50

60

ampl

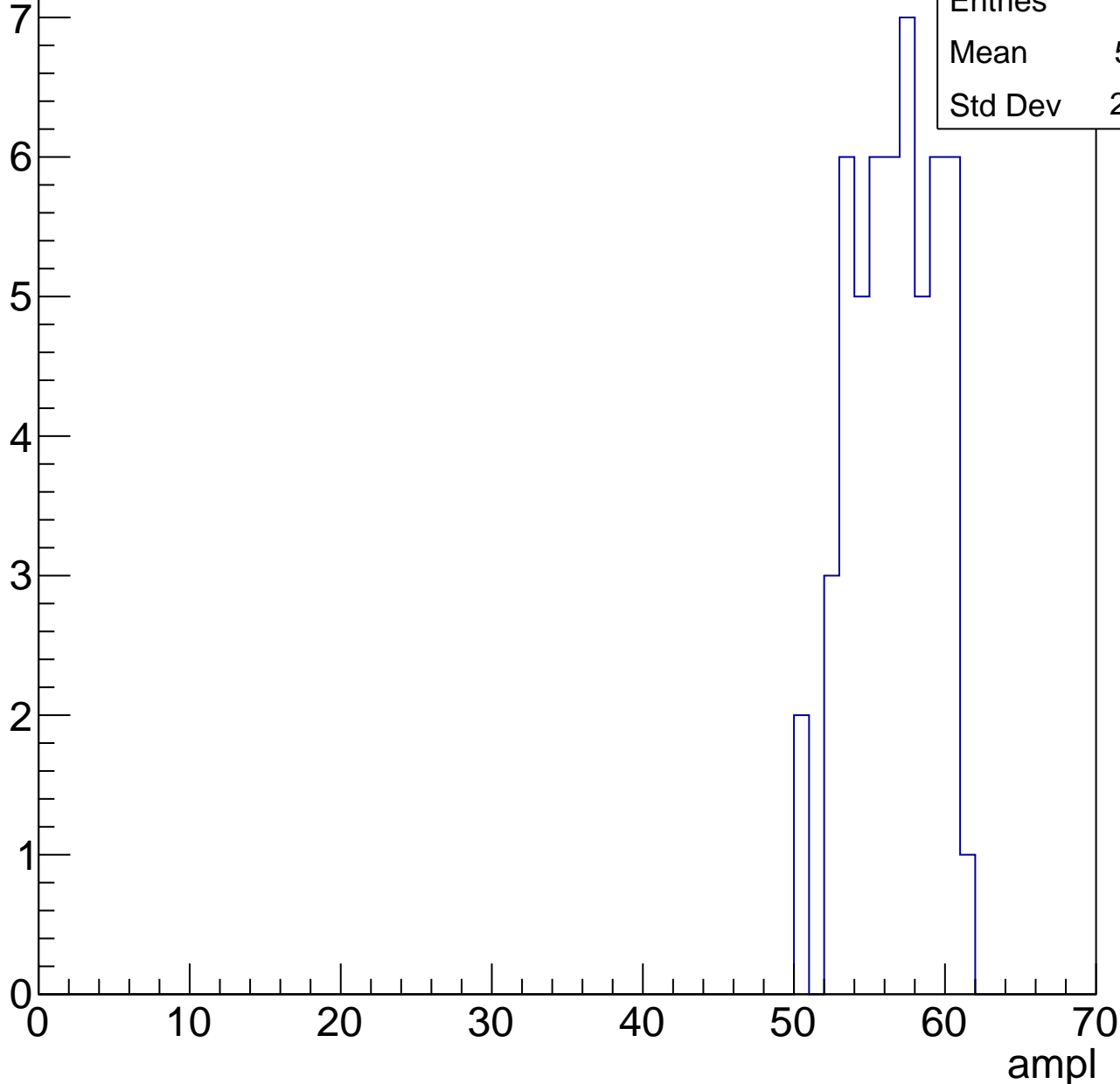


B1L003S, U20-ch73, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56.11
Std Dev	2.752

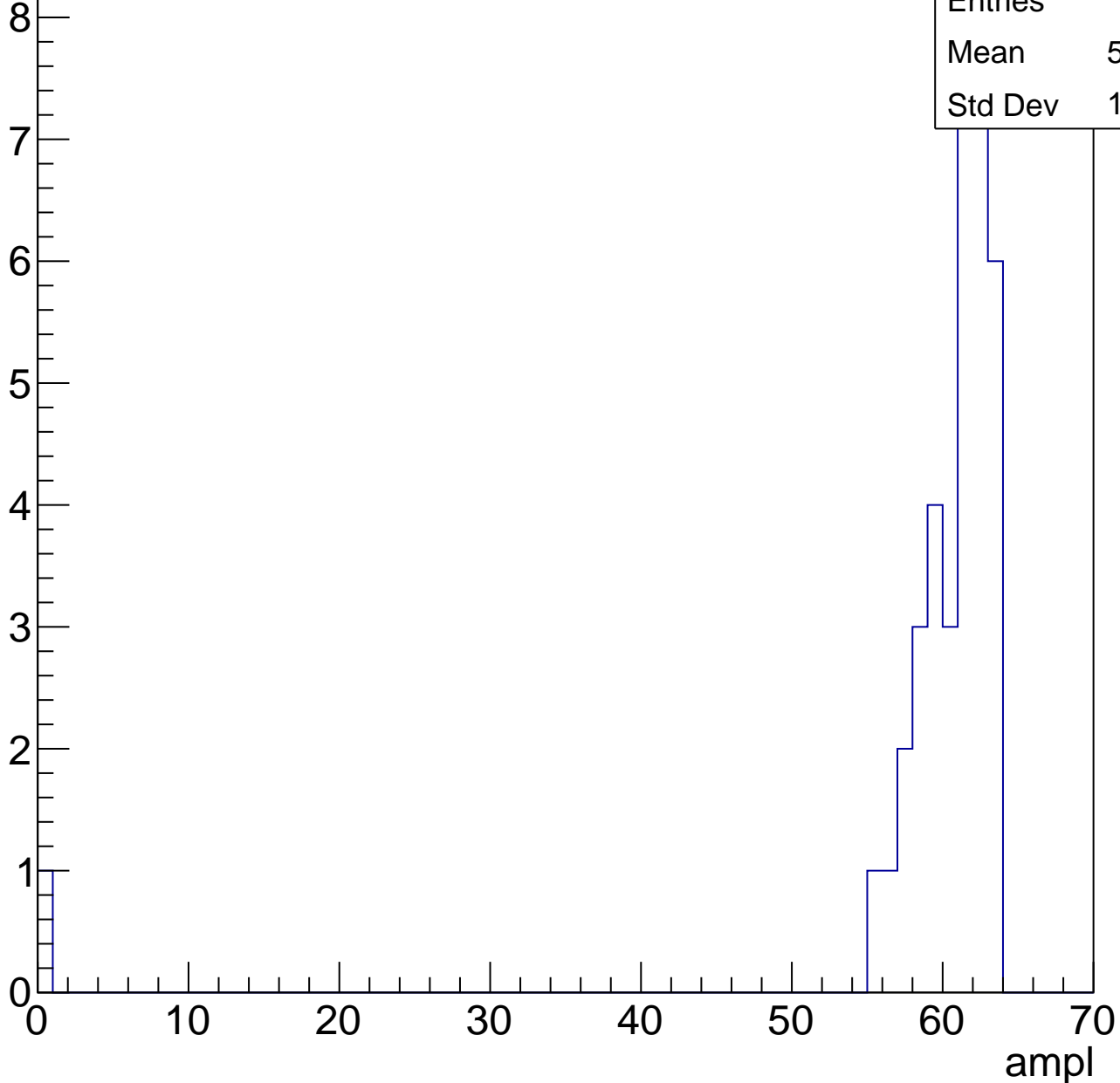


B1L003S, U20-ch73, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	58.84
Std Dev	10.03



B1L003S, U20-ch73, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch73, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U20-ch74, adc0

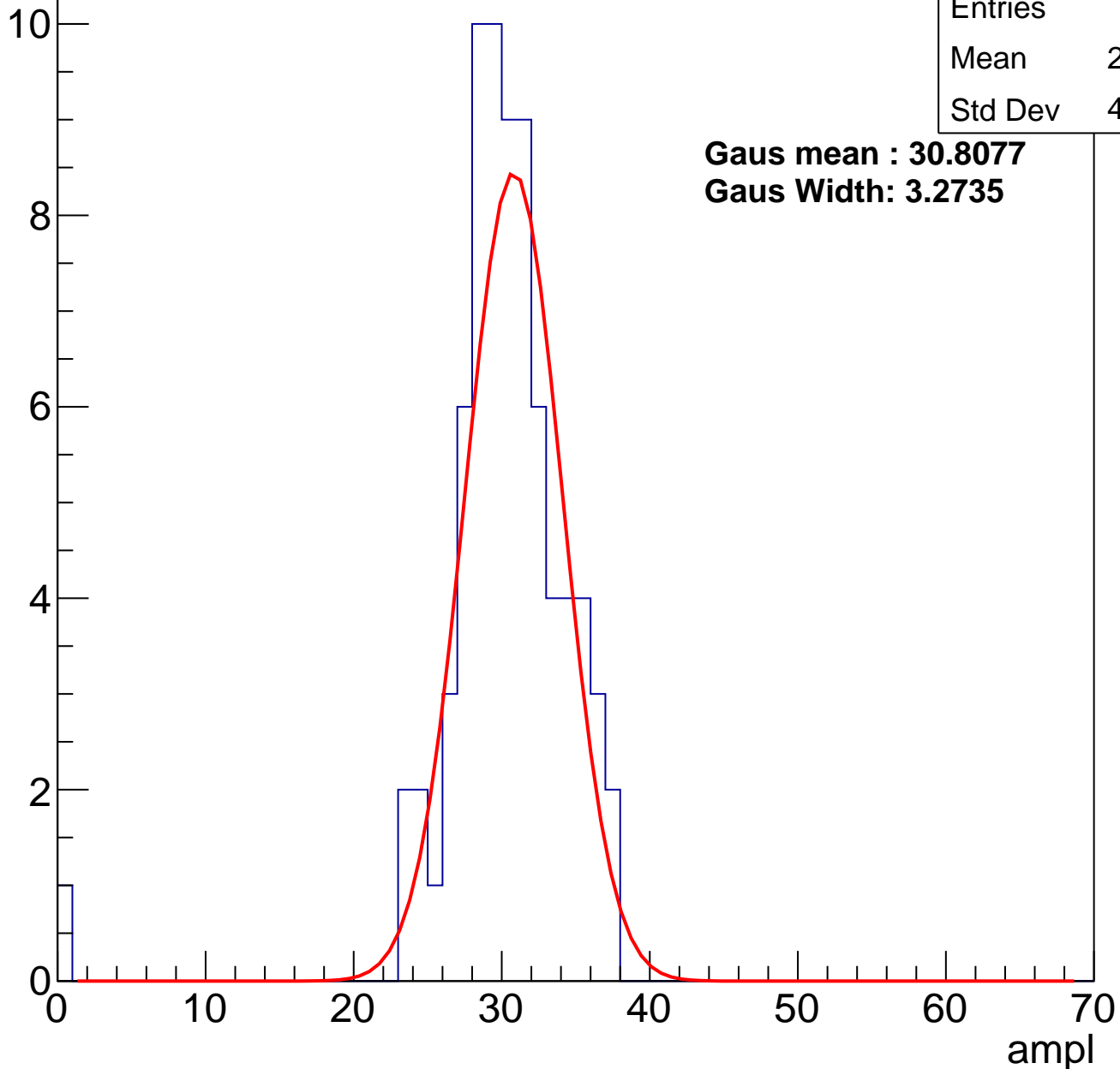
calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.74
Std Dev	4.703

Gaus mean : 30.8077

Gaus Width: 3.2735

Entry



B1L003S, U20-ch74, adc1

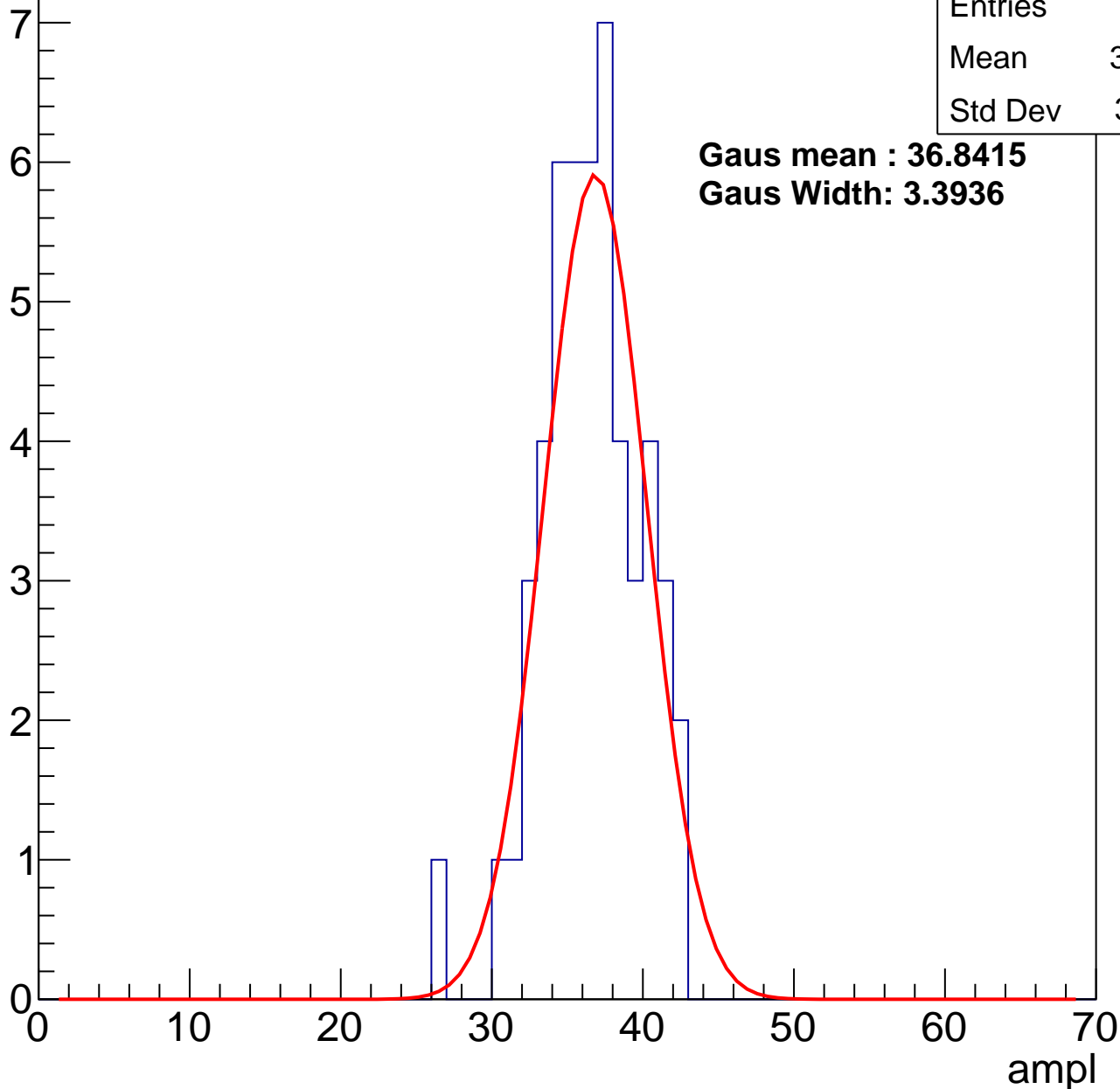
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.08
Std Dev	3.241

Gaus mean : 36.8415

Gaus Width: 3.3936



B1L003S, U20-ch74, adc2

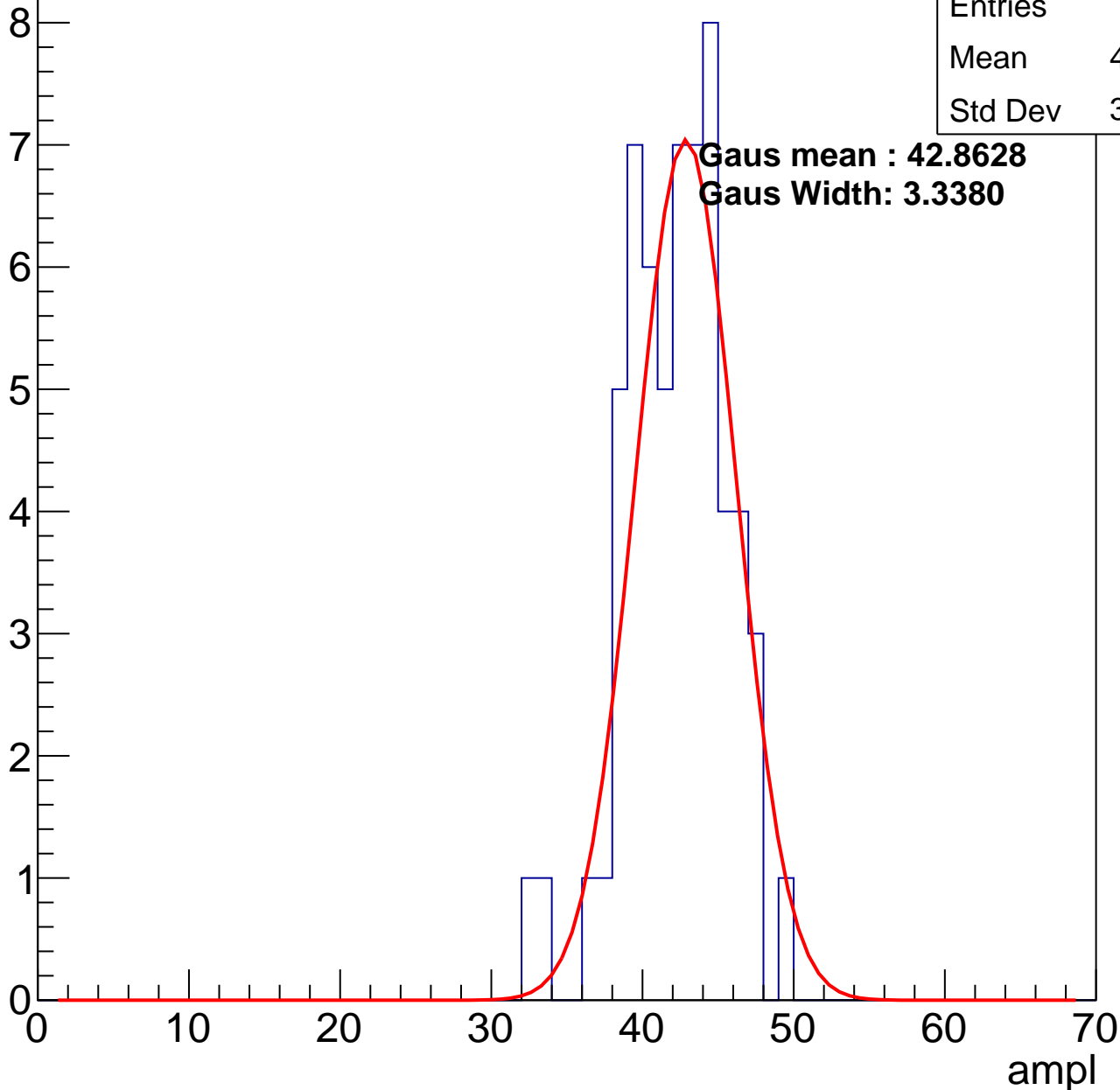
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	41.75
Std Dev	3.322

Gaus mean : 42.8628

Gaus Width: 3.3380

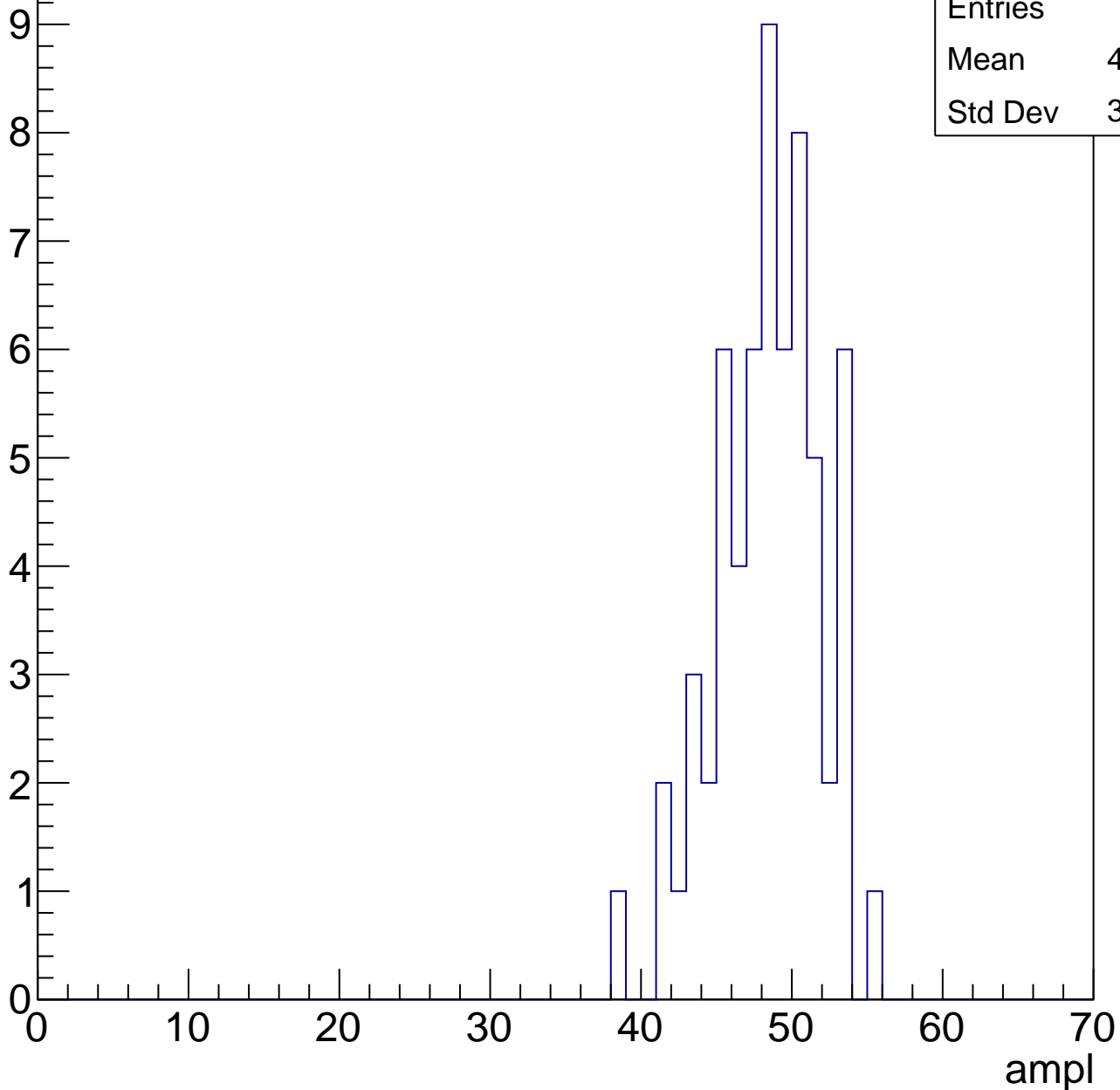


B1L003S, U20-ch74, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

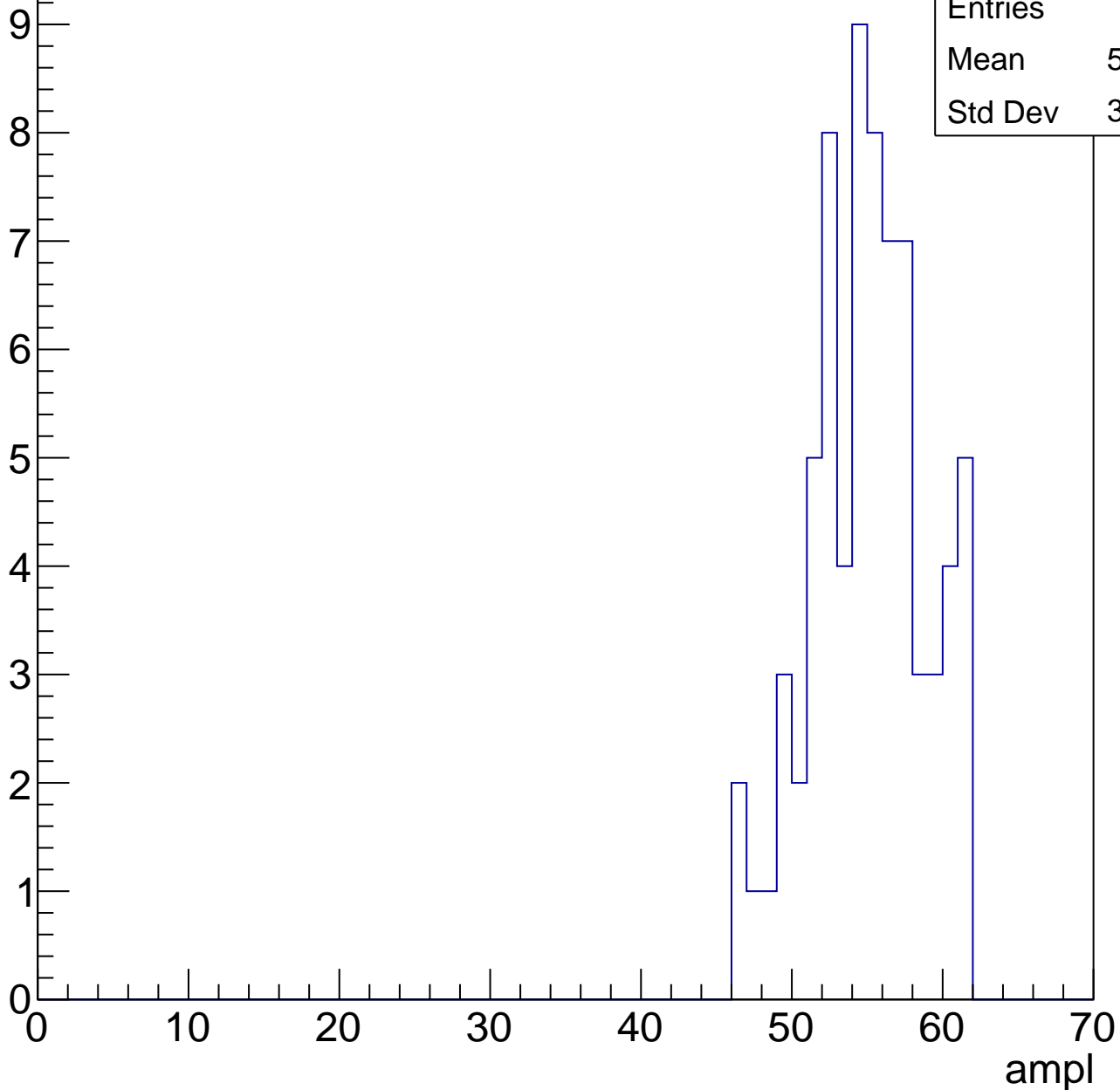
Entries	62
Mean	47.95
Std Dev	3.433



B1L003S, U20-ch74, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



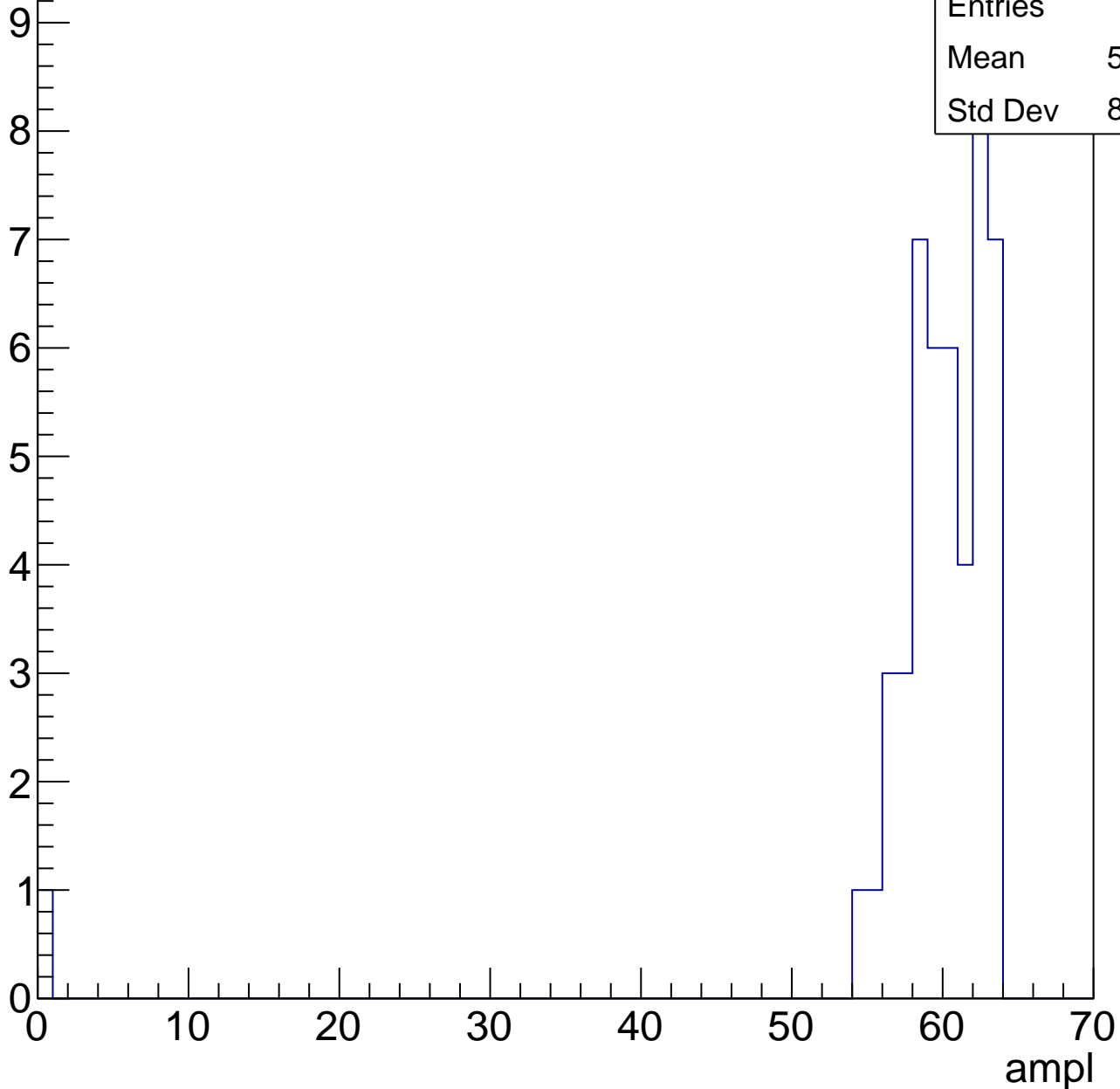
Entries	72
Mean	54.58
Std Dev	3.703

B1L003S, U20-ch74, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

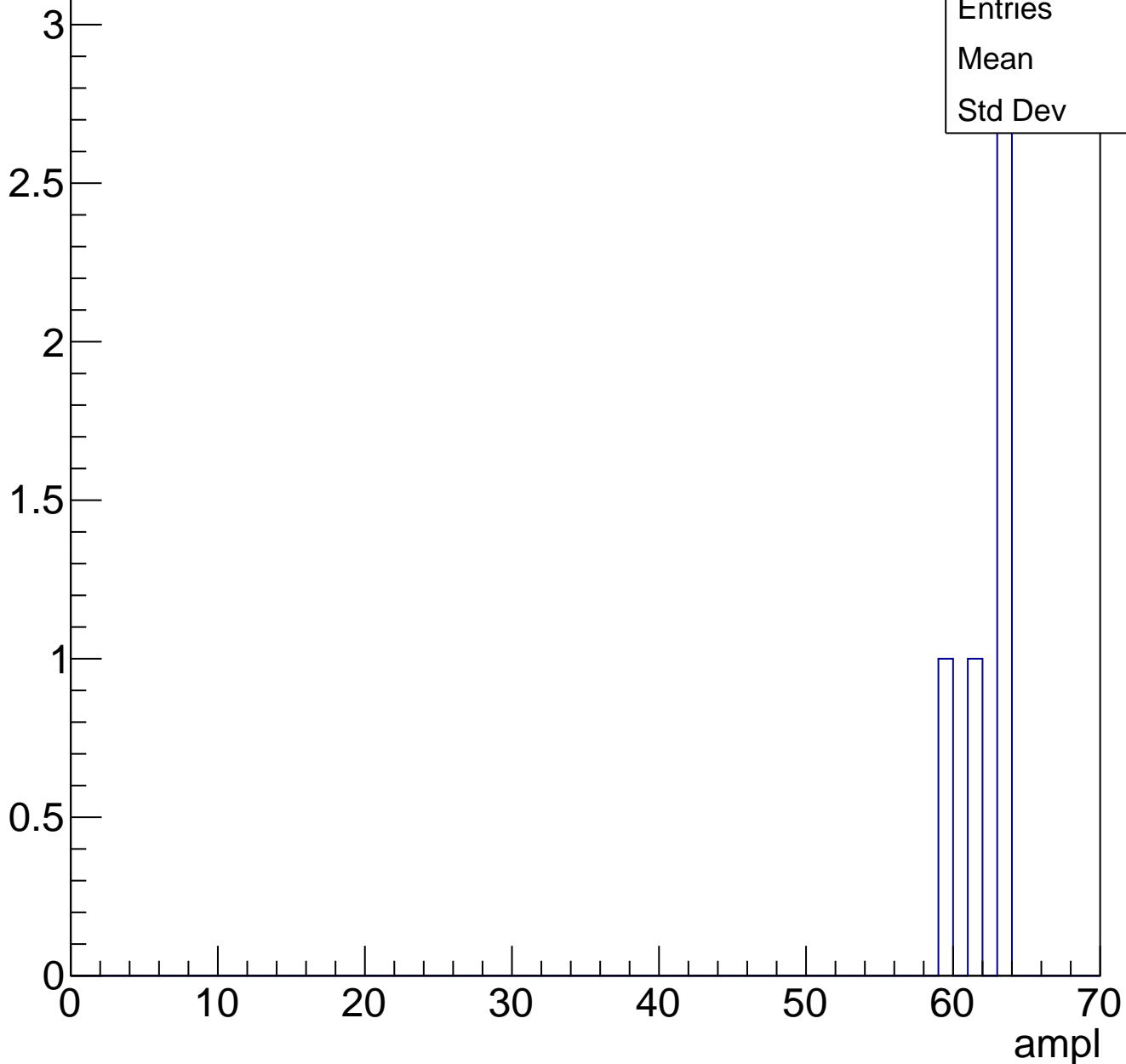
Entries	48
Mean	58.56
Std Dev	8.867



B1L003S, U20-ch74, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	61.8
Std Dev	1.6

B1L003S, U20-ch74, adc7

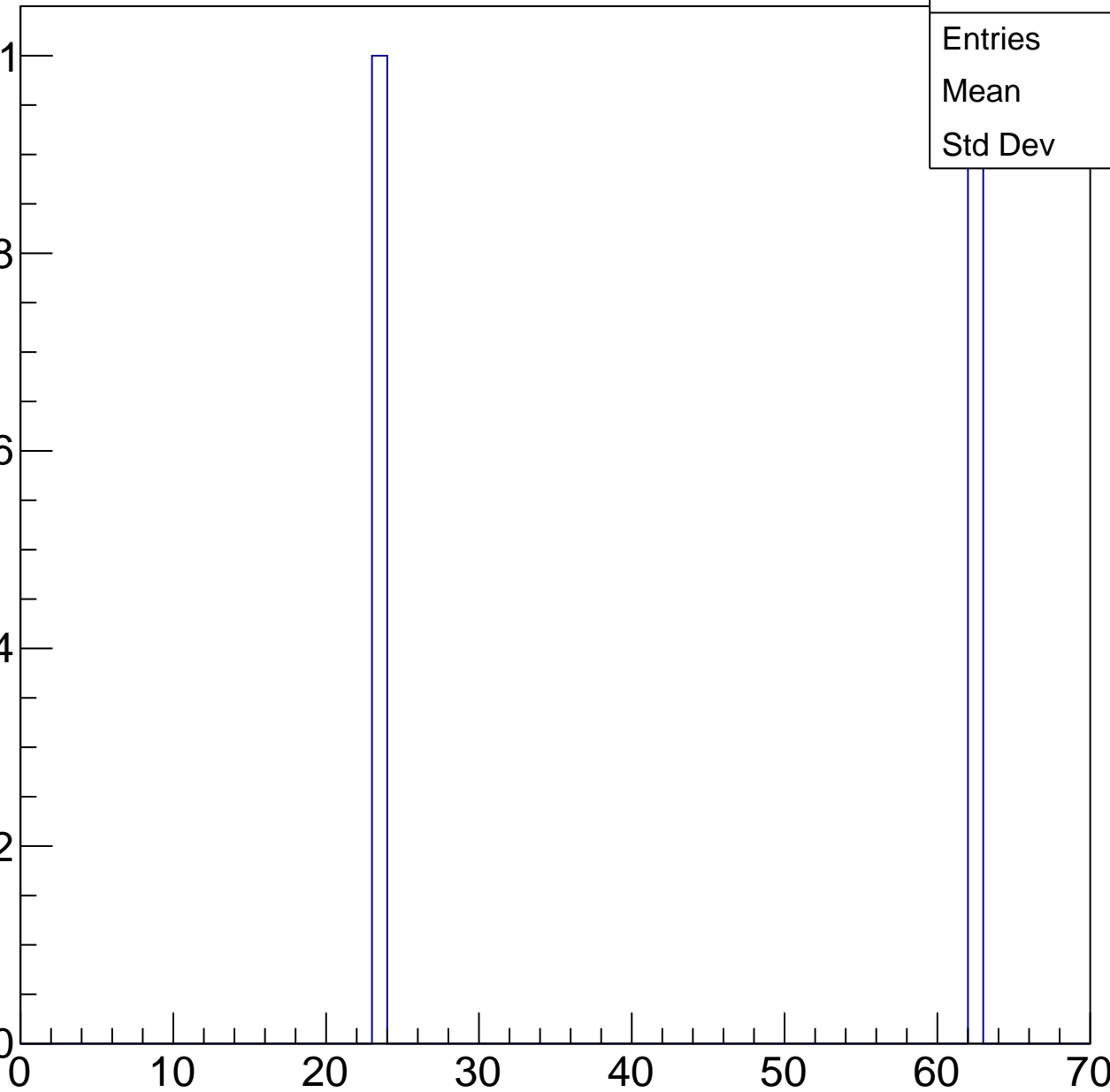
calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	19.5

ampl



B1L003S, U20-ch75, adc0

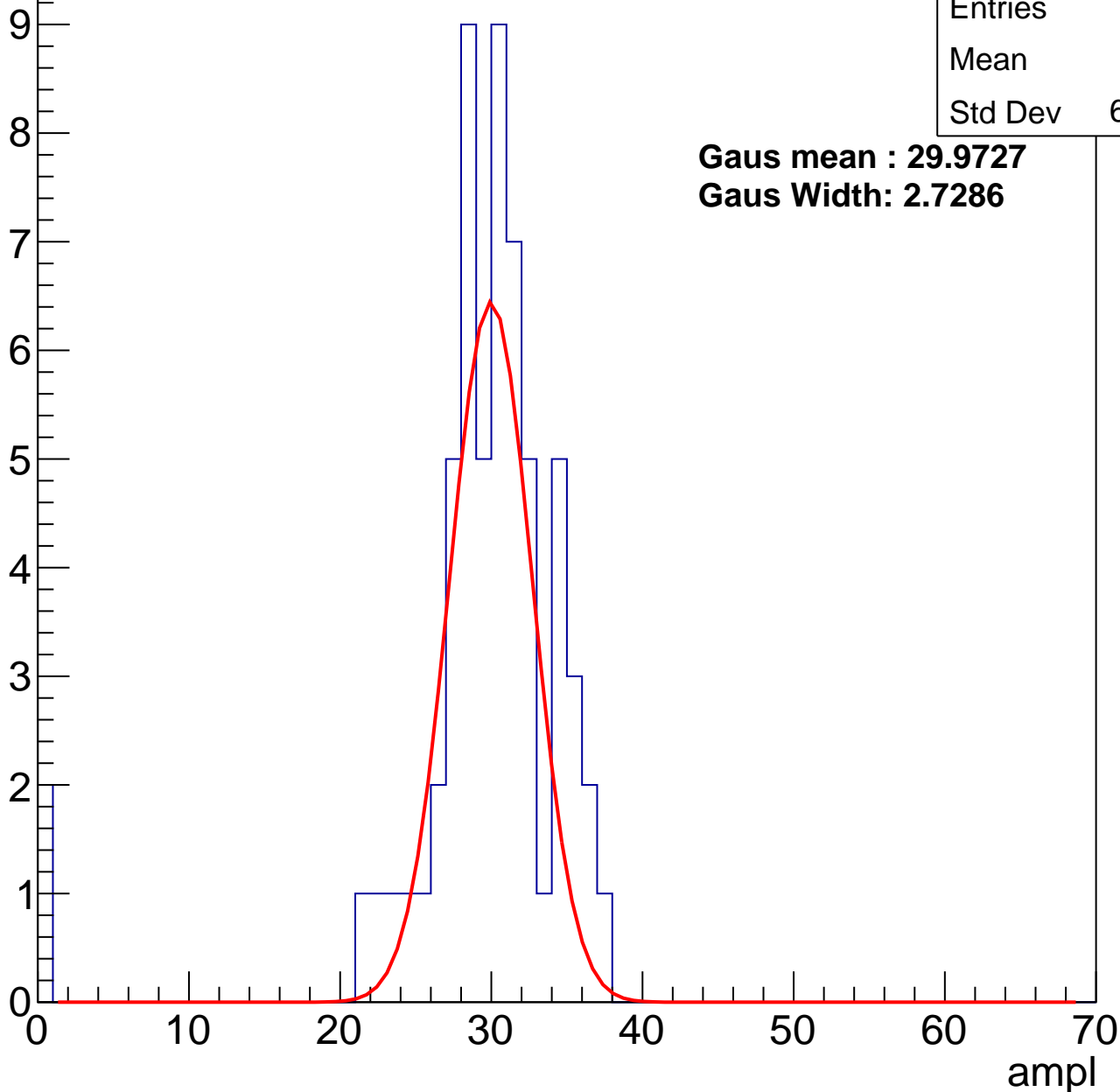
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.9
Std Dev	6.287

Gaus mean : 29.9727

Gaus Width: 2.7286



B1L003S, U20-ch75, adc1

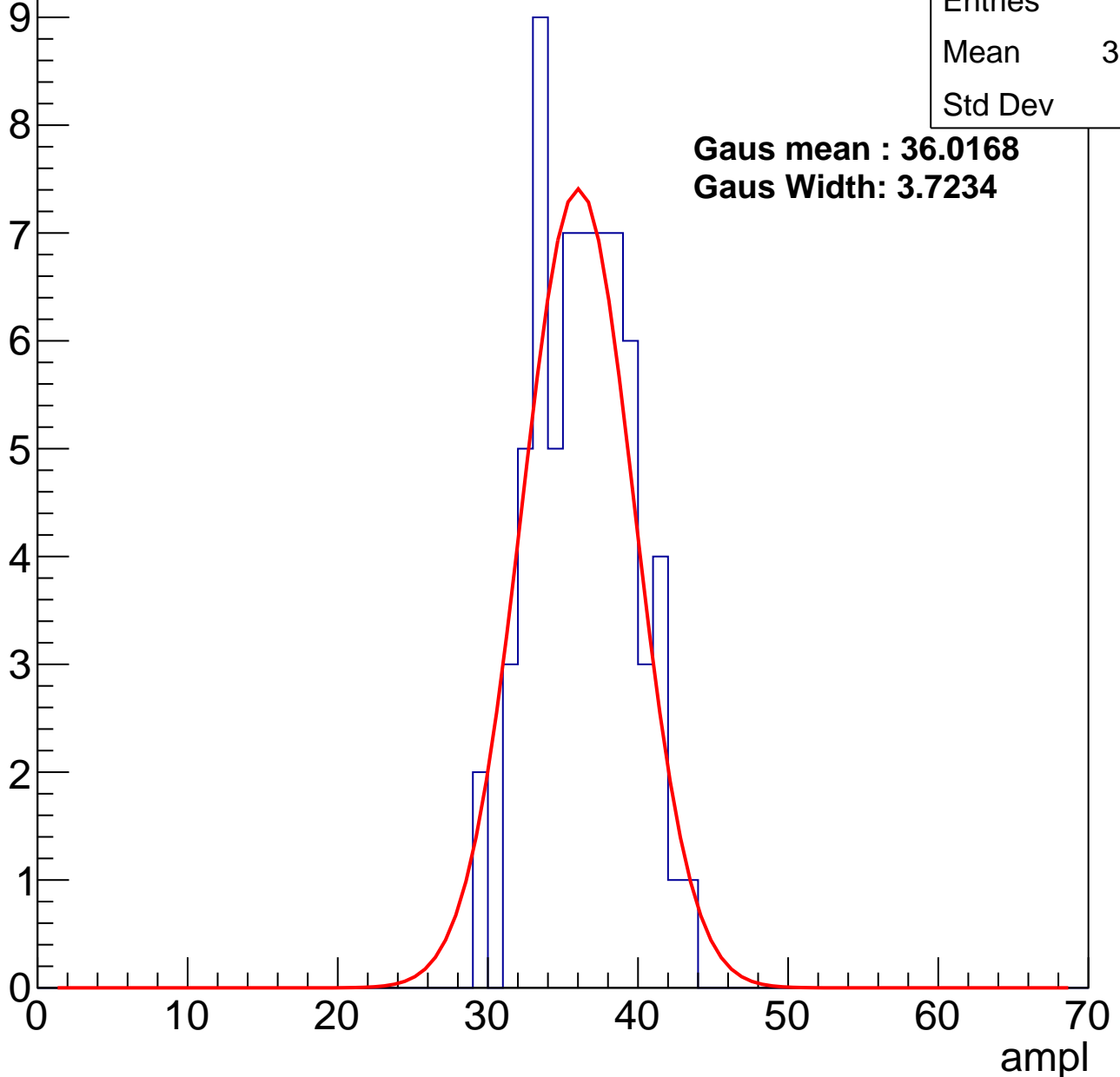
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.87
Std Dev	3.19

Gaus mean : 36.0168

Gaus Width: 3.7234



B1L003S, U20-ch75, adc2

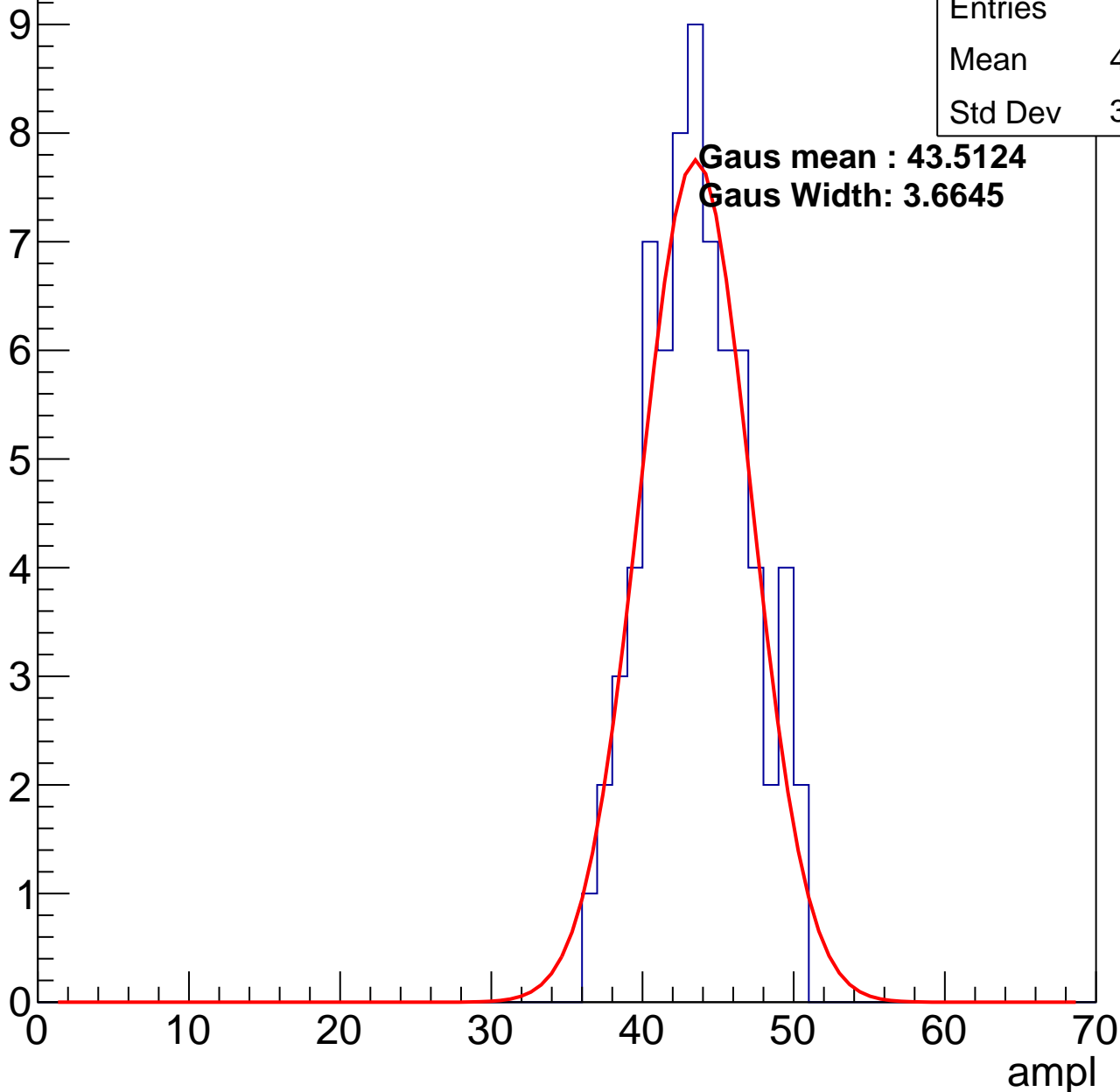
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.14
Std Dev	3.345

Gaus mean : 43.5124

Gaus Width: 3.6645

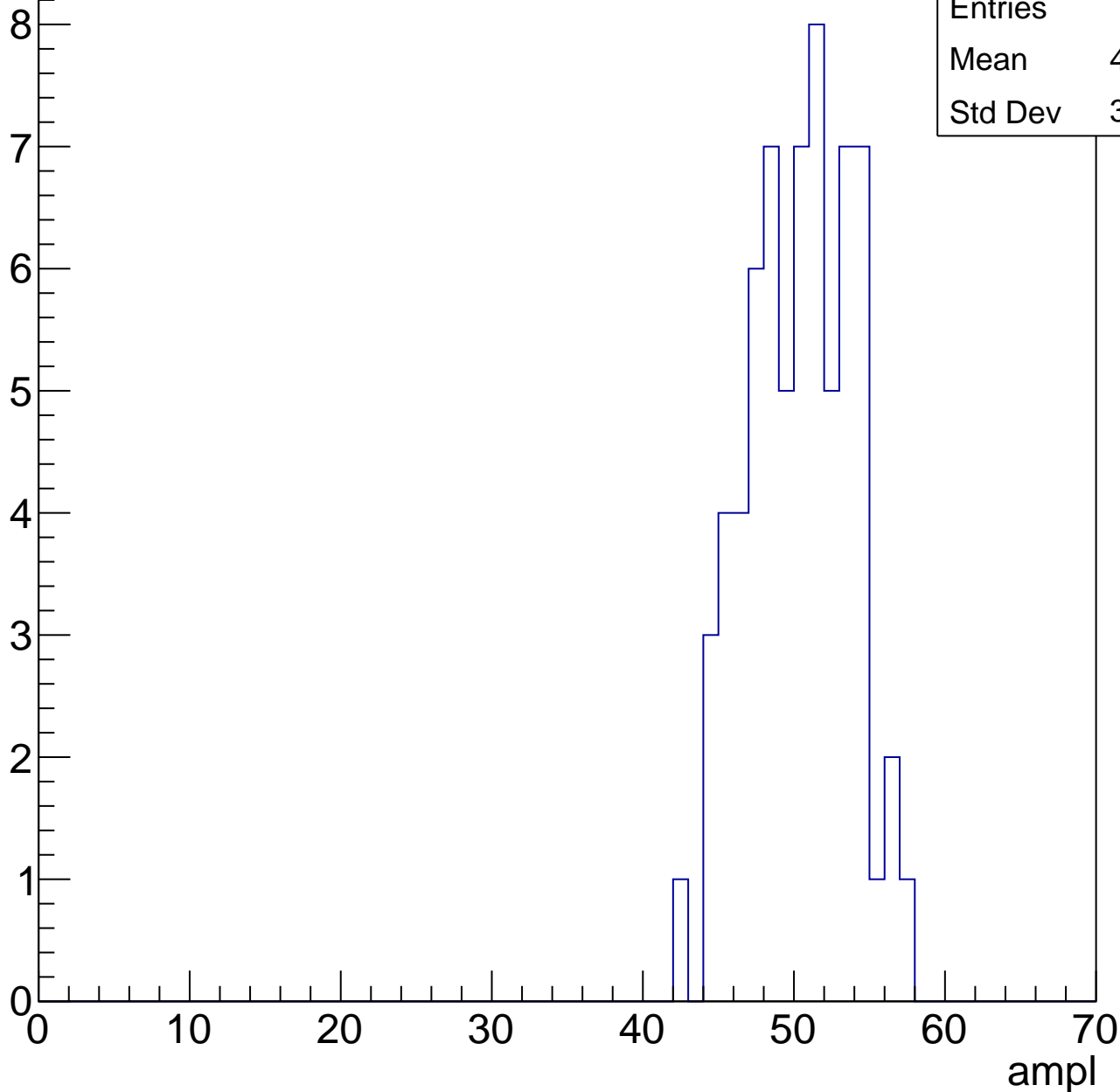


B1L003S, U20-ch75, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

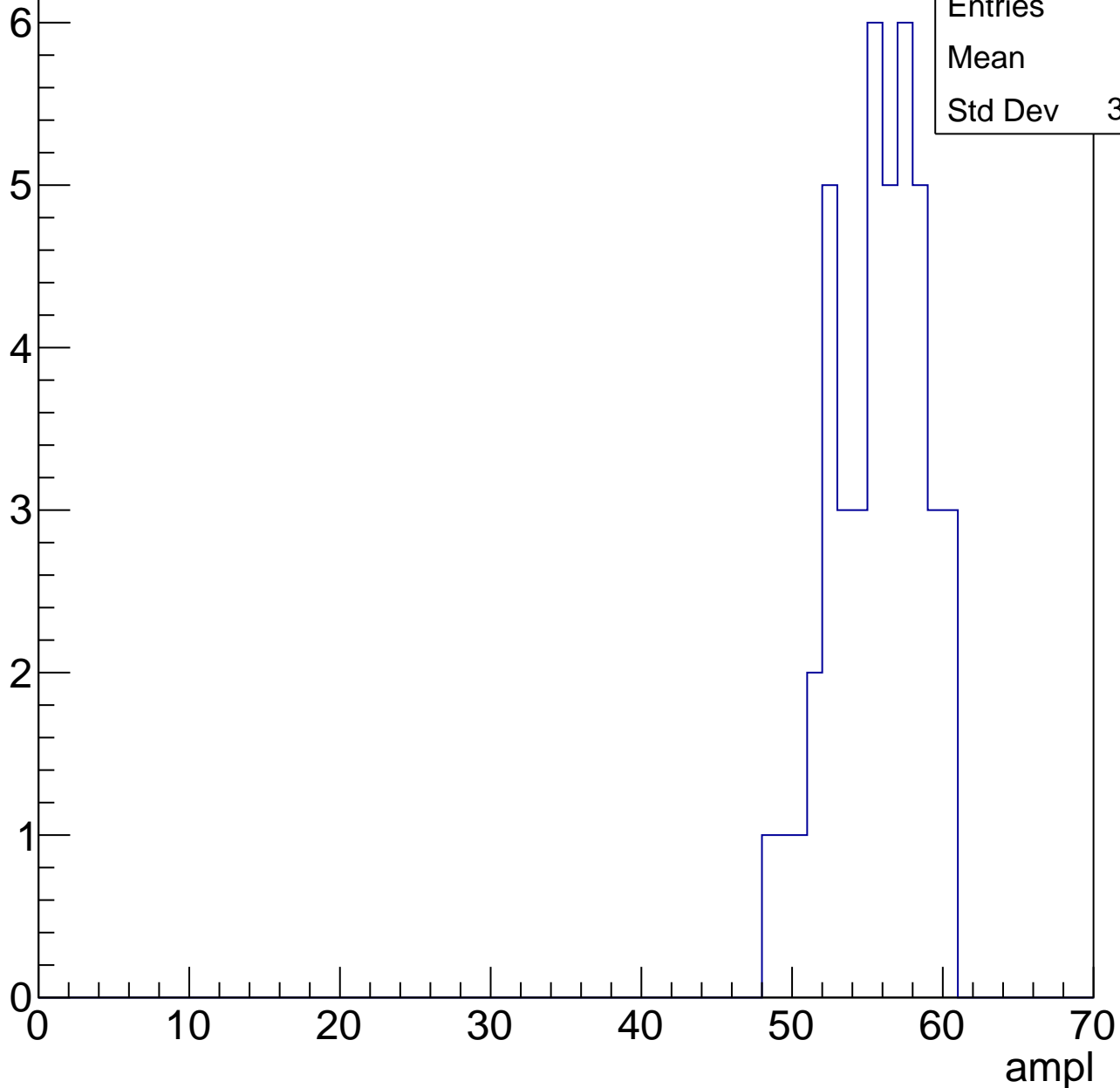
Entries	68
Mean	49.88
Std Dev	3.367



B1L003S, U20-ch75, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



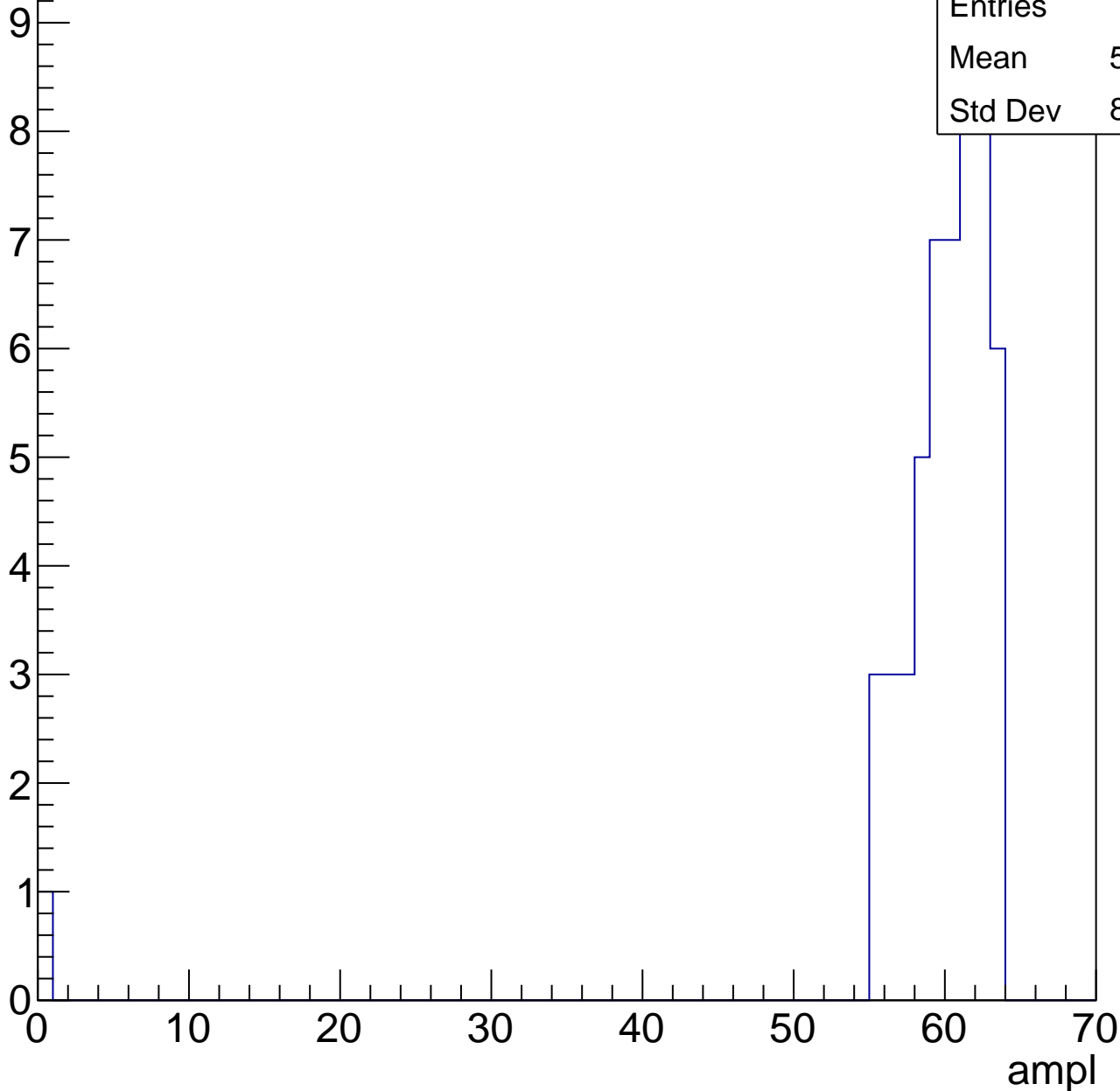
Entries	44
Mean	55.2
Std Dev	3.004

B1L003S, U20-ch75, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

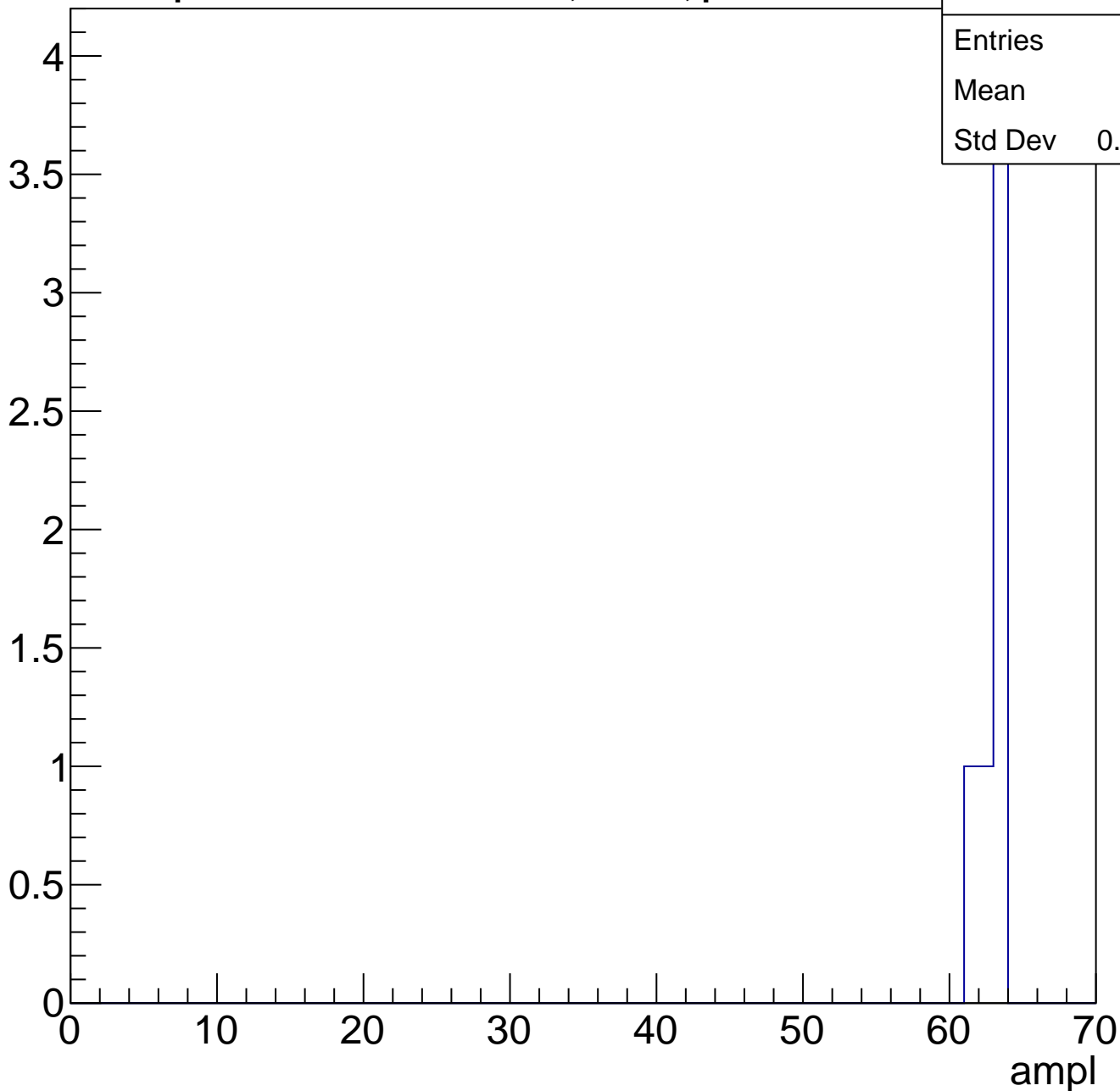
Entries	53
Mean	58.72
Std Dev	8.455



B1L003S, U20-ch75, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch75, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch76, adc0

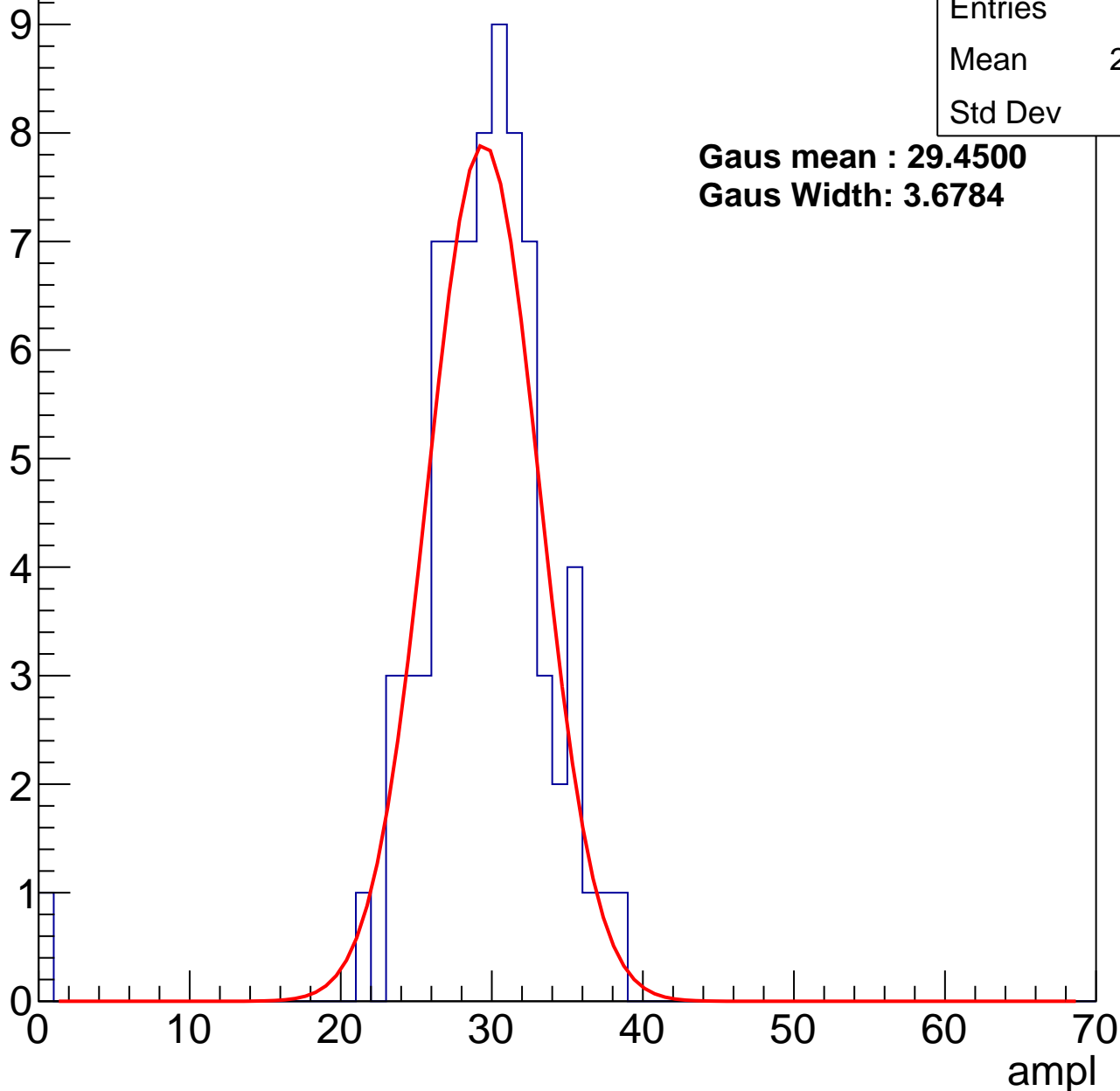
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	28.89
Std Dev	4.83

Gaus mean : 29.4500

Gaus Width: 3.6784



B1L003S, U20-ch76, adc1

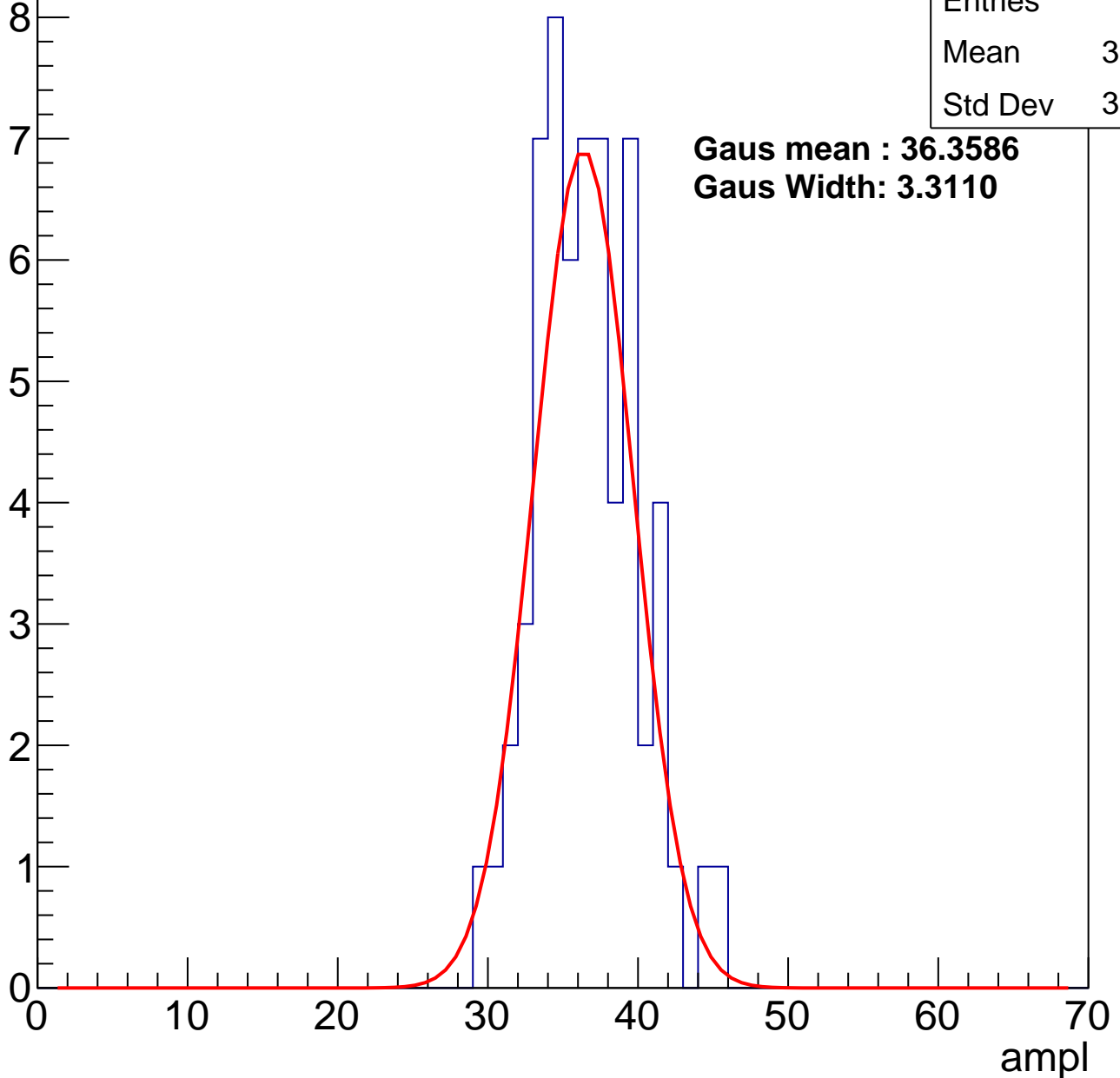
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.15
Std Dev	3.316

Gaus mean : 36.3586

Gaus Width: 3.3110



B1L003S, U20-ch76, adc2

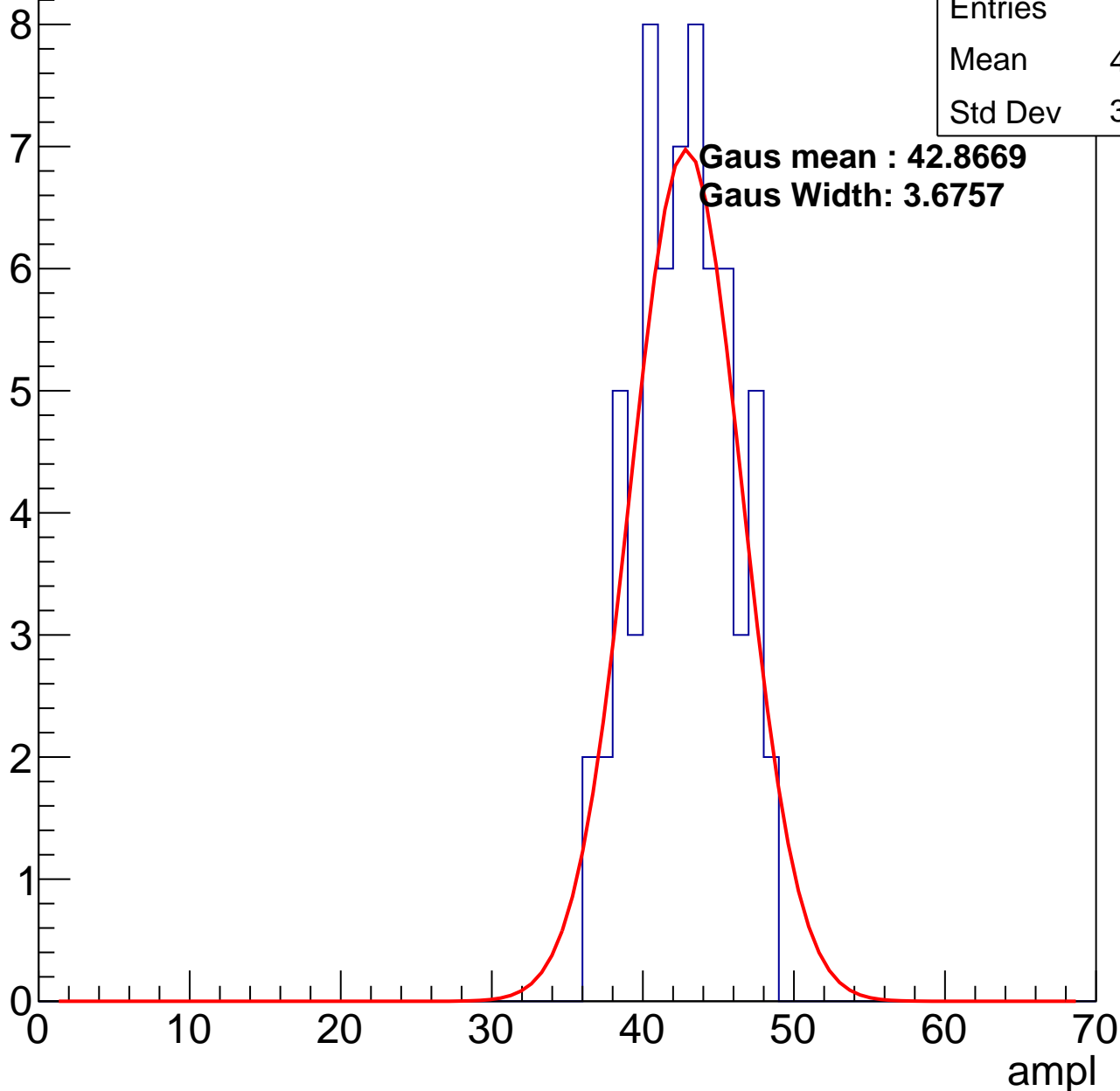
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.22
Std Dev	3.073

Gaus mean : 42.8669

Gaus Width: 3.6757

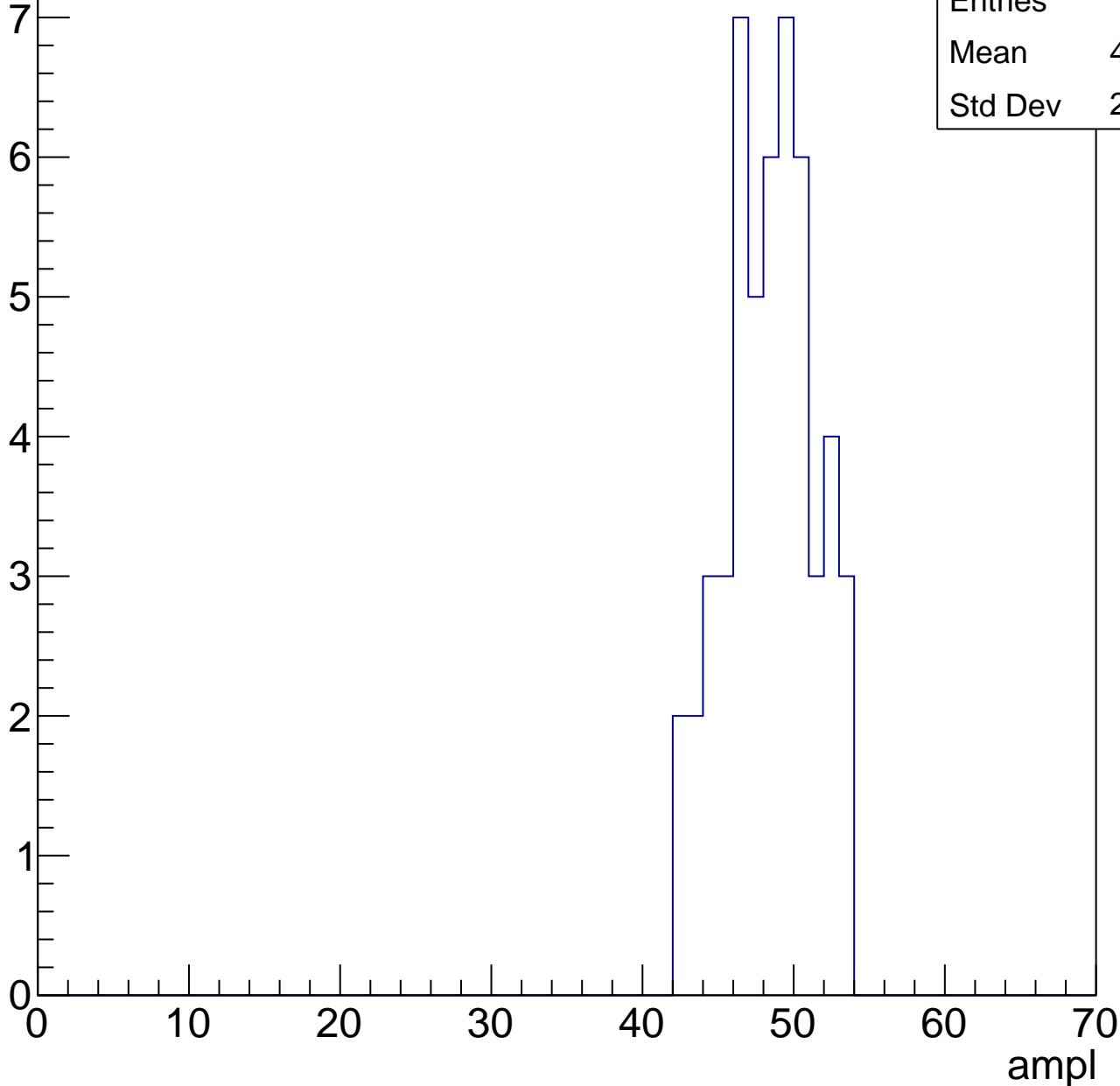


B1L003S, U20-ch76, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	47.94
Std Dev	2.893

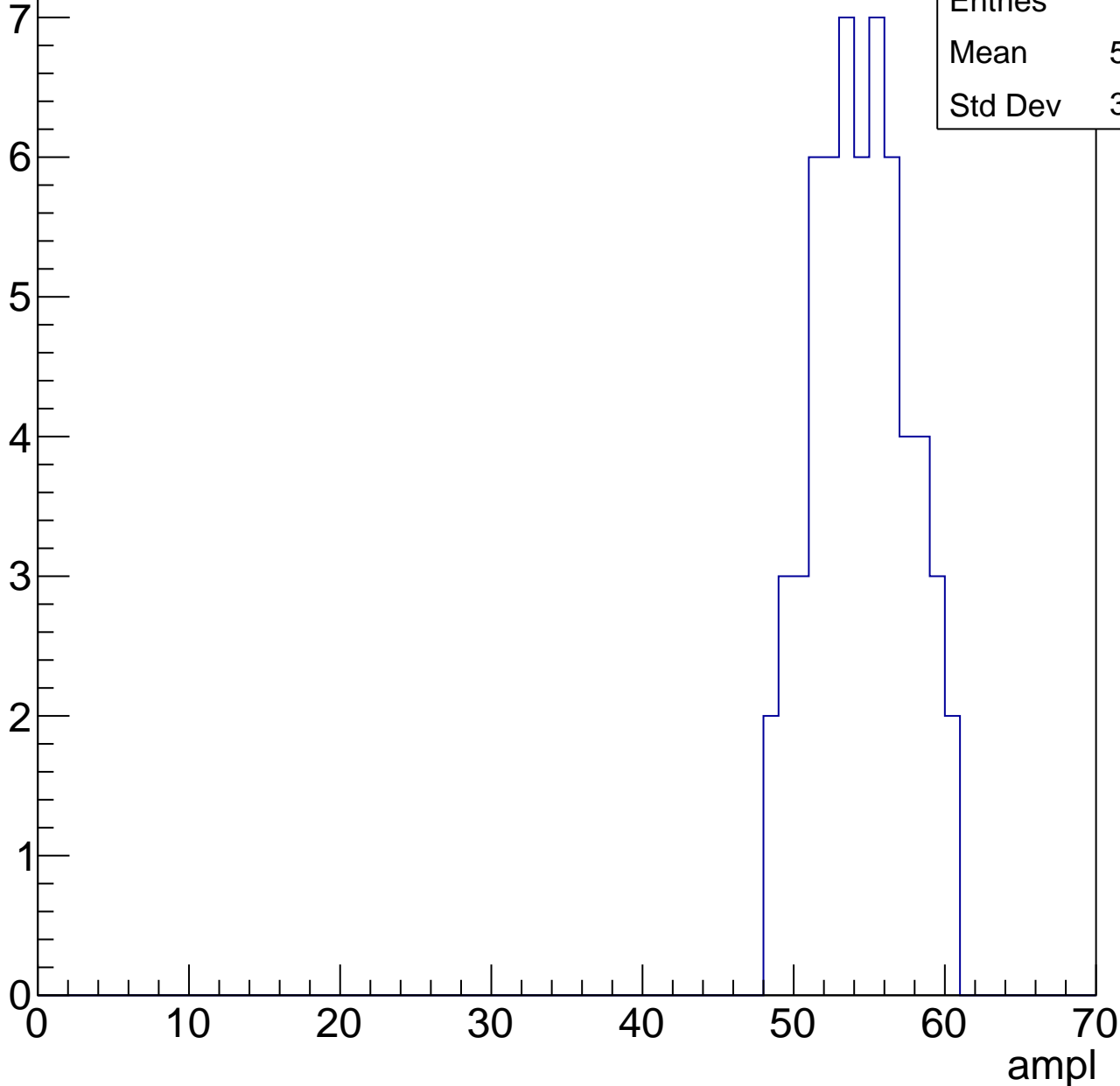


B1L003S, U20-ch76, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	53.97
Std Dev	3.075

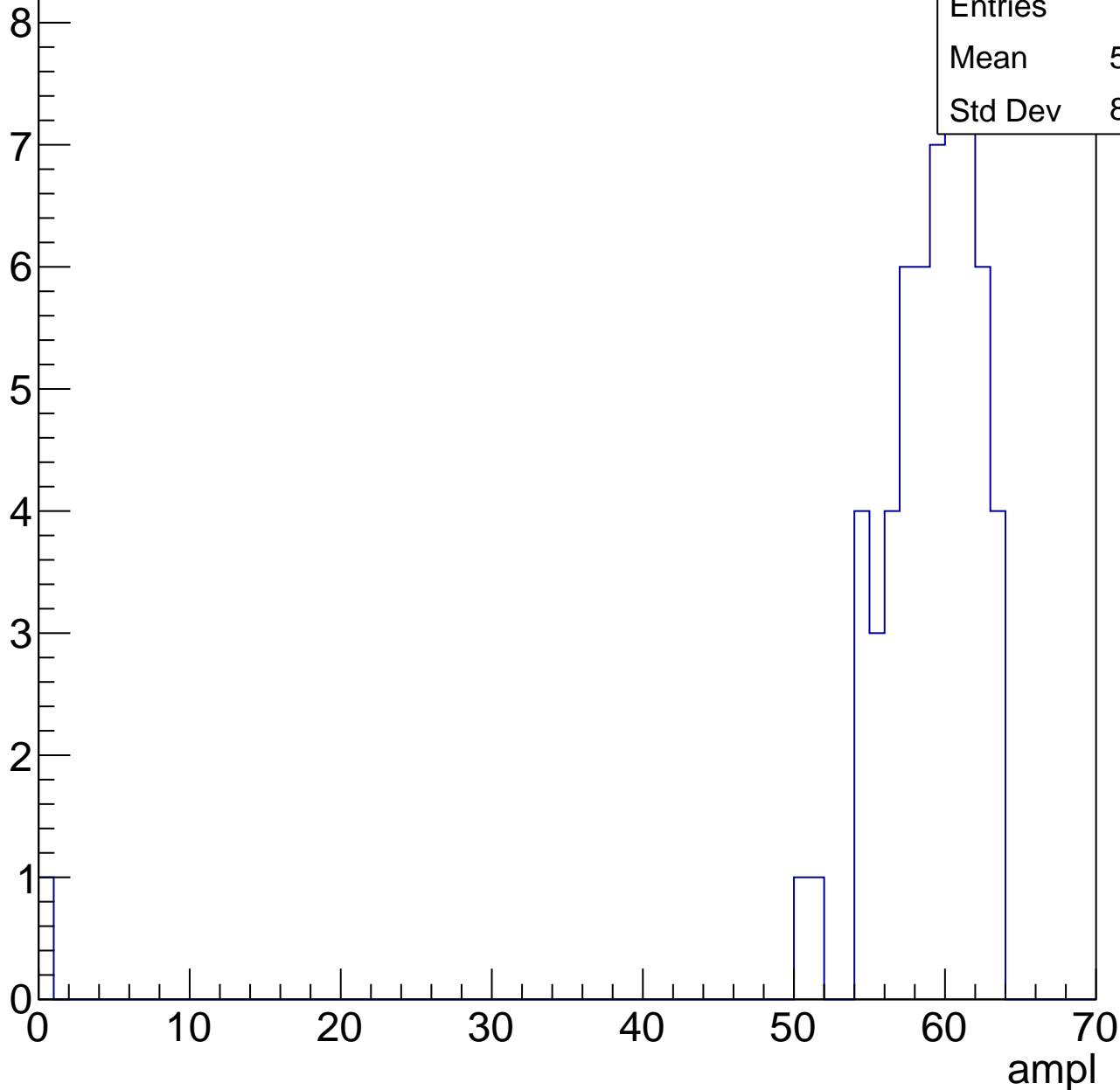


B1L003S, U20-ch76, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	57.64
Std Dev	8.119

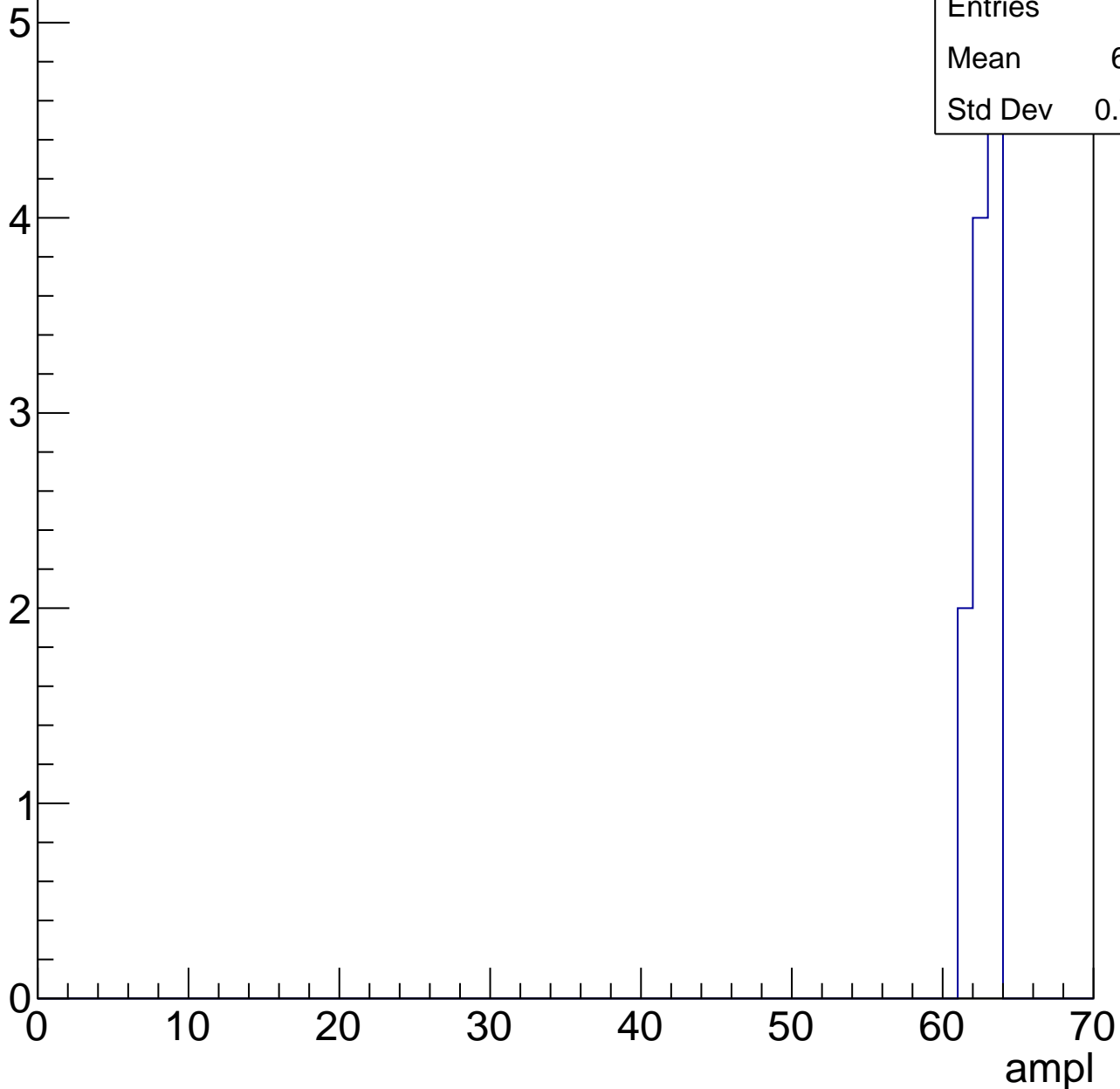


B1L003S, U20-ch76, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.27
Std Dev	0.7497



B1L003S, U20-ch76, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch77, adc0

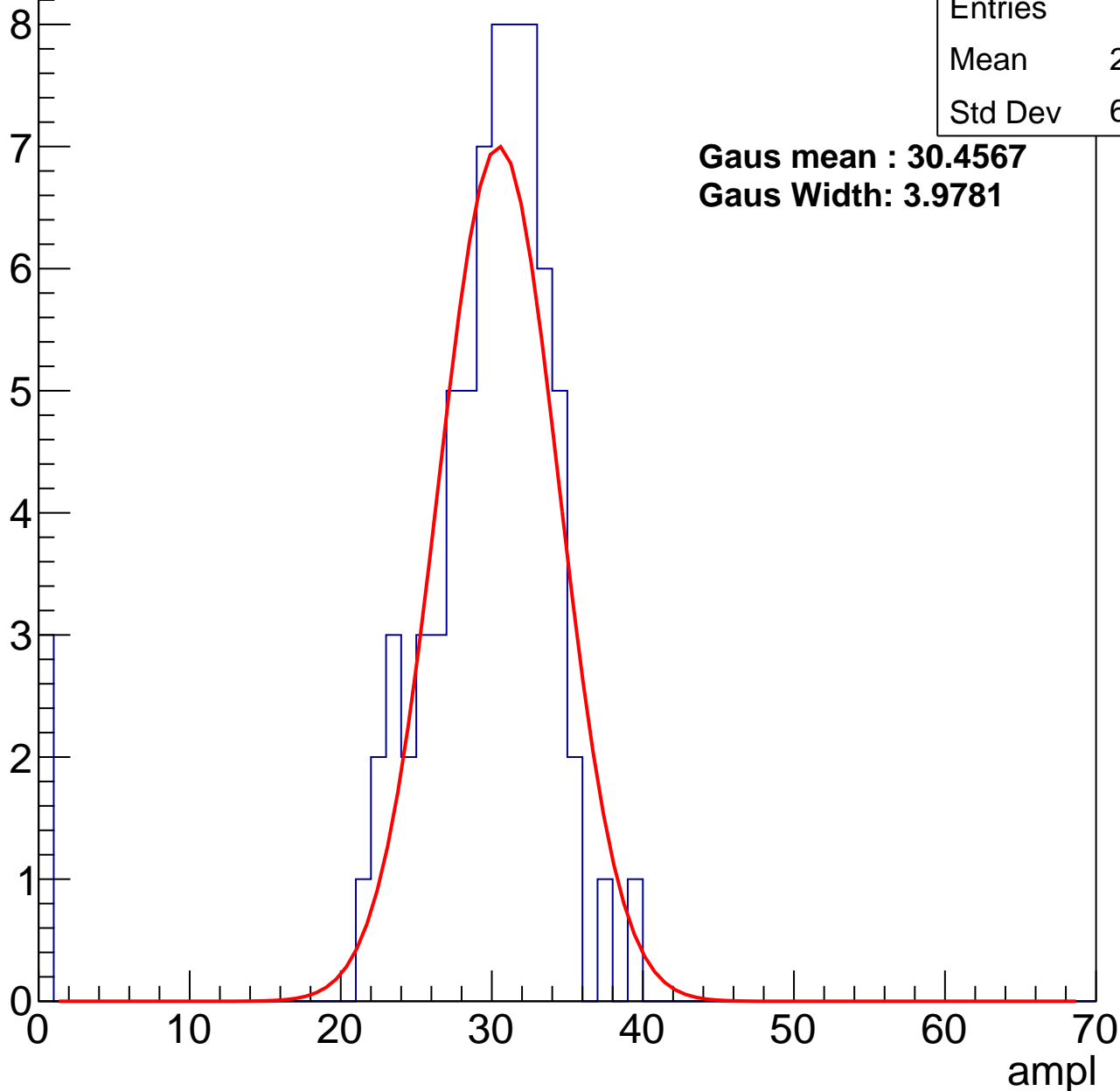
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	28.37
Std Dev	6.907

Gaus mean : 30.4567

Gaus Width: 3.9781



B1L003S, U20-ch77, adc1

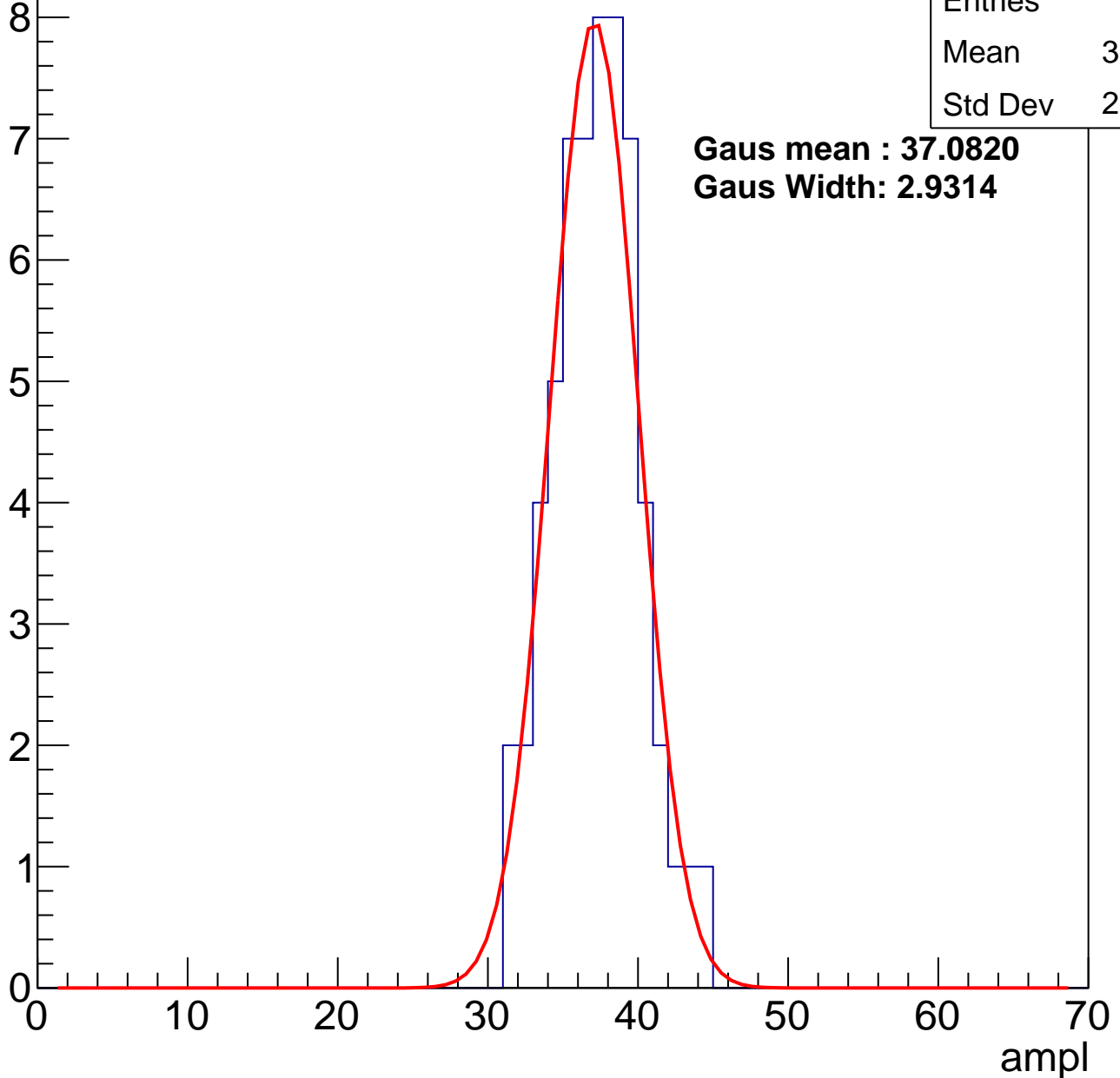
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.76
Std Dev	2.842

Gaus mean : 37.0820

Gaus Width: 2.9314



B1L003S, U20-ch77, adc2

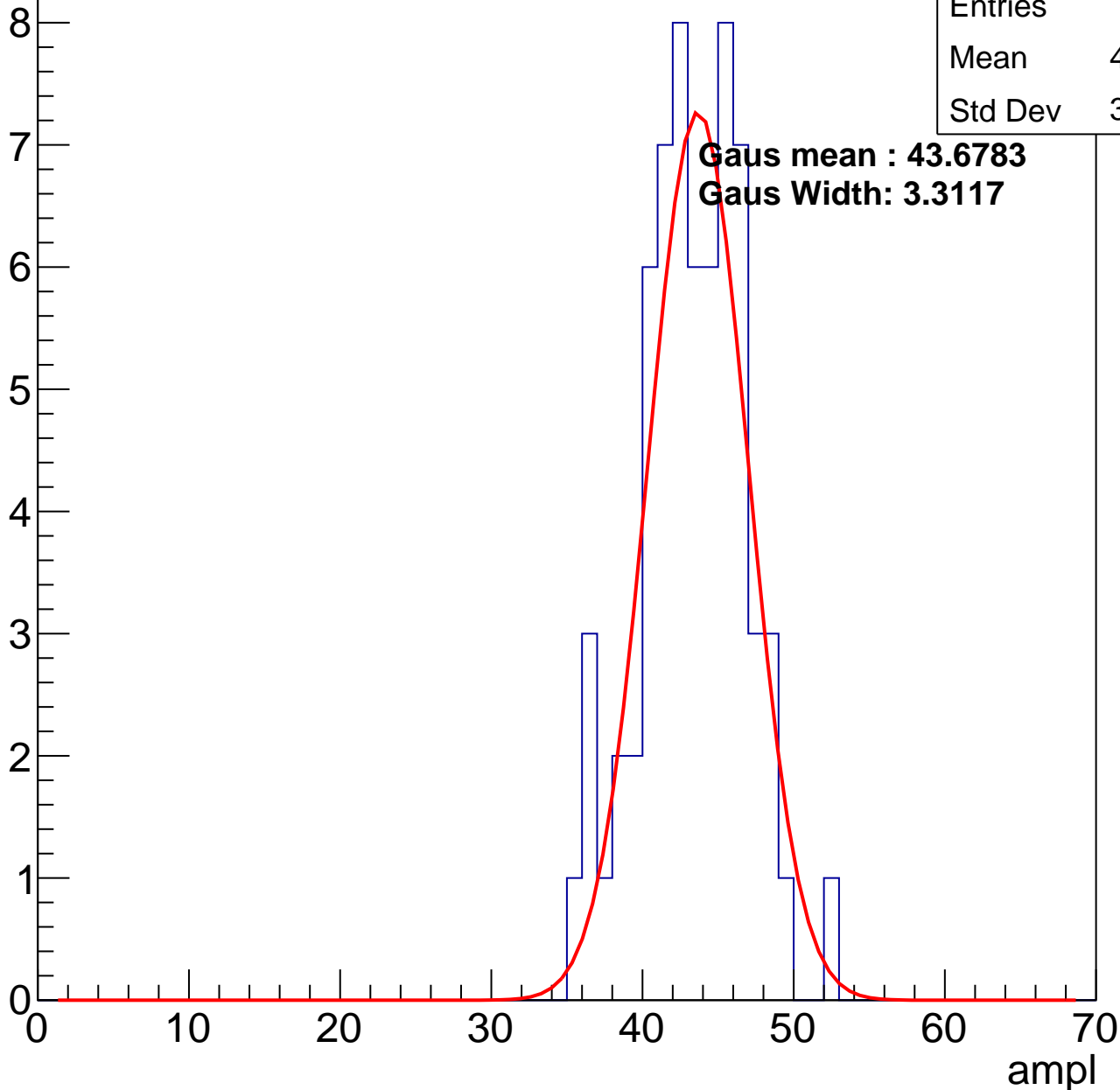
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.88
Std Dev	3.417

Gaus mean : 43.6783

Gaus Width: 3.3117

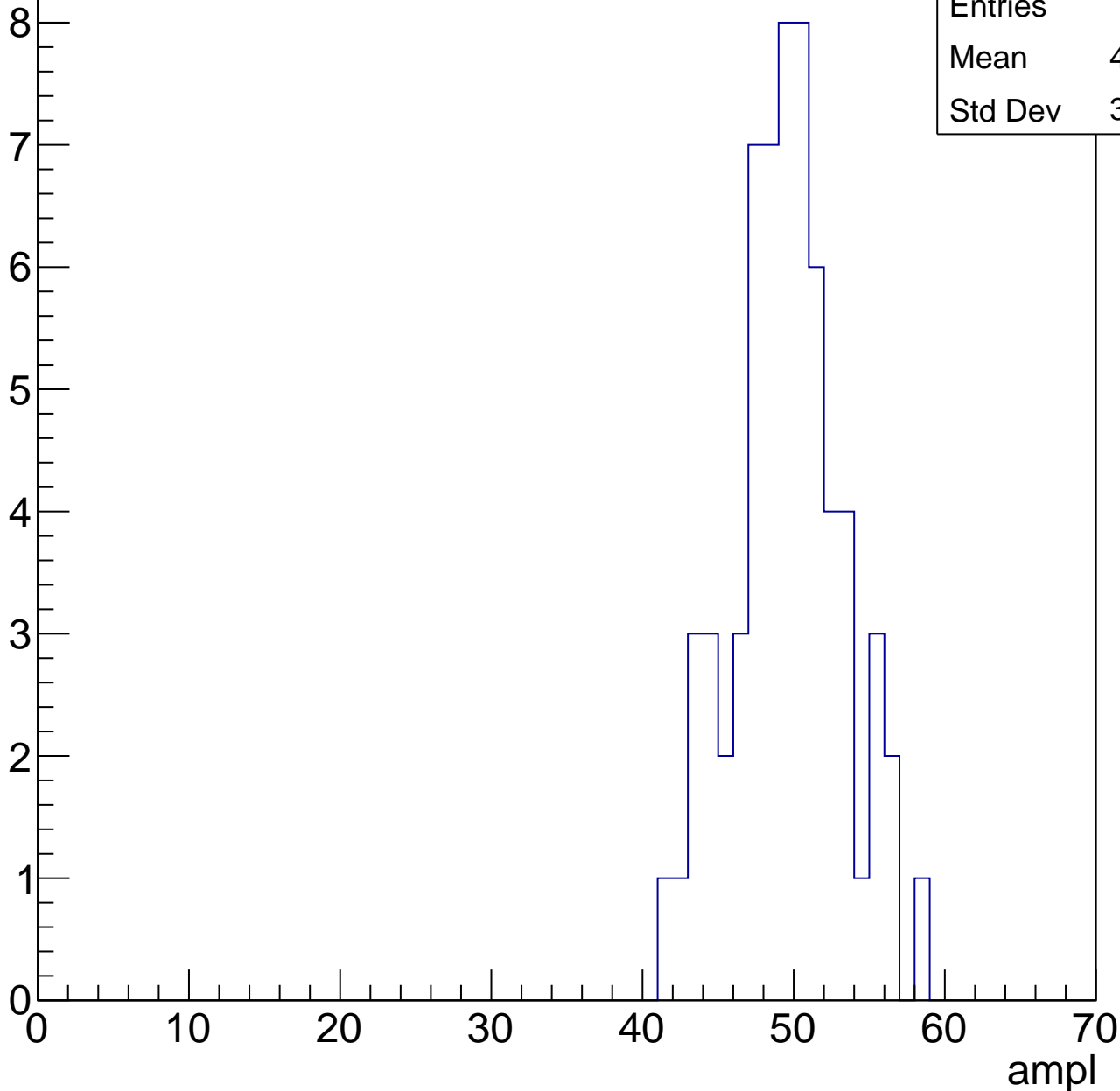


B1L003S, U20-ch77, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.12
Std Dev	3.608

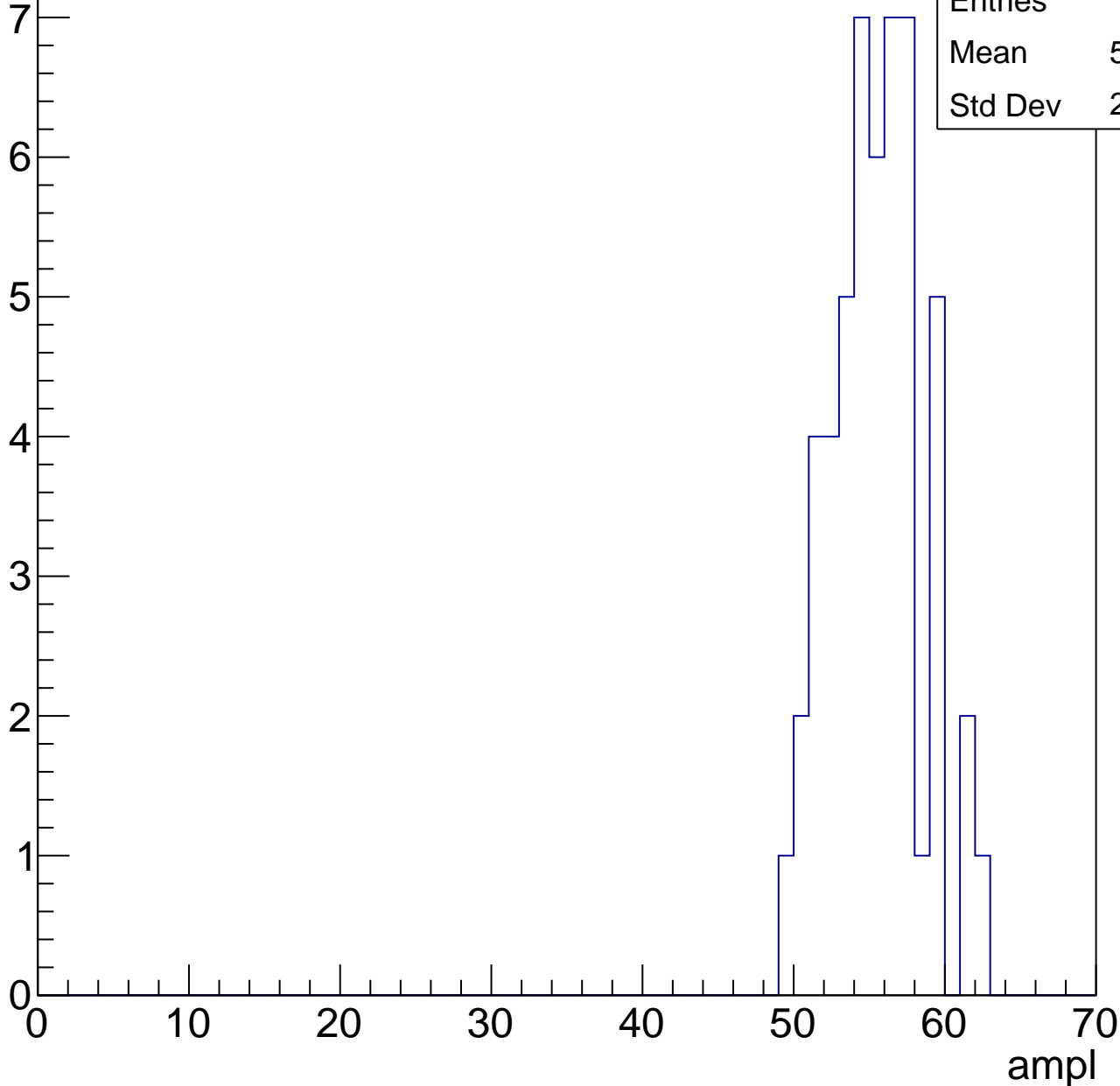


B1L003S, U20-ch77, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.04
Std Dev	2.968



B1L003S, U20-ch77, adc5

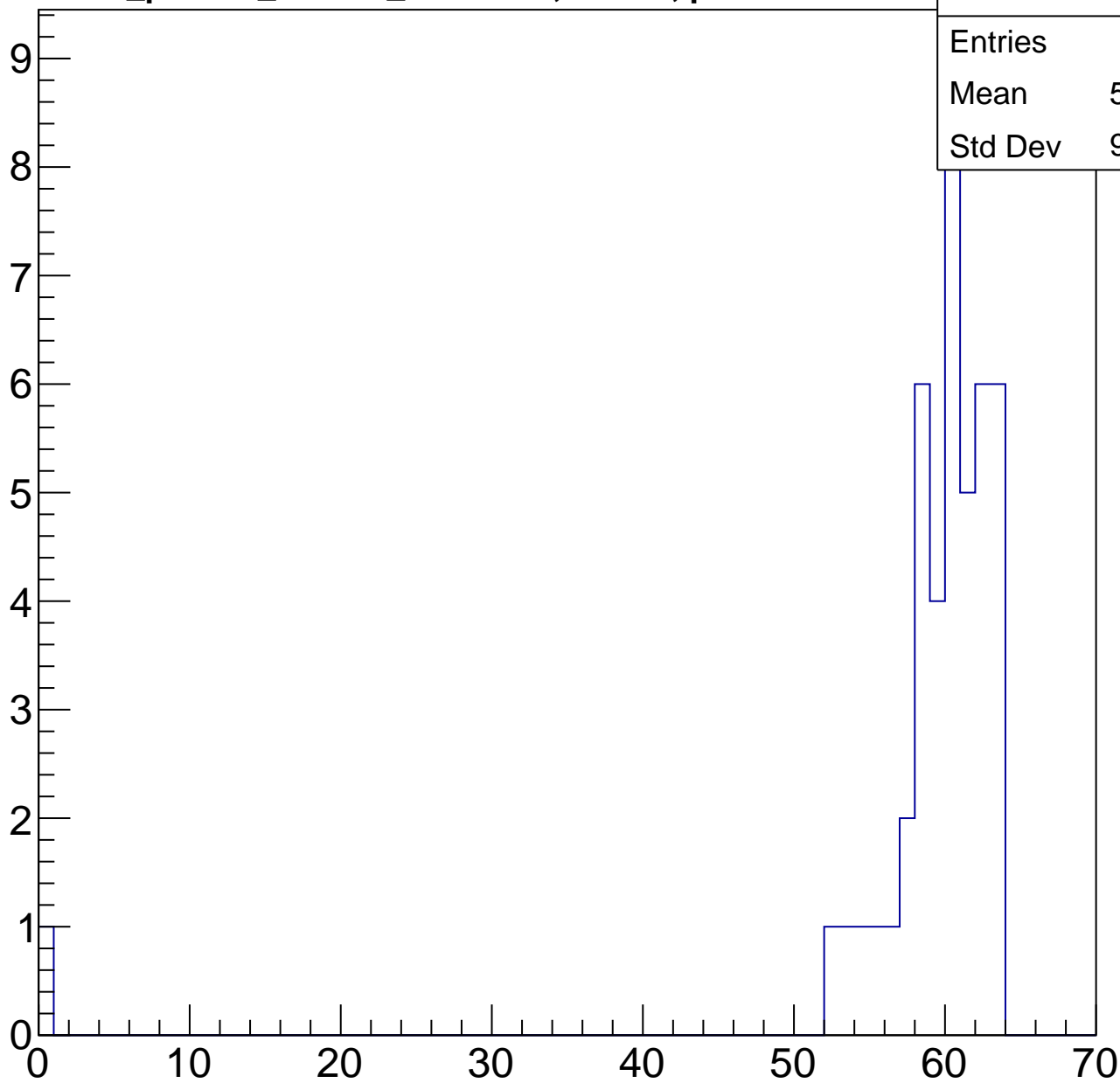
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.25
Std Dev	9.274

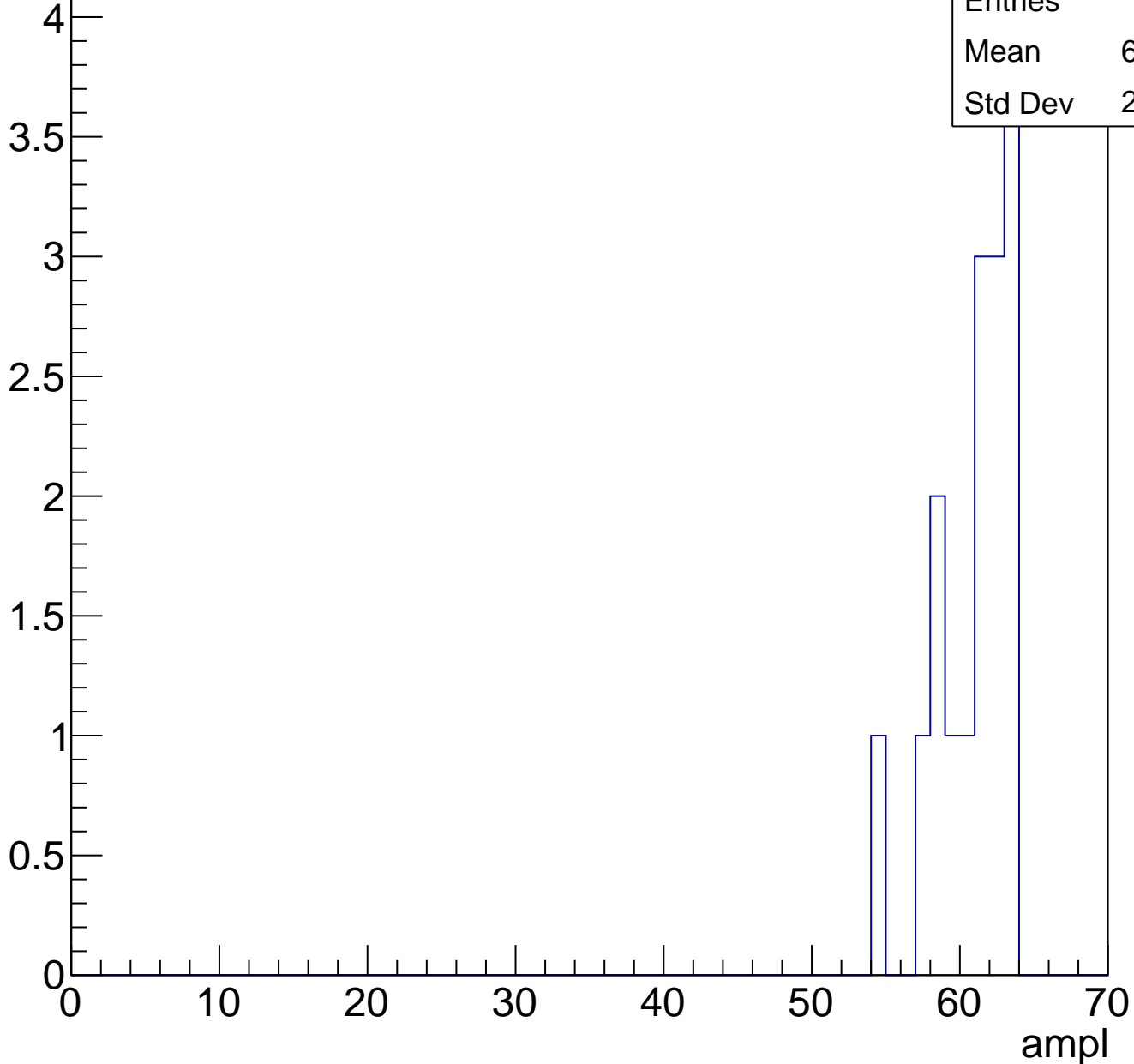
ampl



B1L003S, U20-ch77, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch77, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch78, adc0

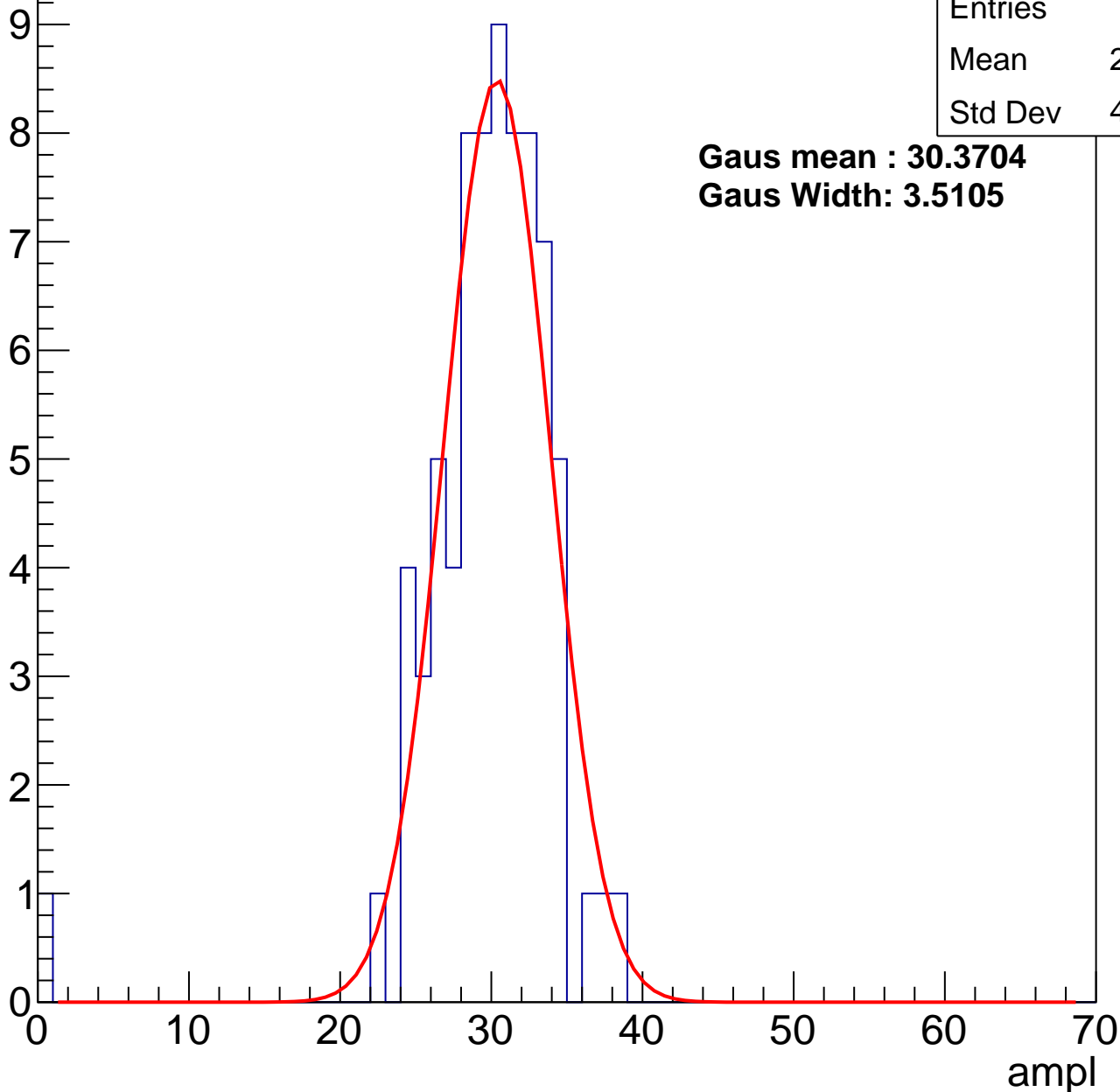
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.36
Std Dev	4.709

Gaus mean : 30.3704

Gaus Width: 3.5105



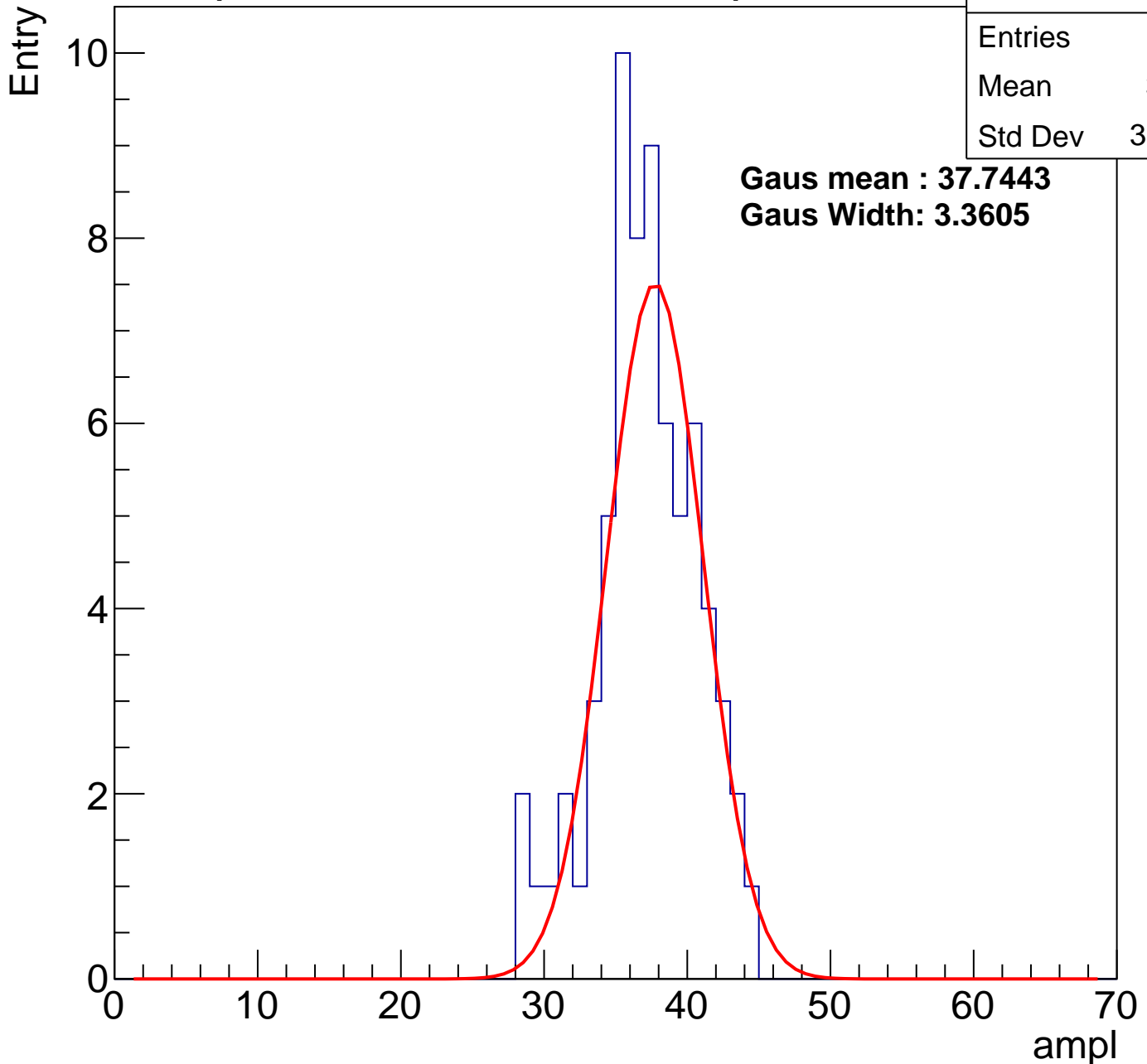
B1L003S, U20-ch78, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	36.7
Std Dev	3.503

Gaus mean : 37.7443

Gaus Width: 3.3605



B1L003S, U20-ch78, adc2

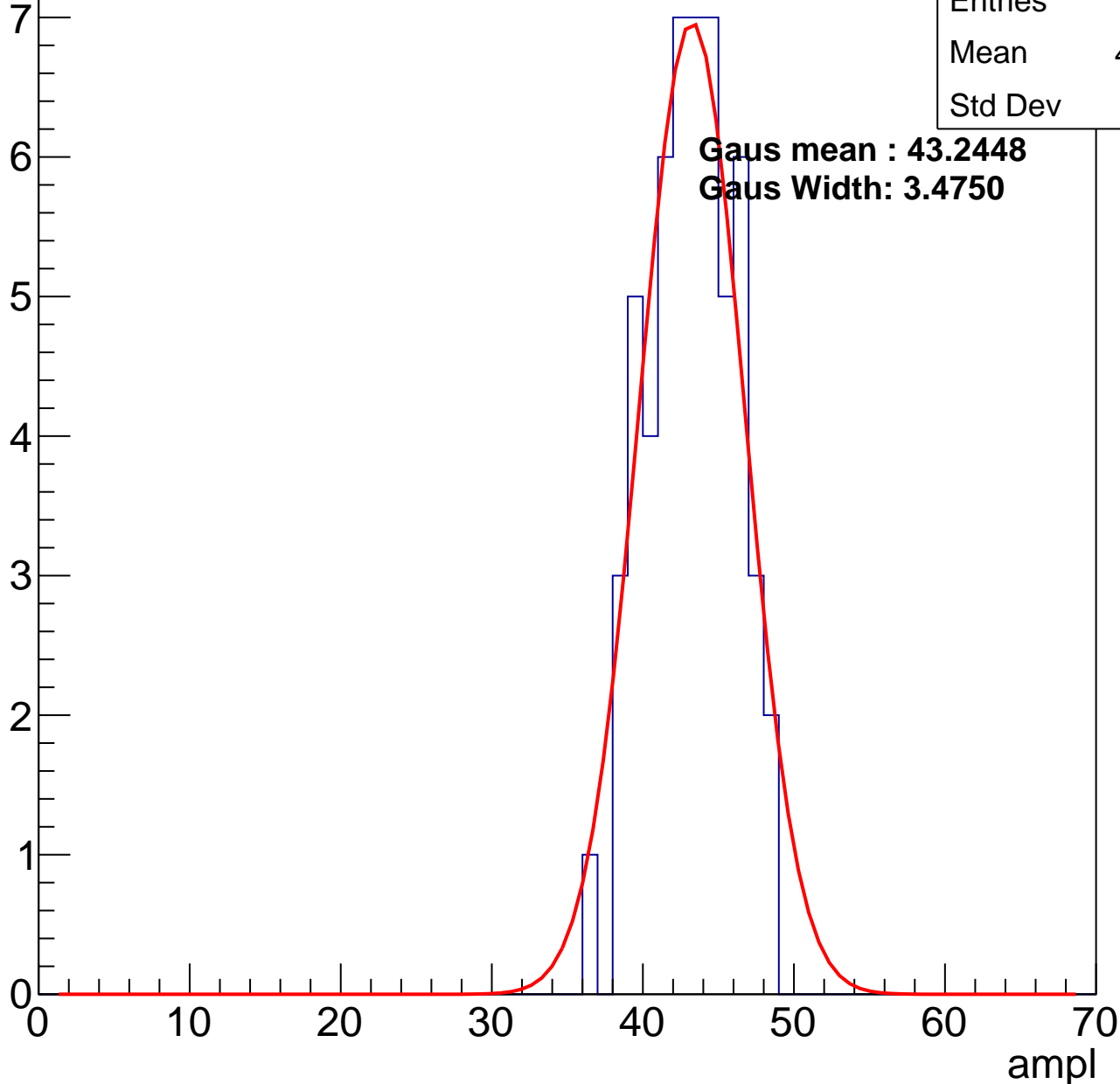
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.71
Std Dev	2.82

Gaus mean : 43.2448

Gaus Width: 3.4750

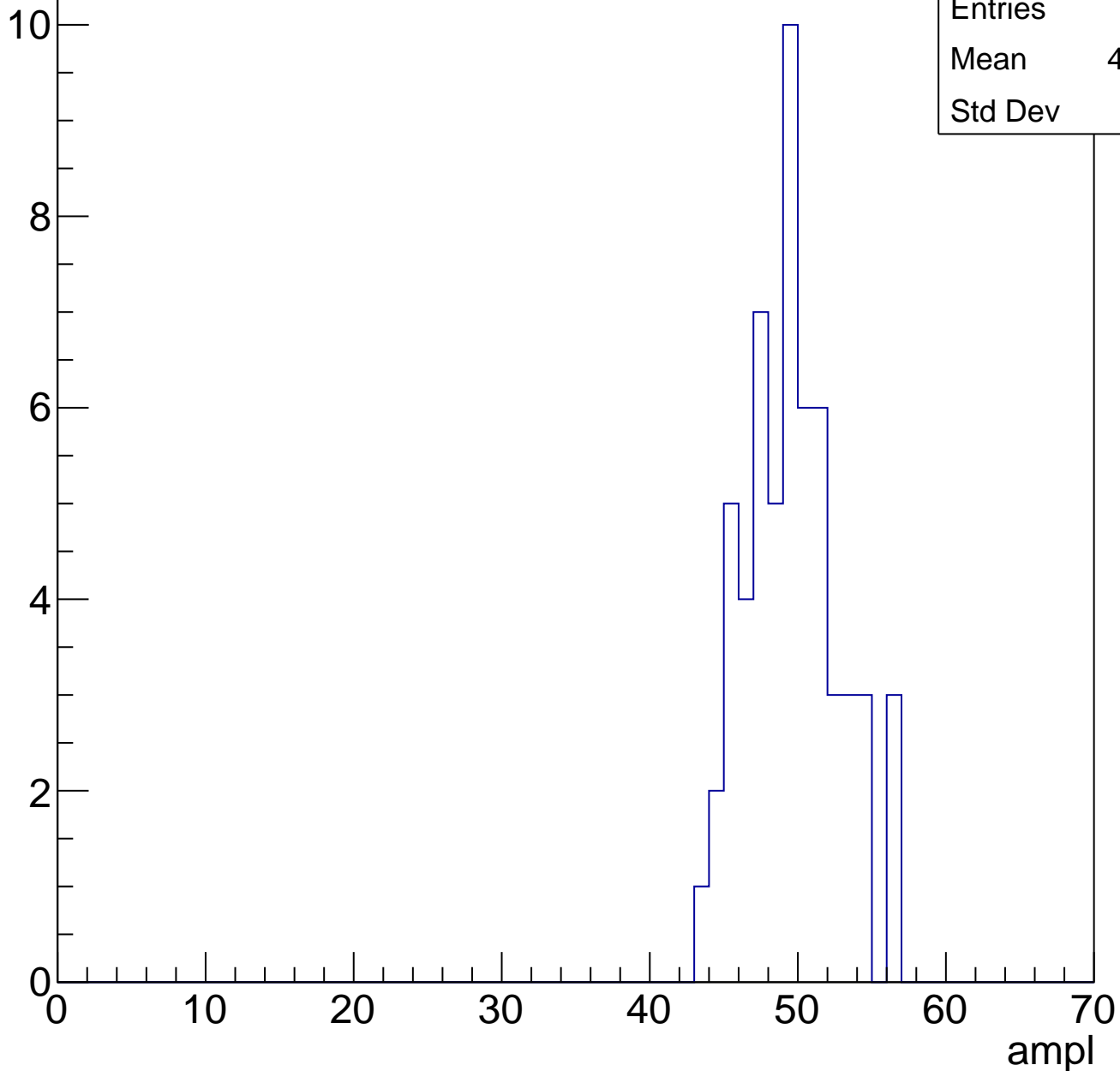


B1L003S, U20-ch78, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

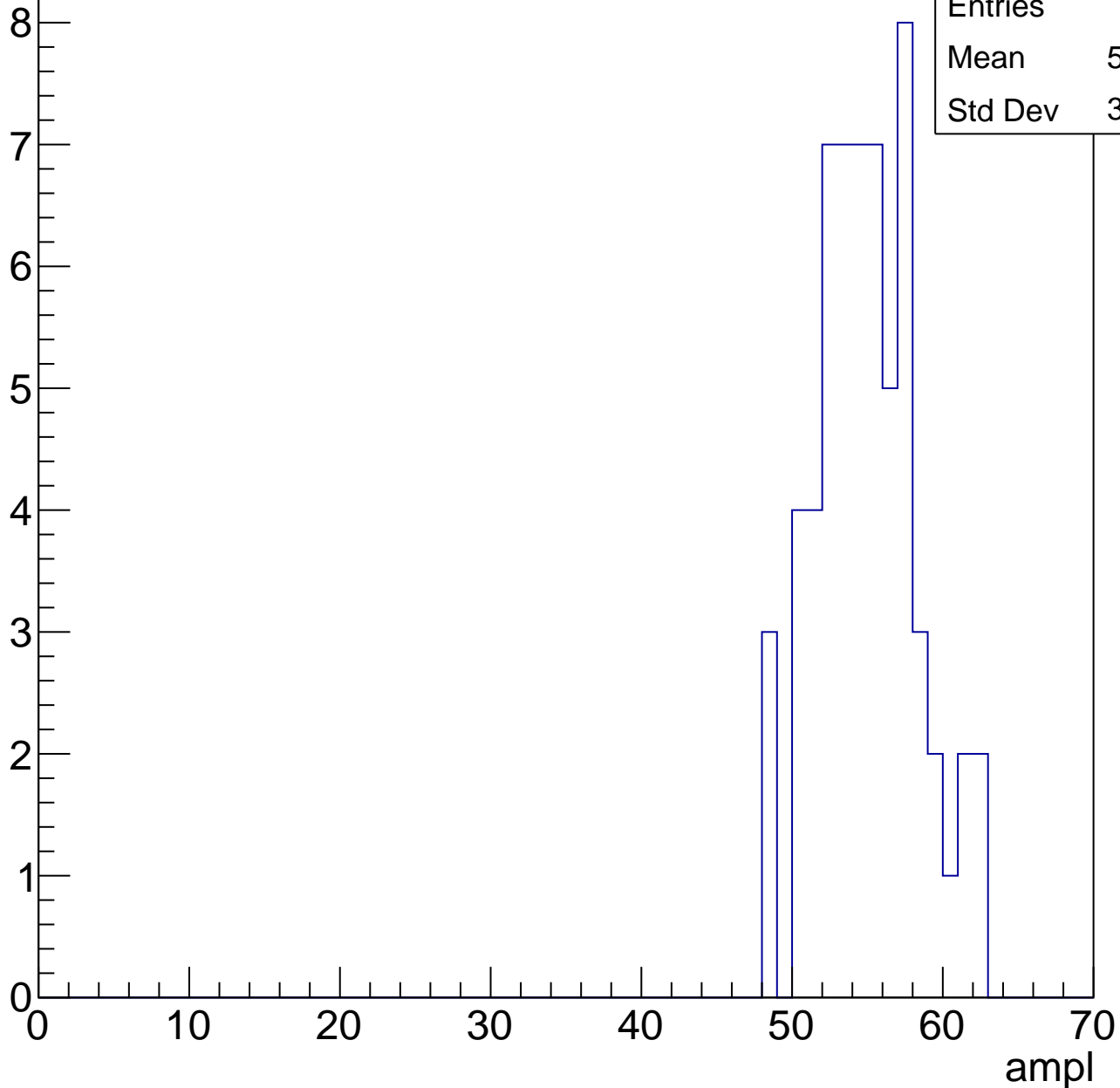
Entries	58
Mean	49.14
Std Dev	3.11



B1L003S, U20-ch78, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



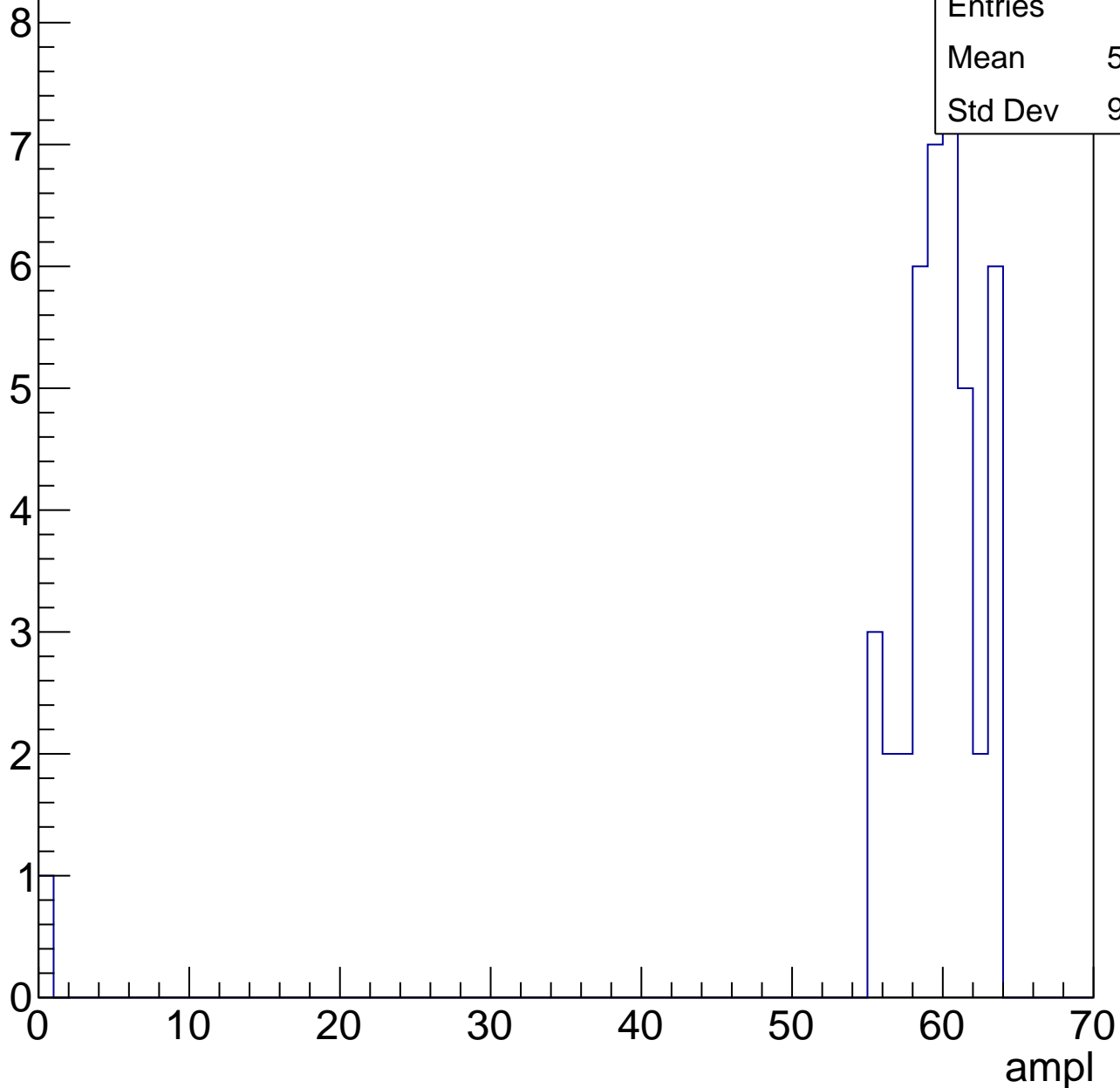
Entries	62
Mean	54.52
Std Dev	3.325

B1L003S, U20-ch78, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.07
Std Dev	9.344

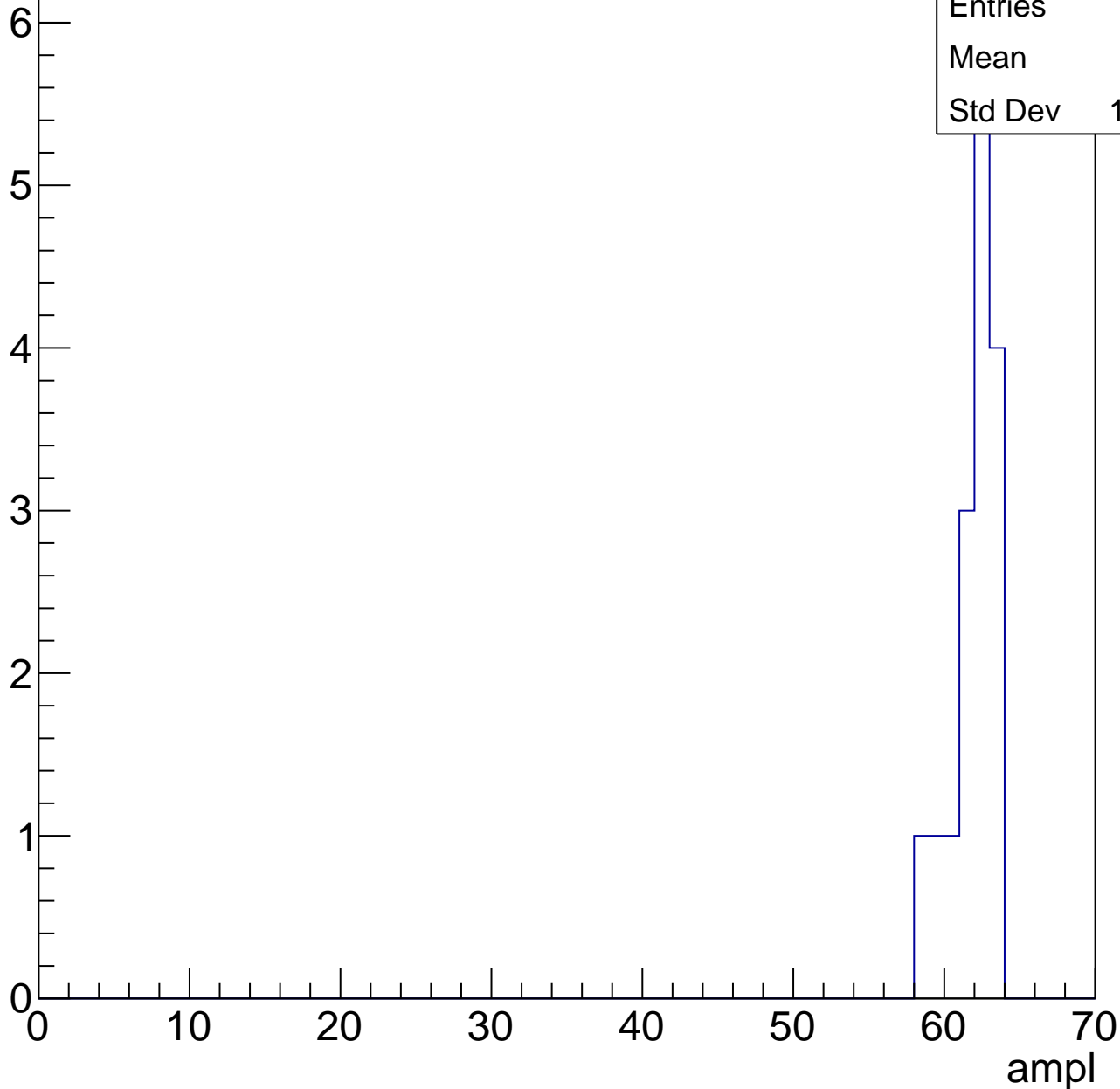


B1L003S, U20-ch78, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

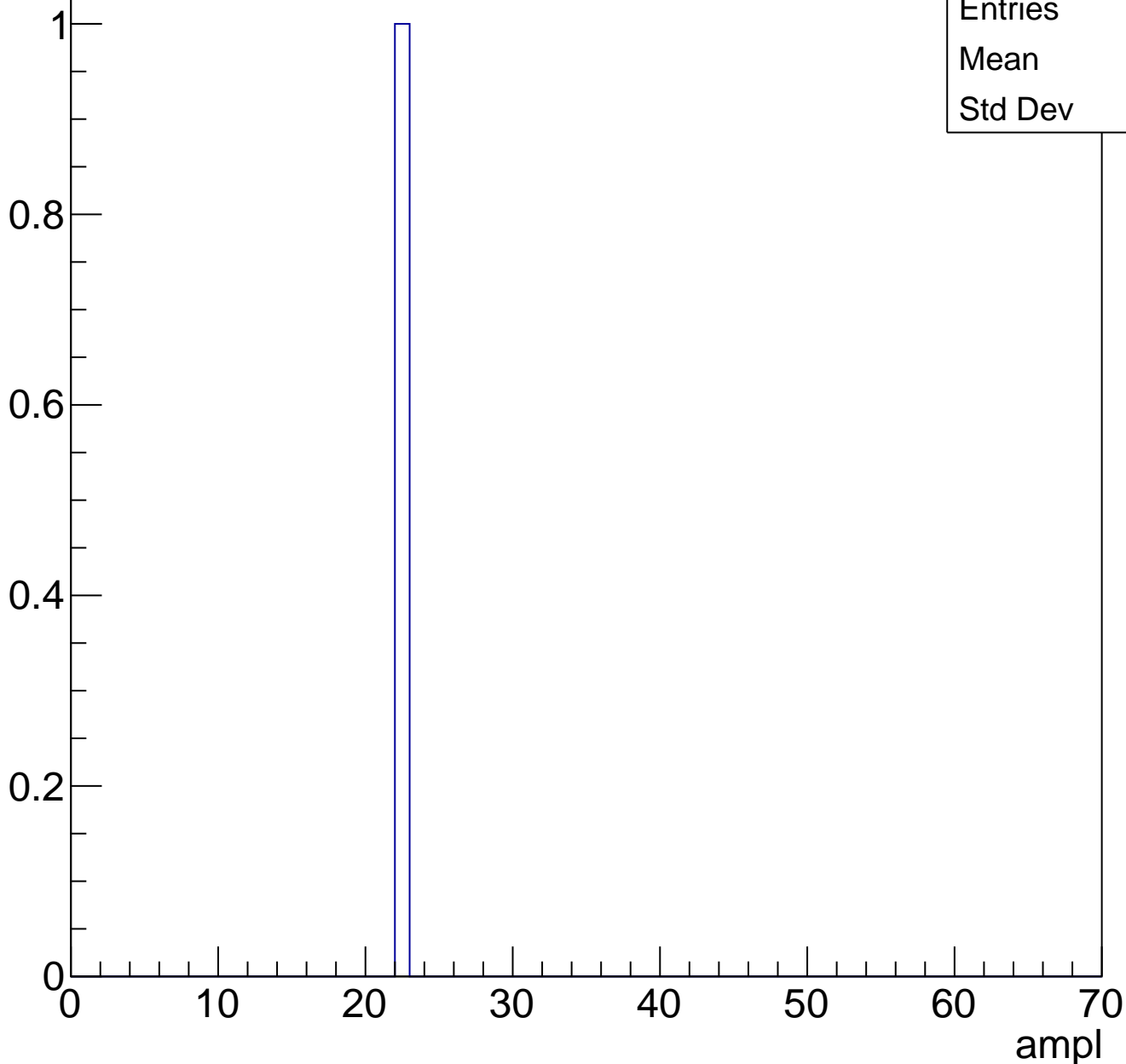
Entries	16
Mean	61.5
Std Dev	1.414



B1L003S, U20-ch78, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L003S, U20-ch79, adc0

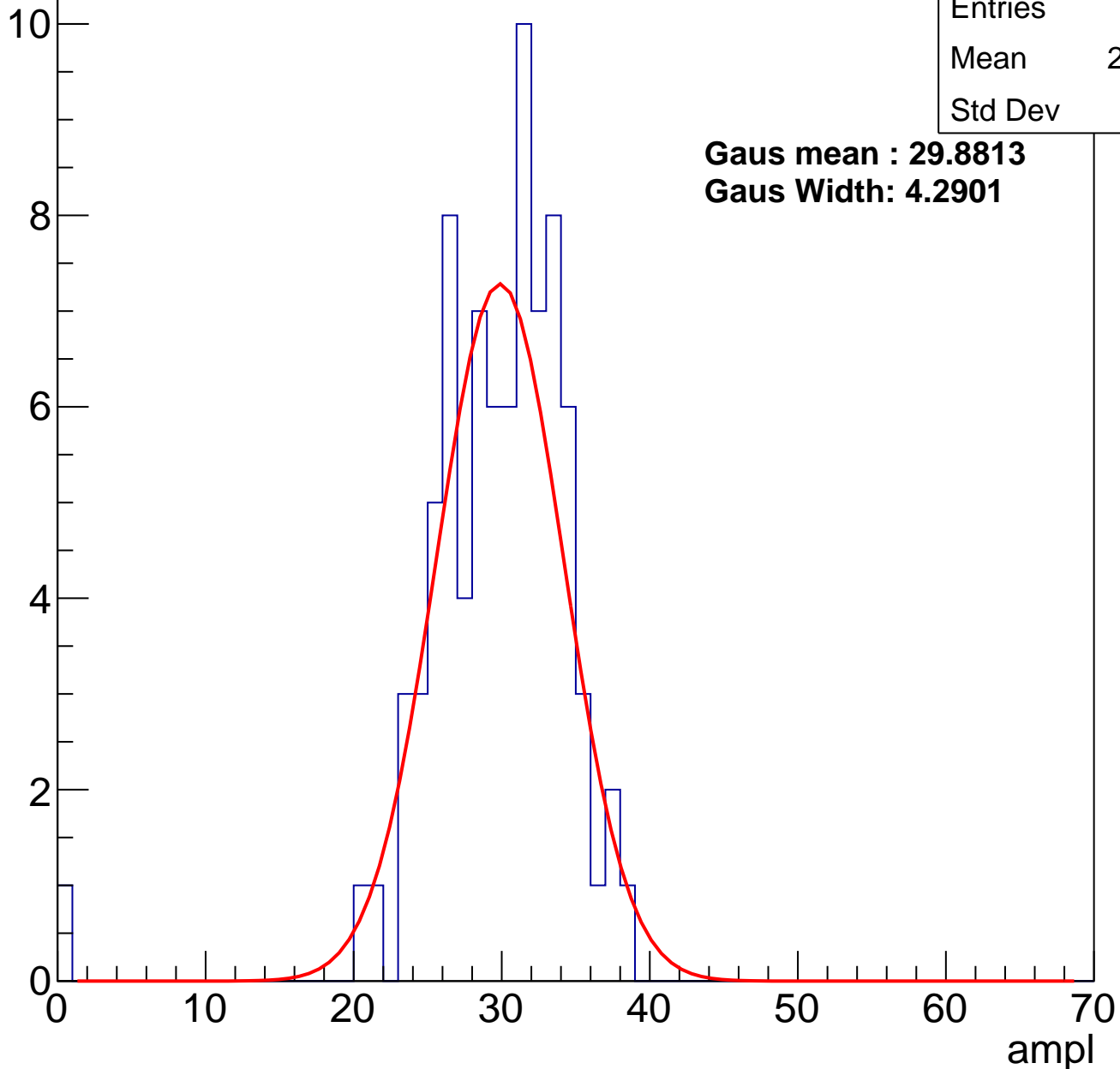
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	29.25
Std Dev	5.02

Gaus mean : 29.8813

Gaus Width: 4.2901

Entry



B1L003S, U20-ch79, adc1

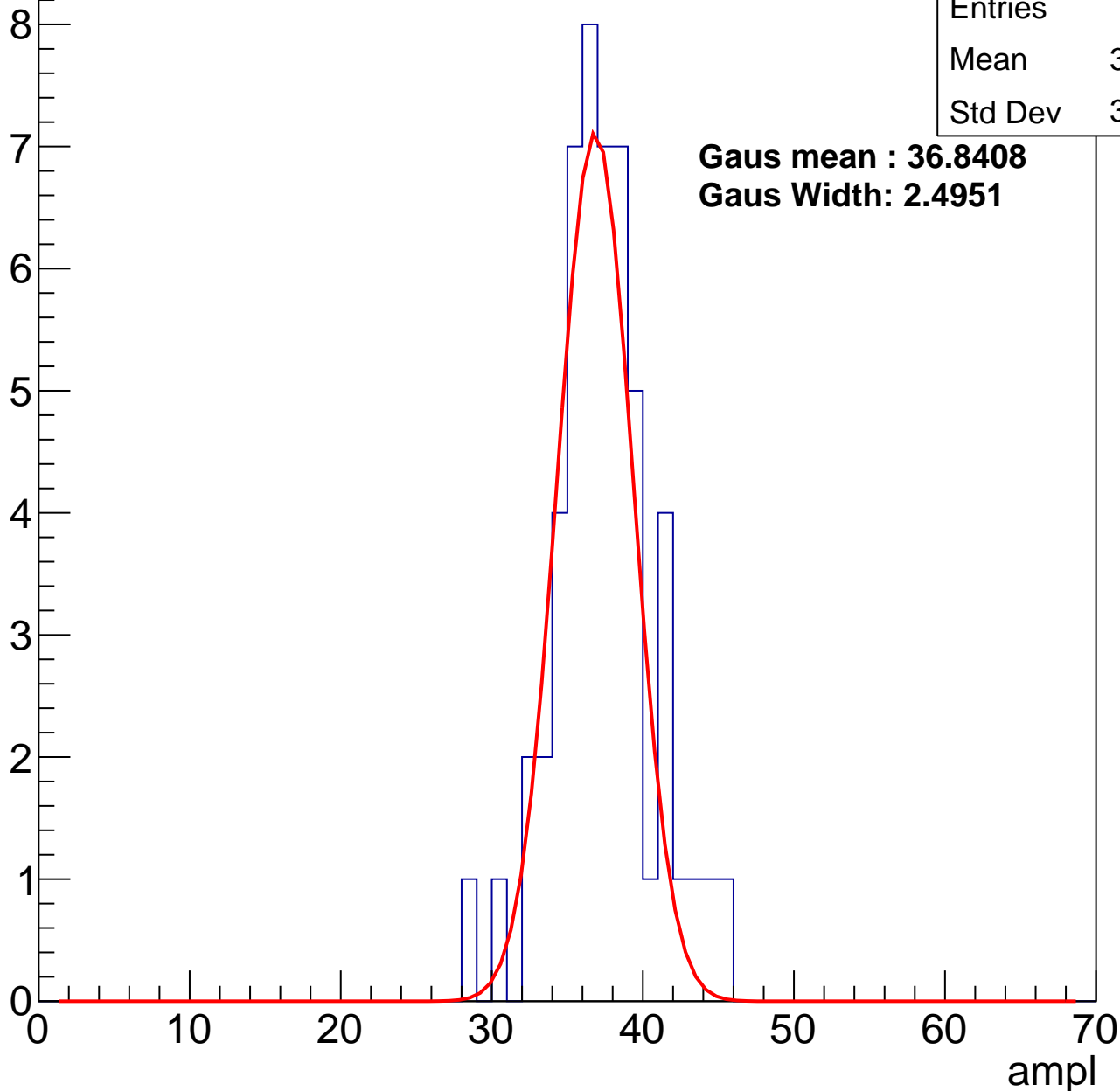
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.89
Std Dev	3.243

Gaus mean : 36.8408

Gaus Width: 2.4951



B1L003S, U20-ch79, adc2

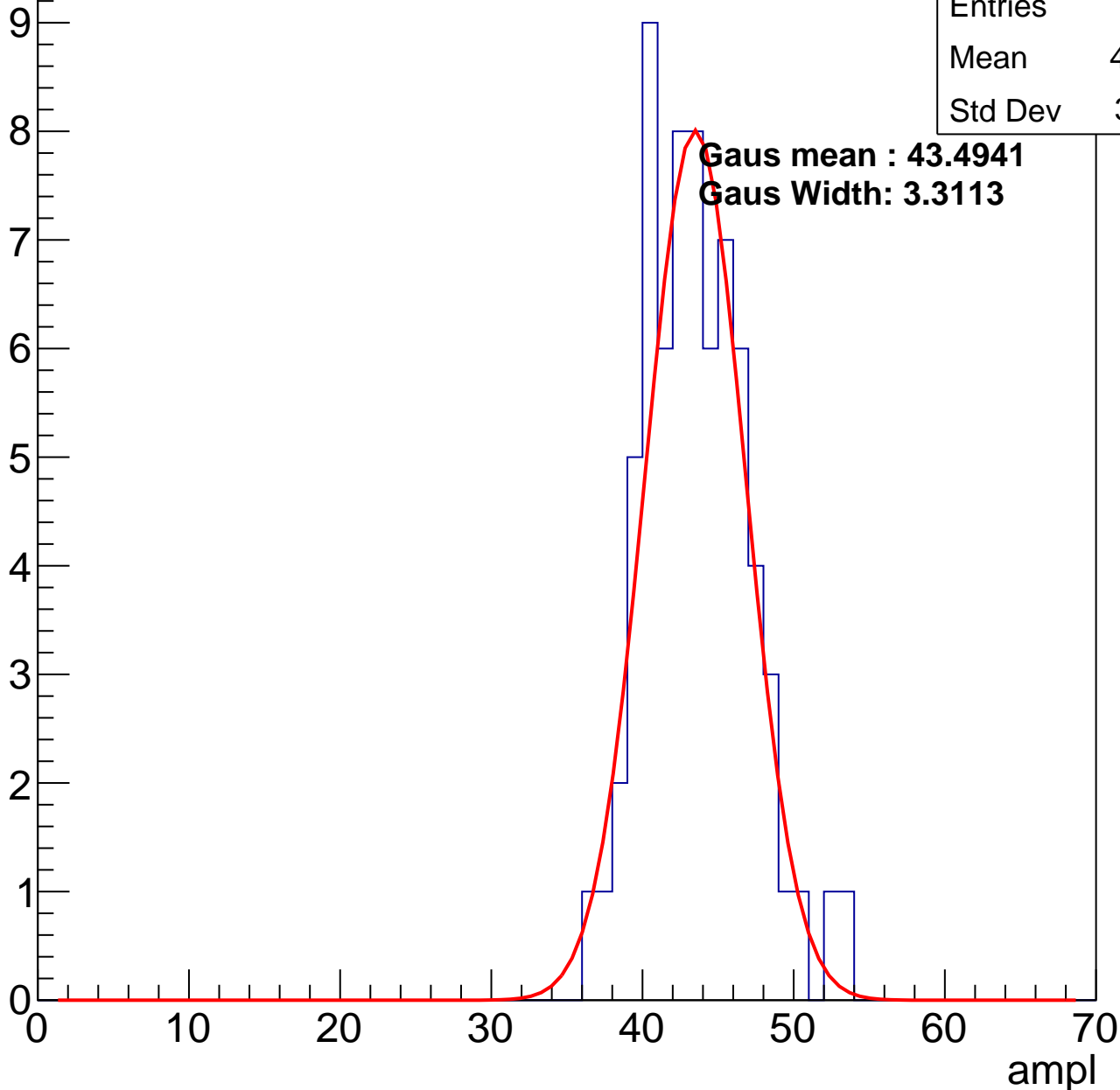
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	43.16
Std Dev	3.421

Gaus mean : 43.4941

Gaus Width: 3.3113

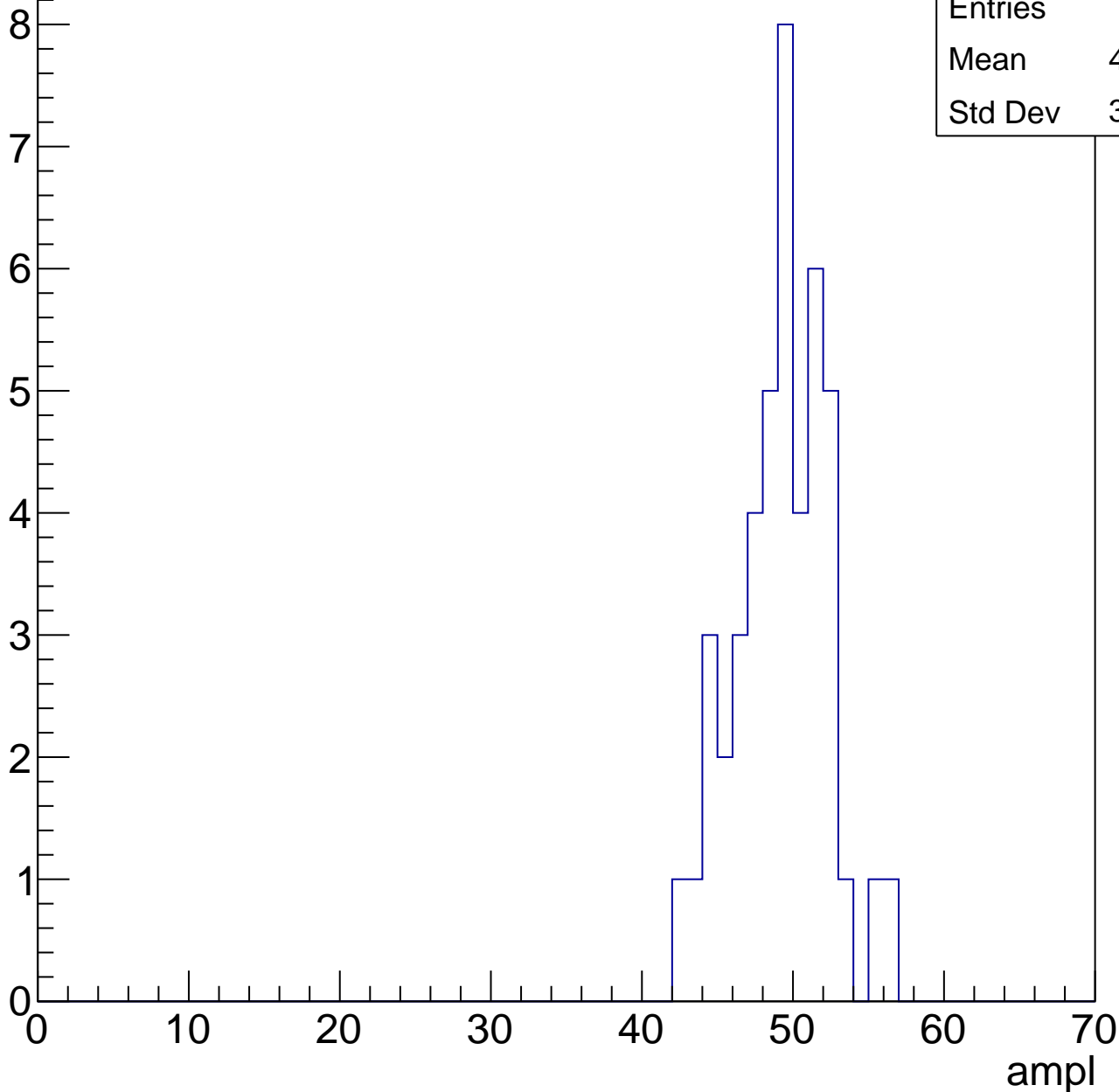


B1L003S, U20-ch79, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	48.78
Std Dev	3.025



B1L003S, U20-ch79, adc4

calib_packv5_042523_0143.root, FC#13, port D2

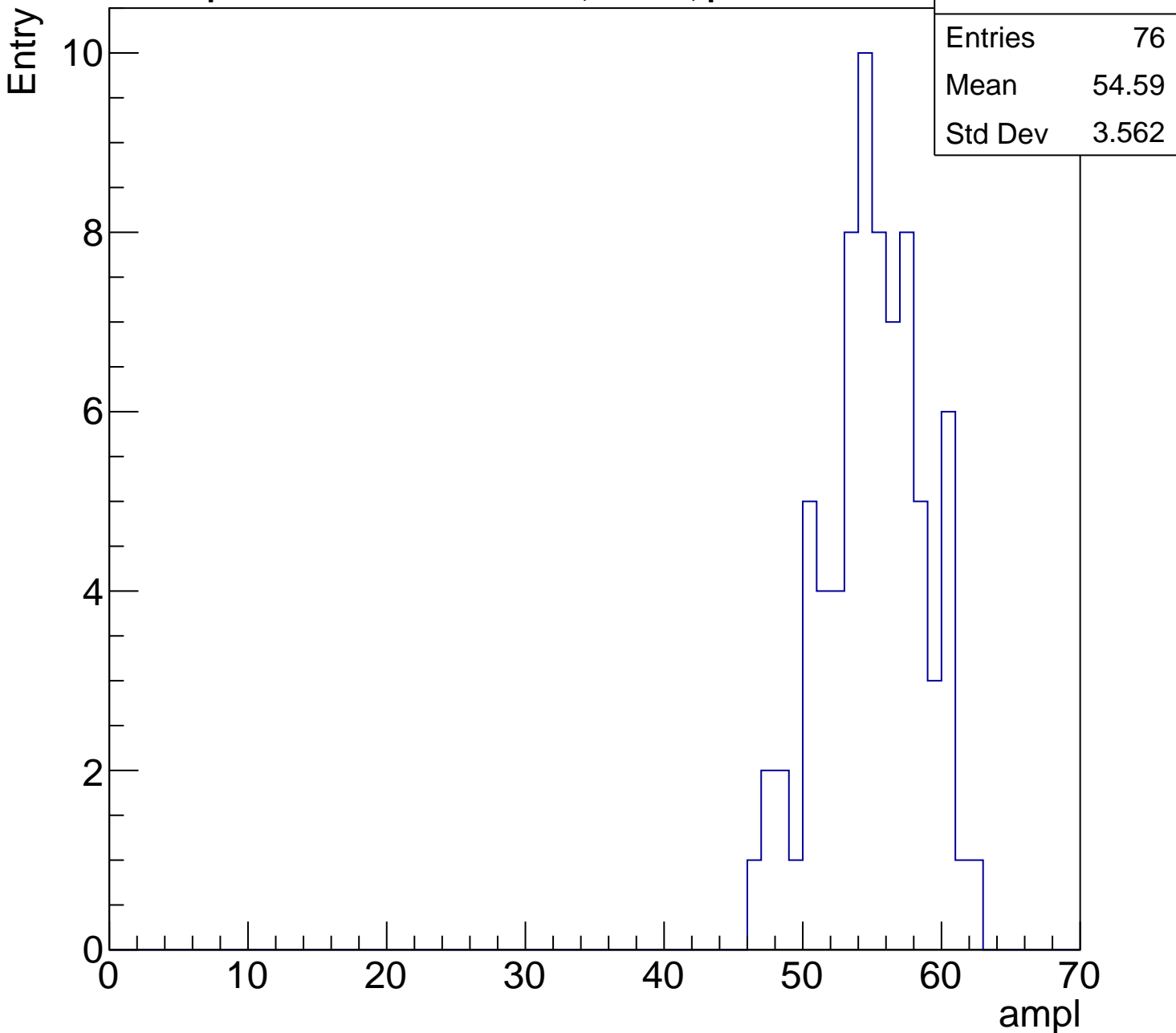
Entries	76
Mean	54.59
Std Dev	3.562

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

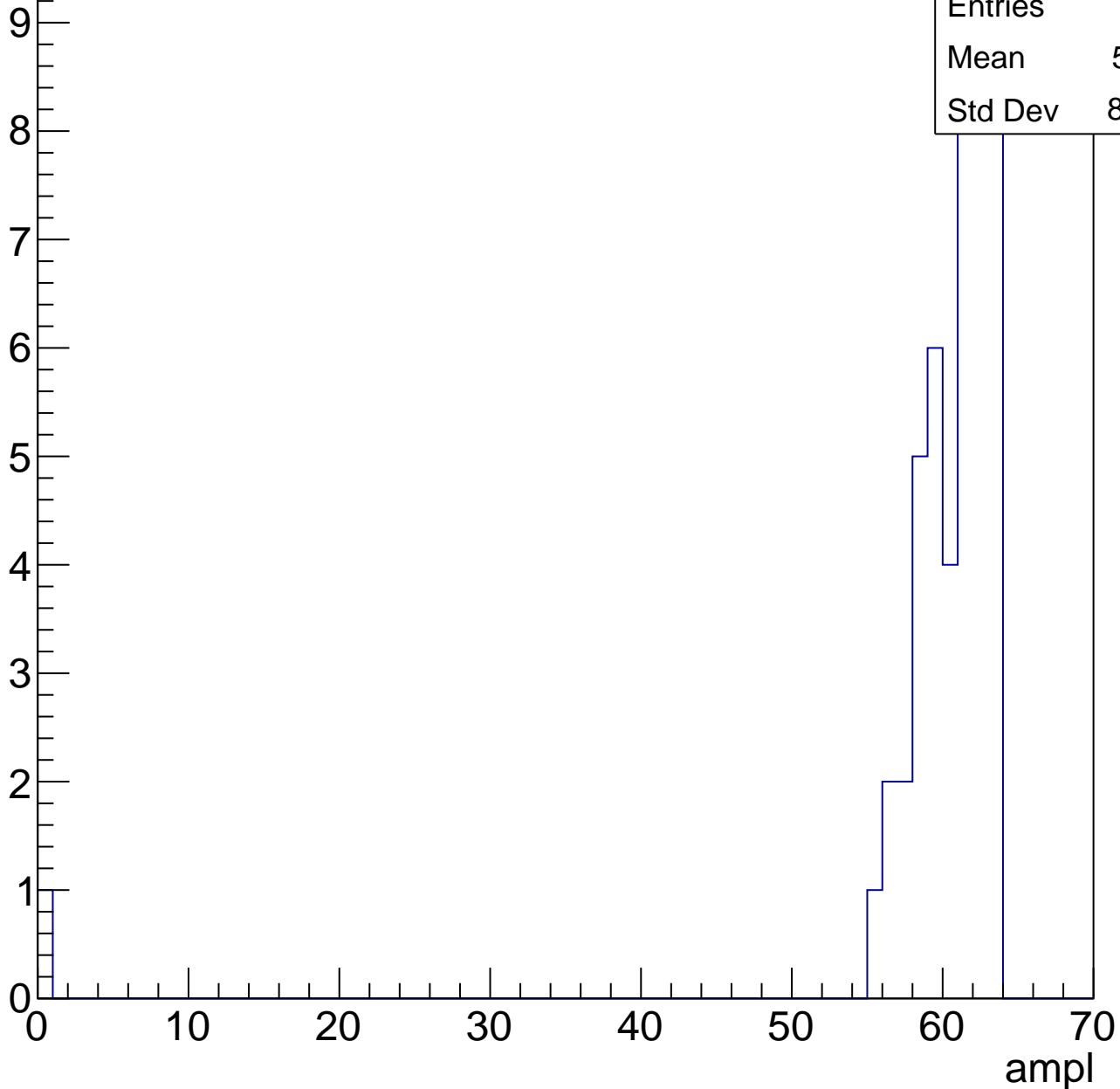
ampl



B1L003S, U20-ch79, adc5

calib_packv5_042523_0143.root, FC#13, port D2

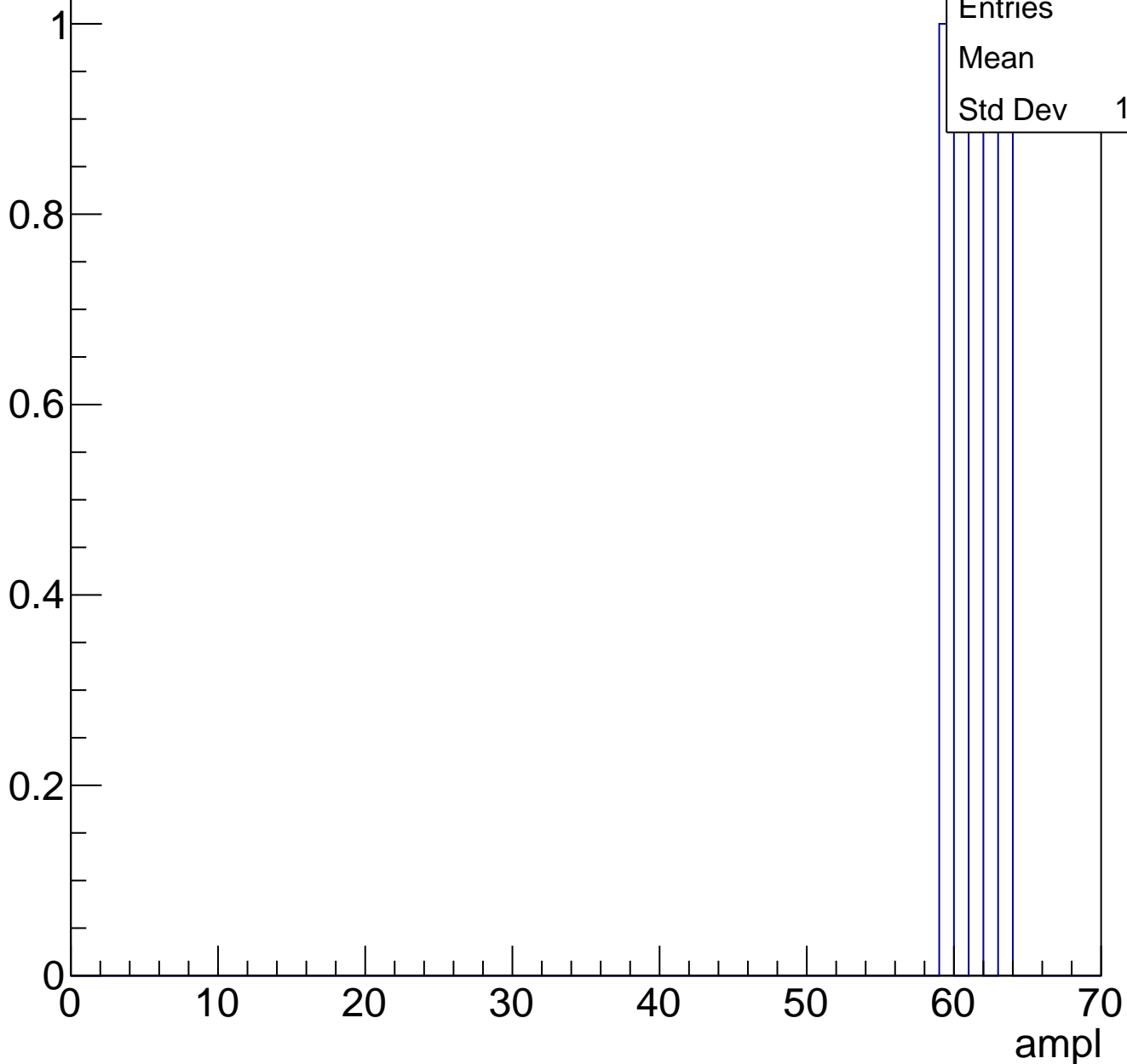
Entry



B1L003S, U20-ch79, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch79, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch80, adc0

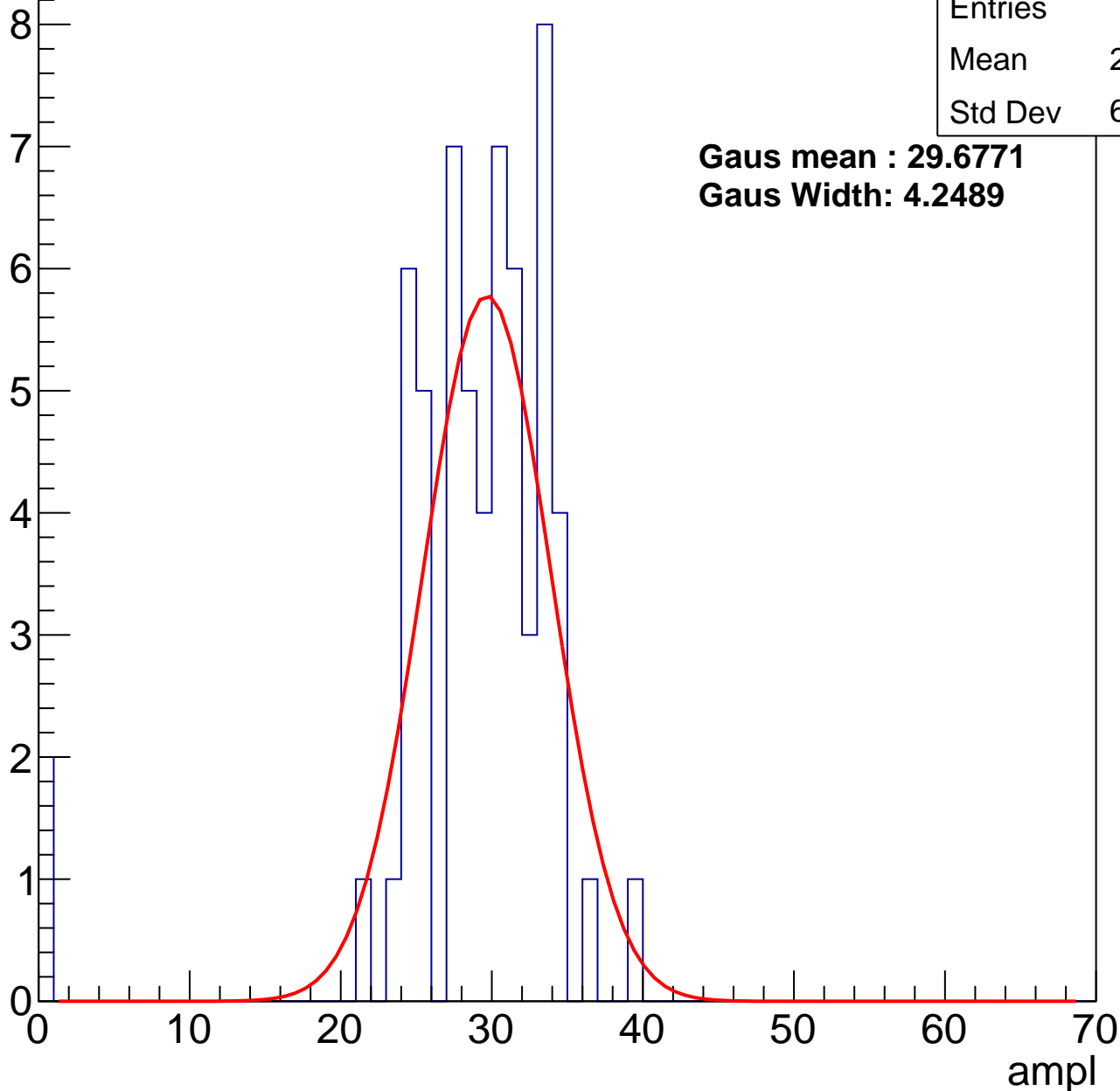
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.28
Std Dev	6.335

Gaus mean : 29.6771

Gaus Width: 4.2489



B1L003S, U20-ch80, adc1

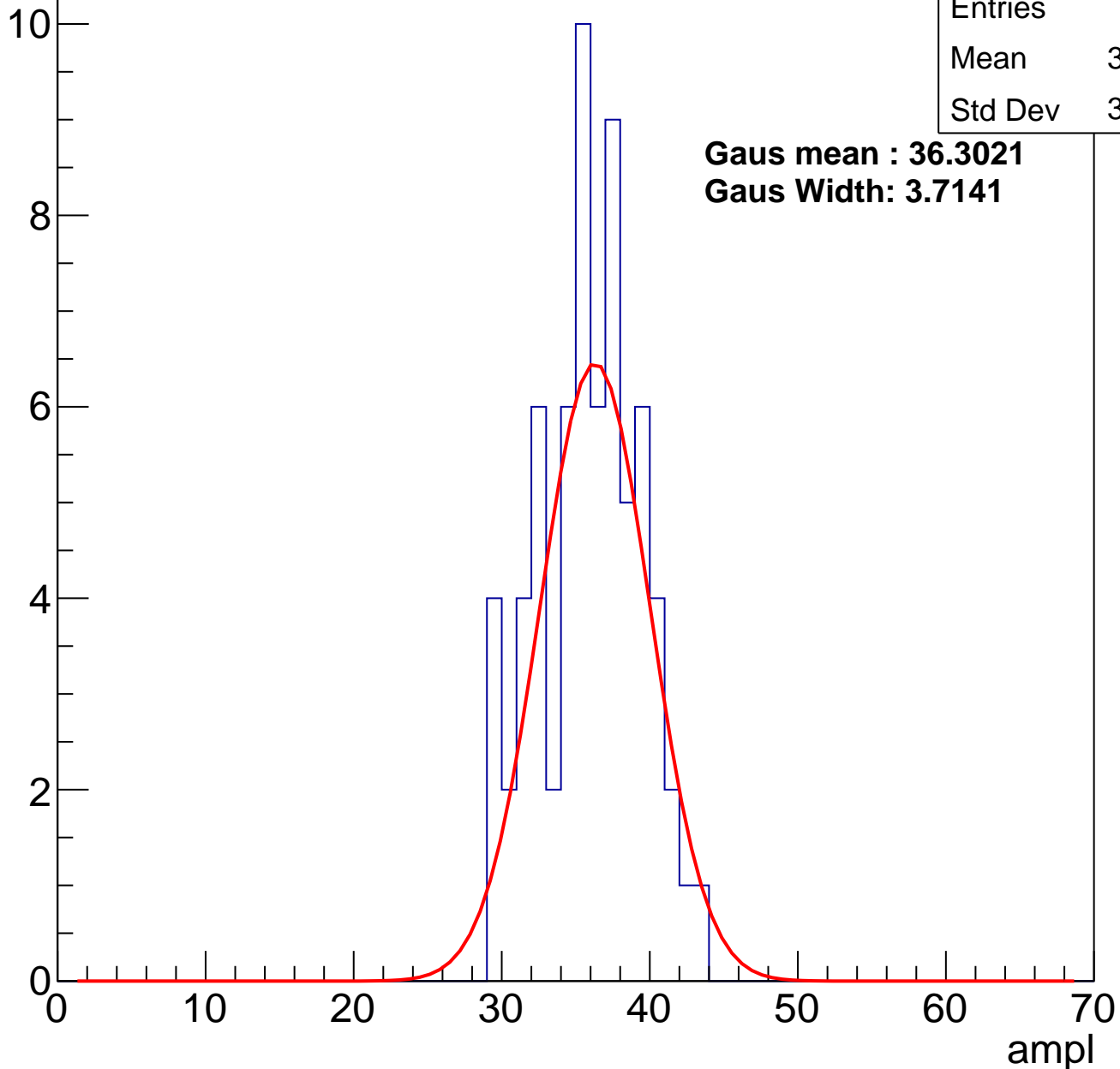
calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	35.47
Std Dev	3.385

Gaus mean : 36.3021

Gaus Width: 3.7141

Entry

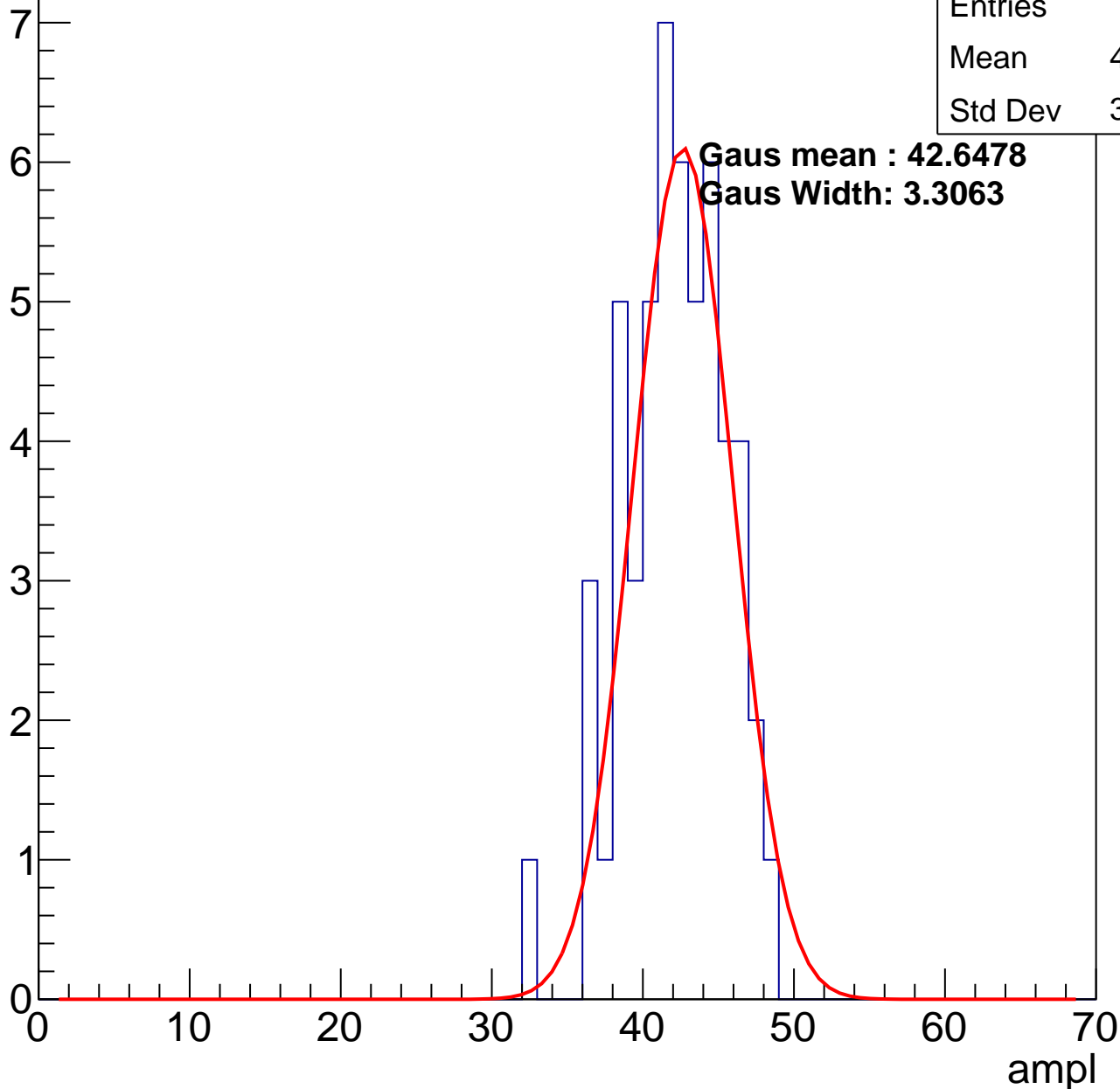


B1L003S, U20-ch80, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	41.66
Std Dev	3.296

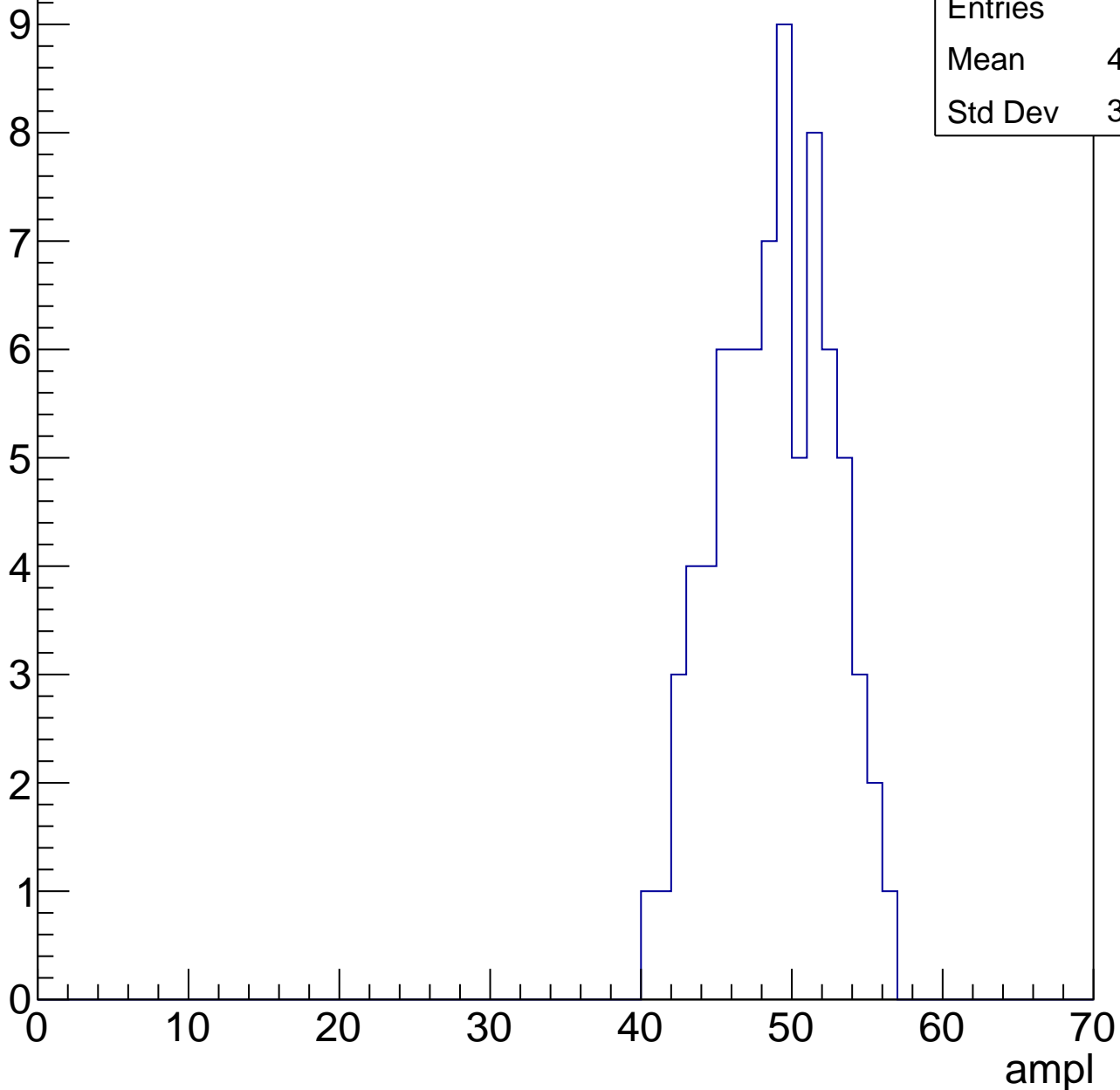


B1L003S, U20-ch80, adc3

calib_packv5_042523_0143.root, FC#13, port D2

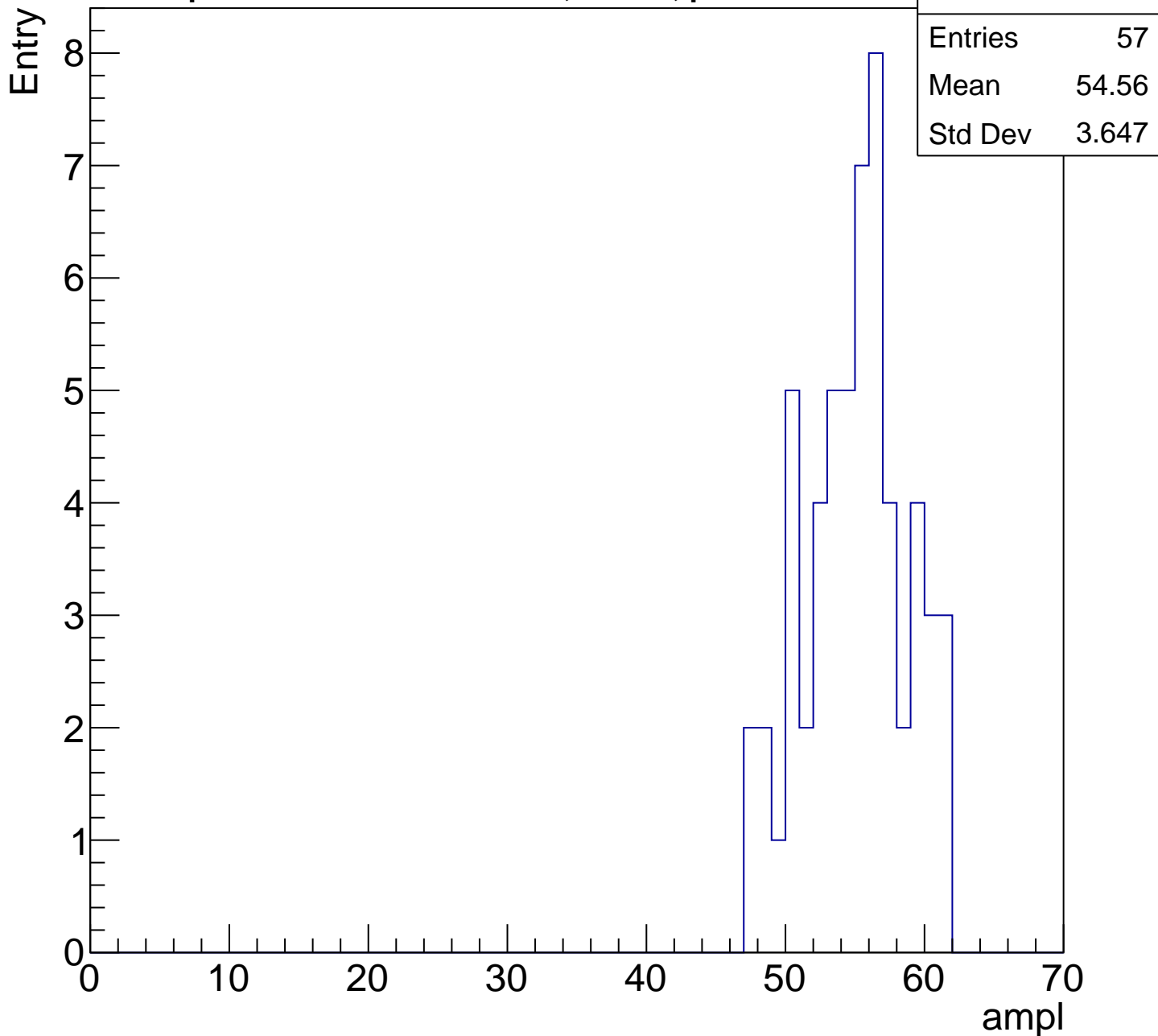
Entry

Entries	77
Mean	48.35
Std Dev	3.695



B1L003S, U20-ch80, adc4

calib_packv5_042523_0143.root, FC#13, port D2

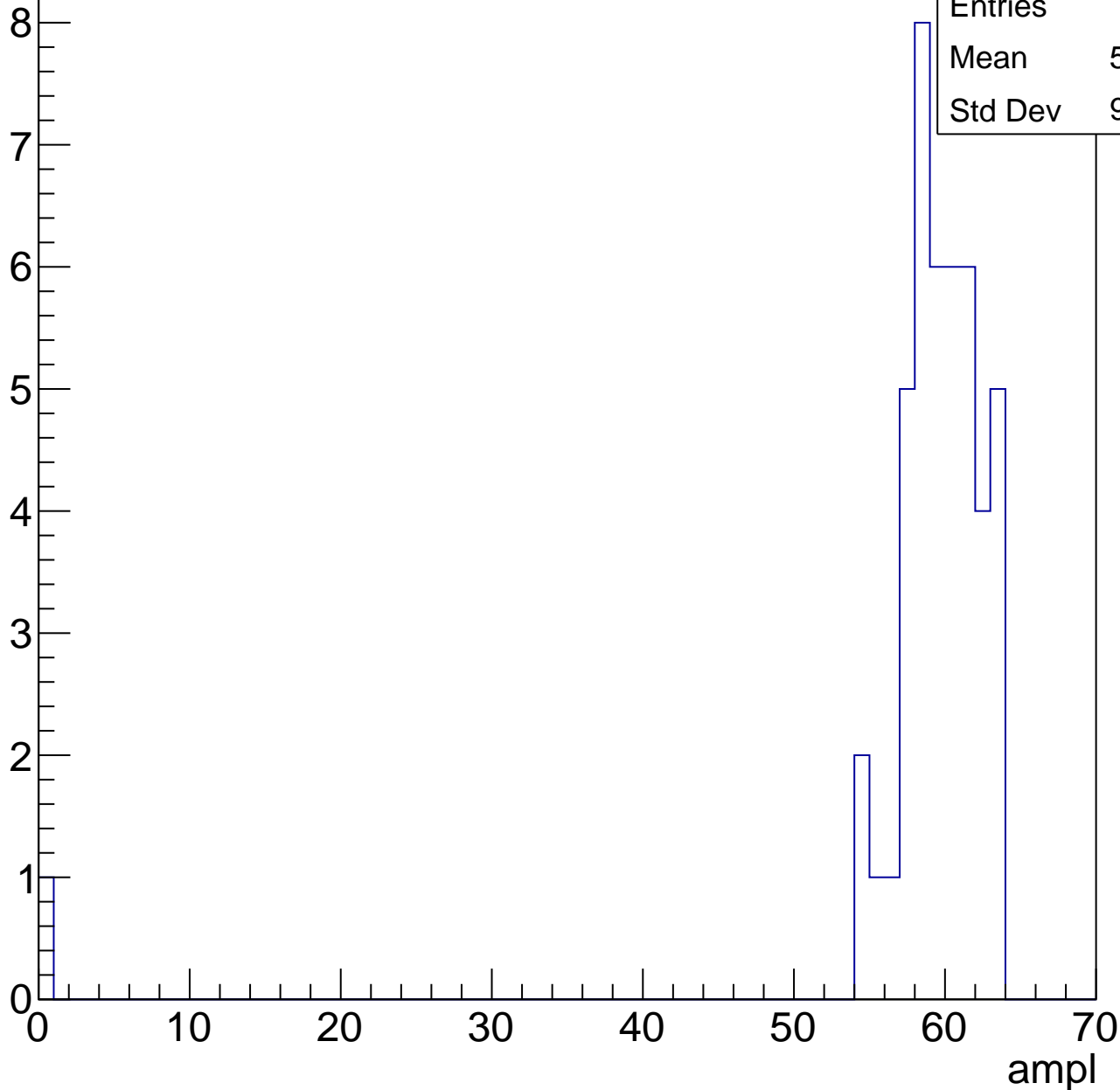


B1L003S, U20-ch80, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.02
Std Dev	9.052

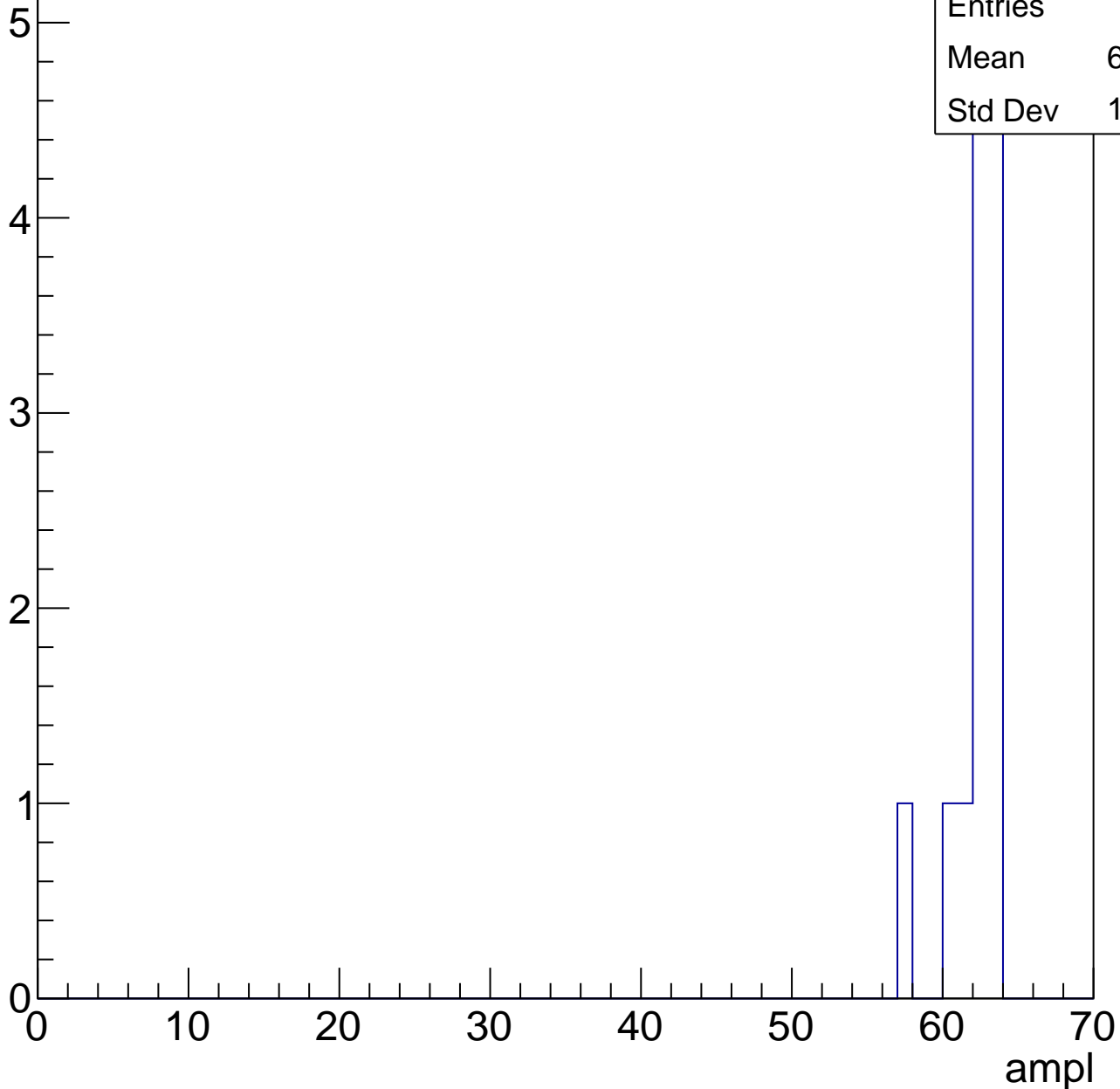


B1L003S, U20-ch80, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.77
Std Dev	1.625



B1L003S, U20-ch80, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch81, adc0

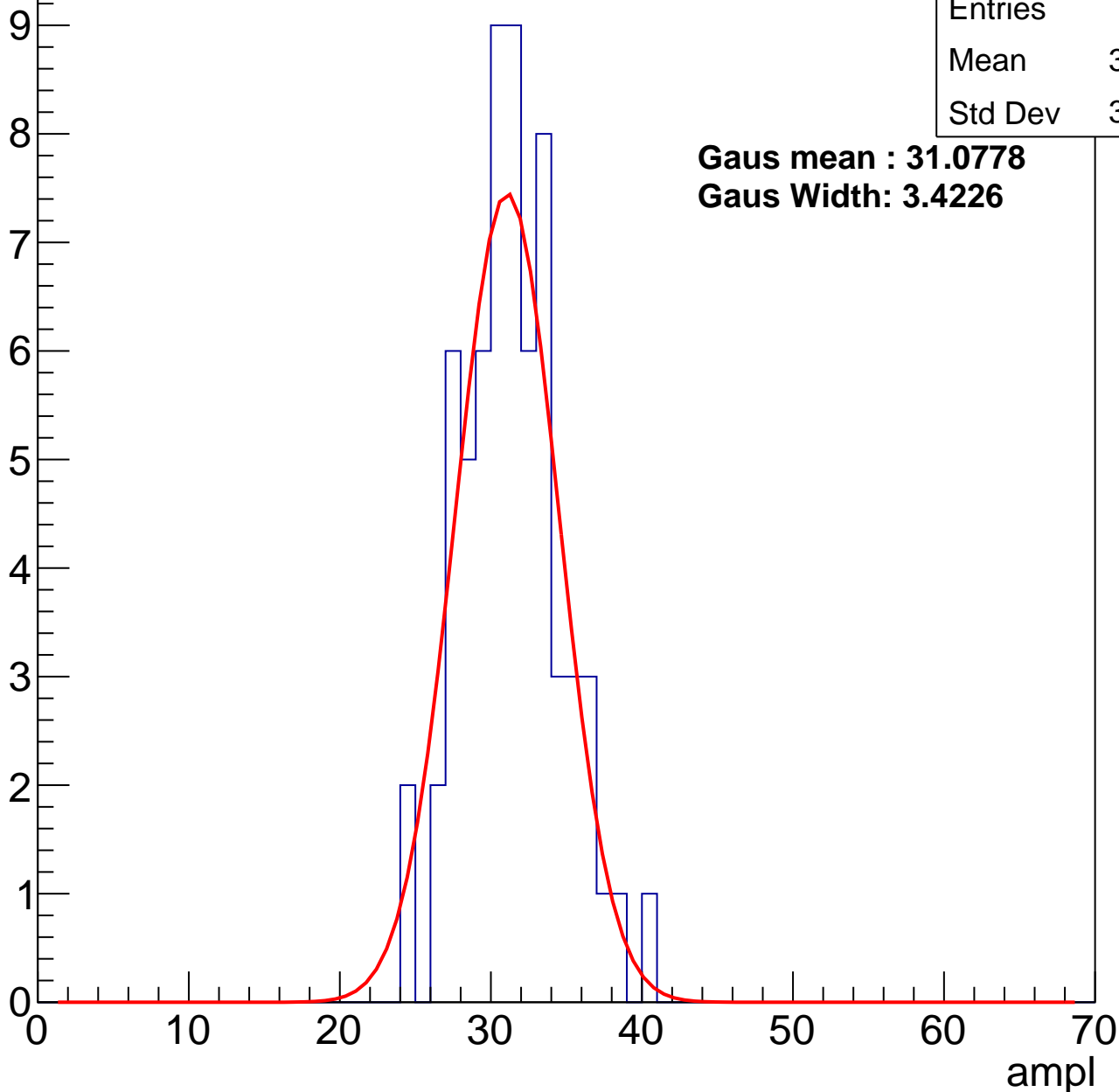
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.94
Std Dev	3.224

Gaus mean : 31.0778

Gaus Width: 3.4226



B1L003S, U20-ch81, adc1

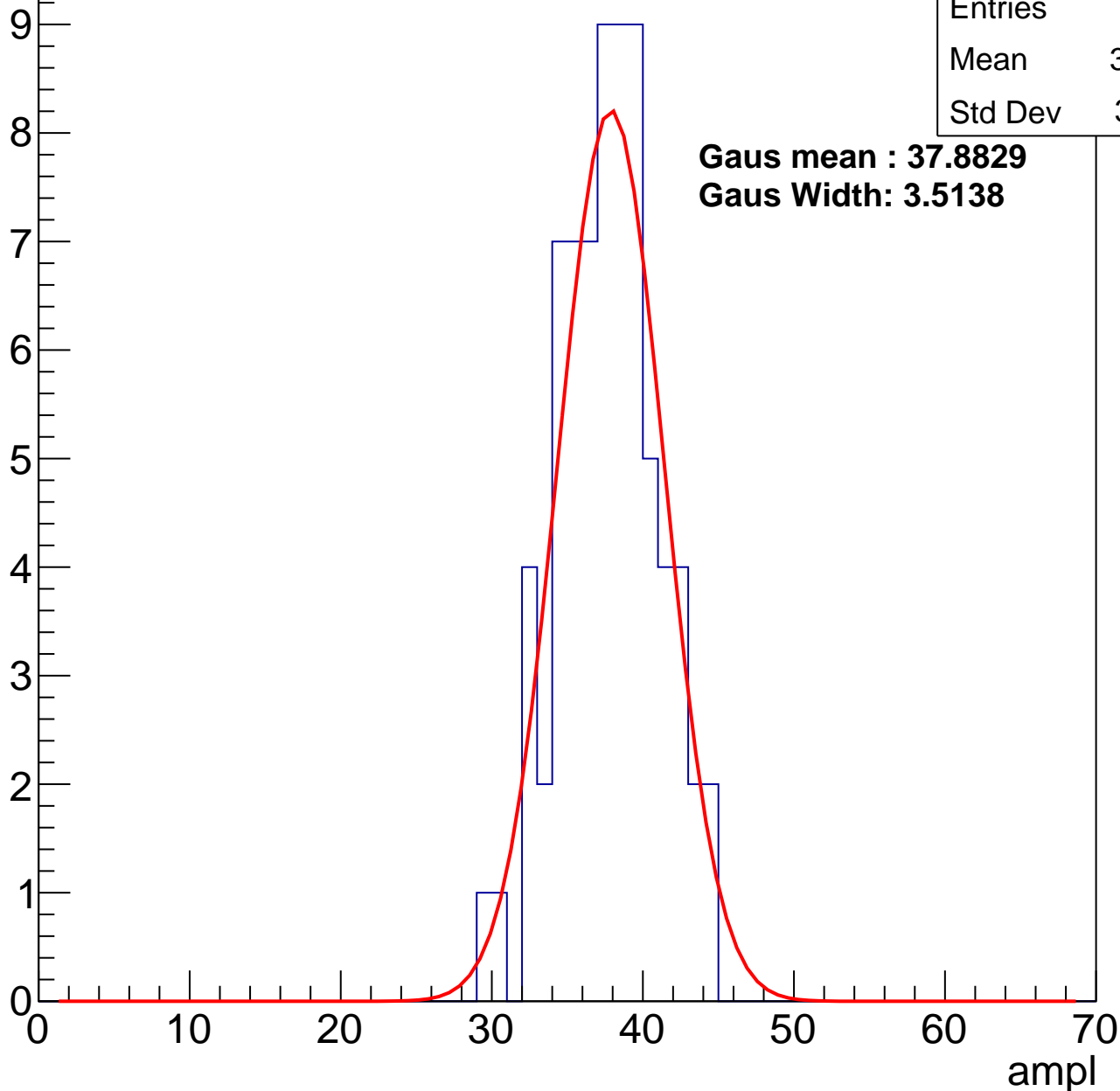
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.26
Std Dev	3.231

Gaus mean : 37.8829

Gaus Width: 3.5138



B1L003S, U20-ch81, adc2

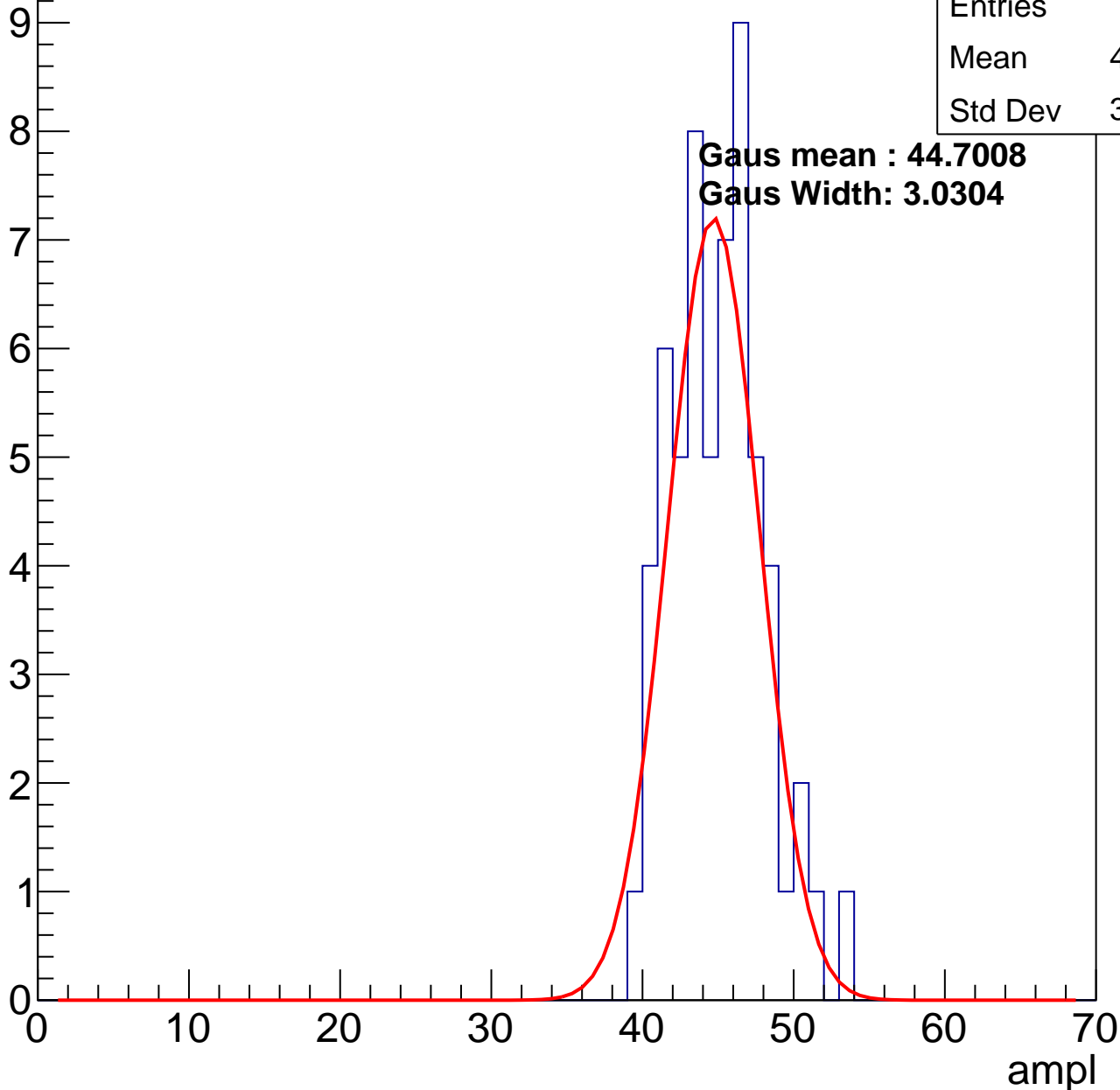
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.54
Std Dev	3.005

Gaus mean : 44.7008

Gaus Width: 3.0304

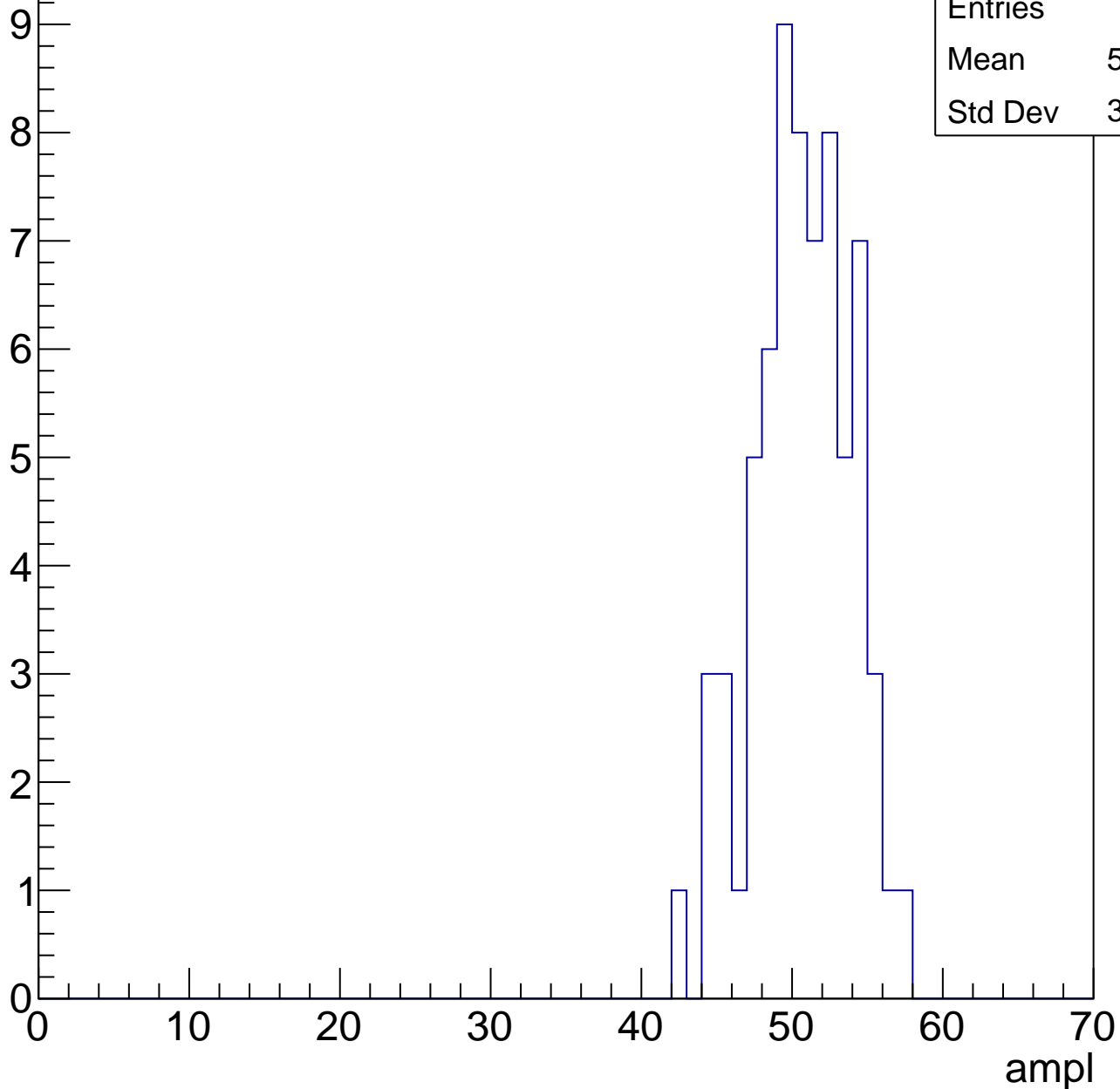


B1L003S, U20-ch81, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.19
Std Dev	3.196

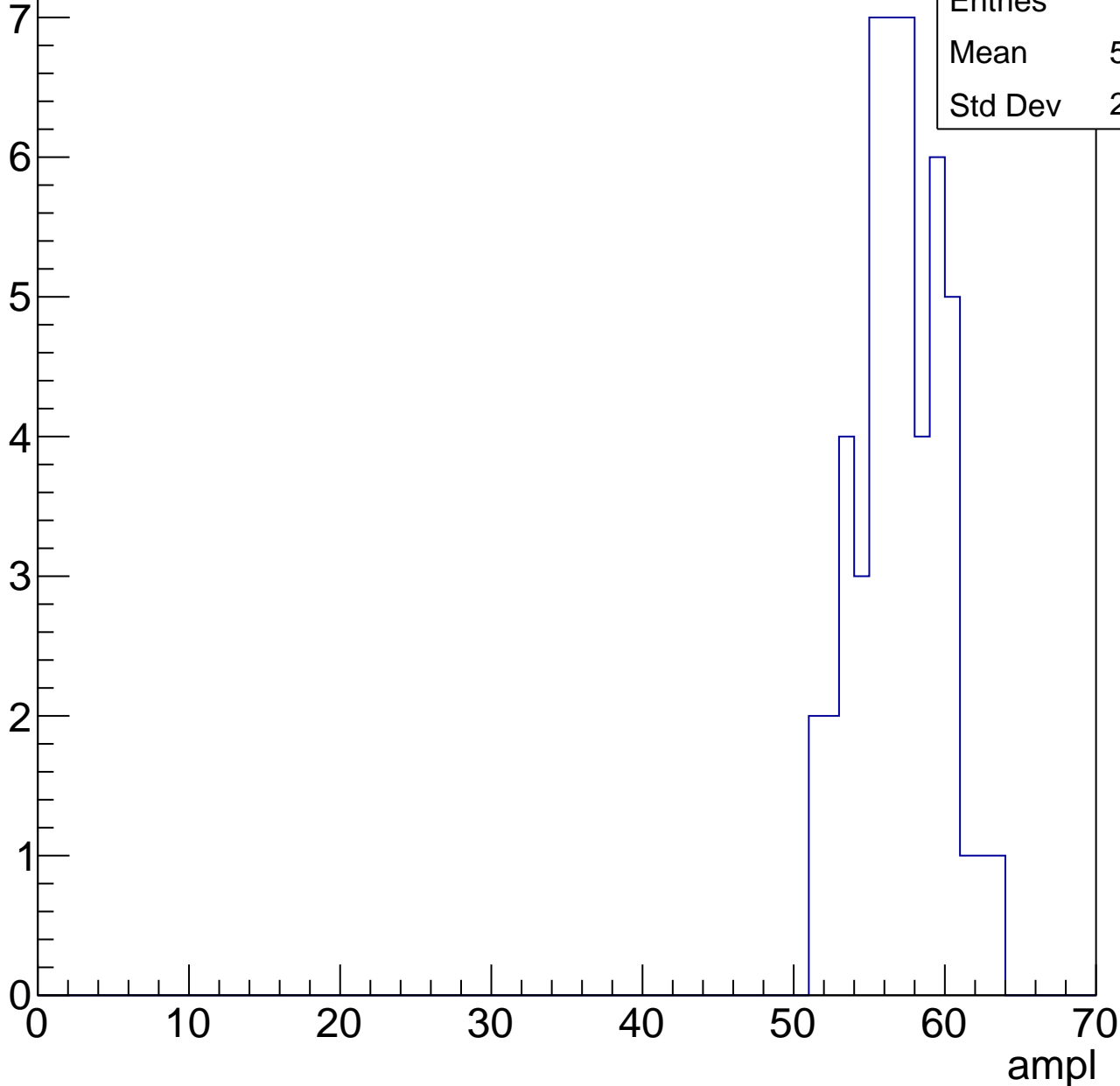


B1L003S, U20-ch81, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	56.56
Std Dev	2.787

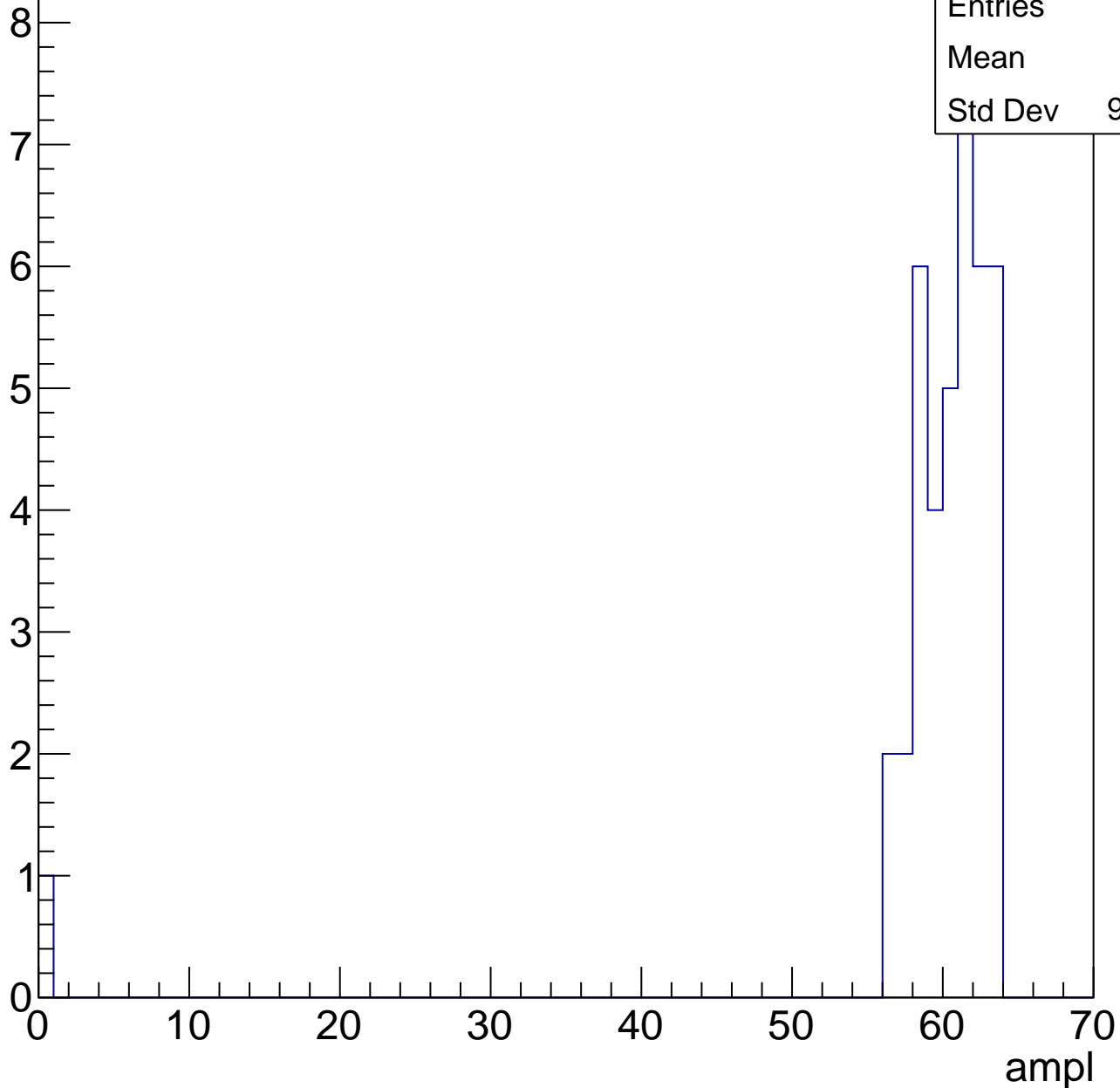


B1L003S, U20-ch81, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	58.7
Std Dev	9.613



B1L003S, U20-ch81, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

7

Mean

62.29

Std Dev

0.6999

B1L003S, U20-ch81, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch82, adc0

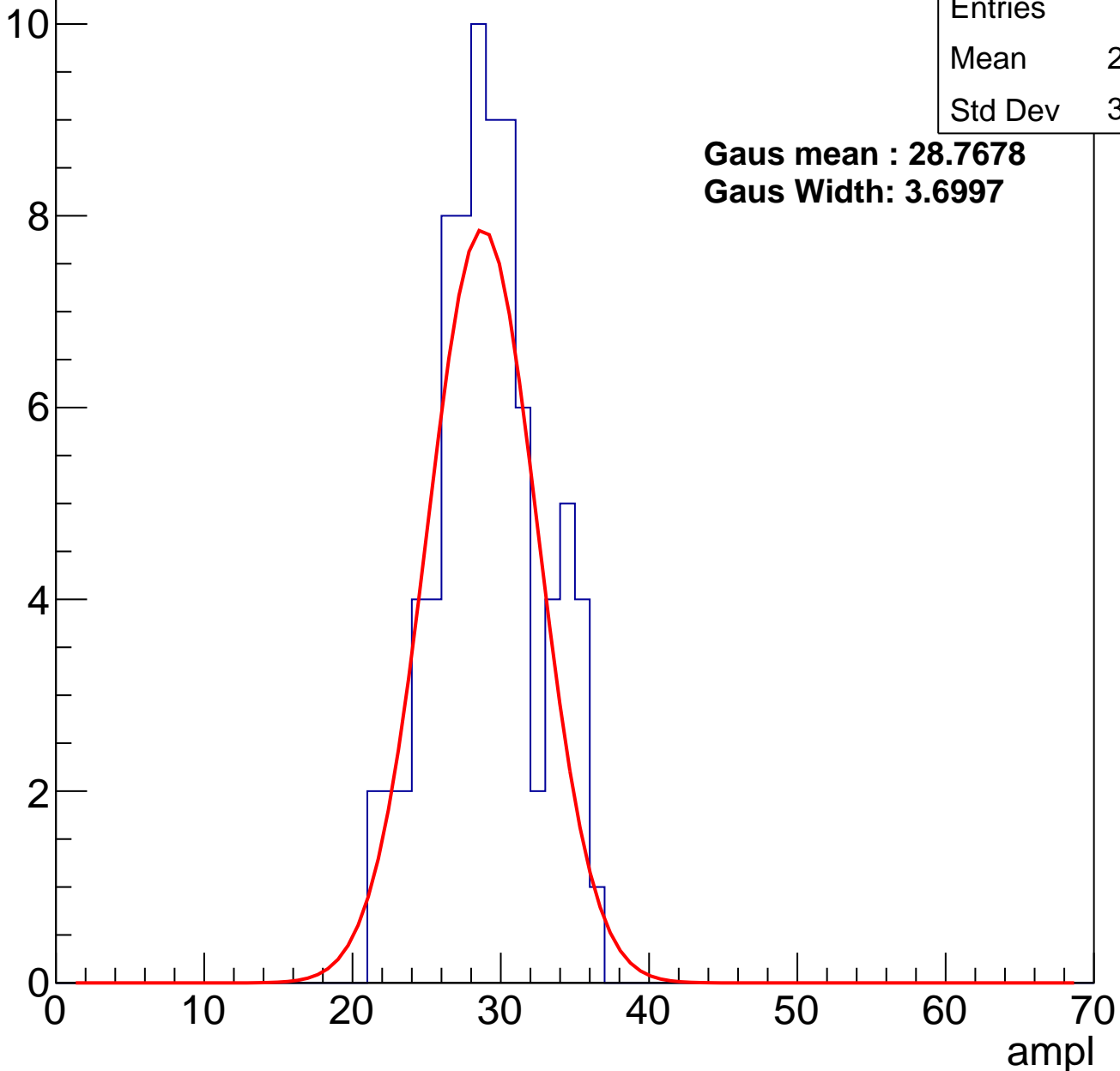
calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	28.64
Std Dev	3.533

Gaus mean : 28.7678

Gaus Width: 3.6997

Entry



B1L003S, U20-ch82, adc1

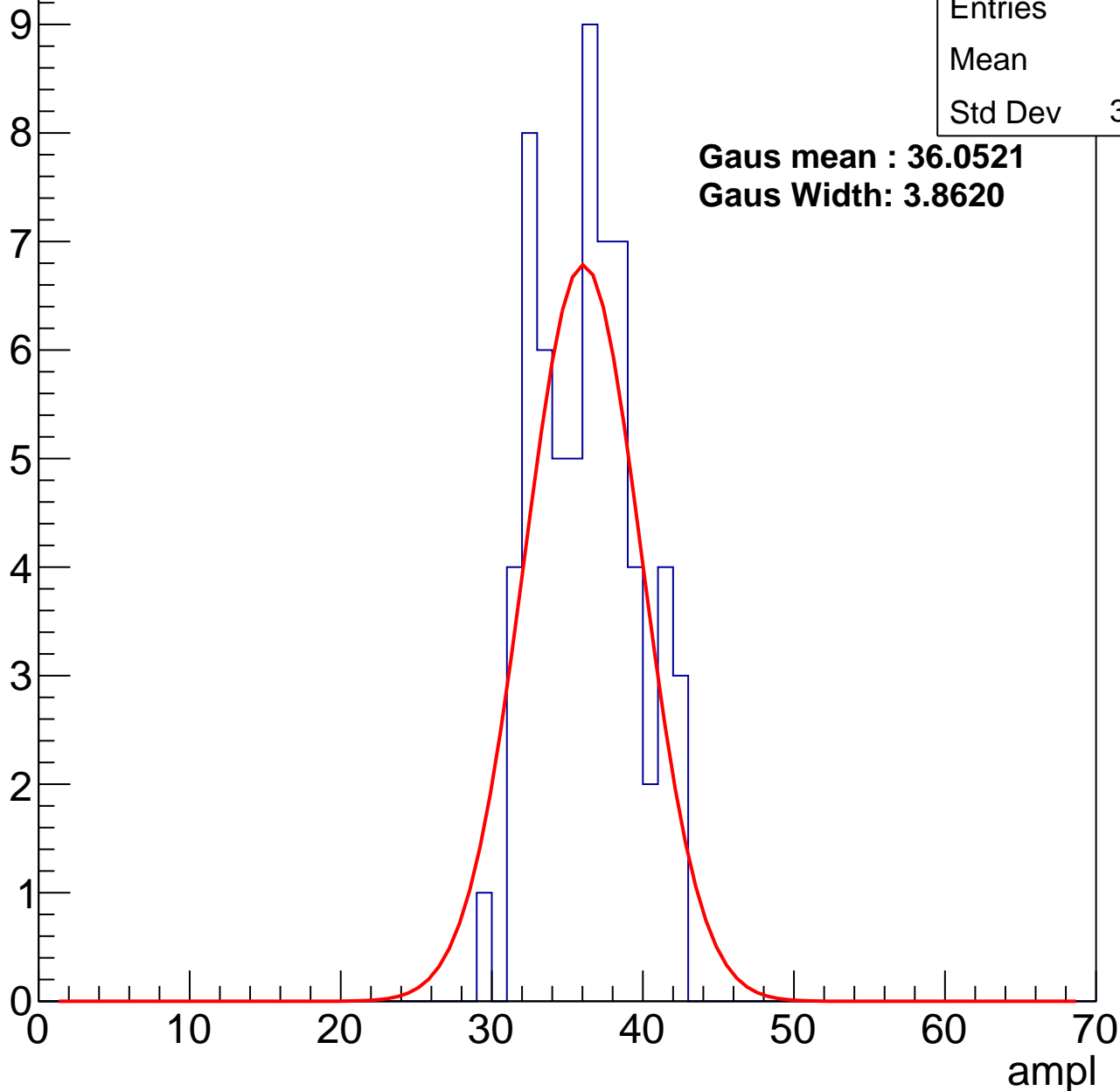
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.8
Std Dev	3.197

Gaus mean : 36.0521

Gaus Width: 3.8620



B1L003S, U20-ch82, adc2

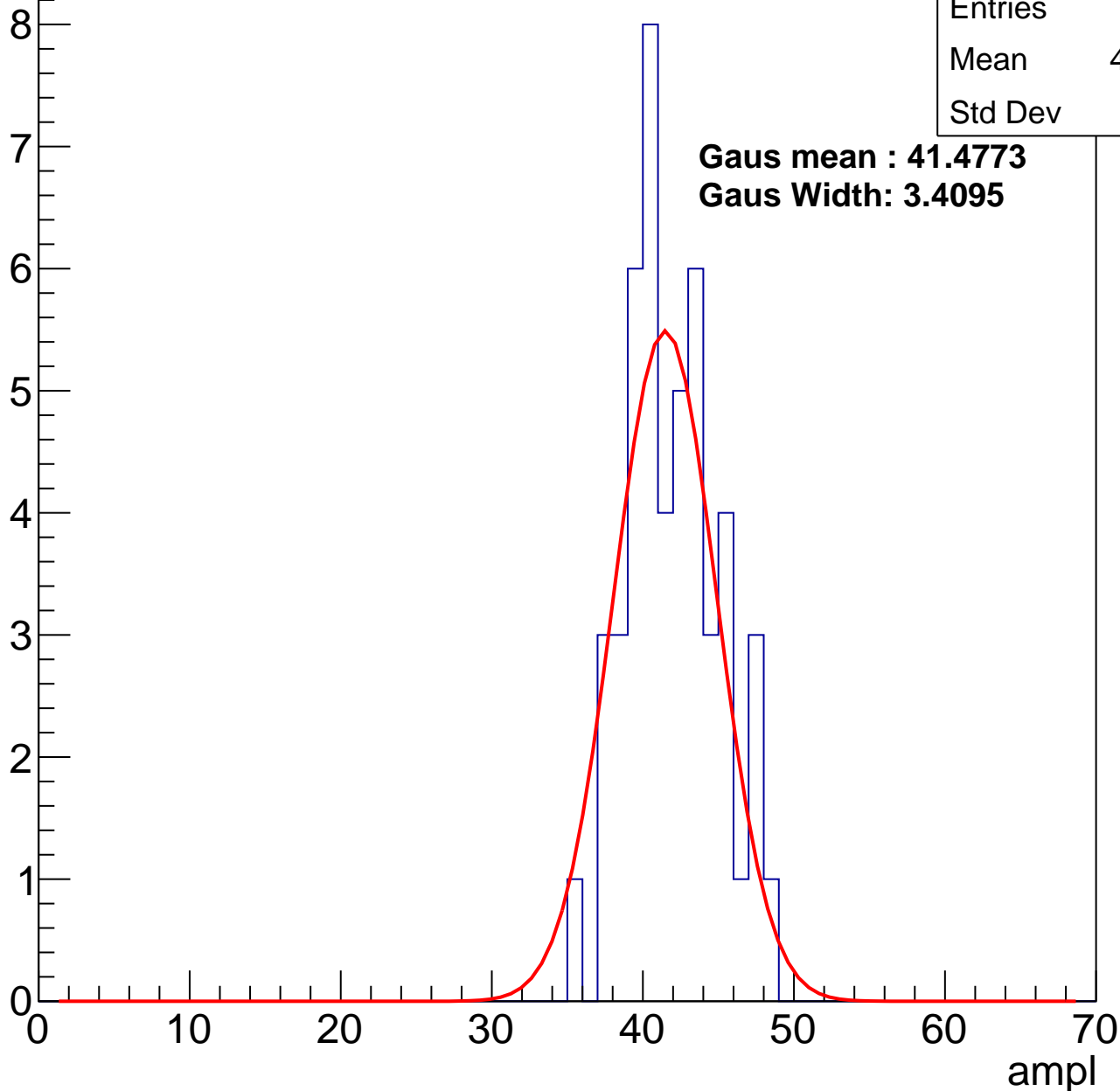
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	41.52
Std Dev	3

Gaus mean : 41.4773

Gaus Width: 3.4095

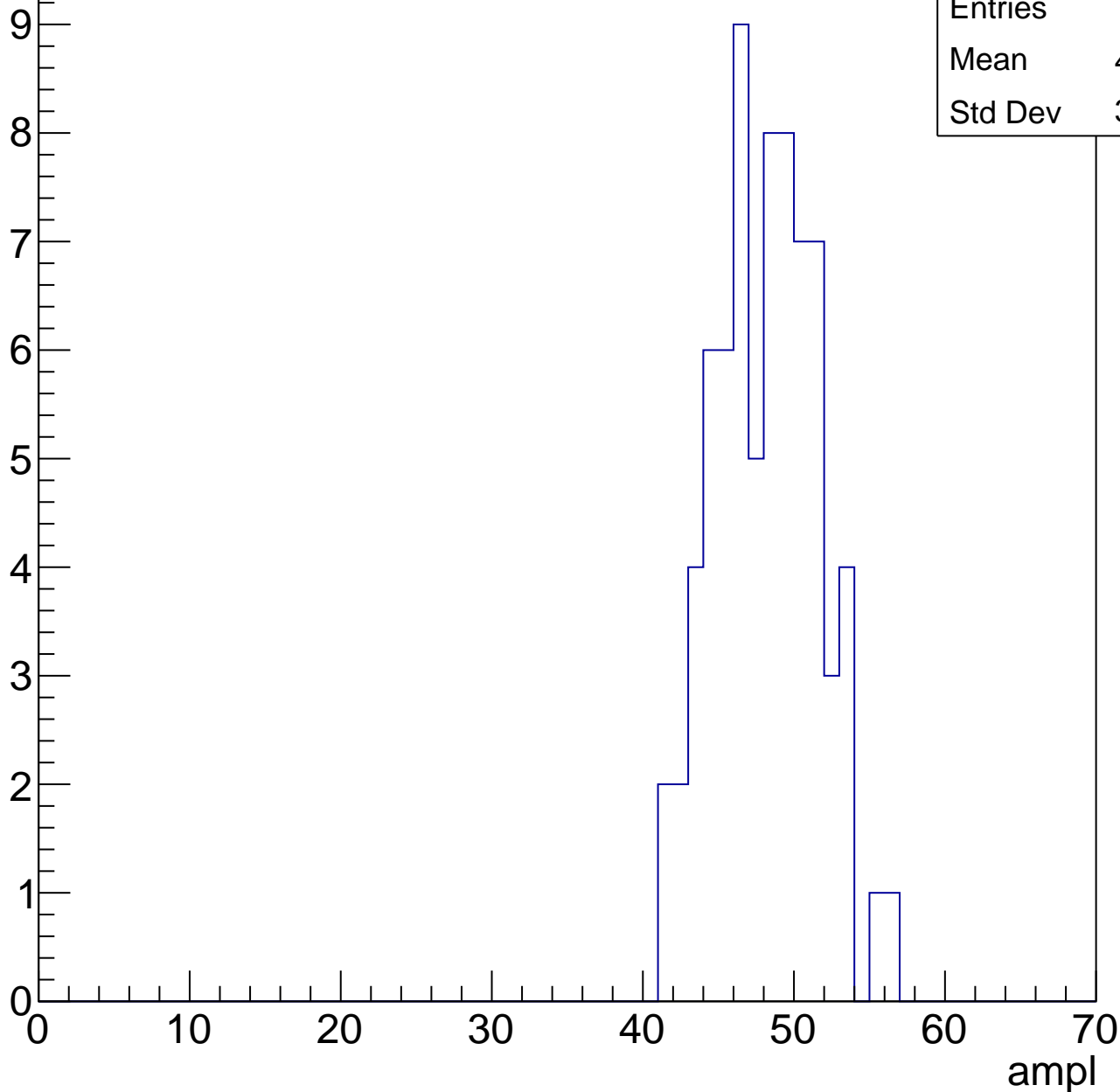


B1L003S, U20-ch82, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	47.71
Std Dev	3.341

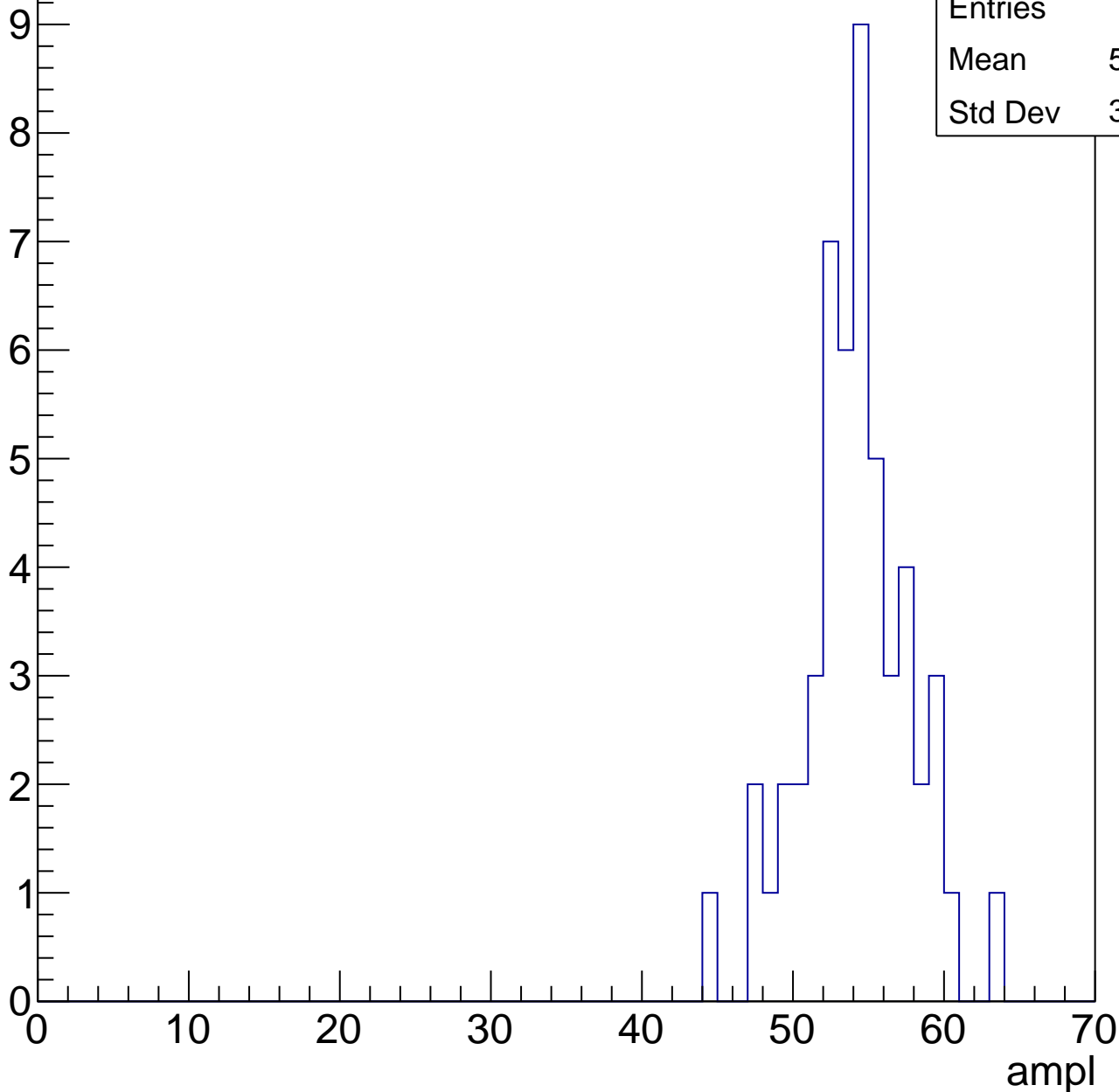


B1L003S, U20-ch82, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	53.69
Std Dev	3.533

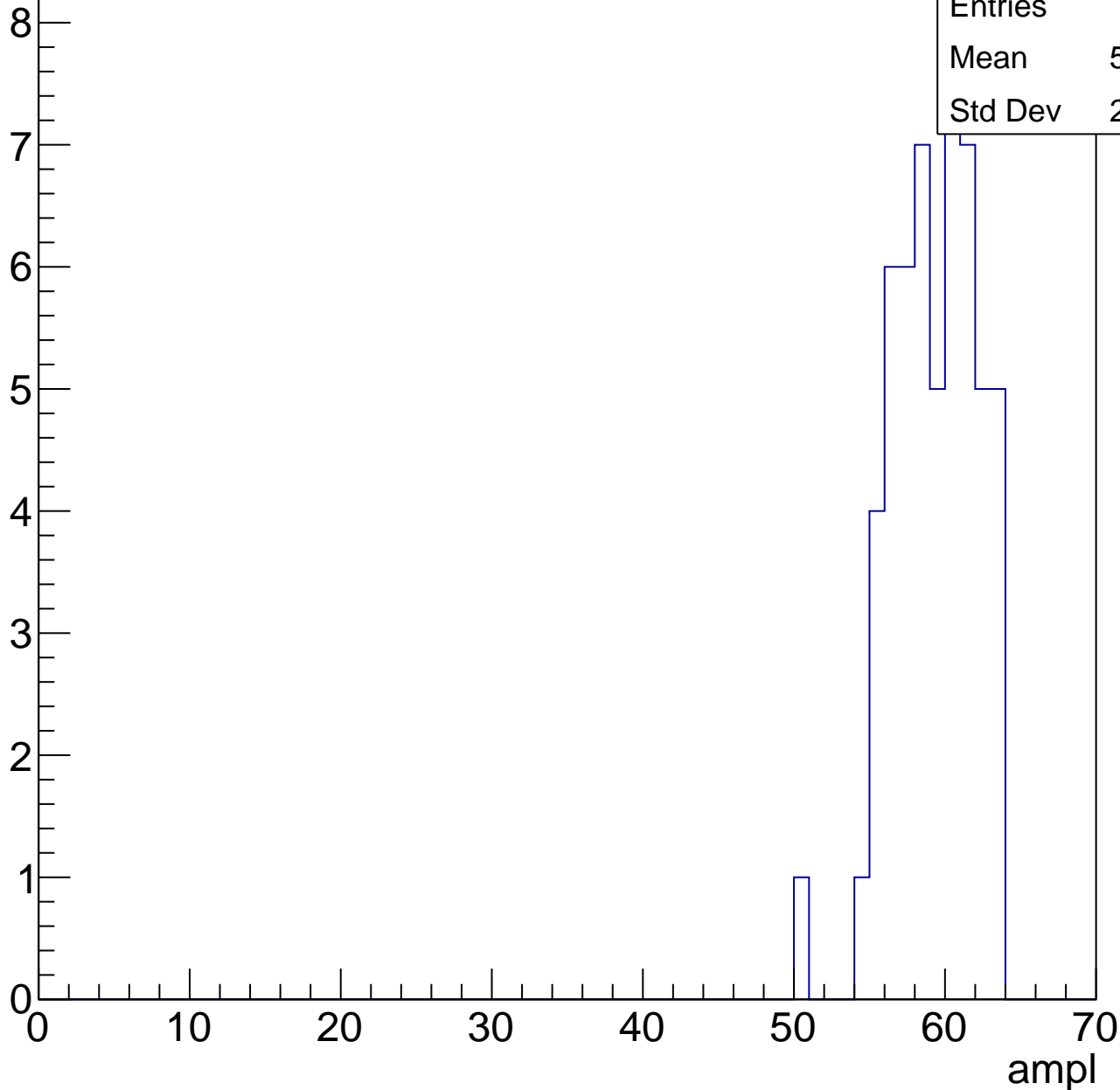


B1L003S, U20-ch82, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

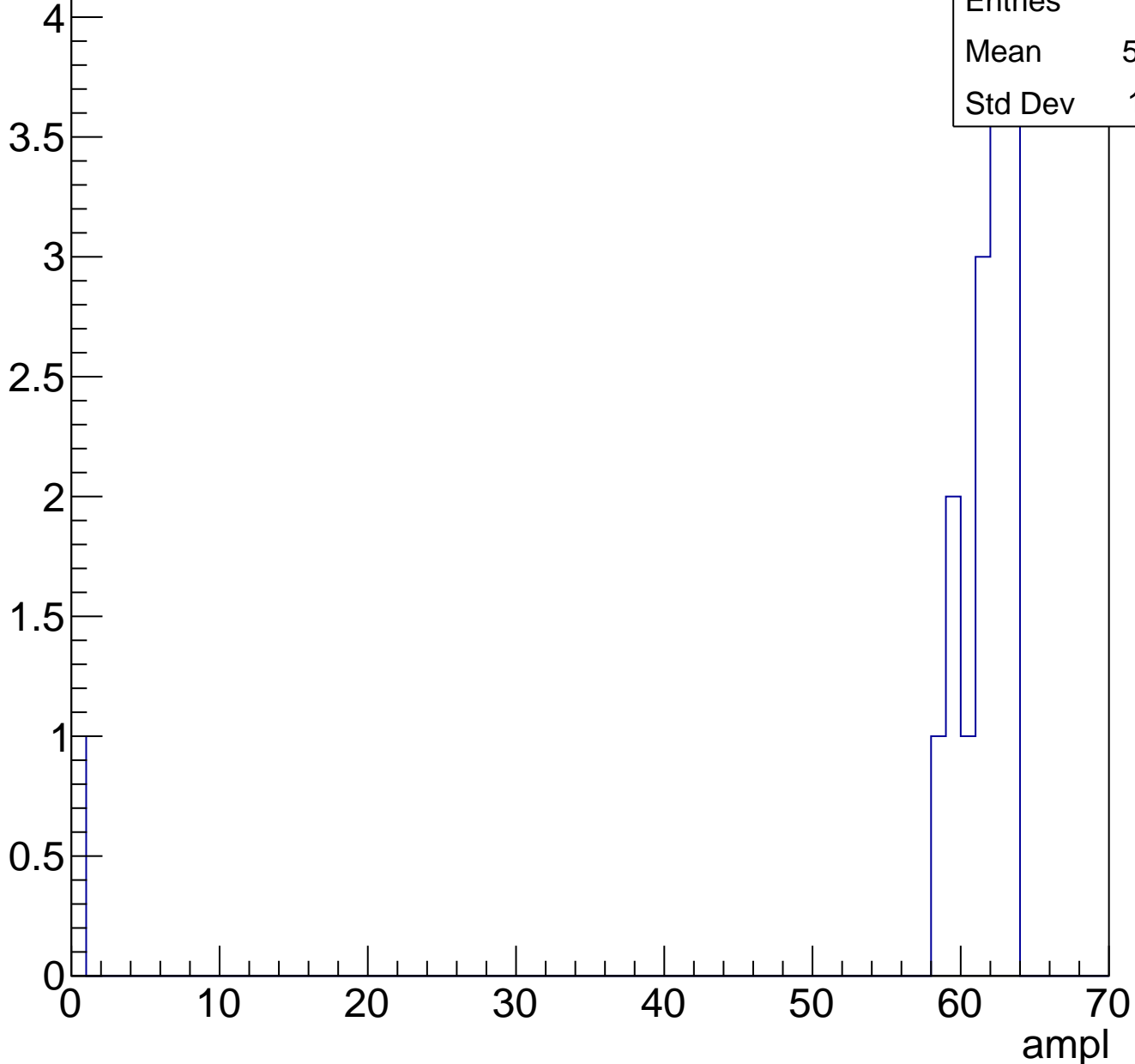
Entries	55
Mean	58.82
Std Dev	2.744



B1L003S, U20-ch82, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch82, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch83, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	28.48
Std Dev	4.796

Gaus mean : 29.3551

Gaus Width: 3.4207

Entry

10
8
6
4
2
0

ampl

0

10

20

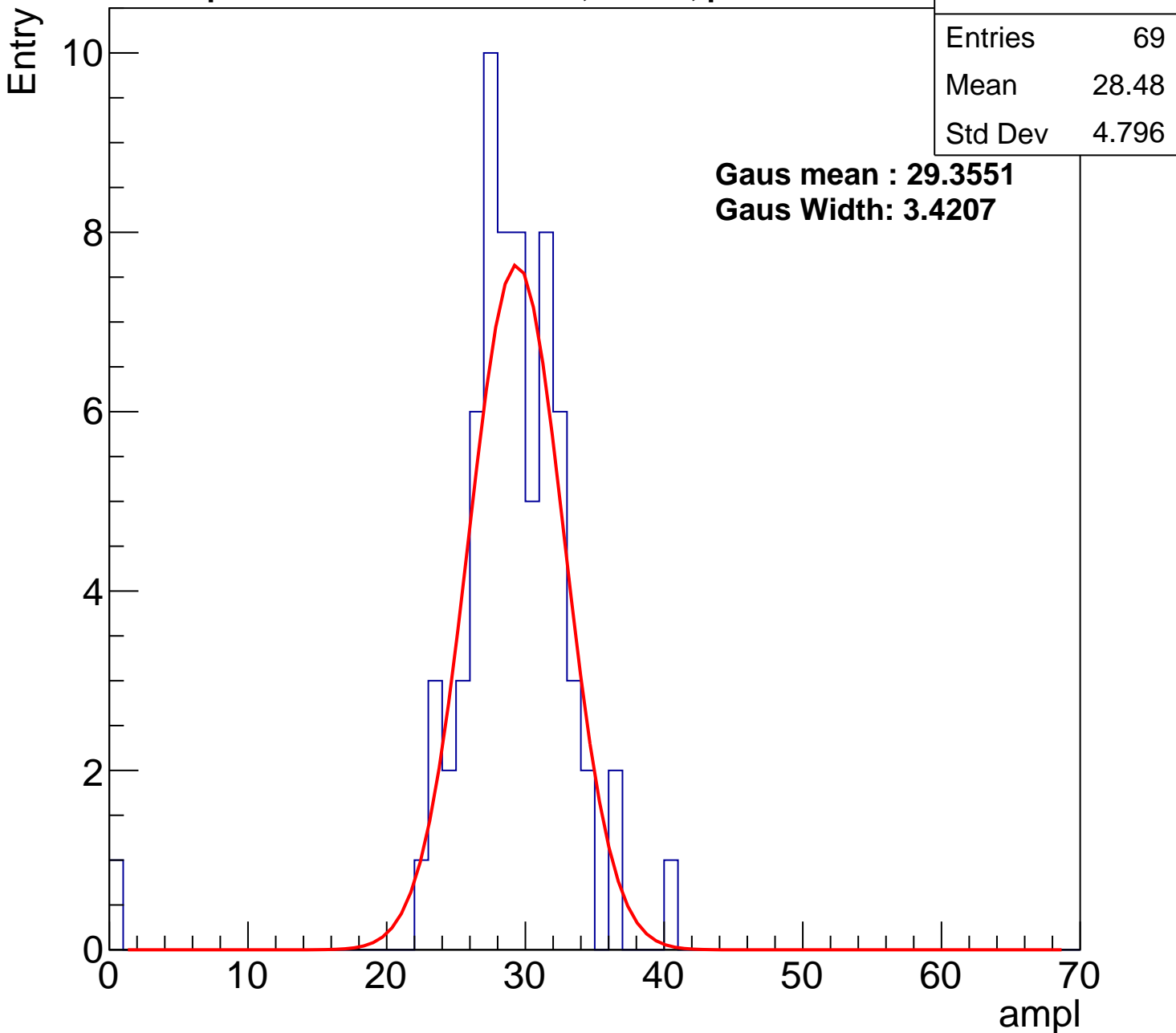
30

40

50

60

70



B1L003S, U20-ch83, adc1

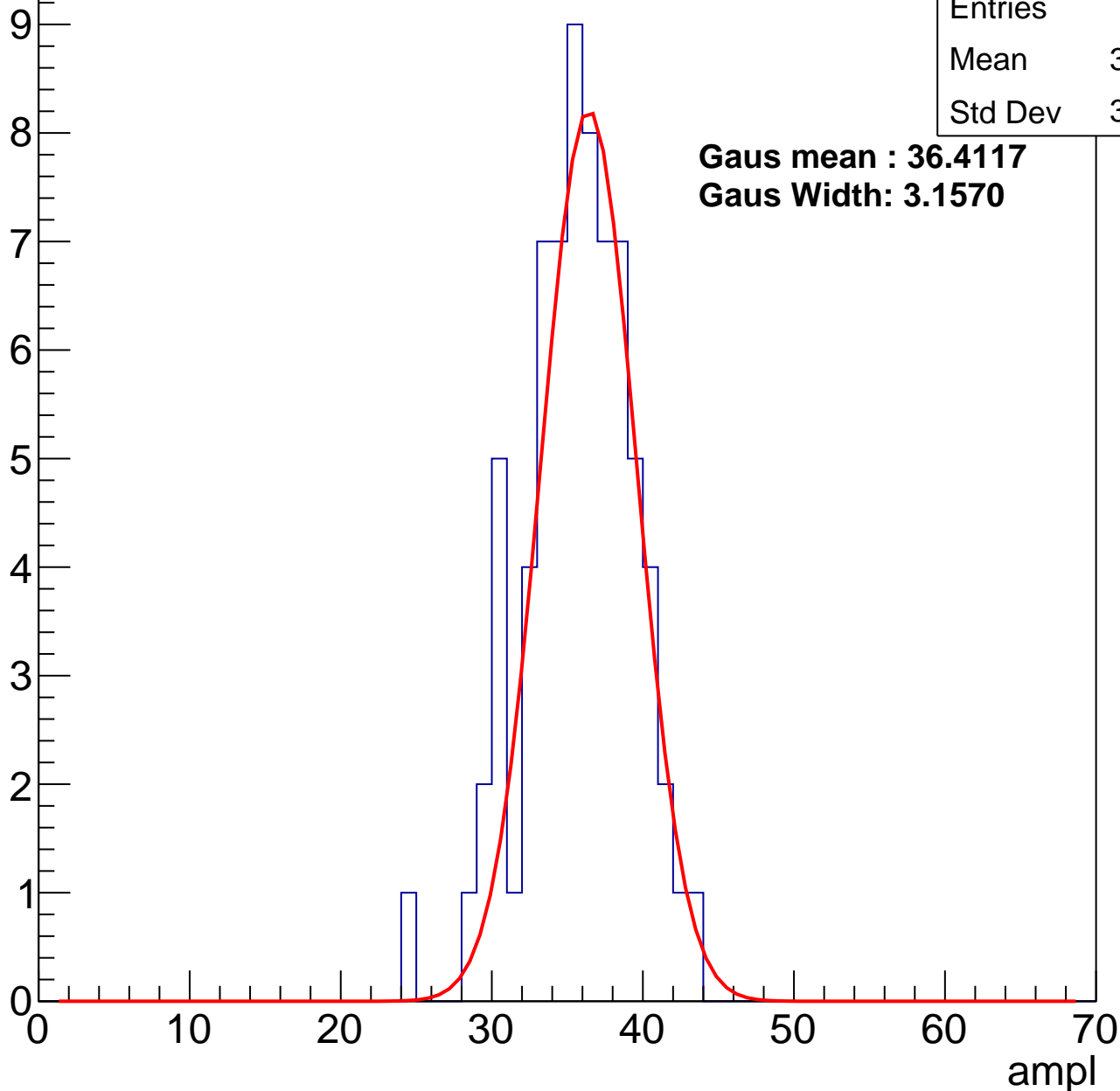
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.25
Std Dev	3.562

Gaus mean : 36.4117

Gaus Width: 3.1570



B1L003S, U20-ch83, adc2

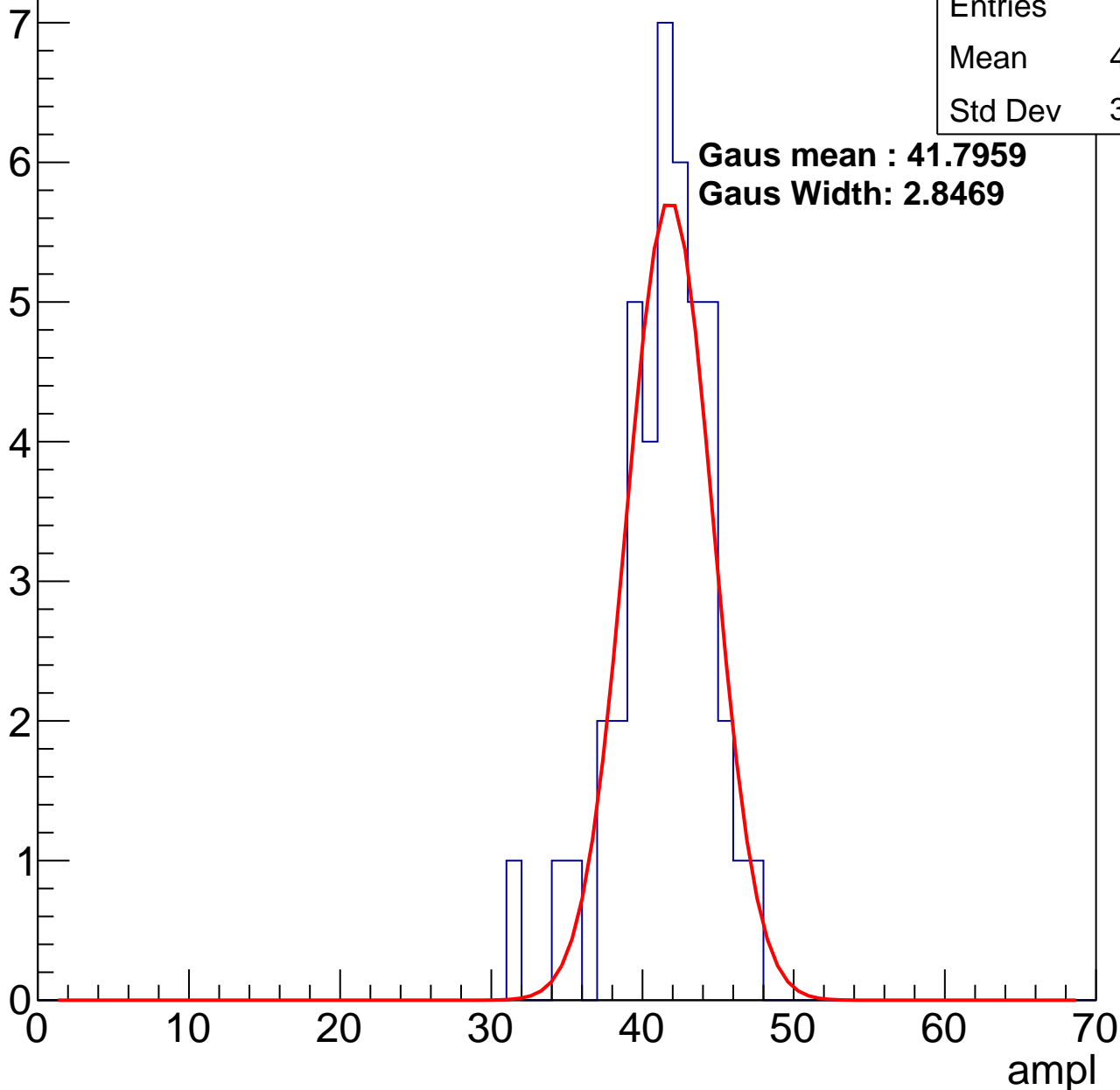
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	40.98
Std Dev	3.136

Gaus mean : 41.7959

Gaus Width: 2.8469

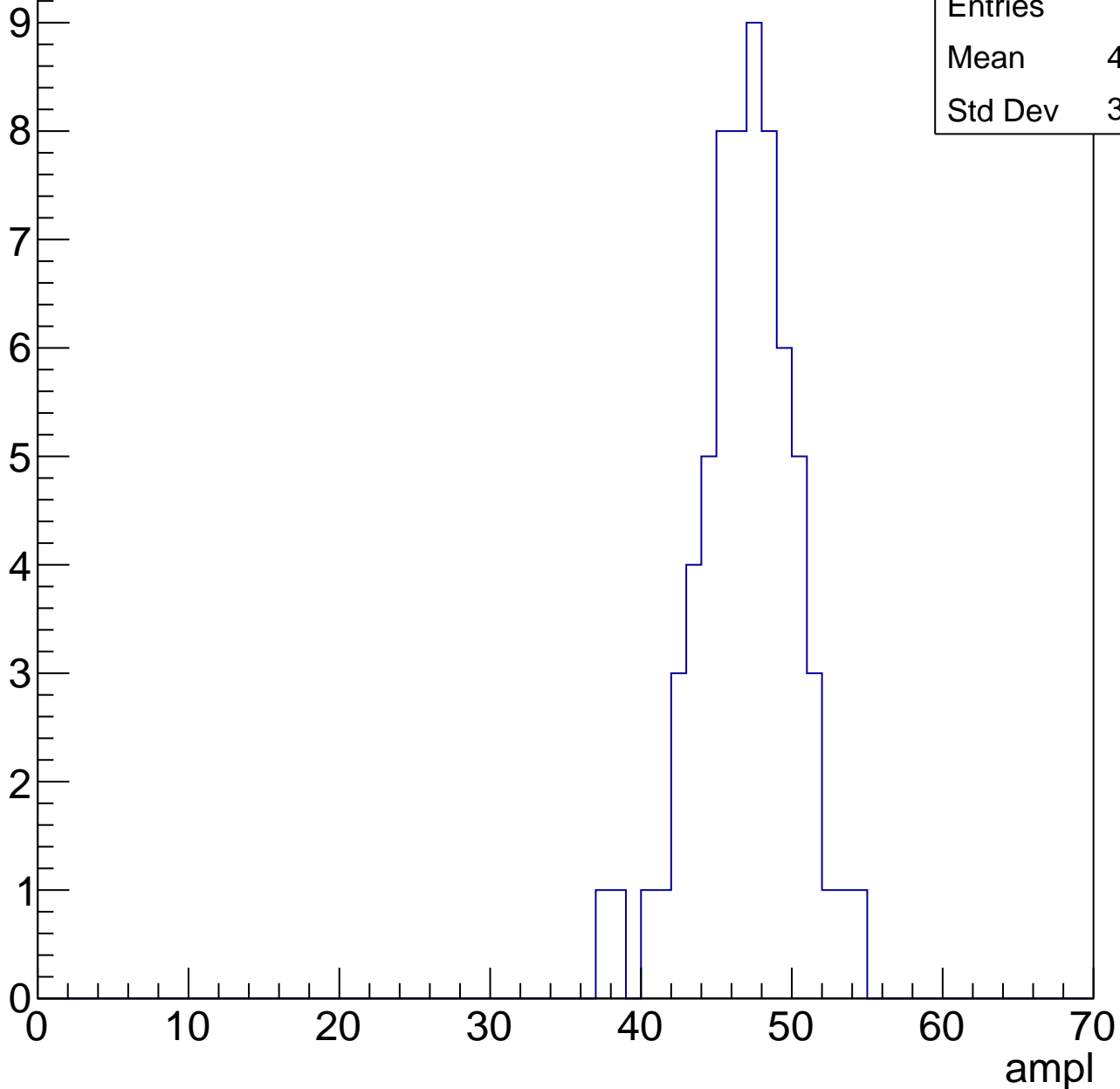


B1L003S, U20-ch83, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	46.44
Std Dev	3.267

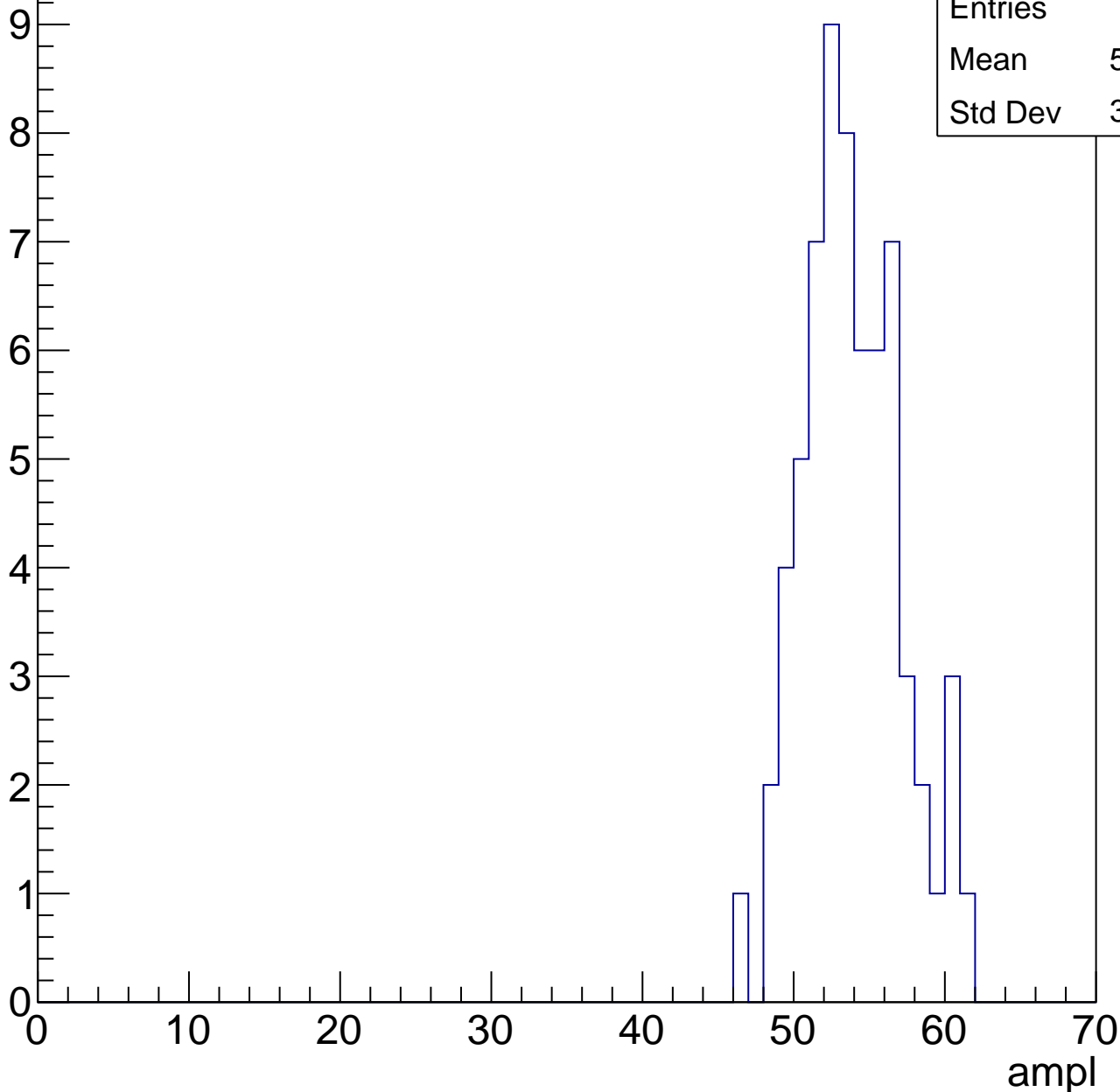


B1L003S, U20-ch83, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	53.38
Std Dev	3.219

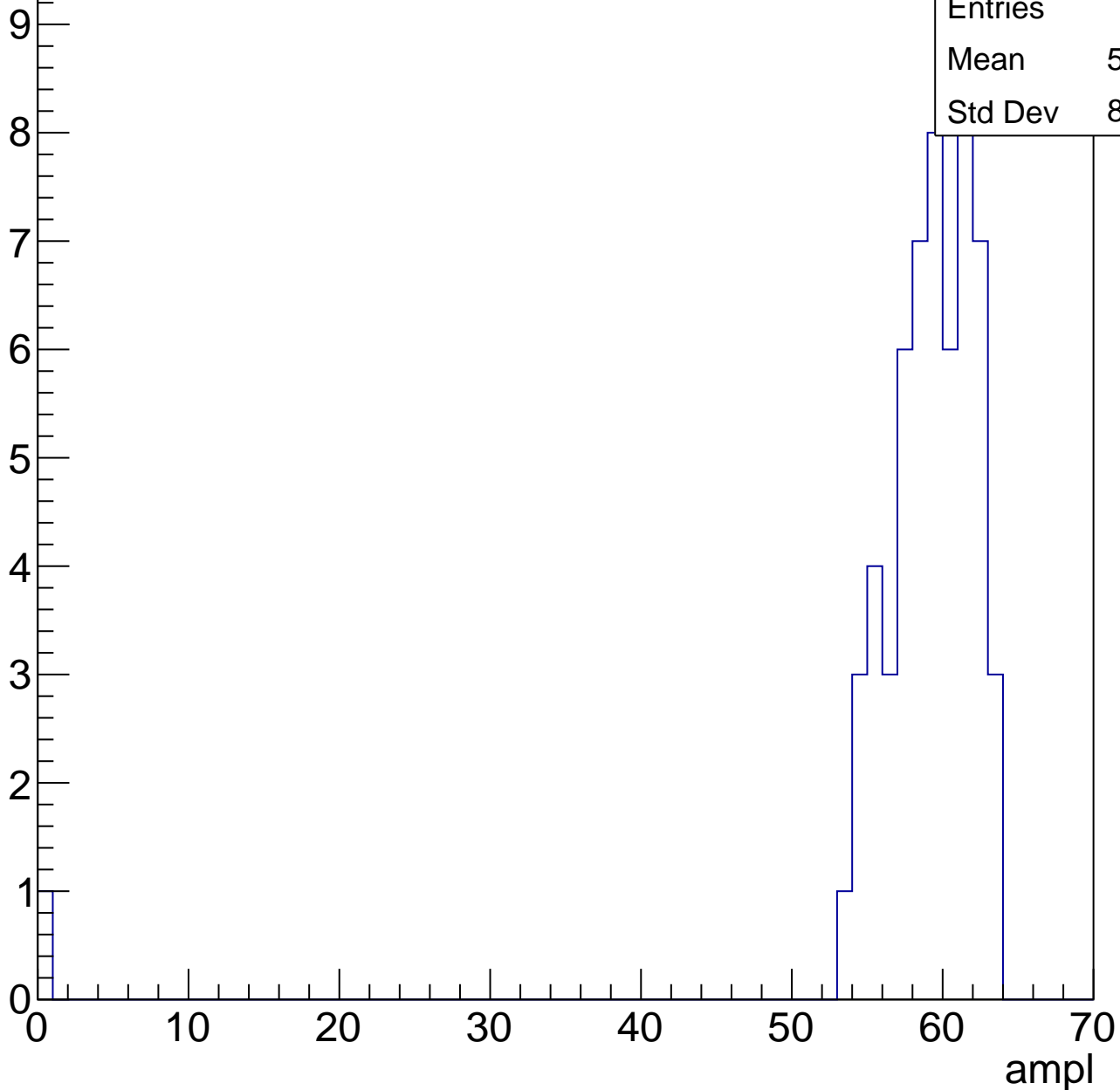


B1L003S, U20-ch83, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.84
Std Dev	8.083

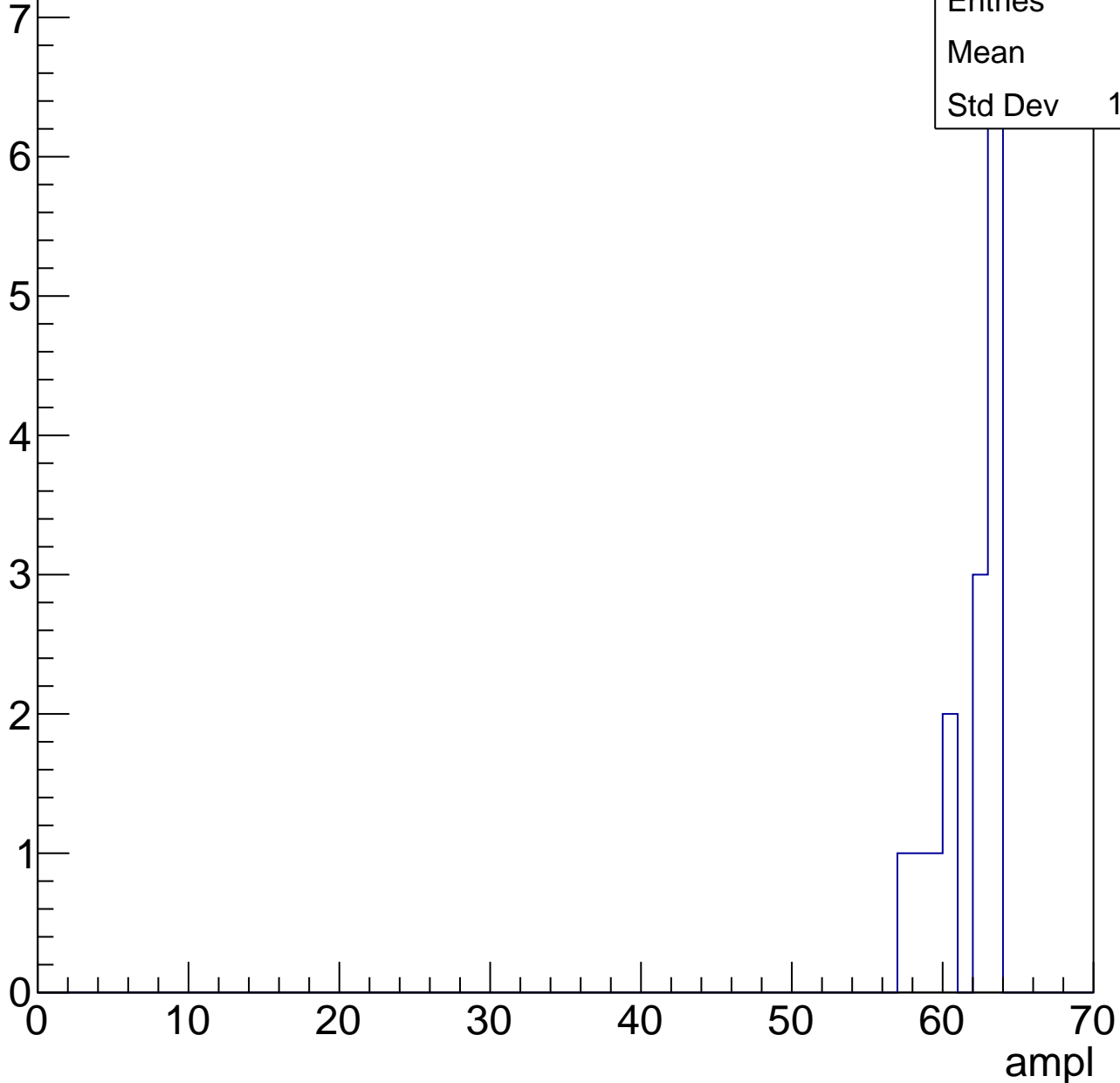


B1L003S, U20-ch83, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.4
Std Dev	1.993



B1L003S, U20-ch83, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch84, adc0

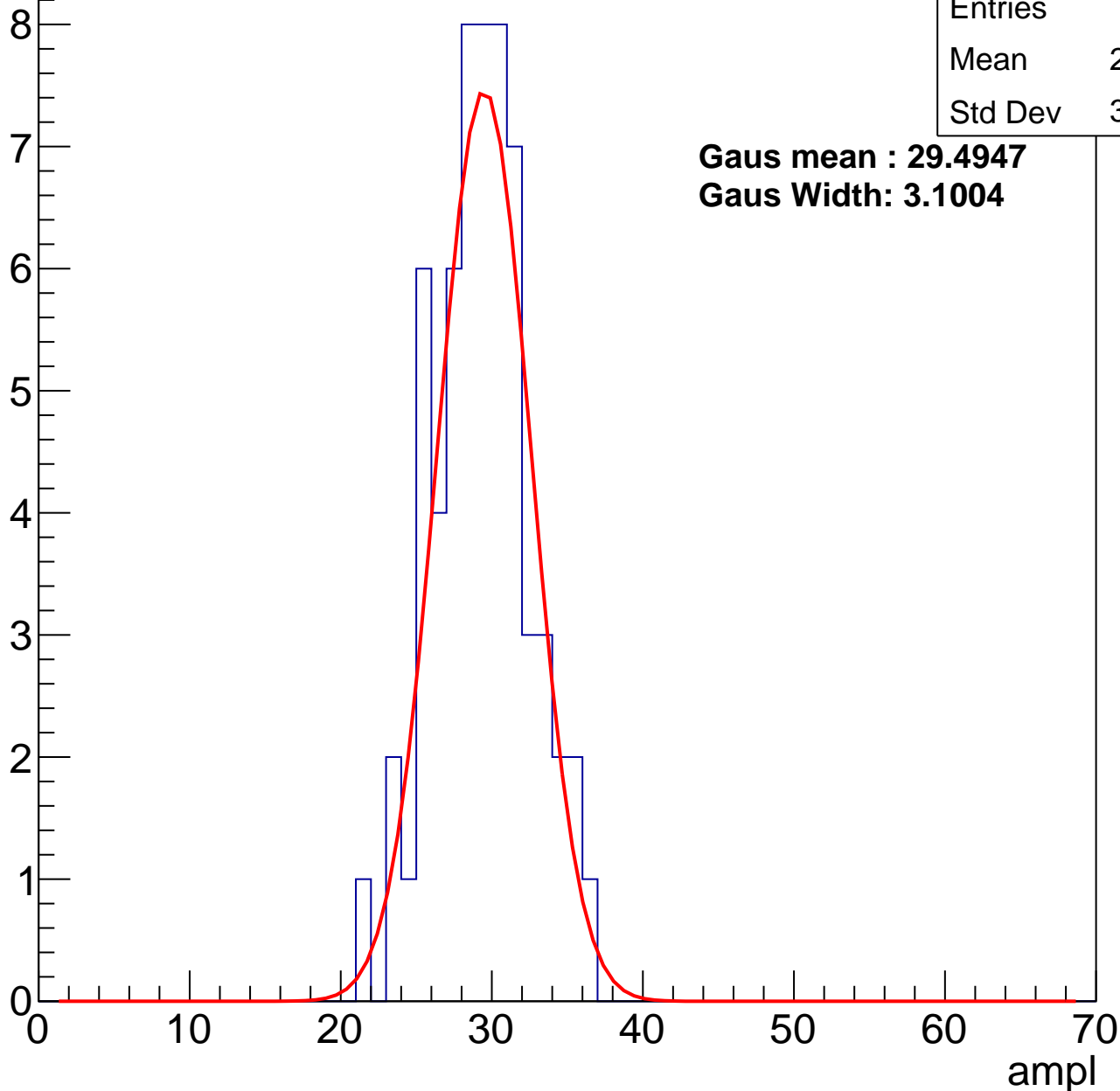
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	28.85
Std Dev	3.126

Gaus mean : 29.4947

Gaus Width: 3.1004



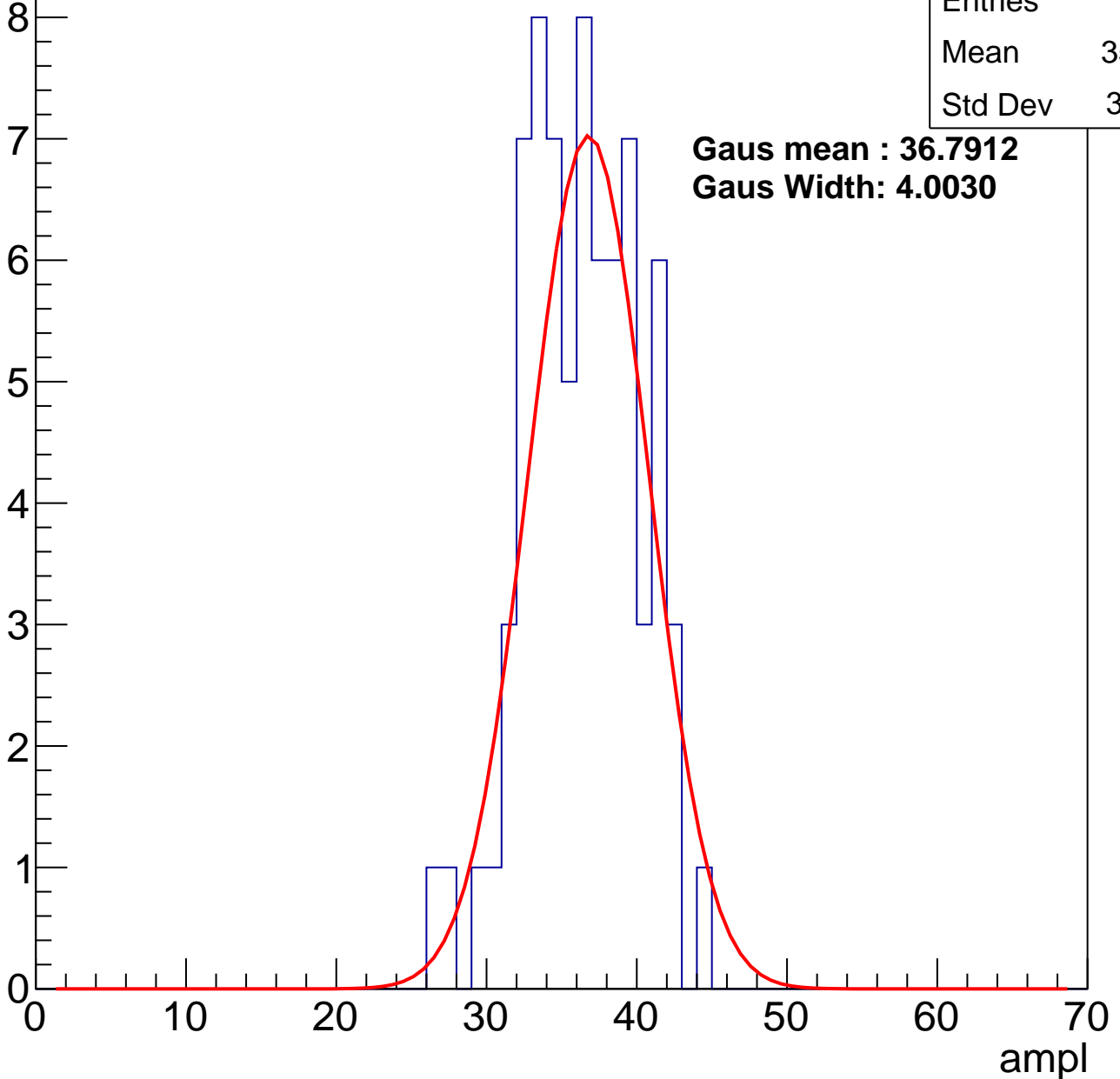
B1L003S, U20-ch84, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.85
Std Dev	3.711

Gaus mean : 36.7912
Gaus Width: 4.0030



B1L003S, U20-ch84, adc2

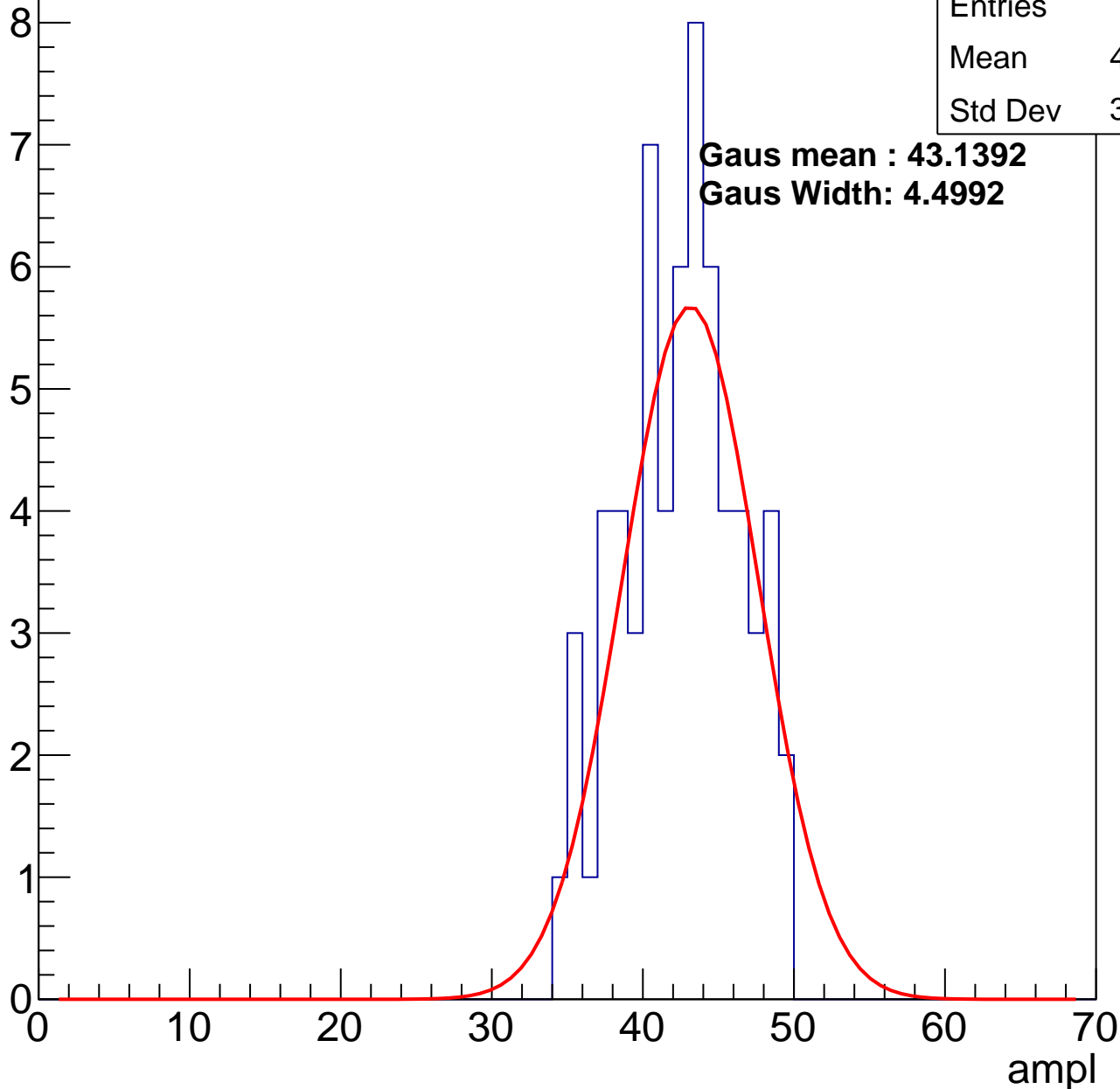
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.05
Std Dev	3.789

Gaus mean : 43.1392

Gaus Width: 4.4992

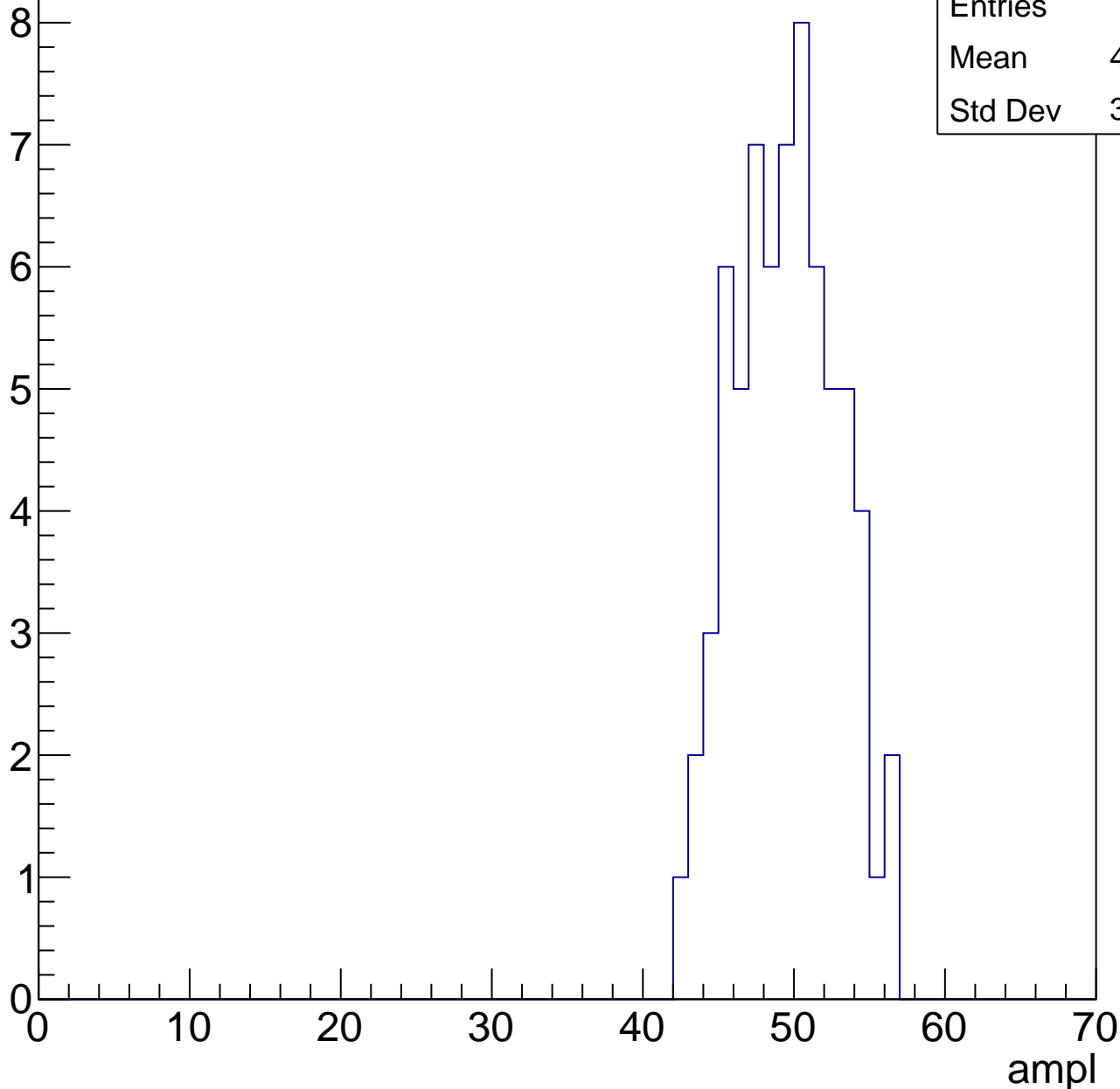


B1L003S, U20-ch84, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.03
Std Dev	3.347

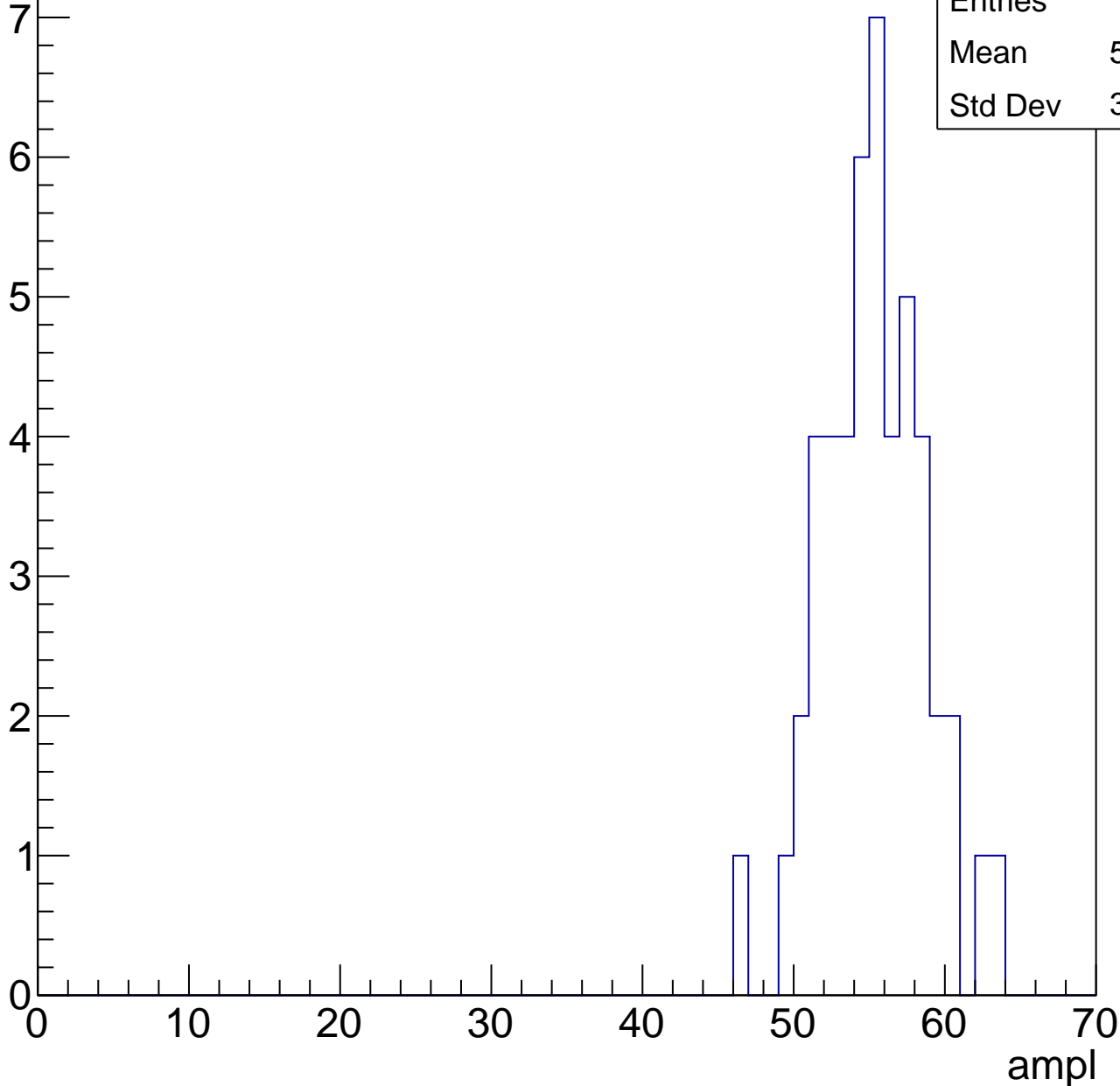


B1L003S, U20-ch84, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	54.83
Std Dev	3.362

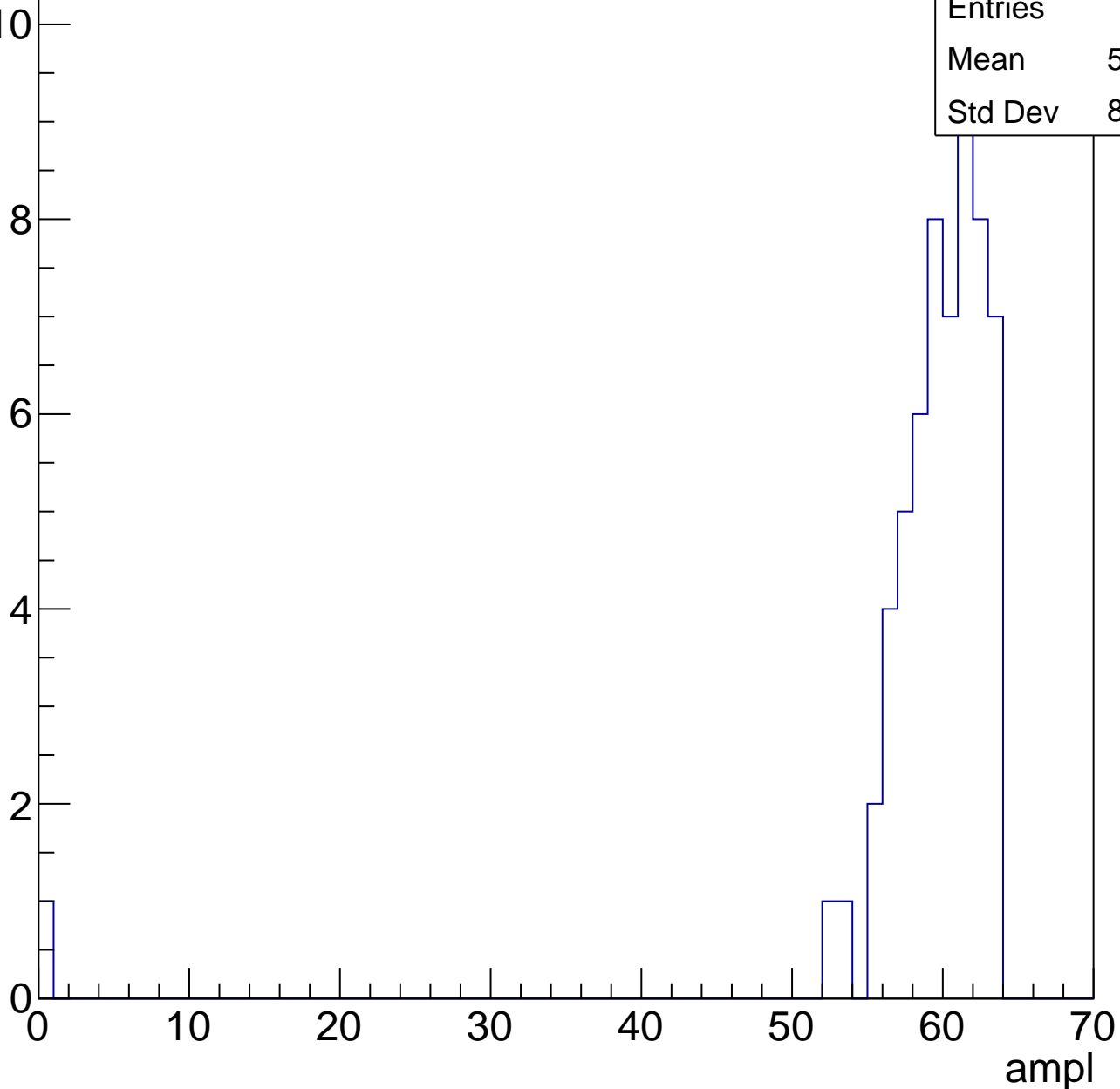


B1L003S, U20-ch84, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	58.52
Std Dev	8.038



B1L003S, U20-ch84, adc6

calib_packv5_042523_0143.root, FC#13, port D2

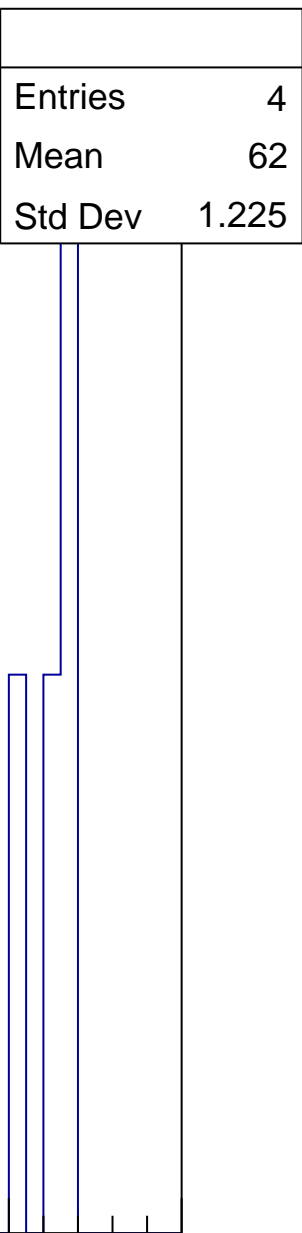
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch84, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch85, adc0

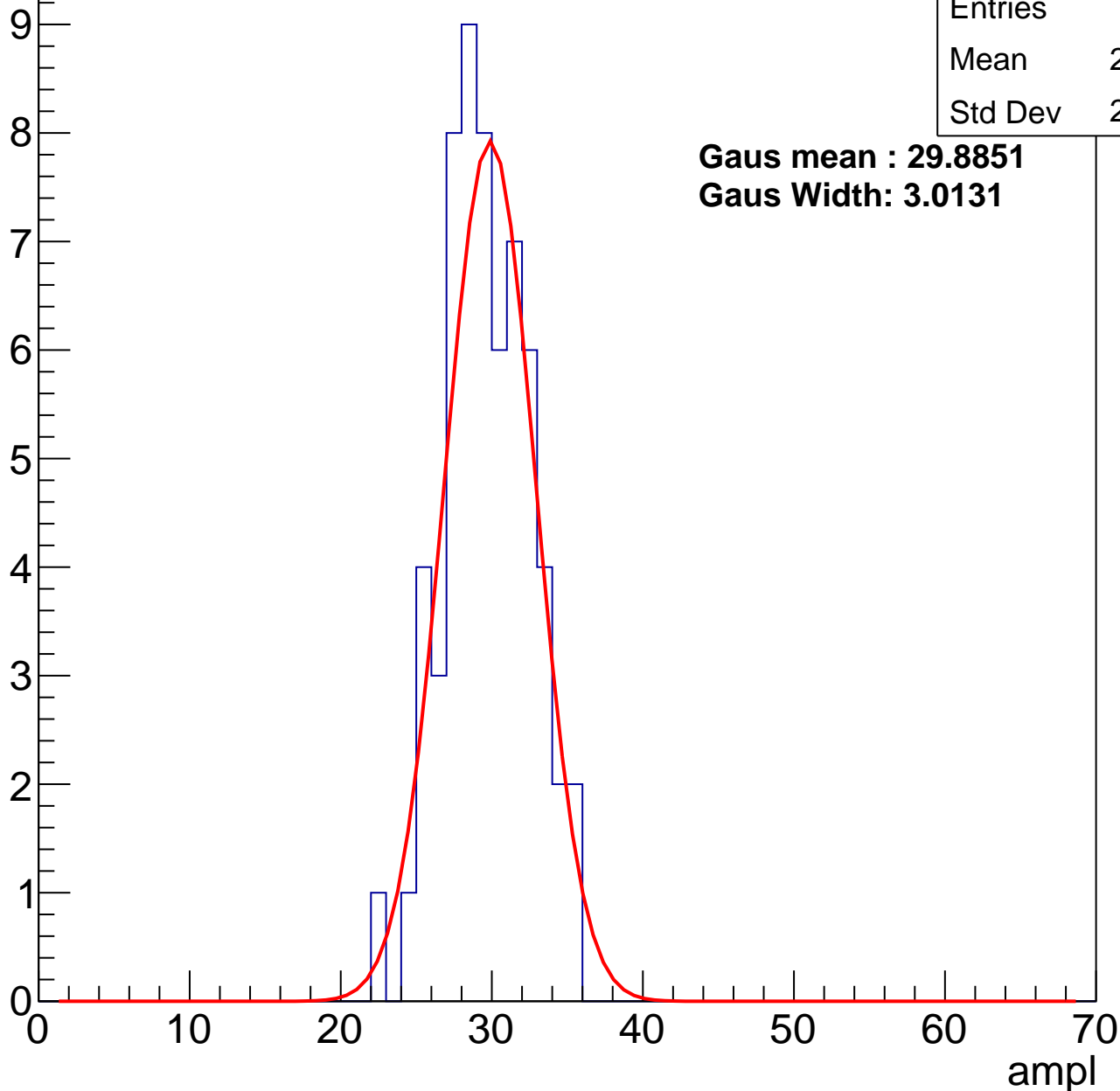
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.23
Std Dev	2.796

Gaus mean : 29.8851

Gaus Width: 3.0131



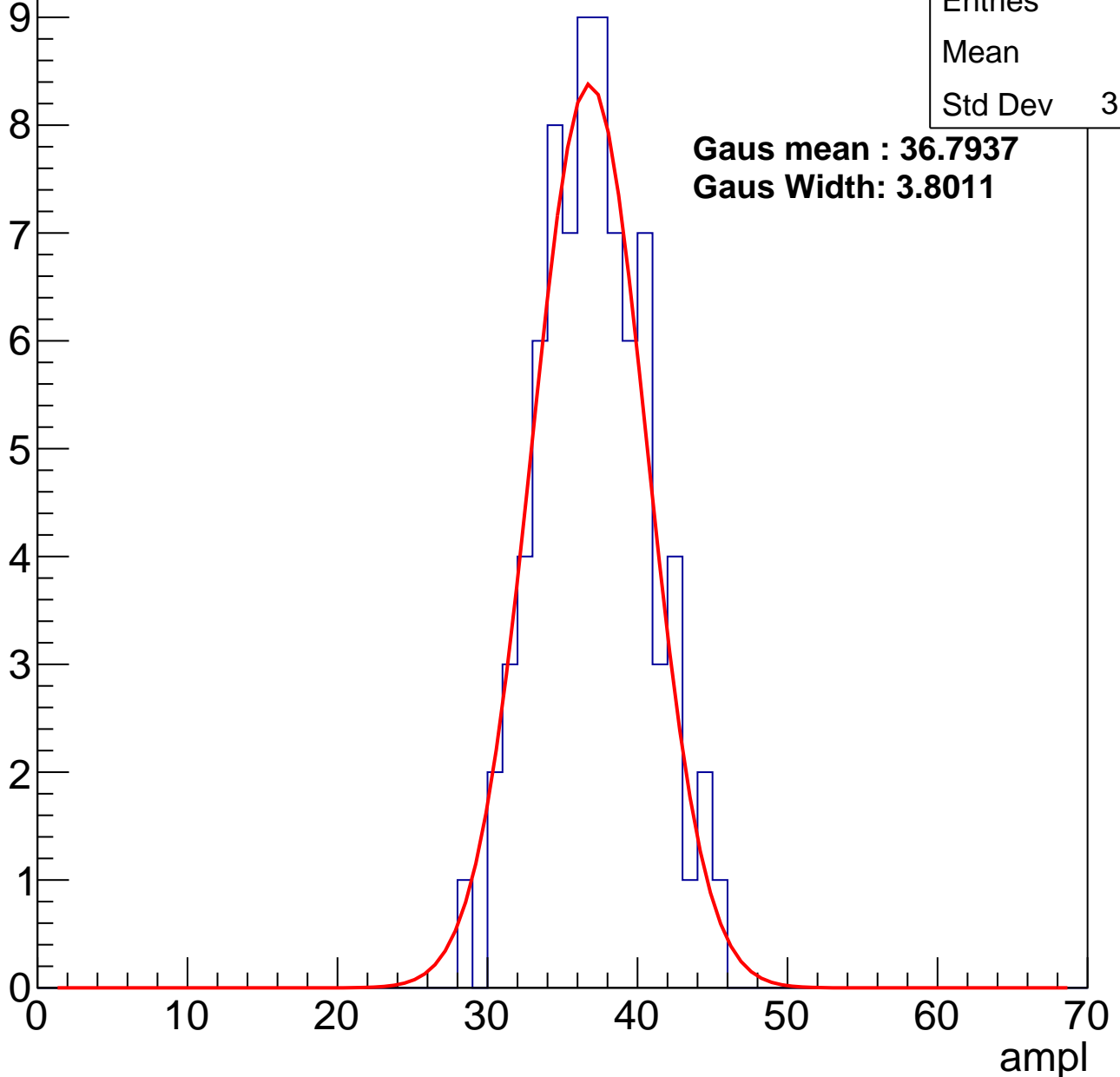
B1L003S, U20-ch85, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	36.6
Std Dev	3.569

Gaus mean : 36.7937
Gaus Width: 3.8011



B1L003S, U20-ch85, adc2

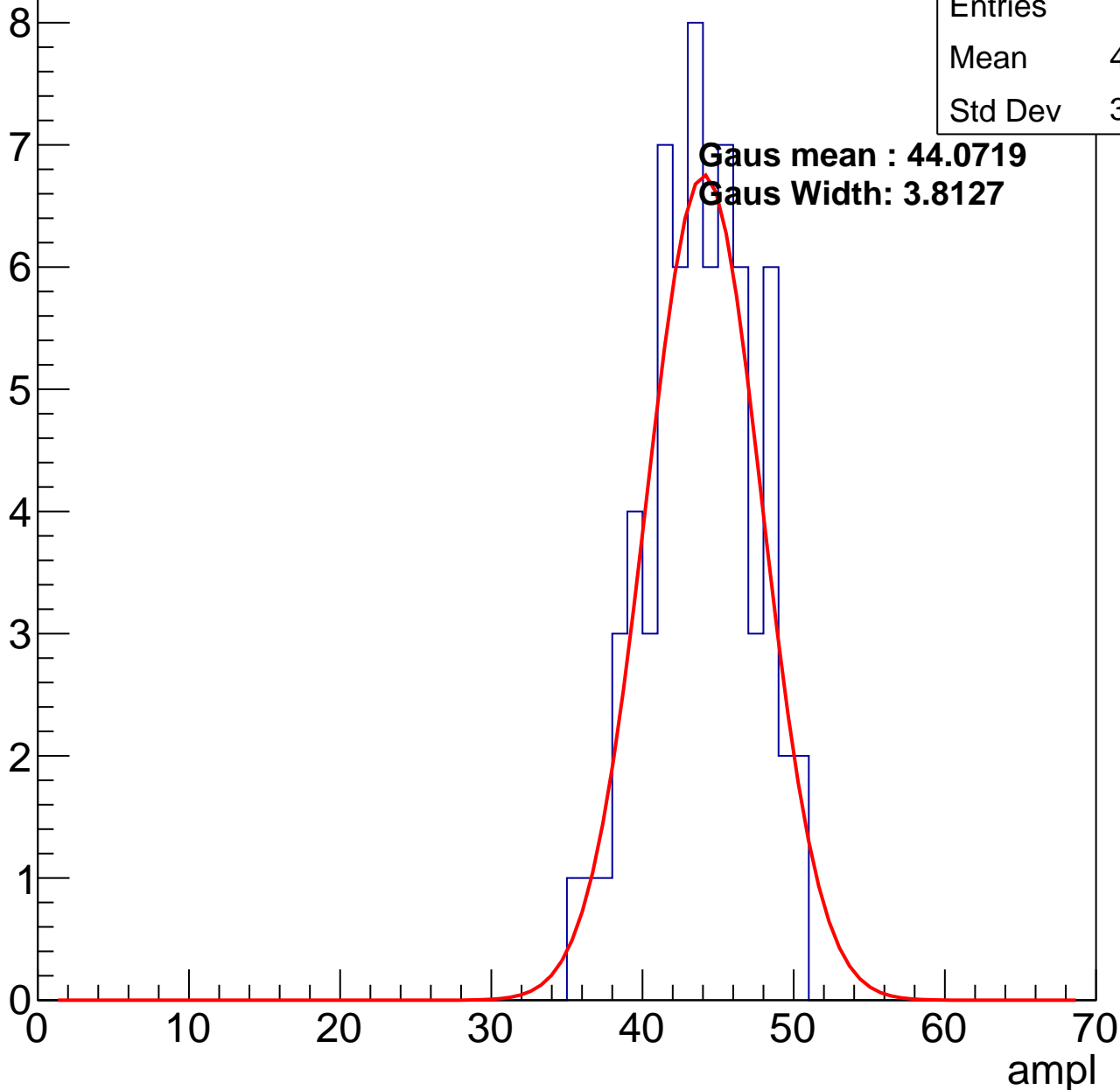
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	43.38
Std Dev	3.472

Gaus mean : 44.0719

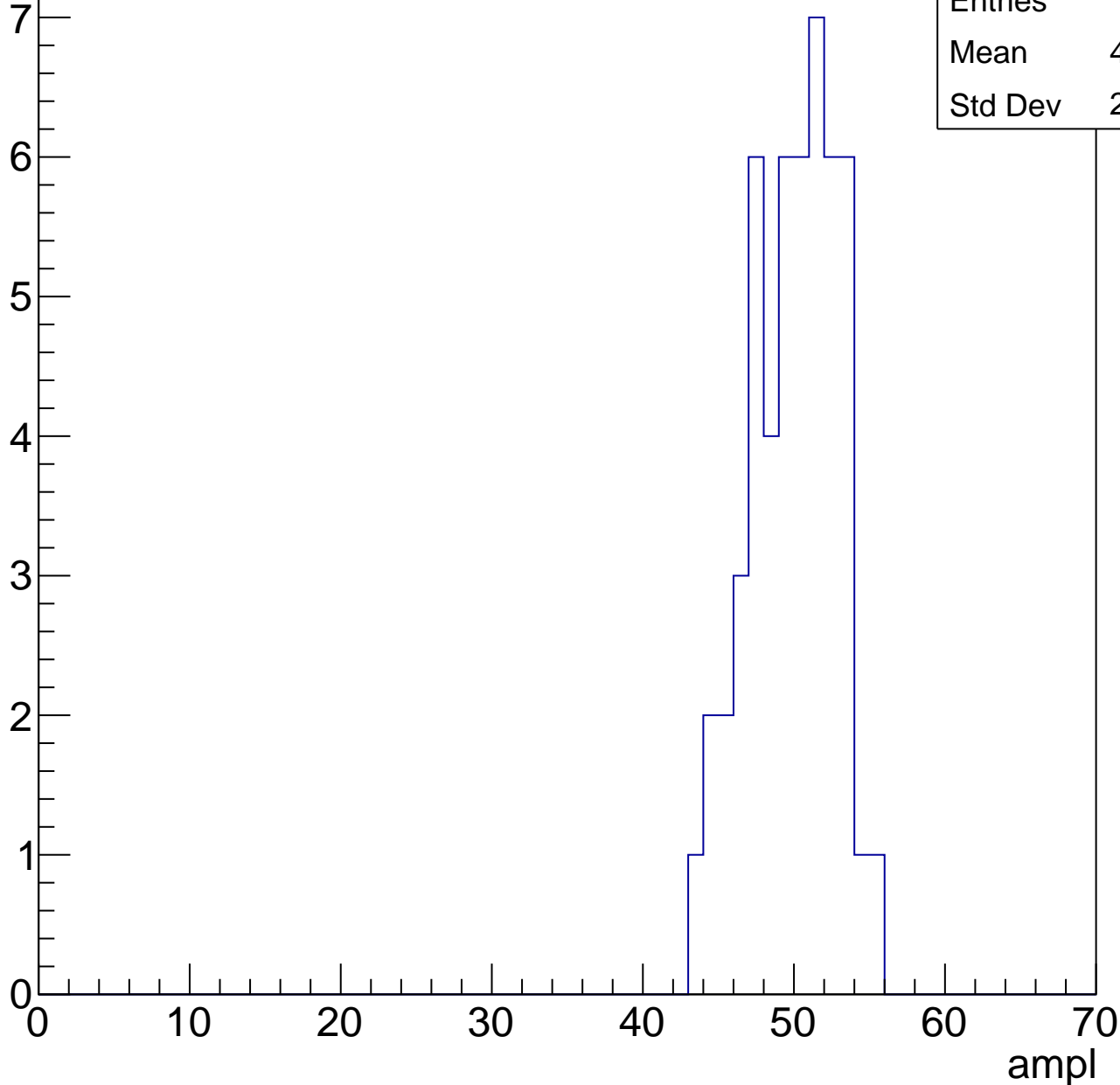
Gaus Width: 3.8127



B1L003S, U20-ch85, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



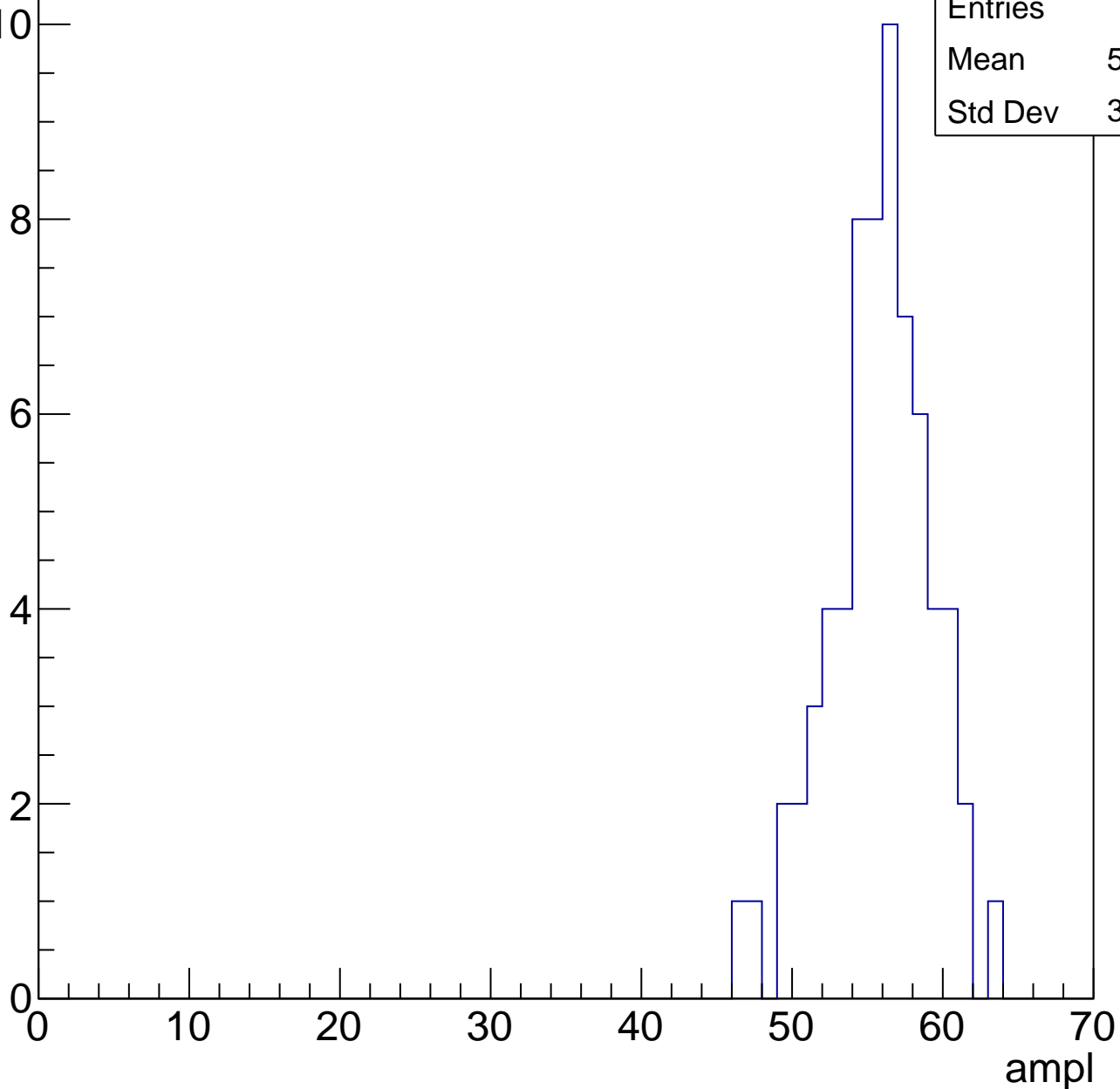
Entries	51
Mean	49.47
Std Dev	2.824

B1L003S, U20-ch85, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.28
Std Dev	3.372

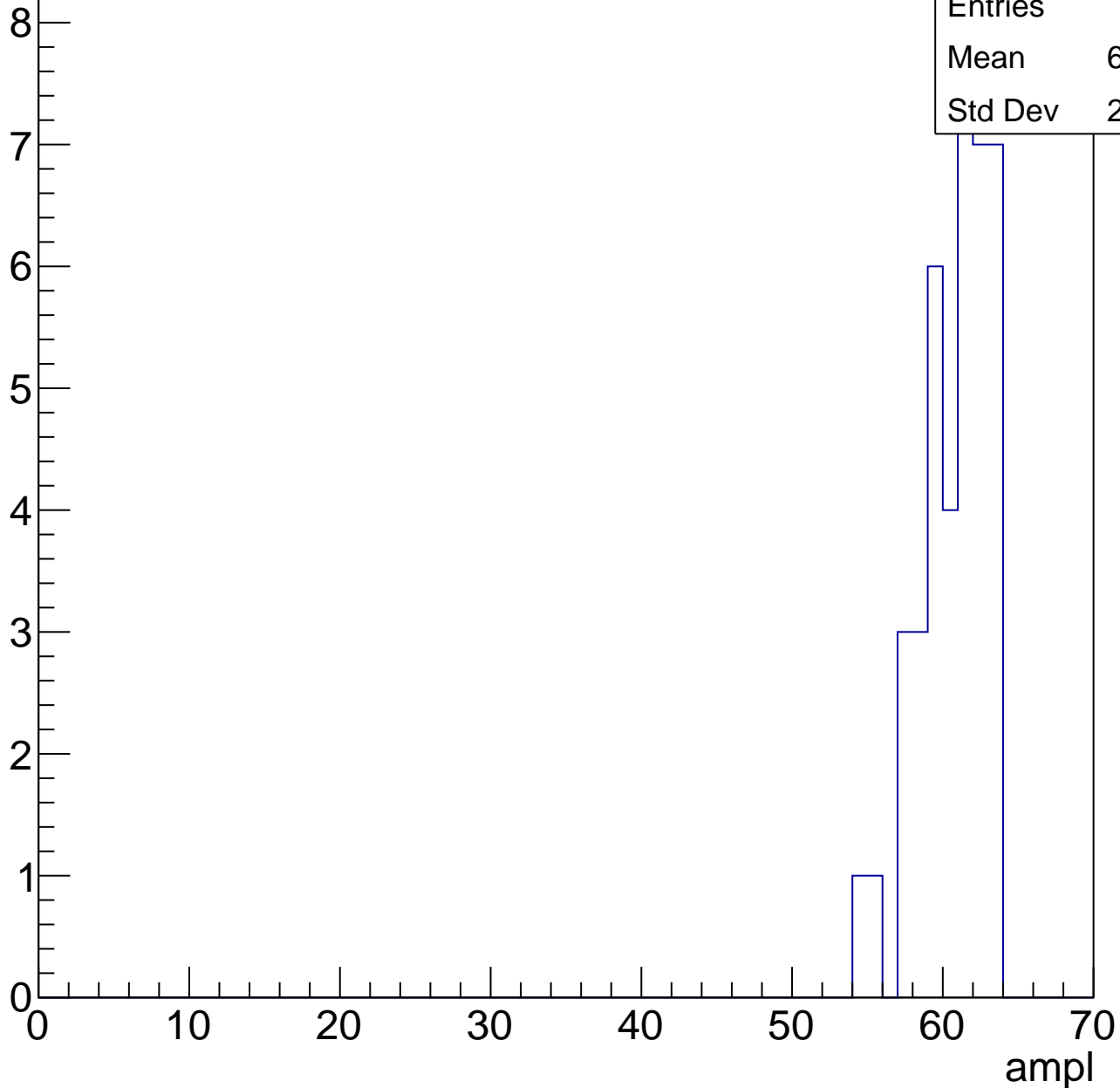


B1L003S, U20-ch85, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	60.27
Std Dev	2.247



B1L003S, U20-ch85, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

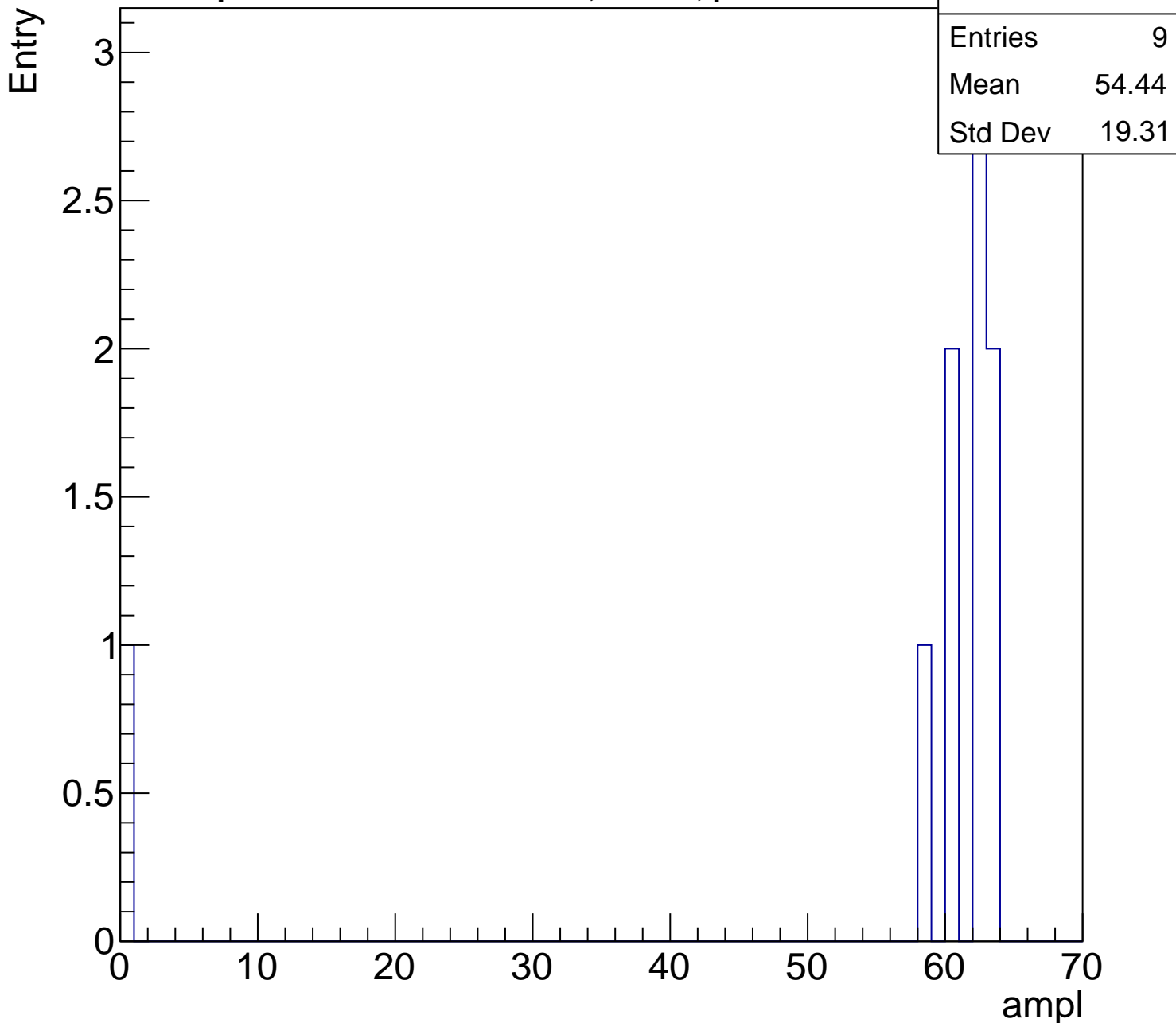
Mean

54.44

Std Dev

19.31

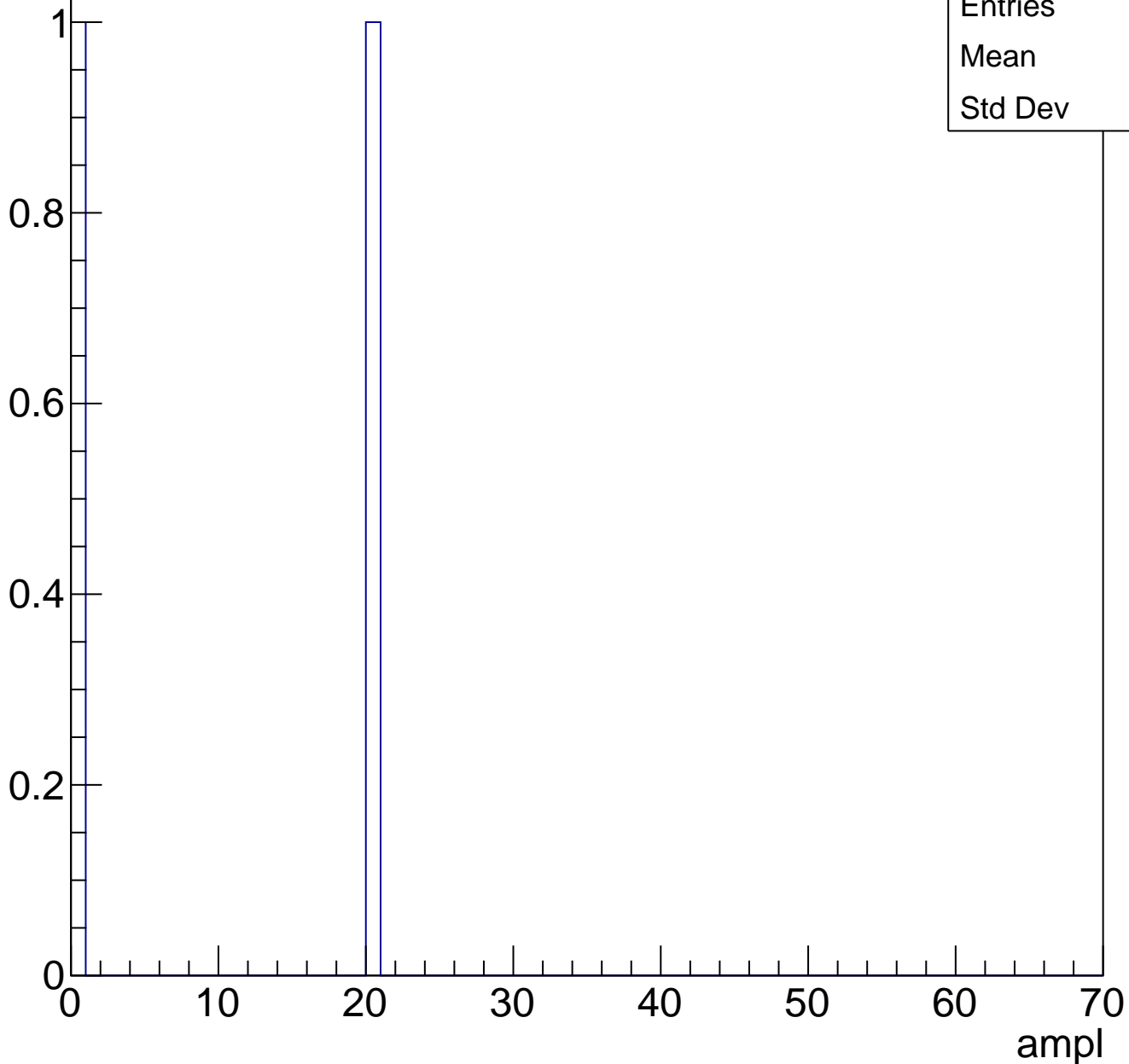
ampl



B1L003S, U20-ch85, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch86, adc0

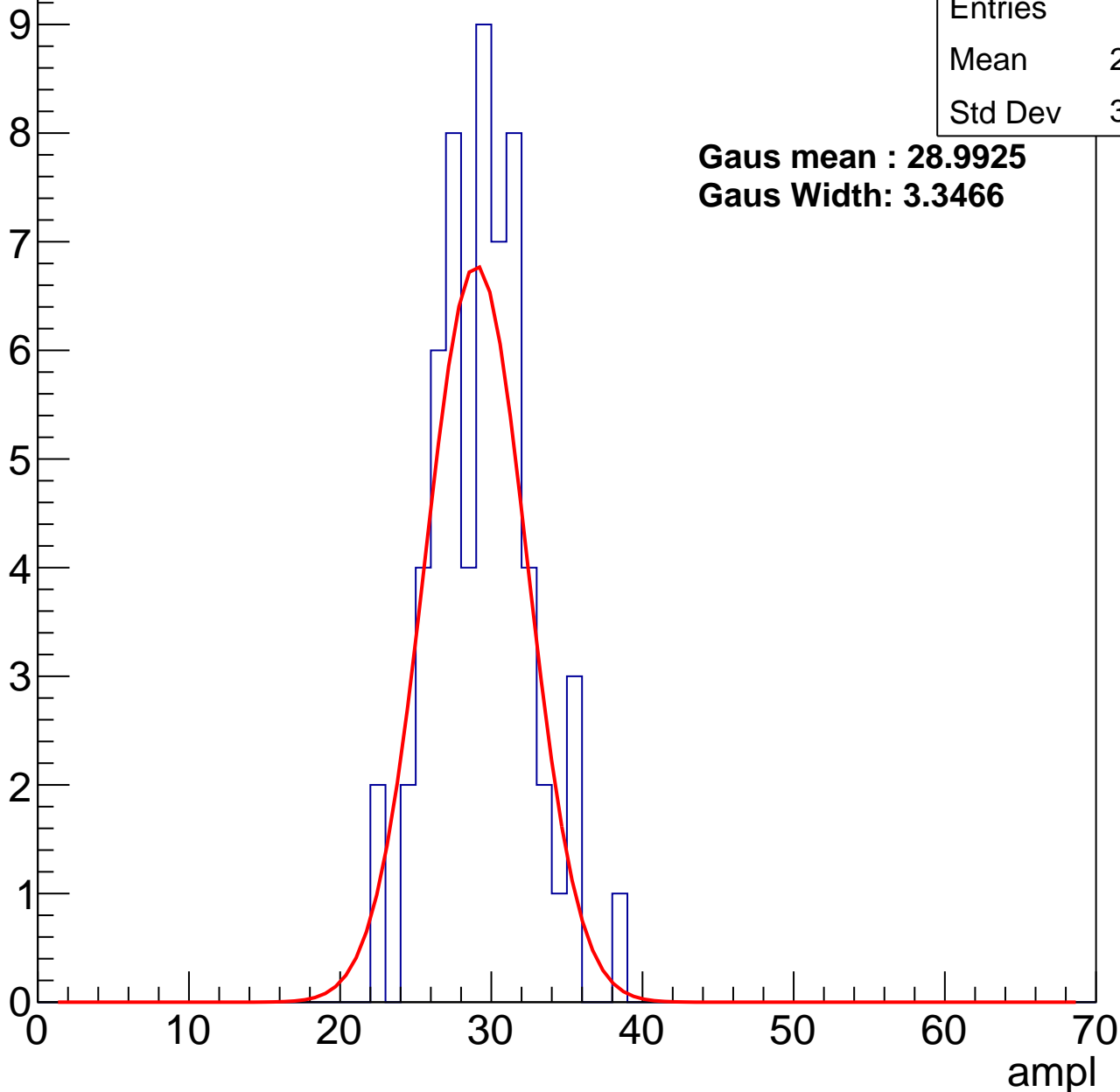
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.95
Std Dev	3.195

Gaus mean : 28.9925

Gaus Width: 3.3466



B1L003S, U20-ch86, adc1

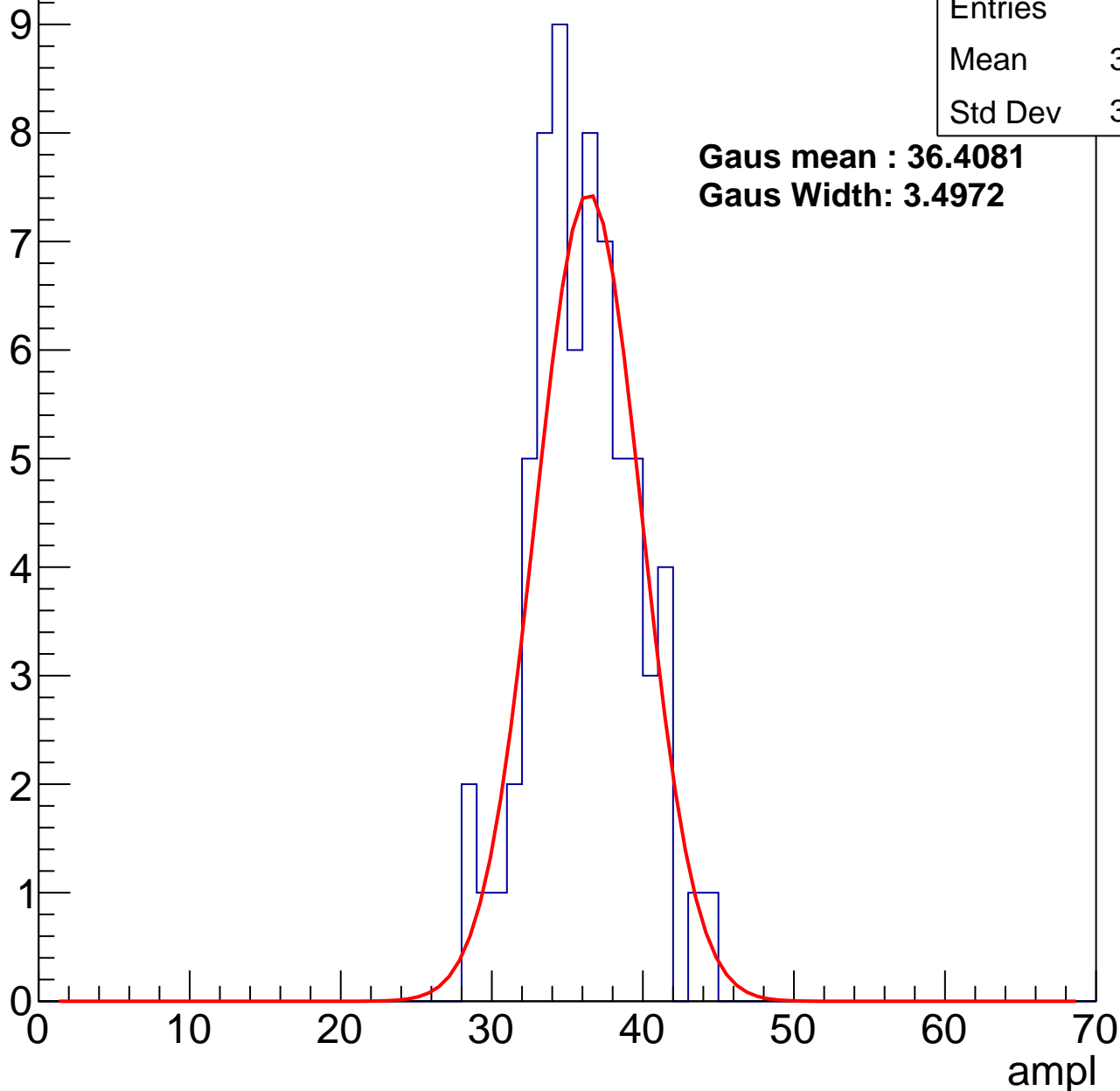
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.59
Std Dev	3.384

Gaus mean : 36.4081

Gaus Width: 3.4972



B1L003S, U20-ch86, adc2

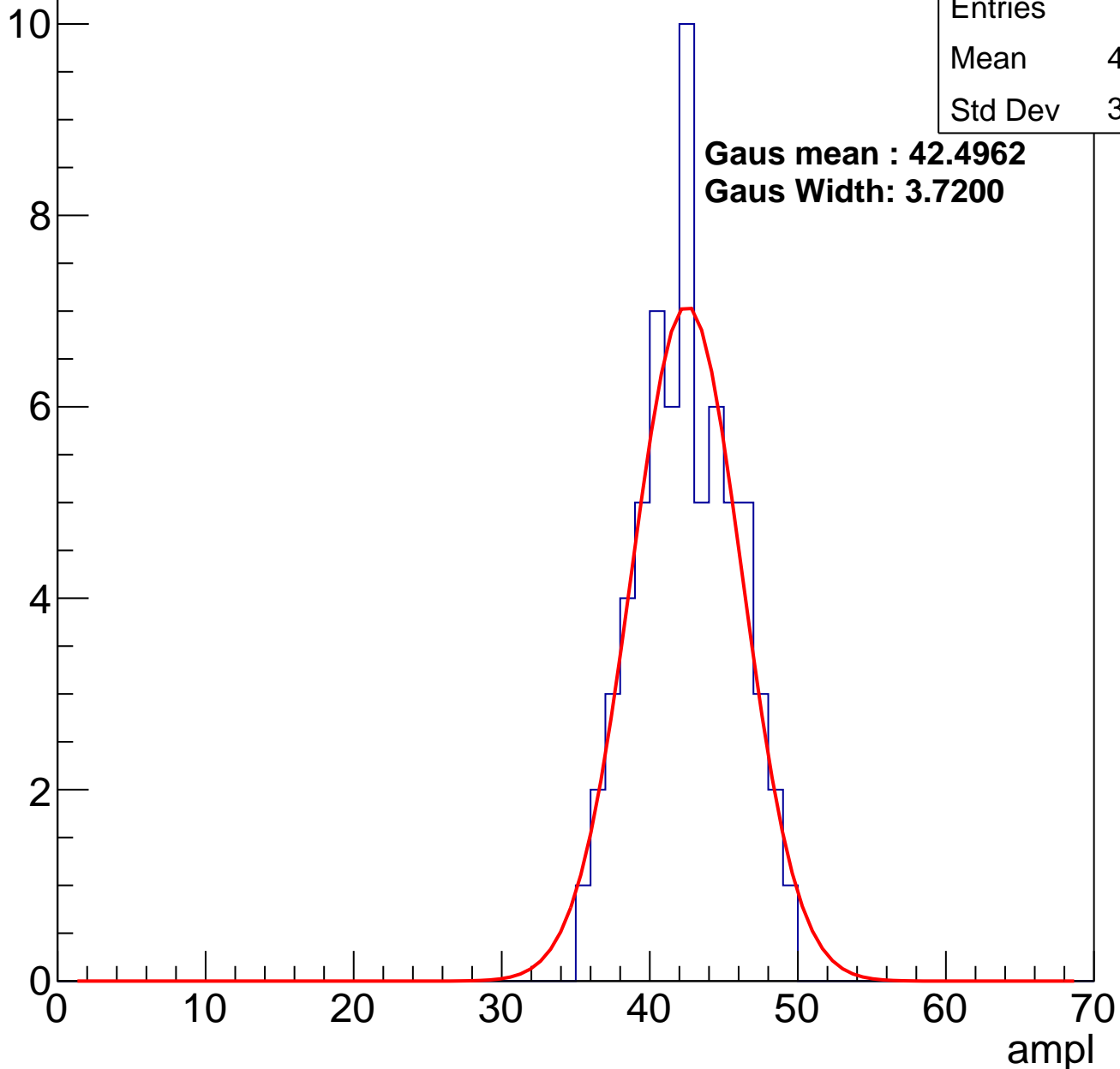
calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	42.02
Std Dev	3.256

Gaus mean : 42.4962

Gaus Width: 3.7200

Entry

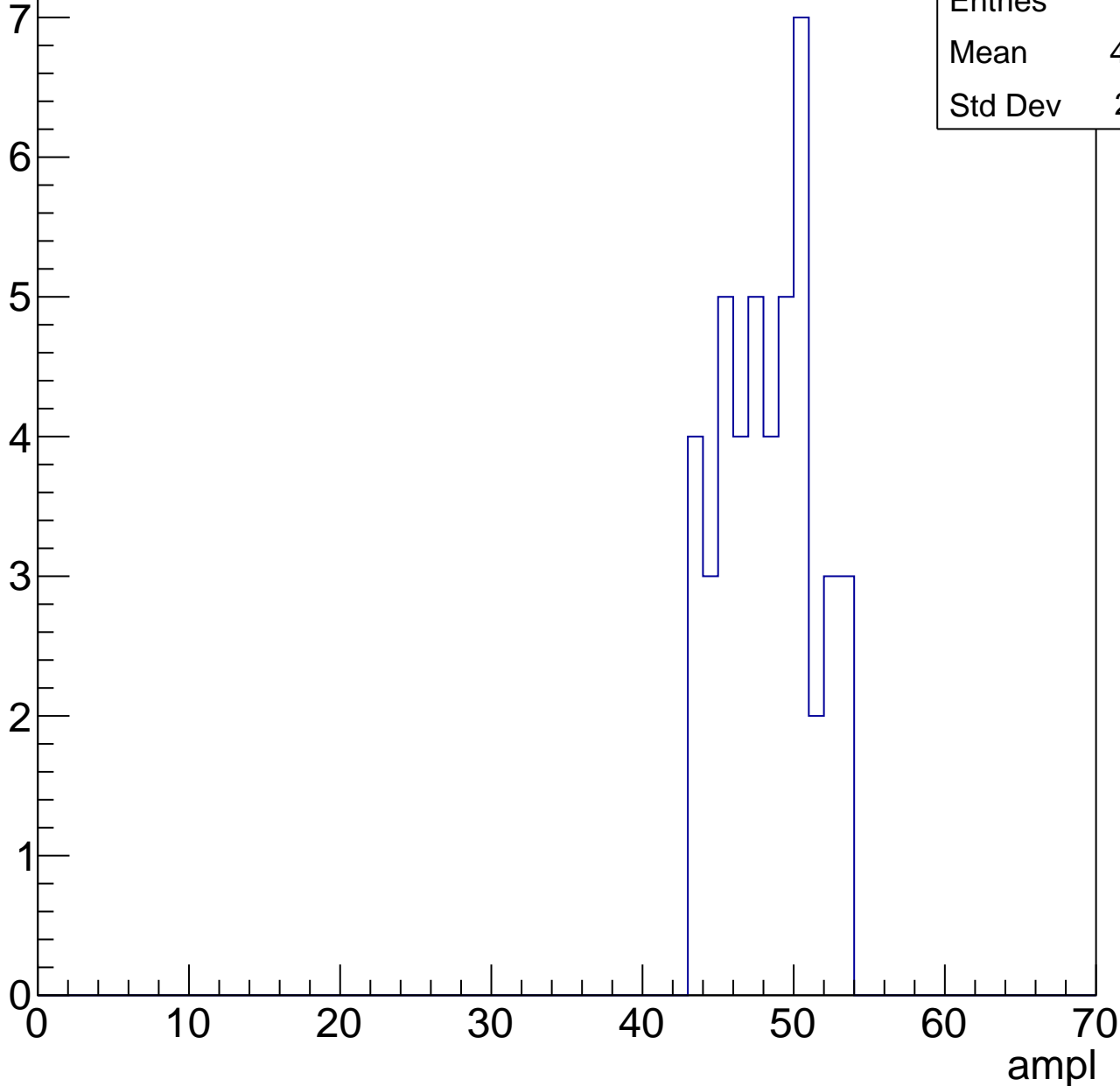


B1L003S, U20-ch86, adc3

calib_packv5_042523_0143.root, FC#13, port D2

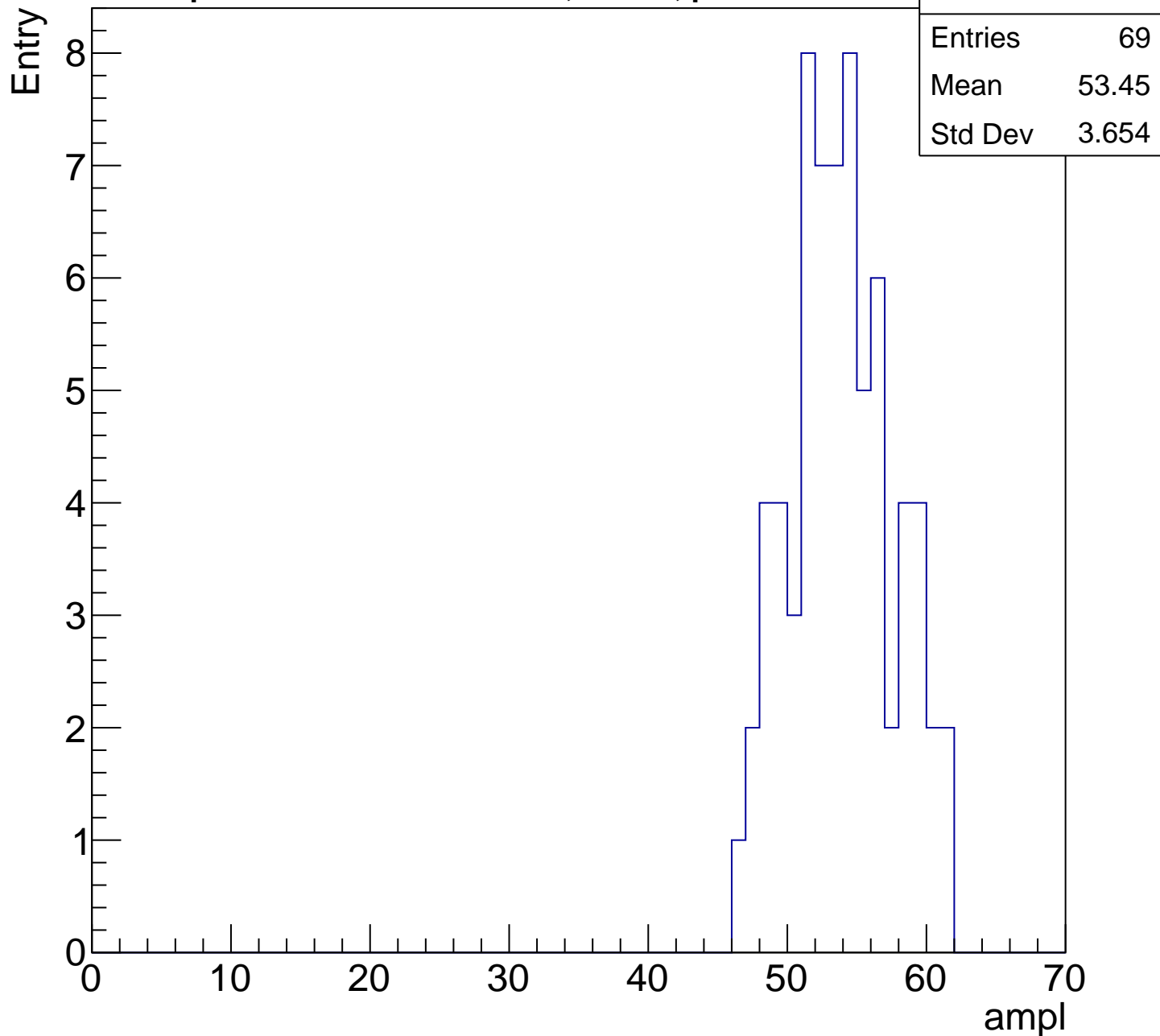
Entry

Entries	45
Mean	47.82
Std Dev	2.931



B1L003S, U20-ch86, adc4

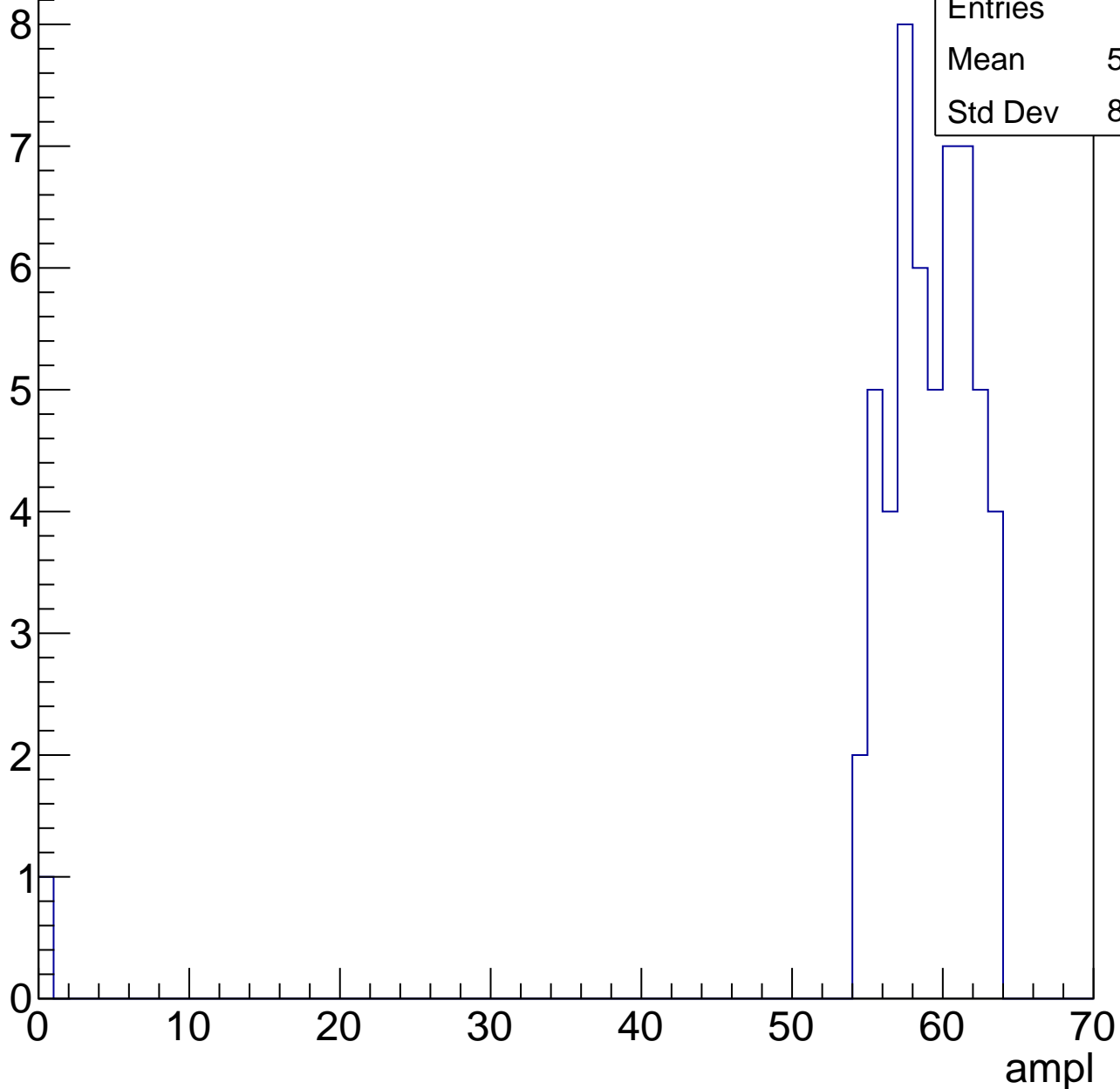
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U20-ch86, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

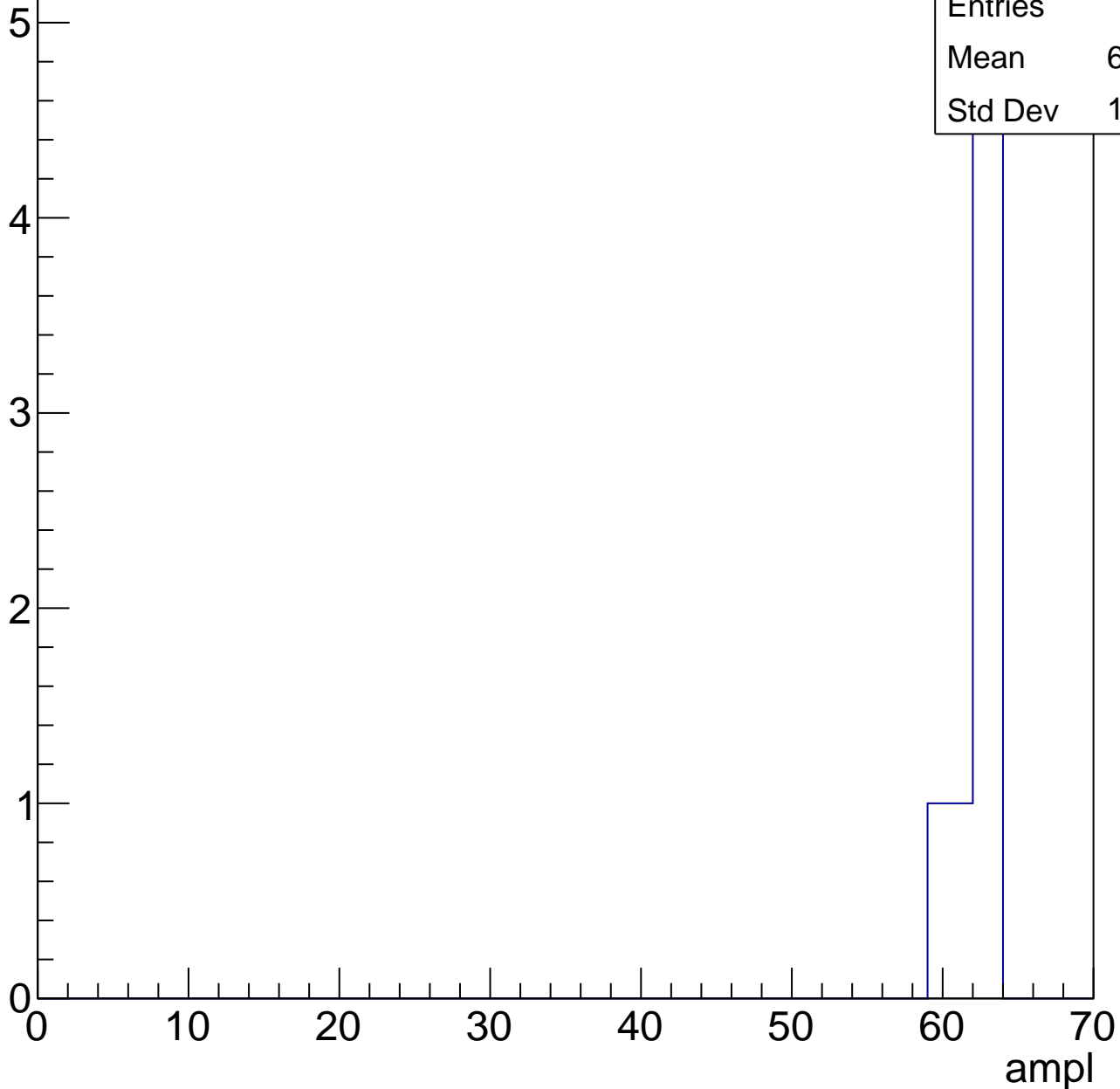


B1L003S, U20-ch86, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.92
Std Dev	1.206



B1L003S, U20-ch86, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch87, adc0

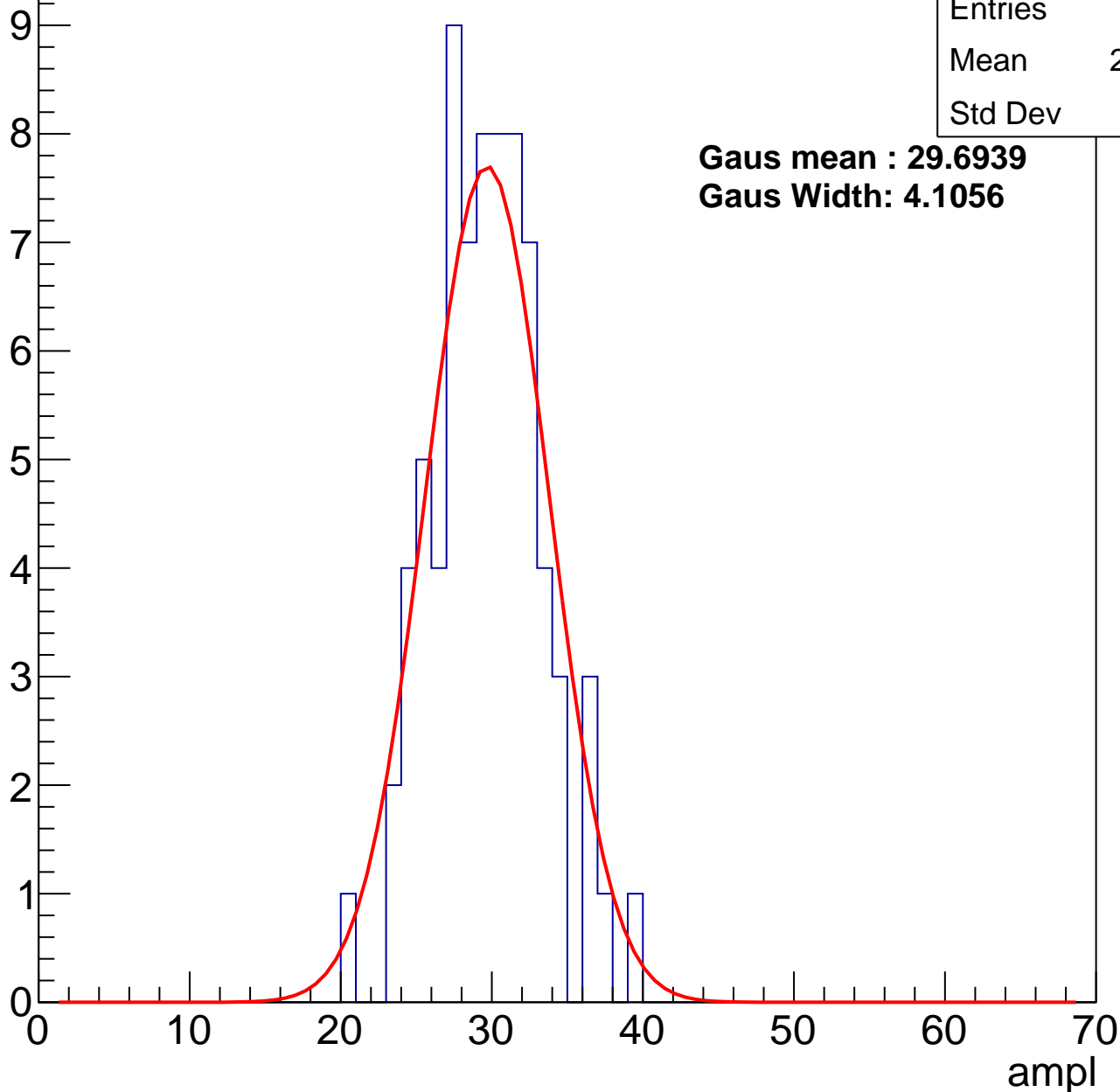
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.23
Std Dev	3.58

Gaus mean : 29.6939

Gaus Width: 4.1056



B1L003S, U20-ch87, adc1

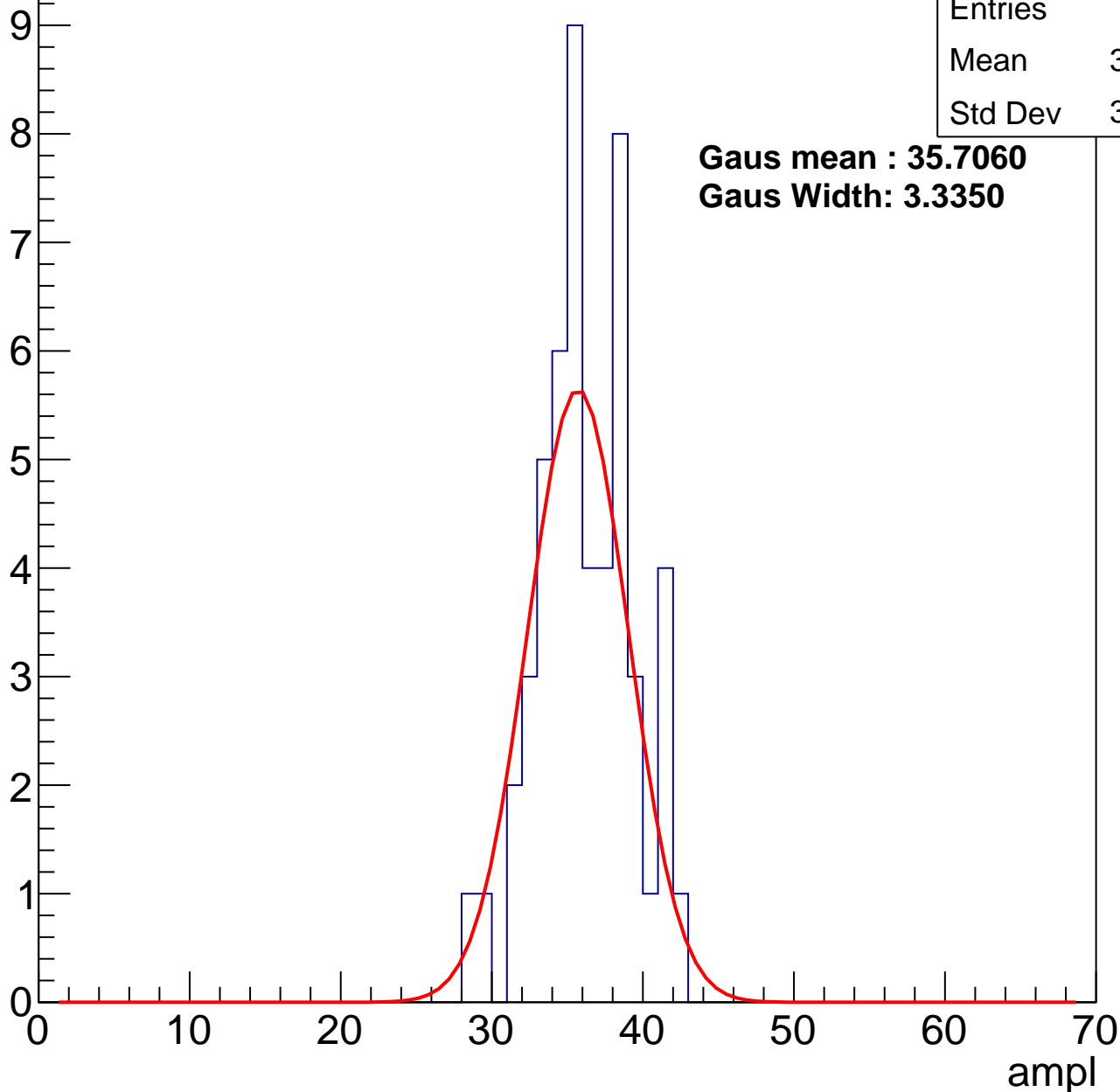
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	35.73
Std Dev	3.102

Gaus mean : 35.7060

Gaus Width: 3.3350



B1L003S, U20-ch87, adc2

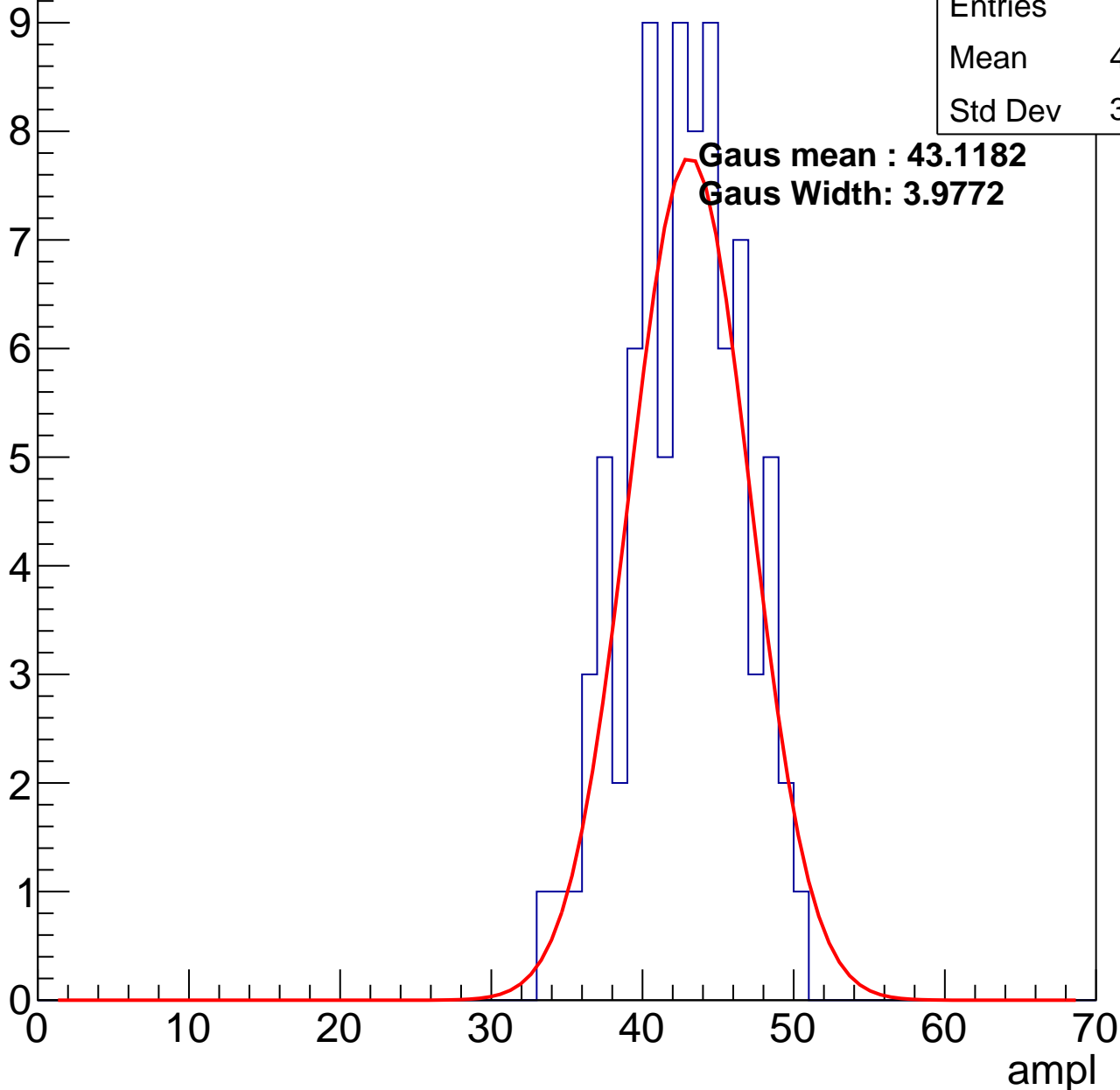
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	42.28
Std Dev	3.762

Gaus mean : 43.1182

Gaus Width: 3.9772

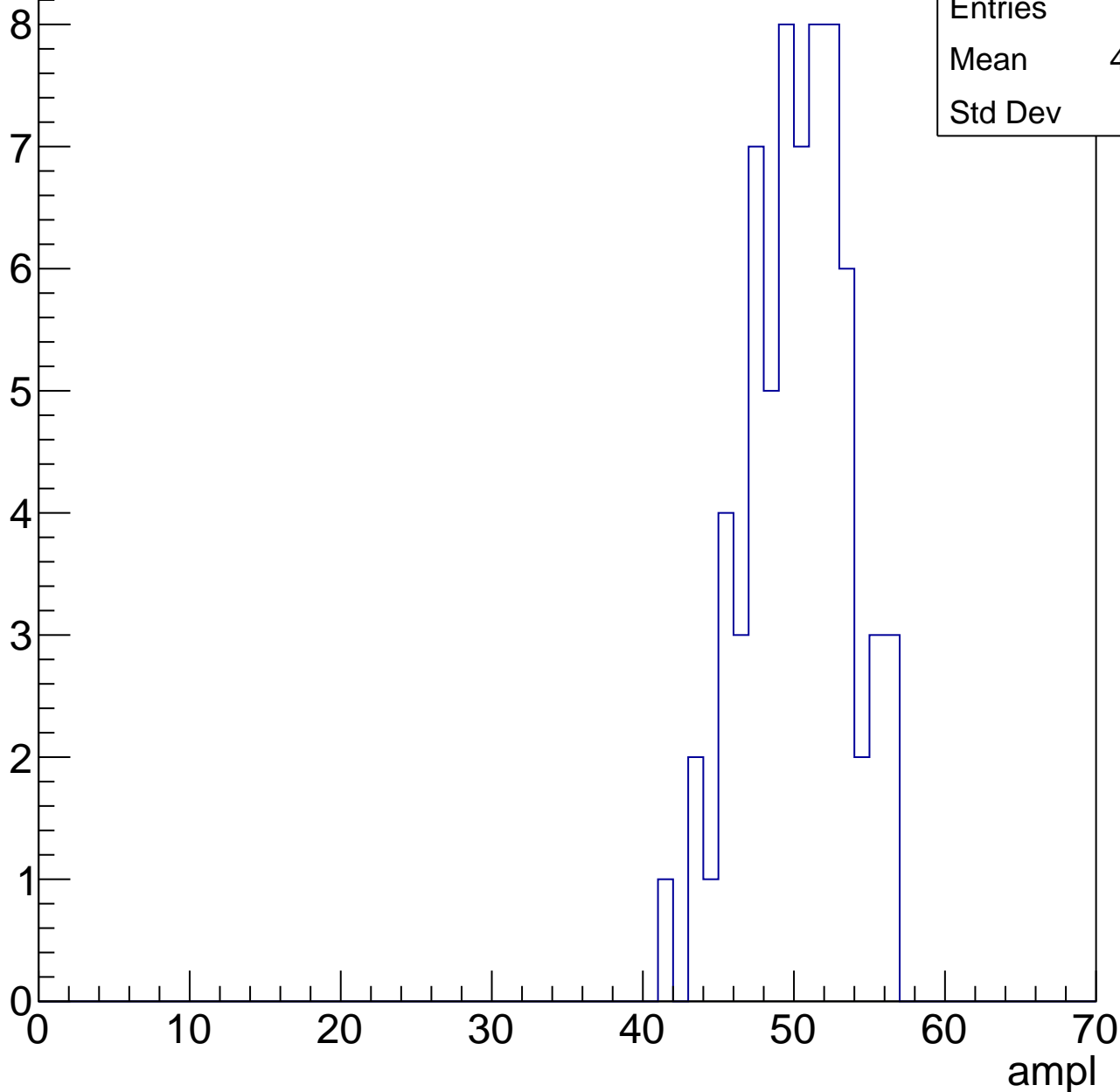


B1L003S, U20-ch87, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.75
Std Dev	3.34

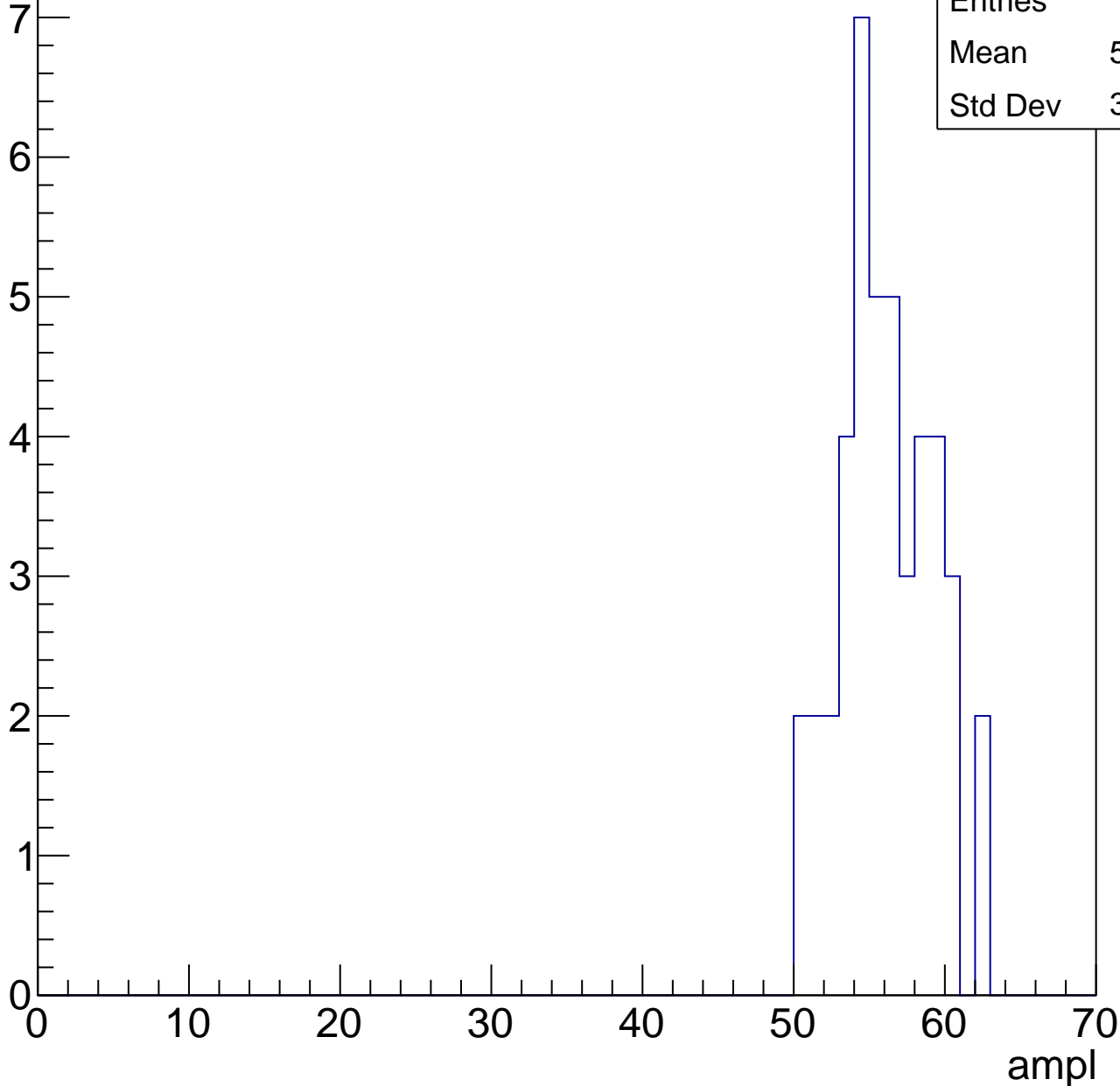


B1L003S, U20-ch87, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.67
Std Dev	3.025

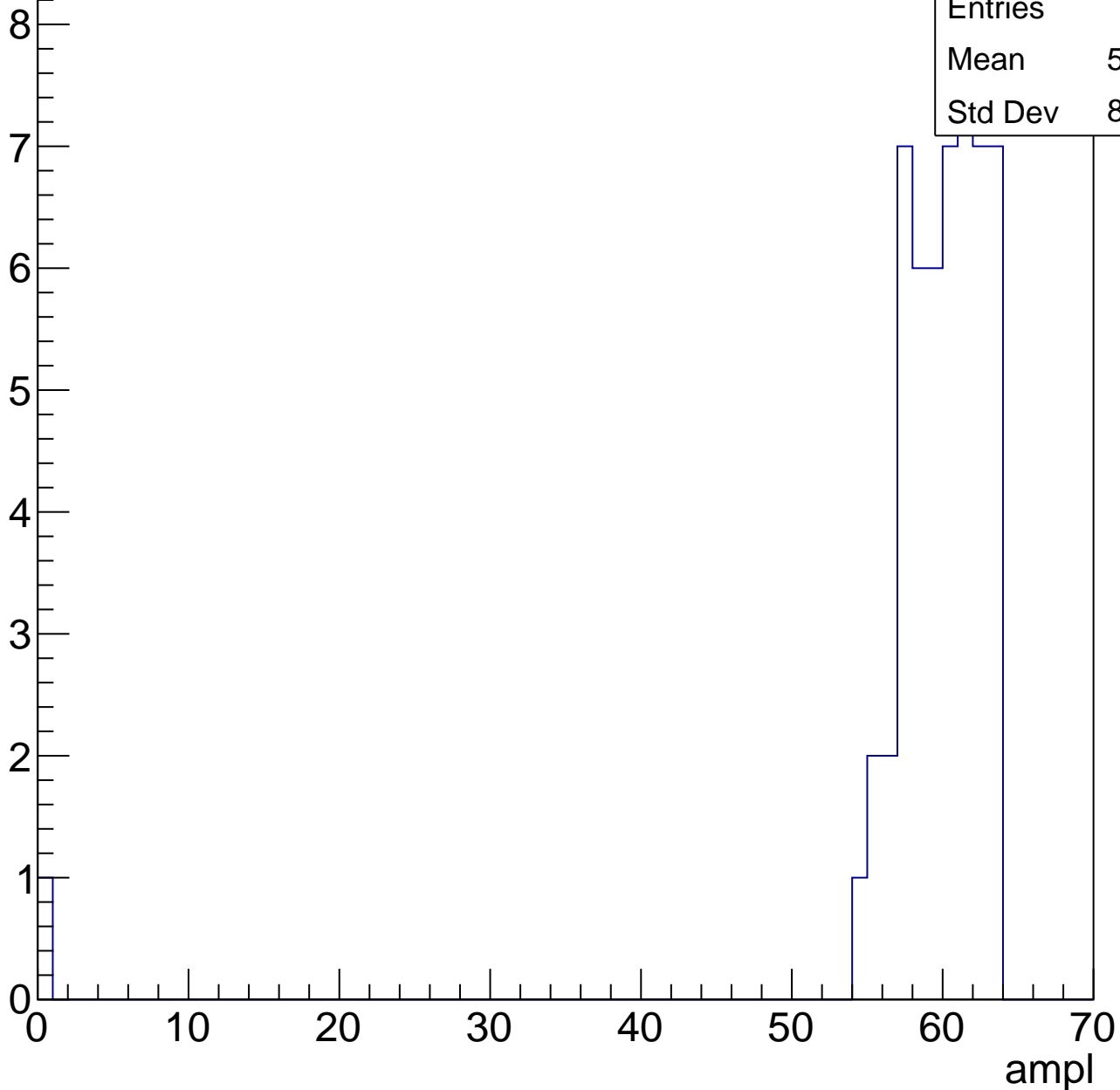


B1L003S, U20-ch87, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.52
Std Dev	8.379



B1L003S, U20-ch87, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch87, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch88, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	29.09
Std Dev	3.068

Gaus mean : 29.4037

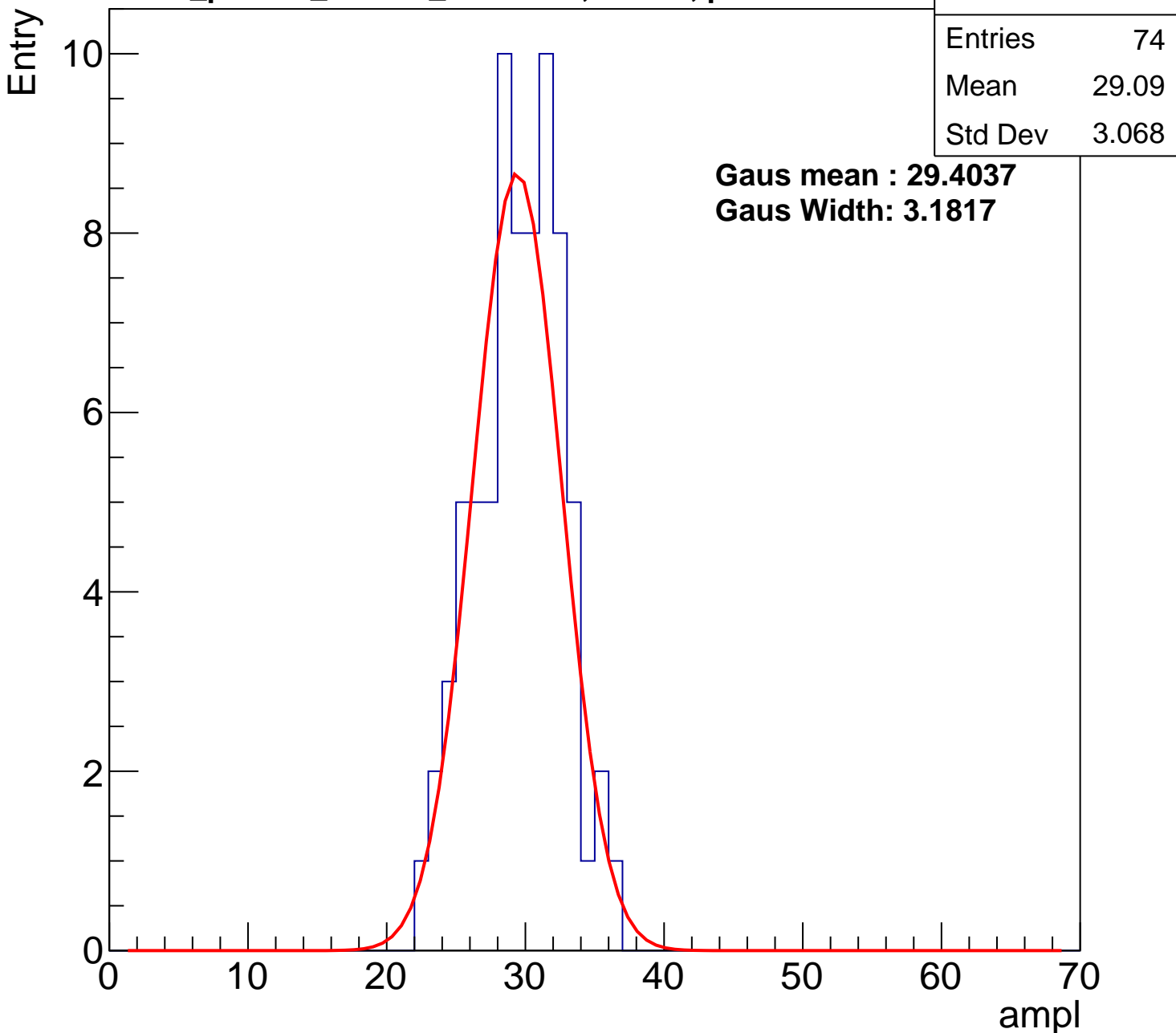
Gaus Width: 3.1817

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch88, adc1

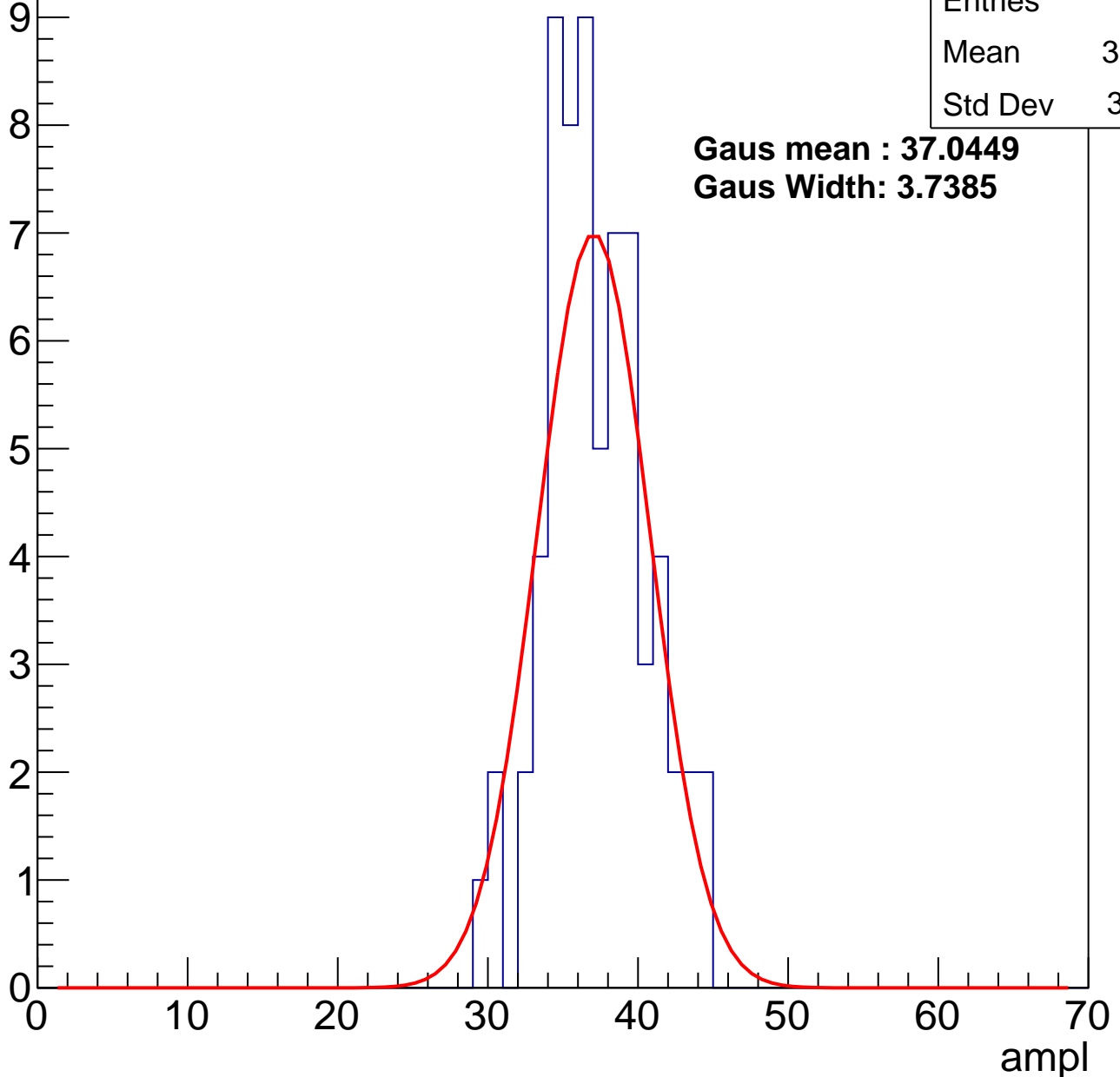
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.73
Std Dev	3.321

Gaus mean : 37.0449

Gaus Width: 3.7385



B1L003S, U20-ch88, adc2

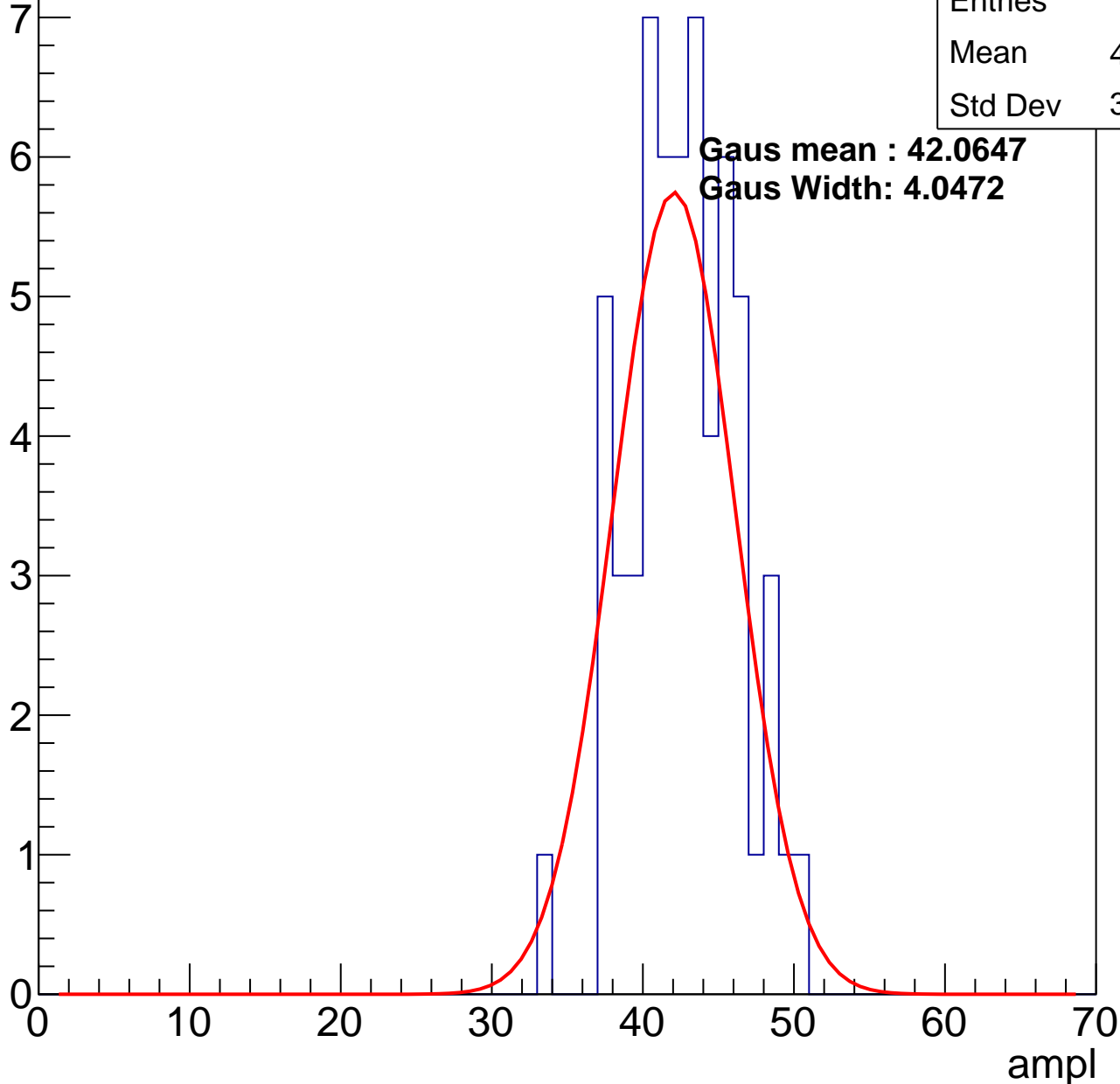
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.27
Std Dev	3.478

Gaus mean : 42.0647

Gaus Width: 4.0472

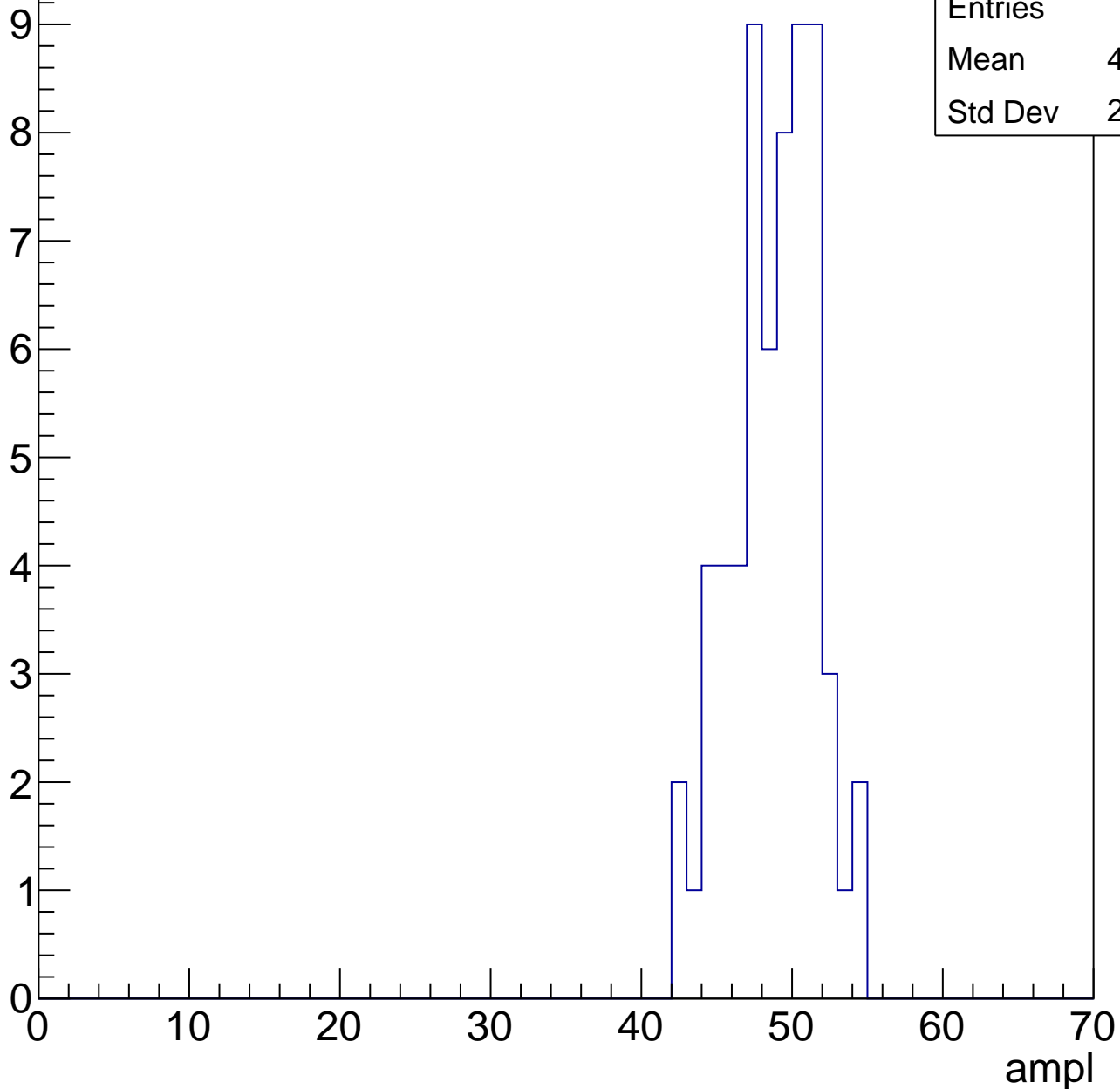


B1L003S, U20-ch88, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.32
Std Dev	2.798

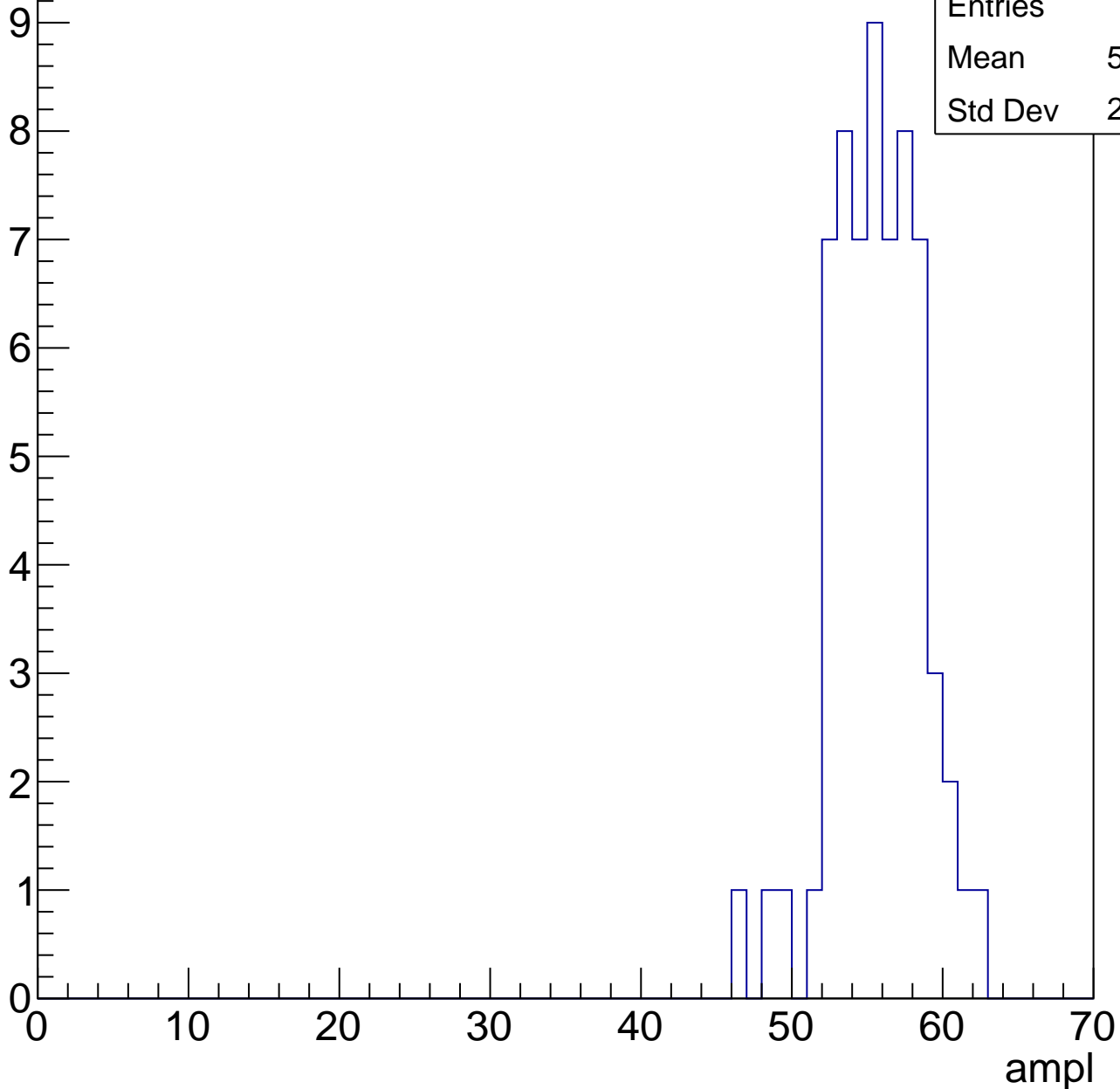


B1L003S, U20-ch88, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.14
Std Dev	2.978



B1L003S, U20-ch88, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	59.72
Std Dev	2.511

ampl

10

20

30

40

50

60

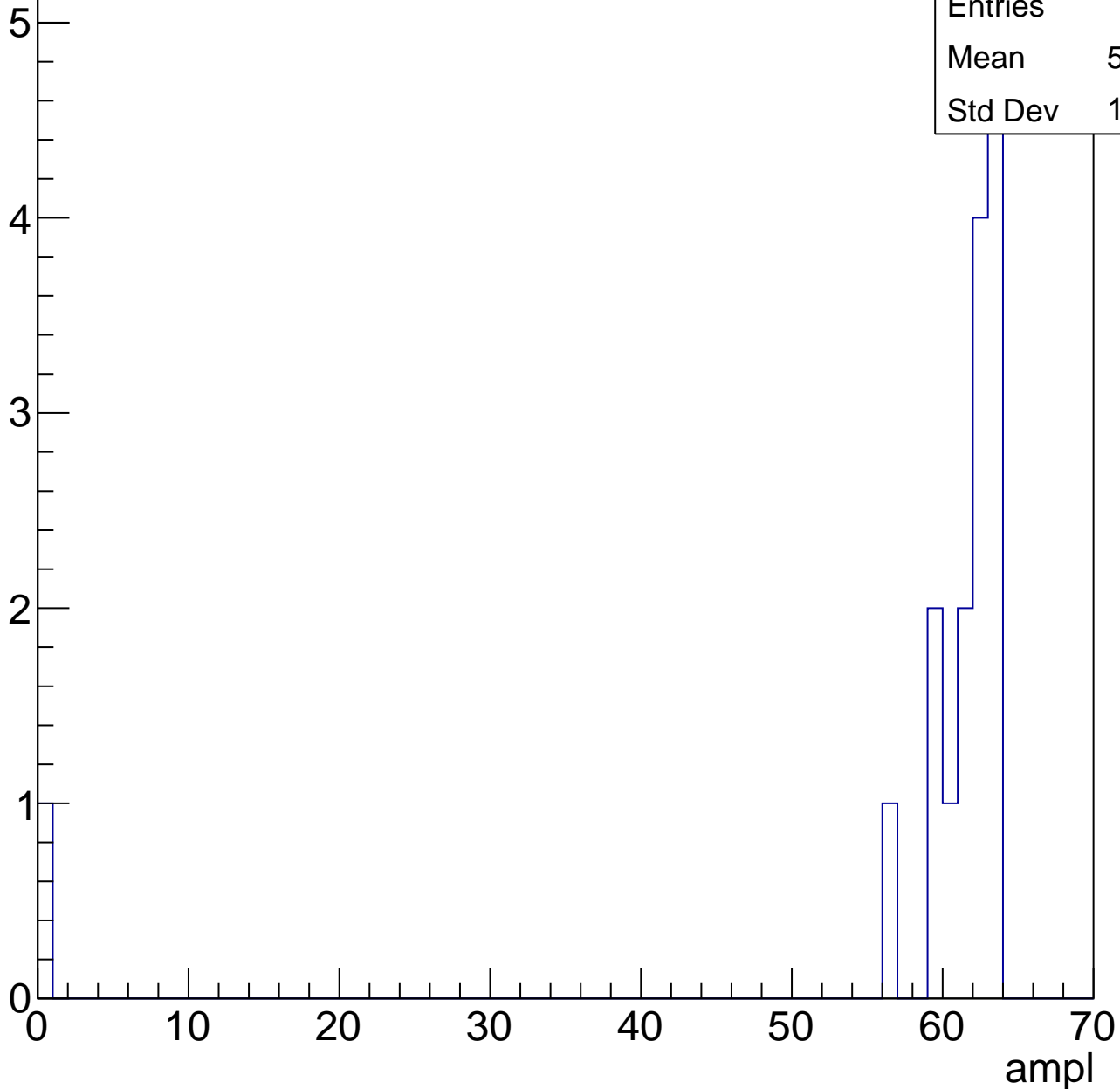
70

B1L003S, U20-ch88, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

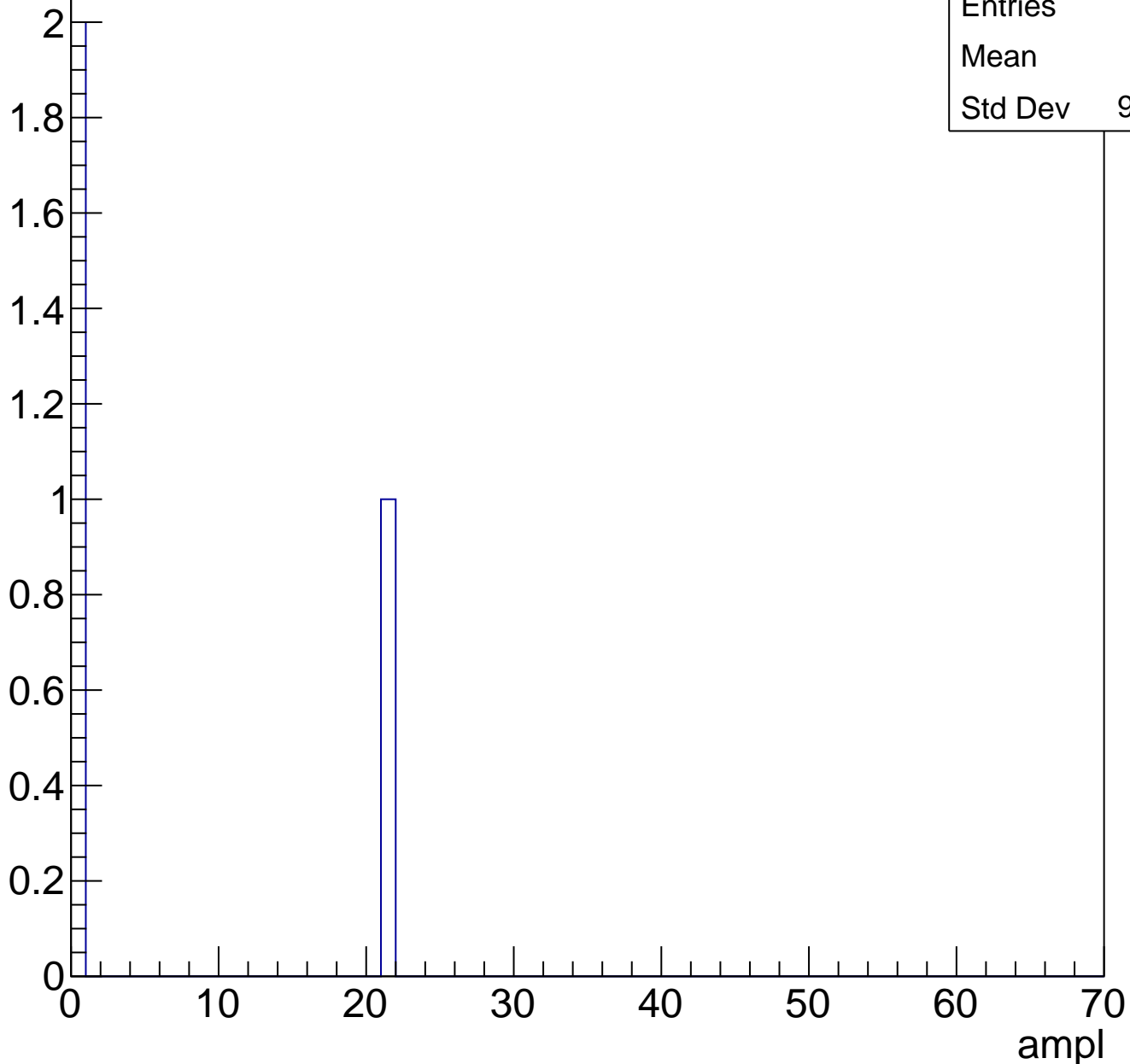
Entries	16
Mean	57.44
Std Dev	14.95



B1L003S, U20-ch88, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L003S, U20-ch89, adc0

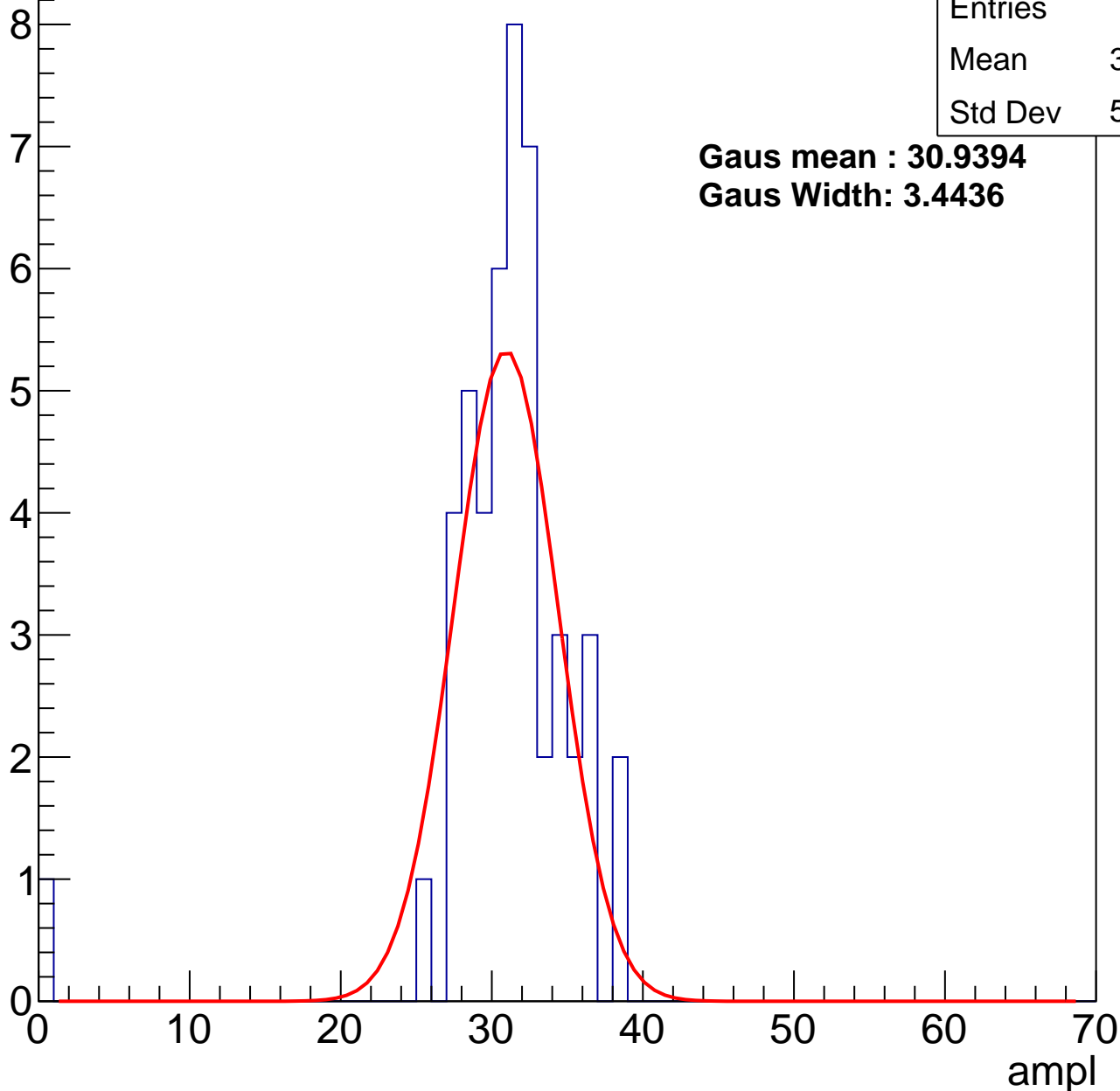
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	30.48
Std Dev	5.327

Gaus mean : 30.9394

Gaus Width: 3.4436



B1L003S, U20-ch89, adc1

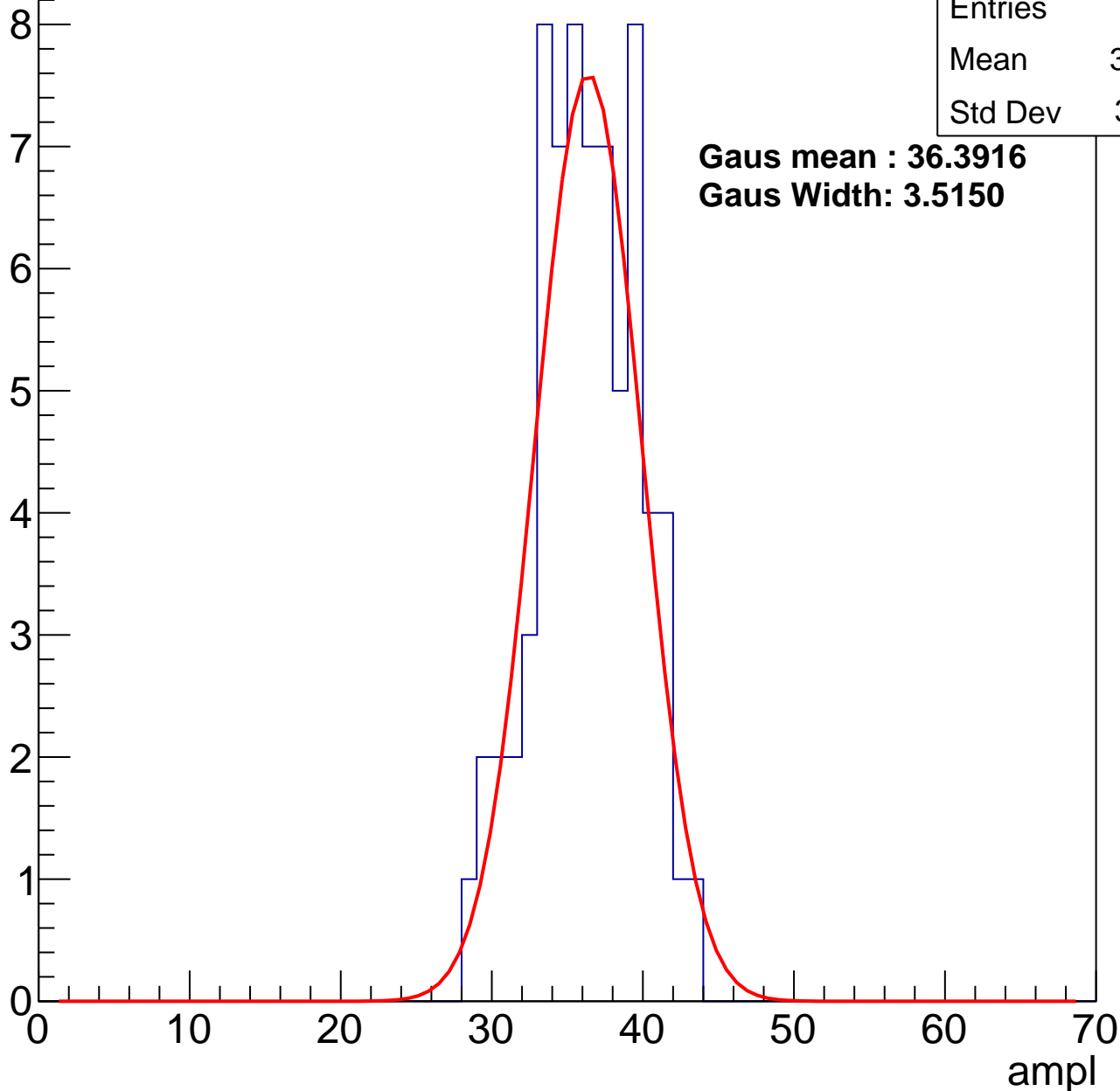
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.83
Std Dev	3.351

Gaus mean : 36.3916

Gaus Width: 3.5150



B1L003S, U20-ch89, adc2

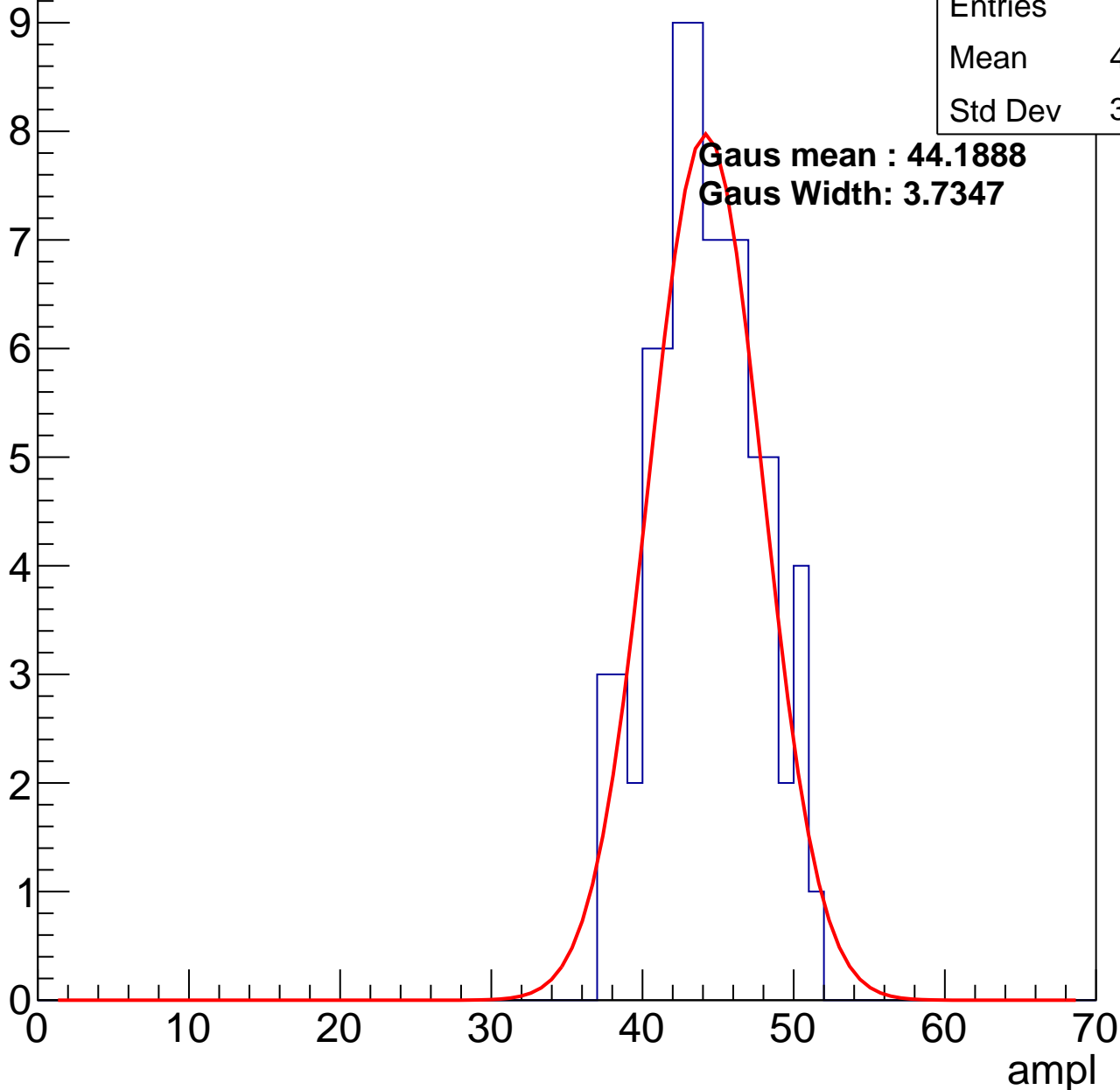
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	43.72
Std Dev	3.436

Gaus mean : 44.1888

Gaus Width: 3.7347

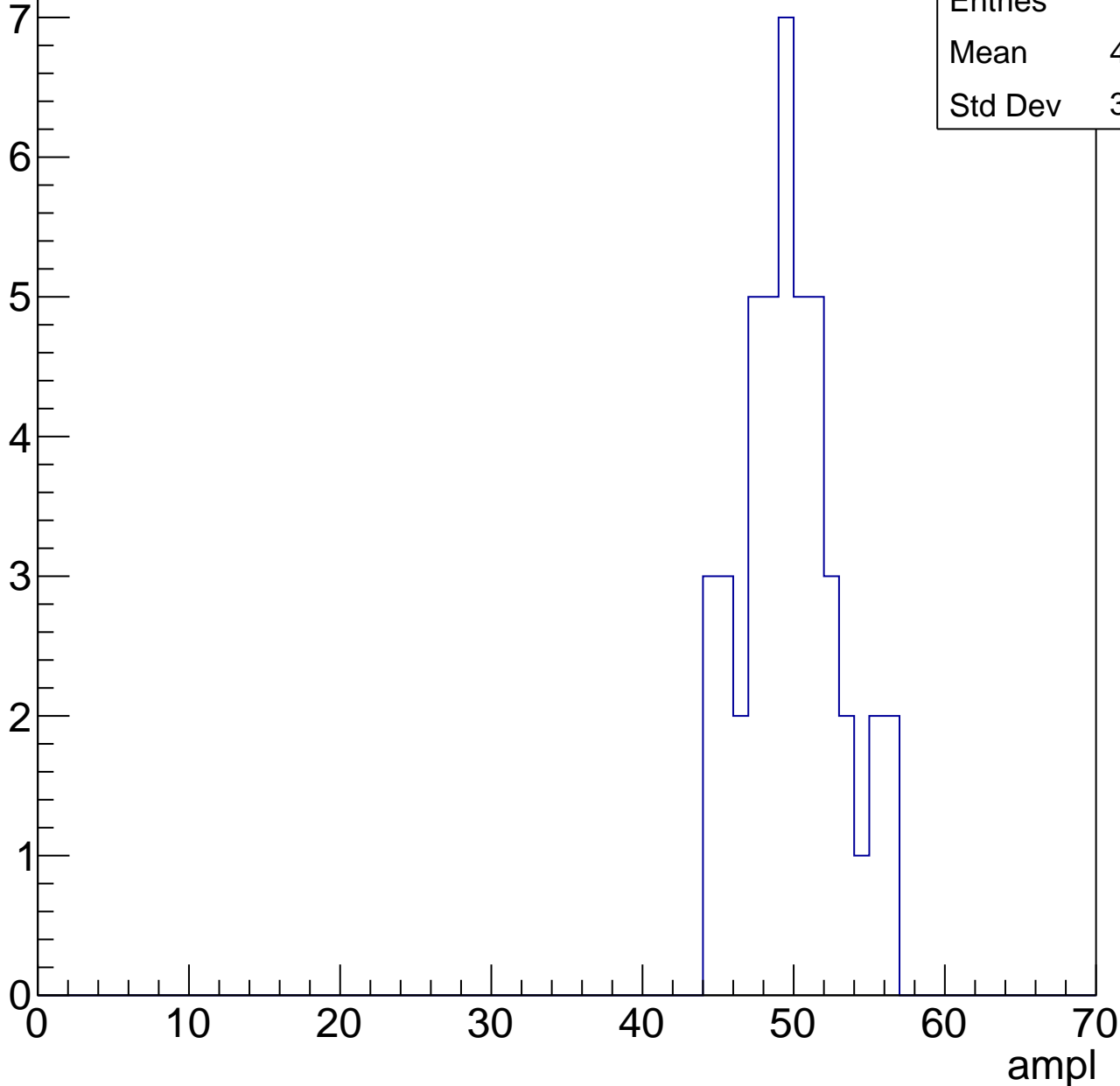


B1L003S, U20-ch89, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

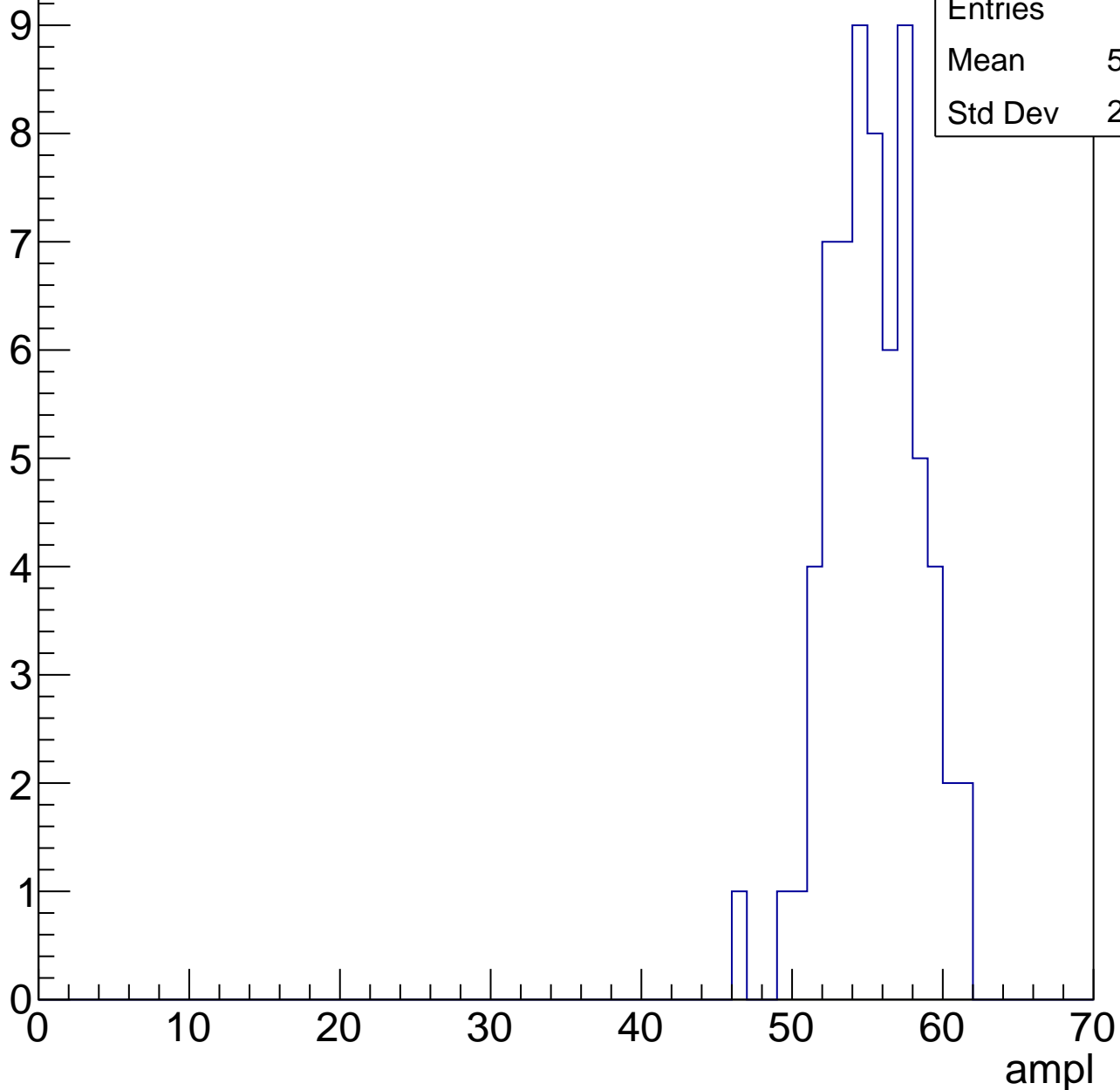
Entries	45
Mean	49.33
Std Dev	3.127



B1L003S, U20-ch89, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

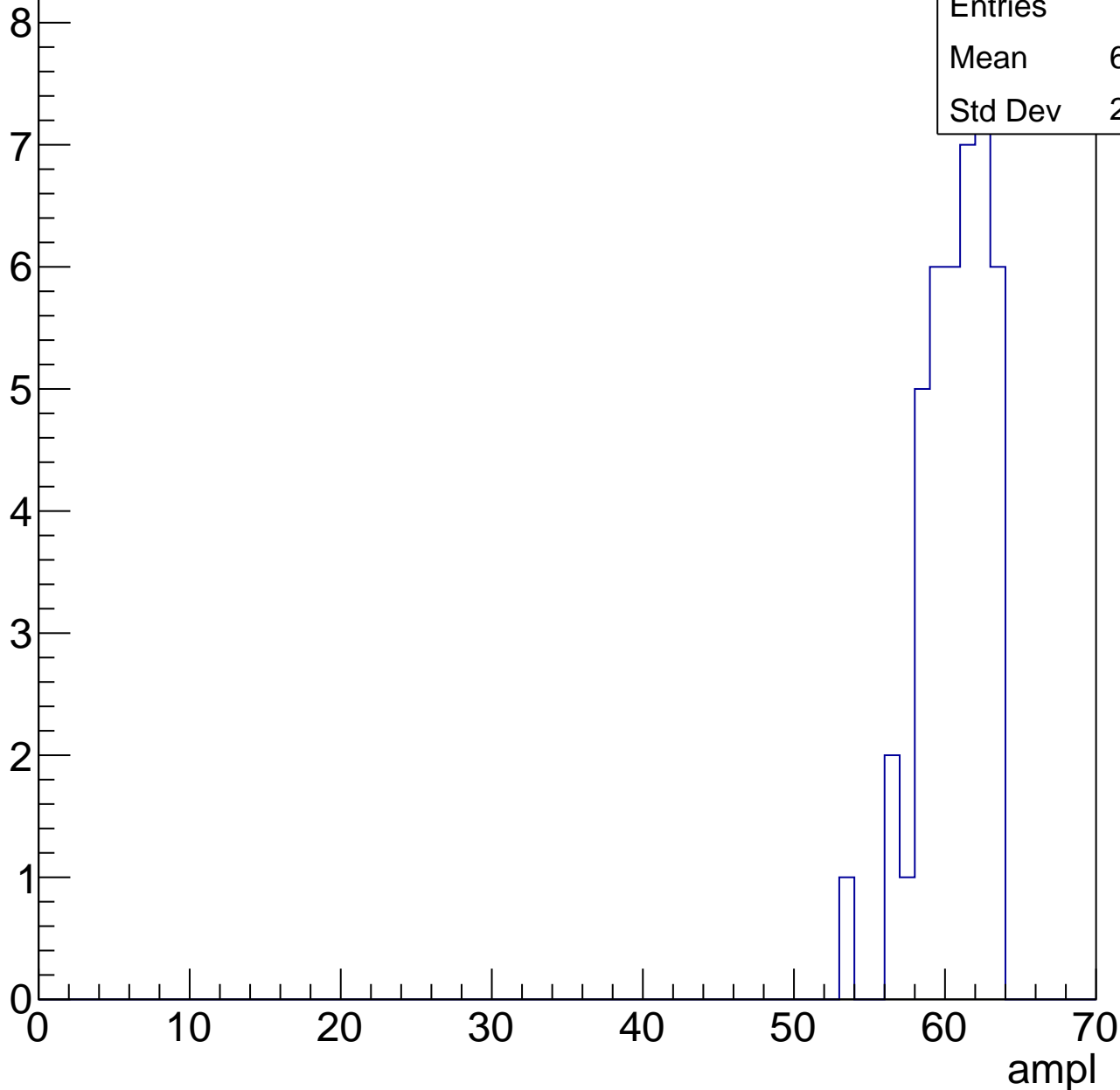


B1L003S, U20-ch89, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

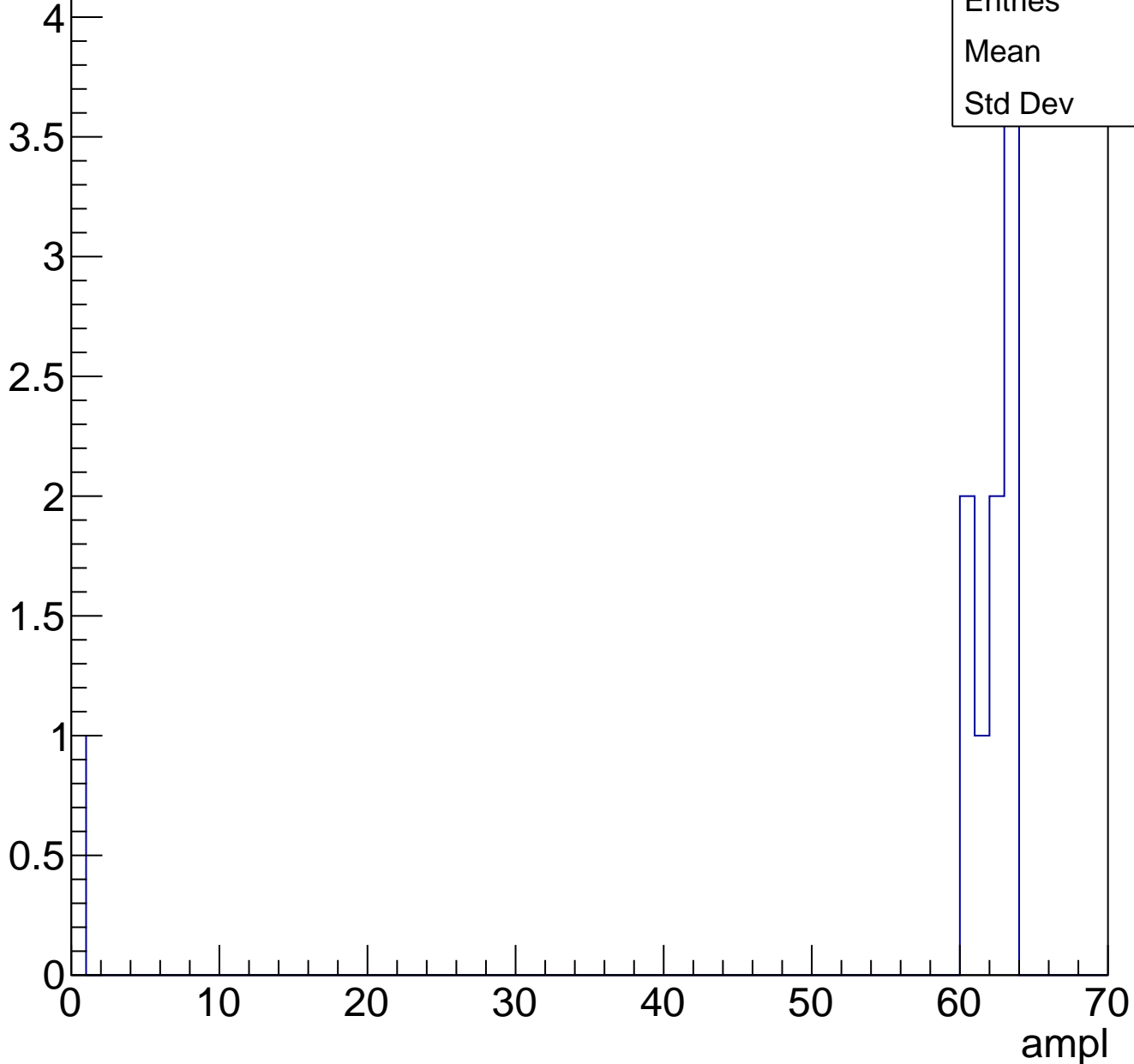
Entries	42
Mean	60.17
Std Dev	2.225



B1L003S, U20-ch89, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch89, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch90, adc0

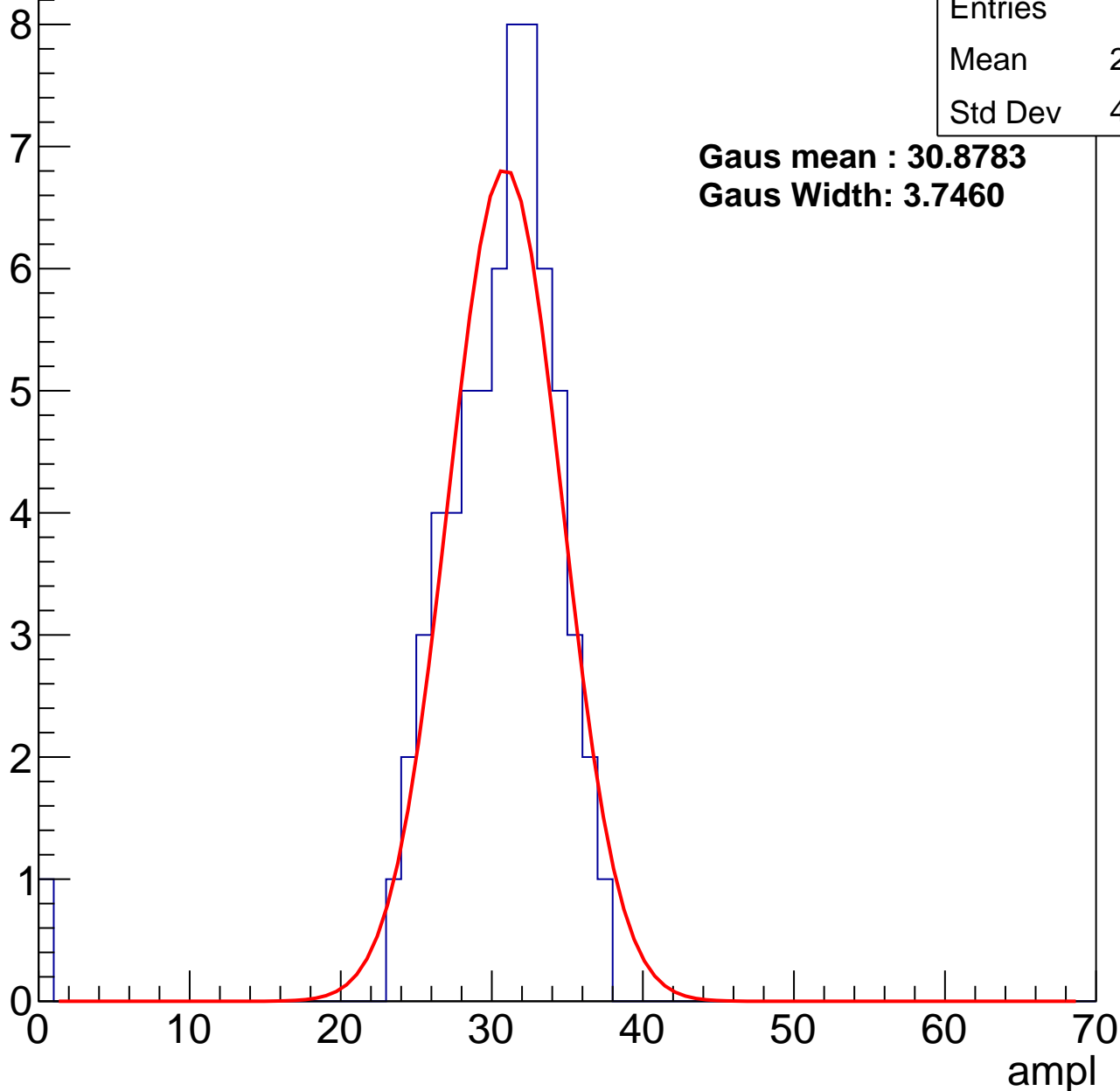
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.83
Std Dev	4.983

Gaus mean : 30.8783

Gaus Width: 3.7460



B1L003S, U20-ch90, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	37.18
Std Dev	3.38

Gaus mean : 37.6714

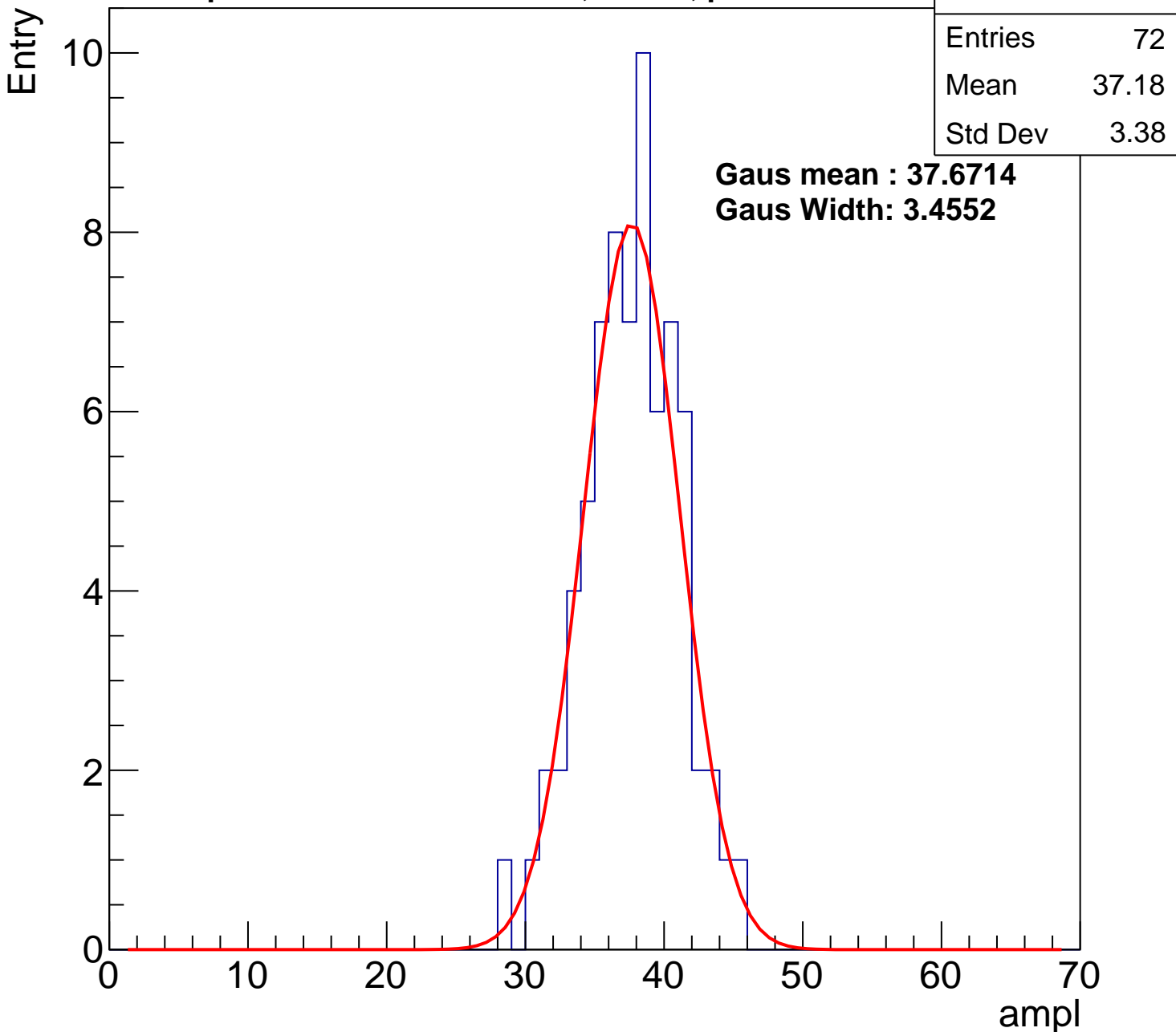
Gaus Width: 3.4552

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch90, adc2

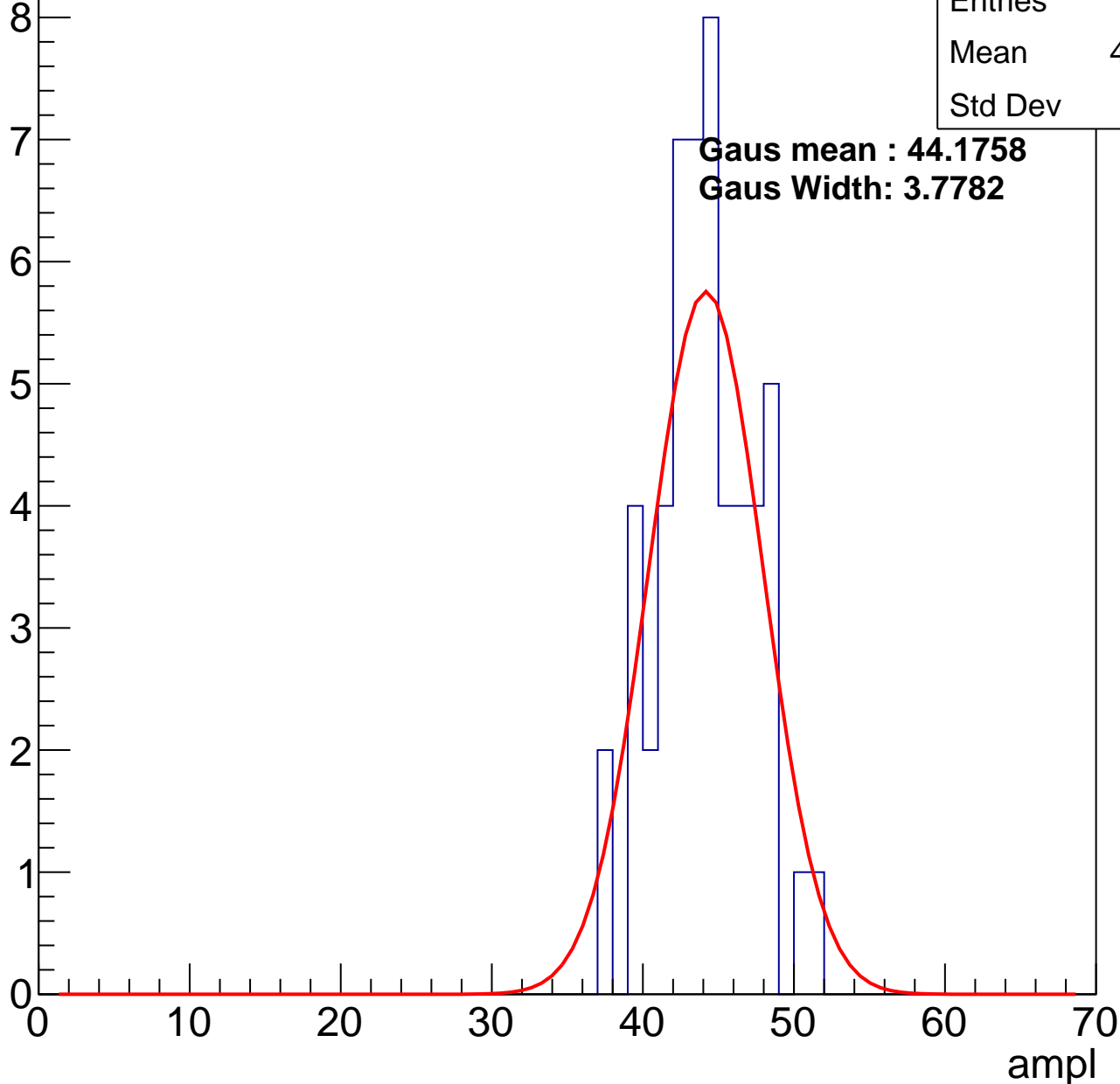
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.66
Std Dev	3.12

Gaus mean : 44.1758

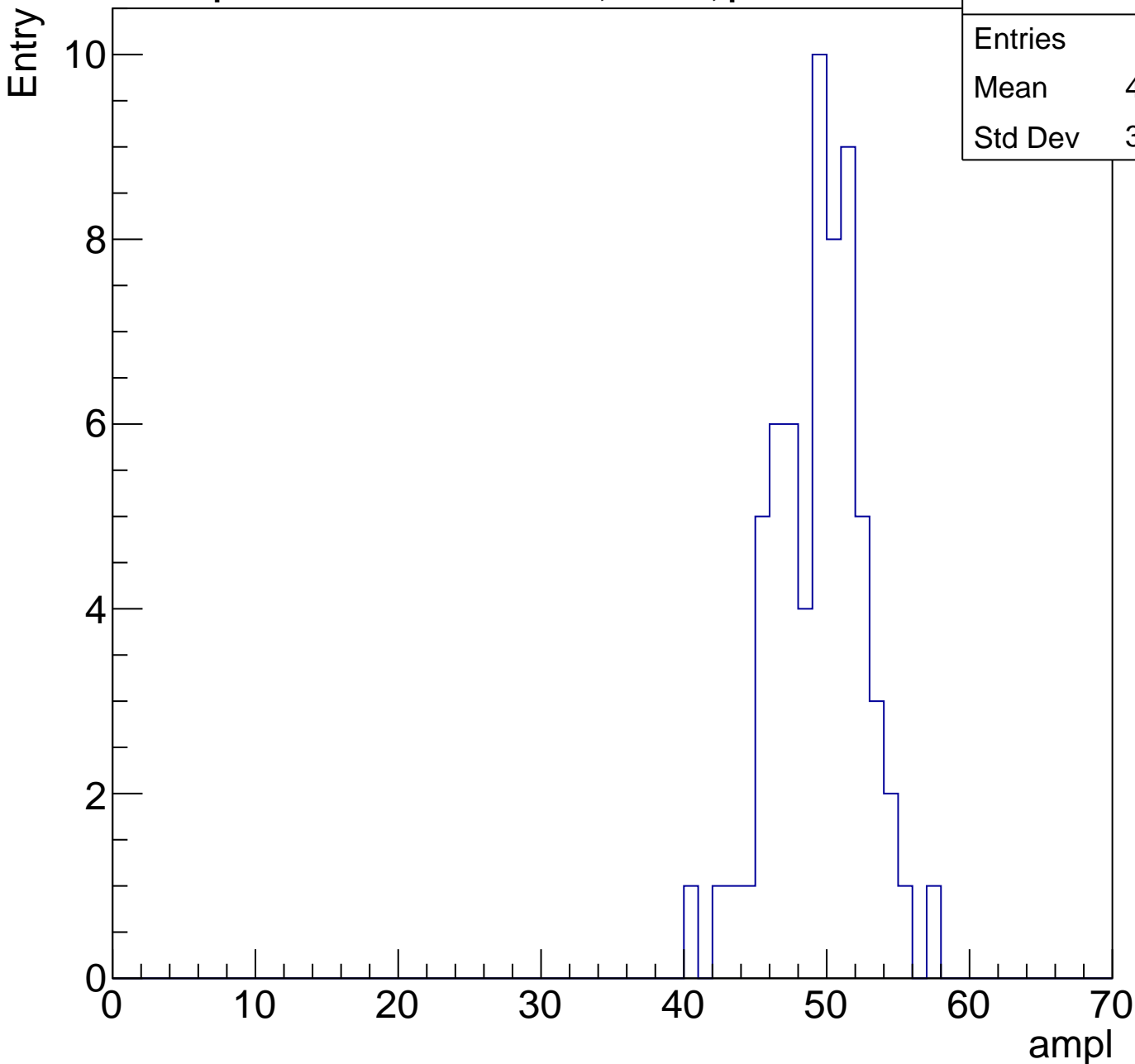
Gaus Width: 3.7782



B1L003S, U20-ch90, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	48.94
Std Dev	3.162

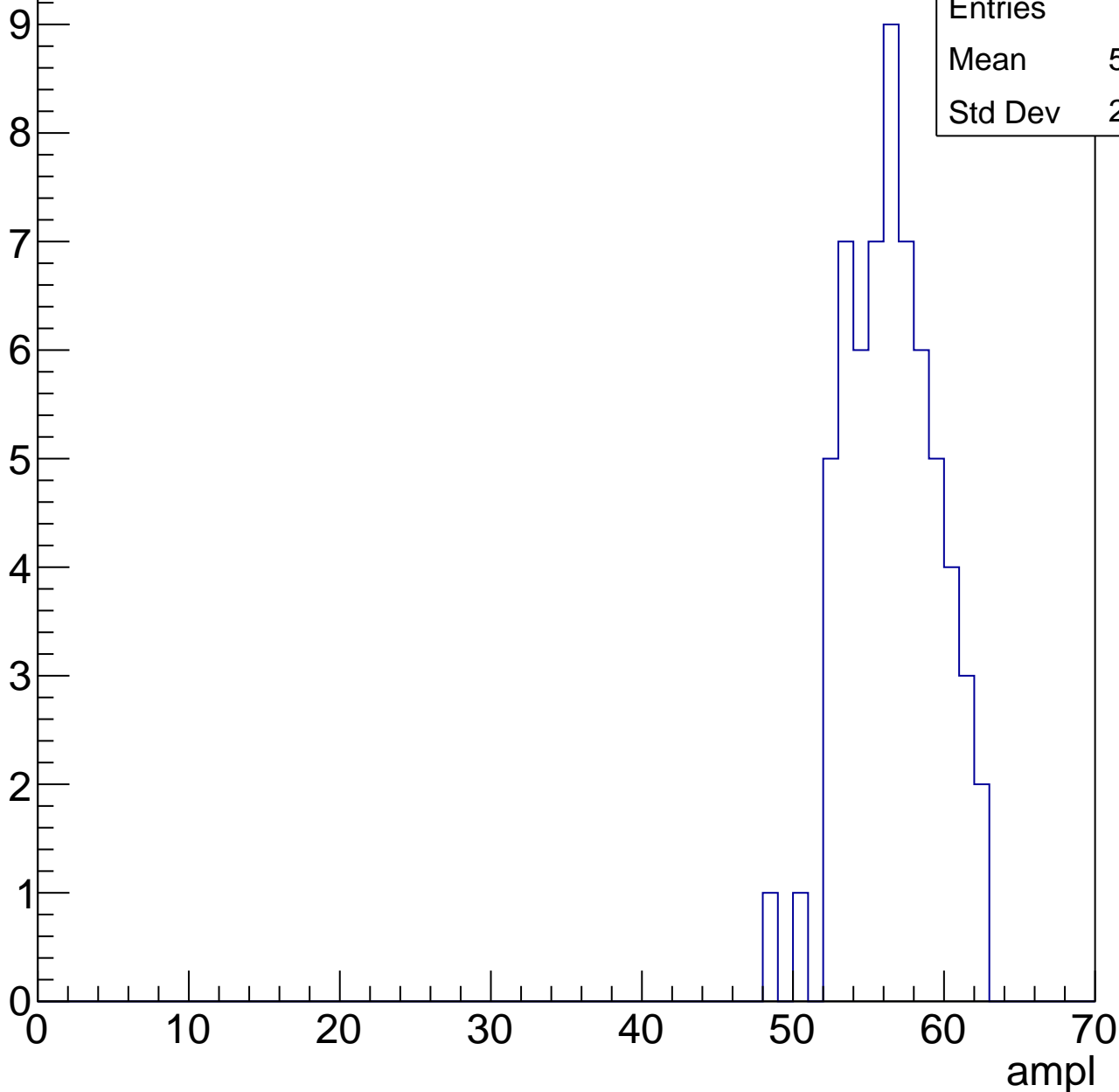


B1L003S, U20-ch90, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.05
Std Dev	2.984

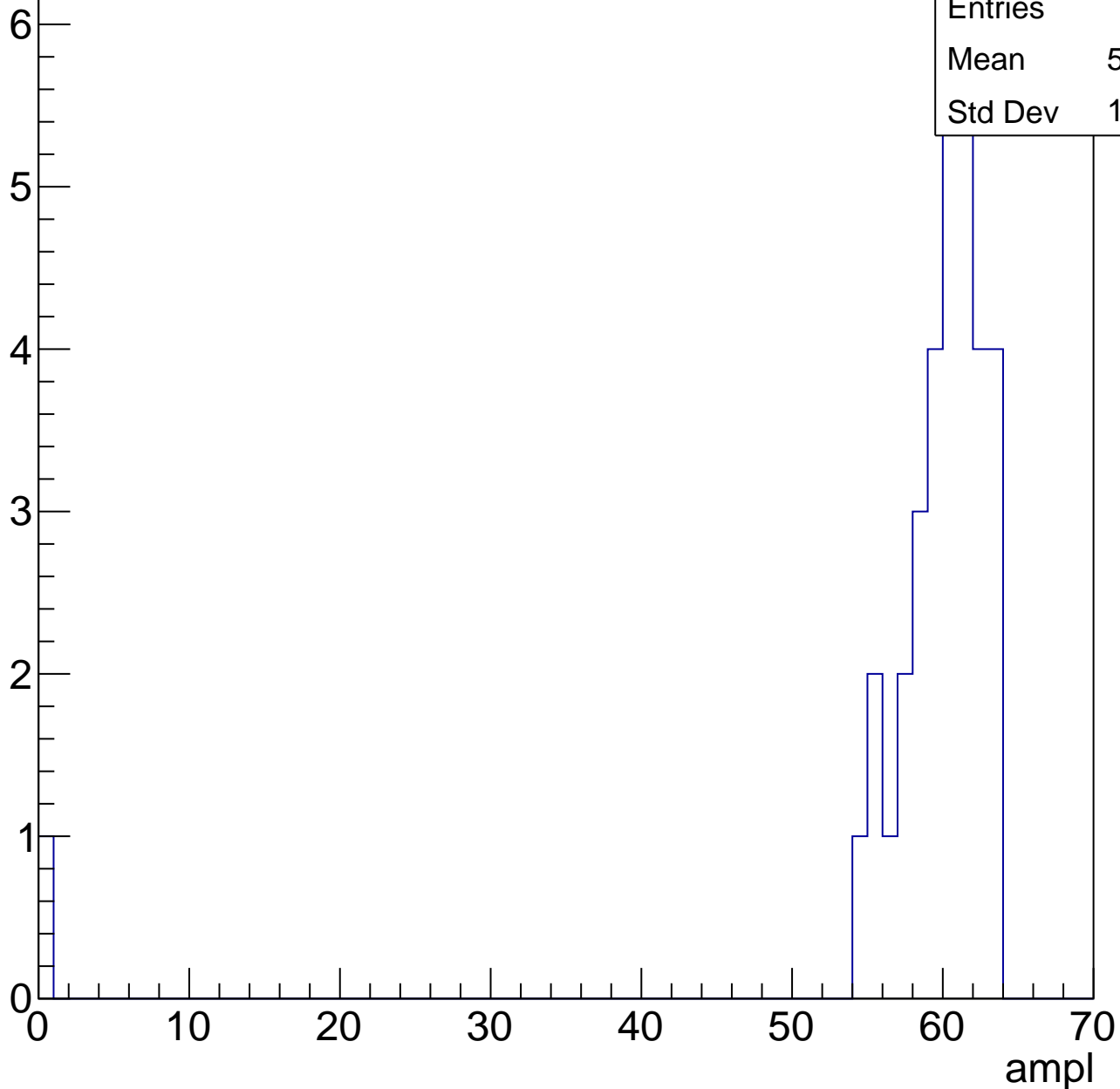


B1L003S, U20-ch90, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	57.94
Std Dev	10.36

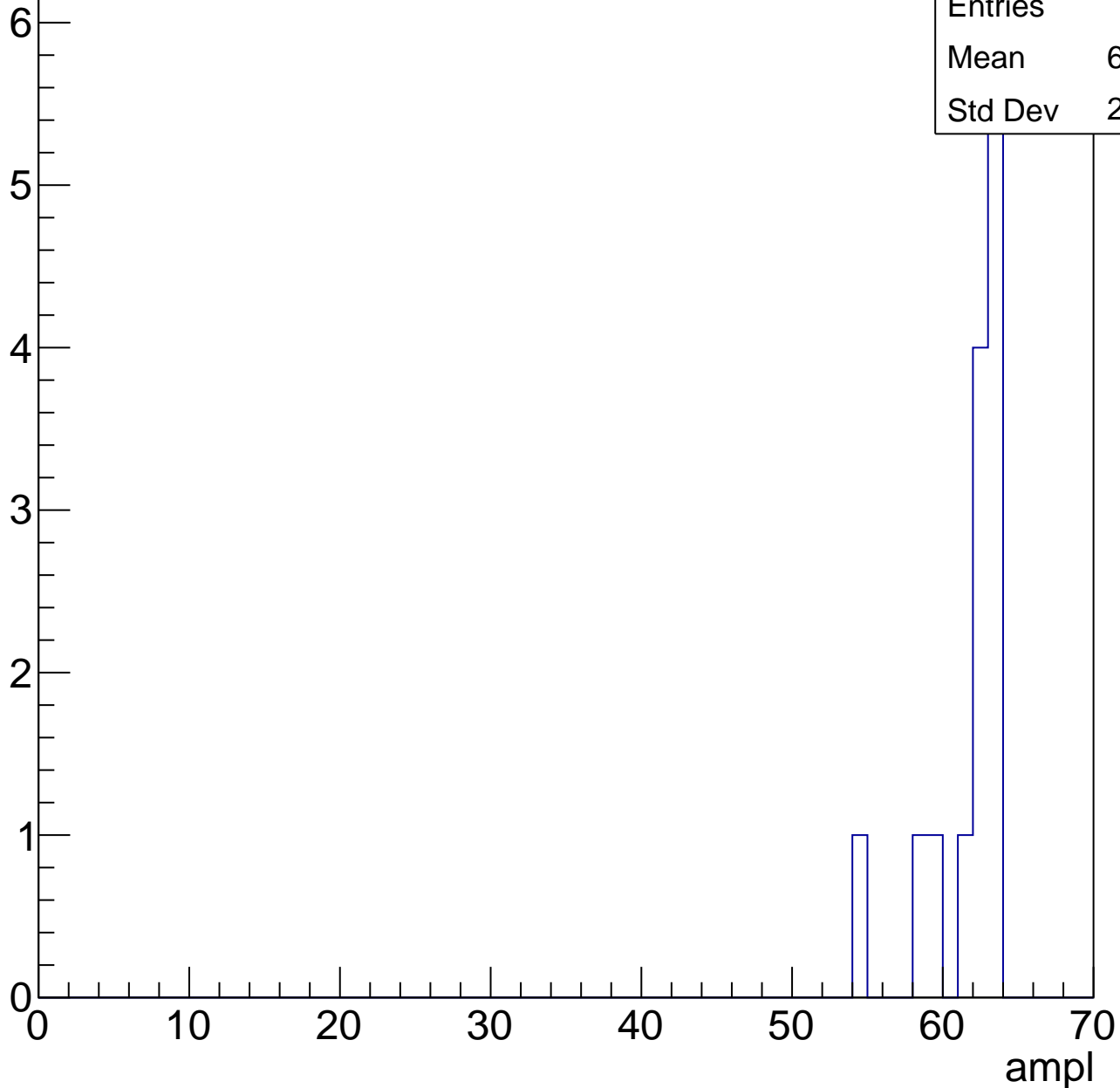


B1L003S, U20-ch90, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.29
Std Dev	2.519



B1L003S, U20-ch90, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch91, adc0

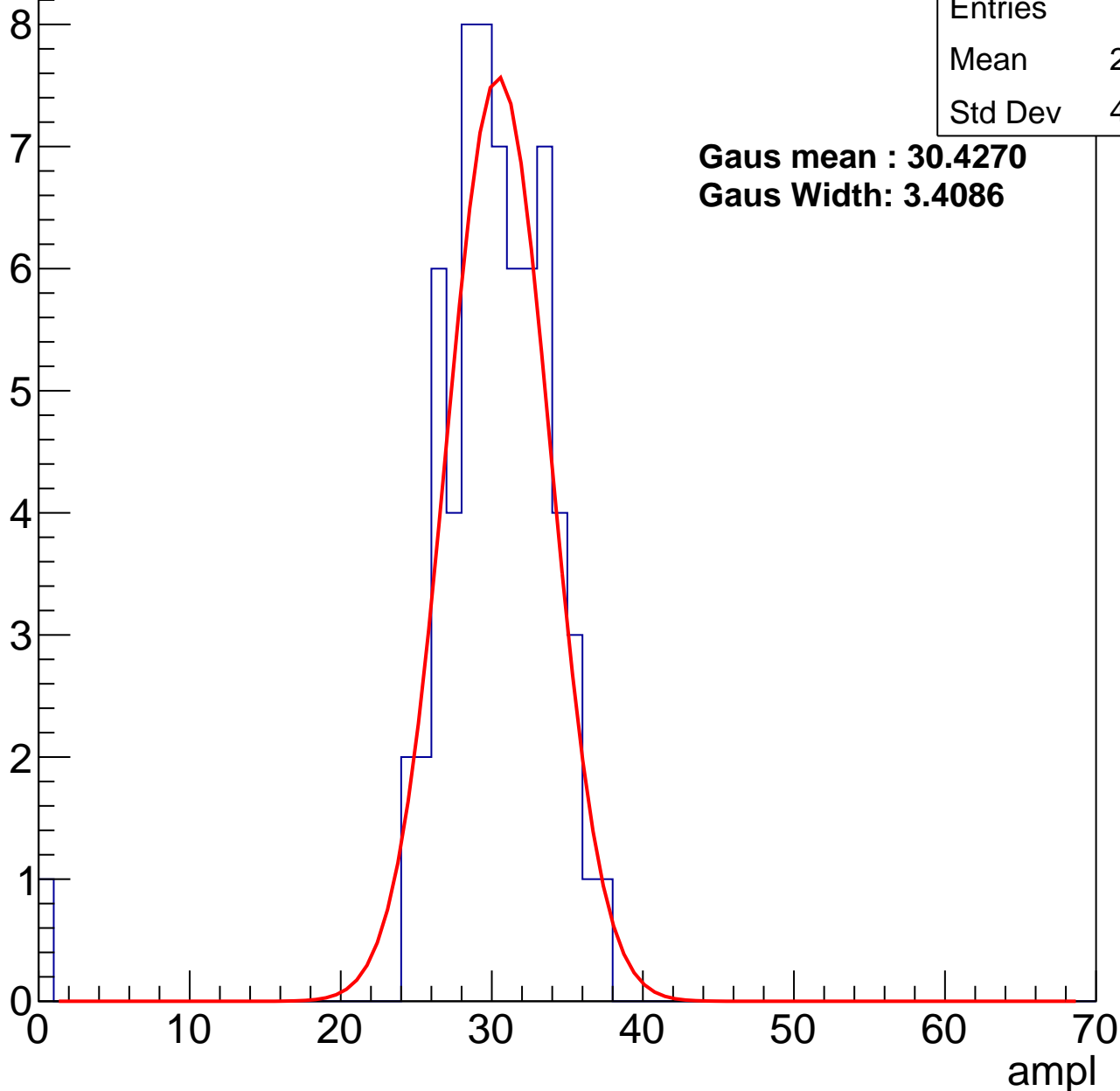
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.56
Std Dev	4.765

Gaus mean : 30.4270

Gaus Width: 3.4086



B1L003S, U20-ch91, adc1

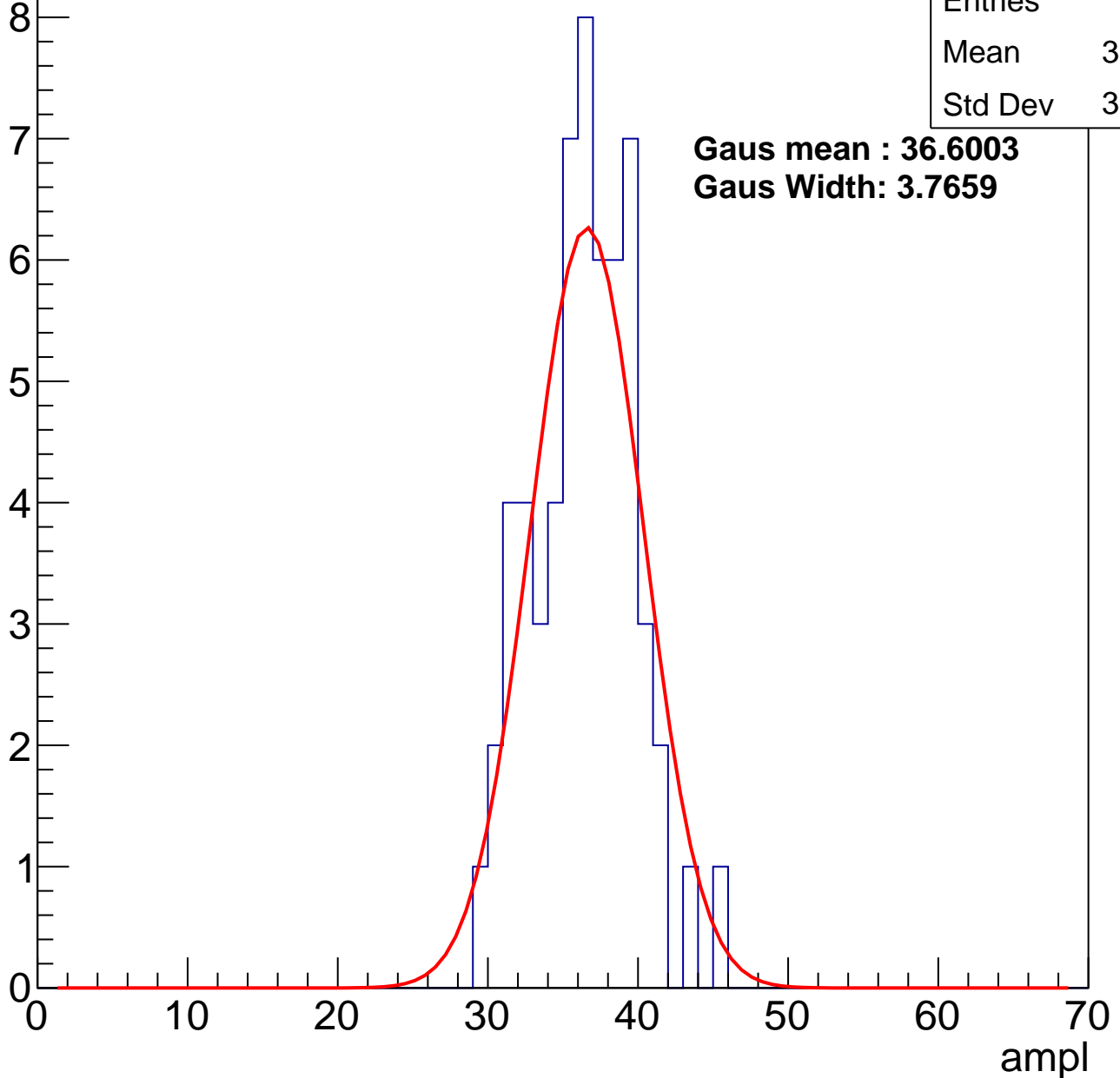
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.97
Std Dev	3.334

Gaus mean : 36.6003

Gaus Width: 3.7659



B1L003S, U20-ch91, adc2

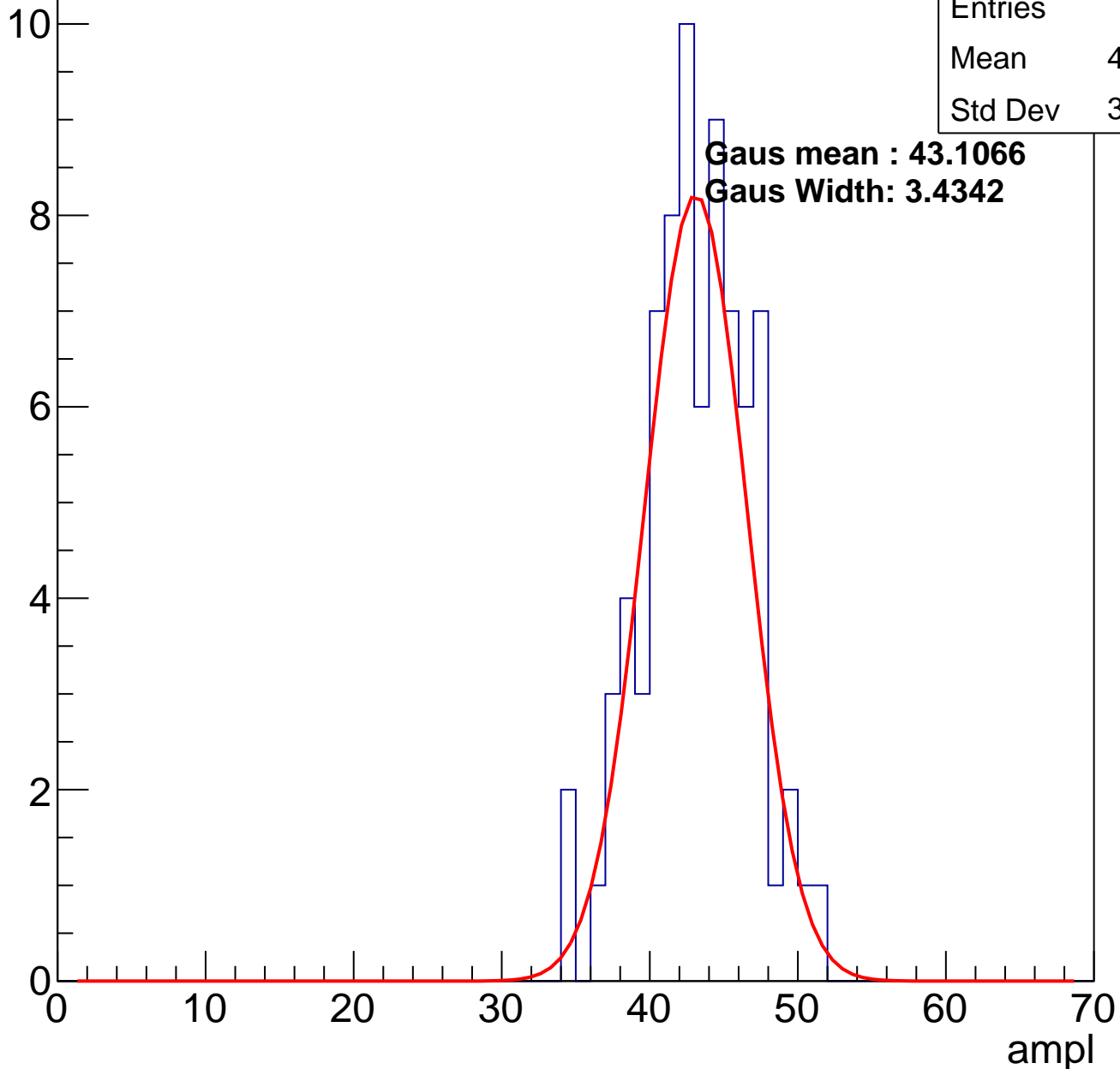
calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	42.73
Std Dev	3.543

Gaus mean : 43.1066

Gaus Width: 3.4342

Entry

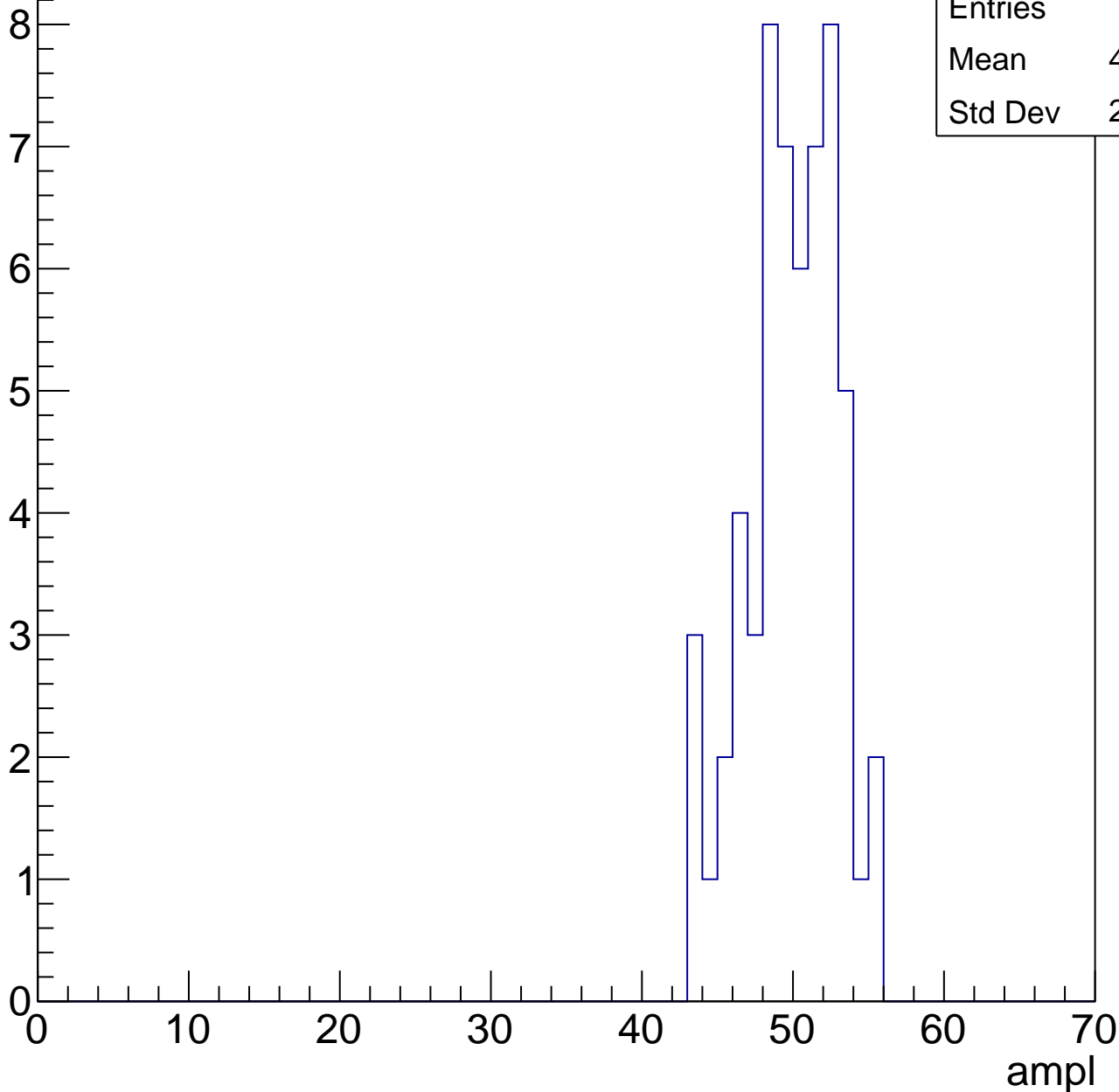


B1L003S, U20-ch91, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

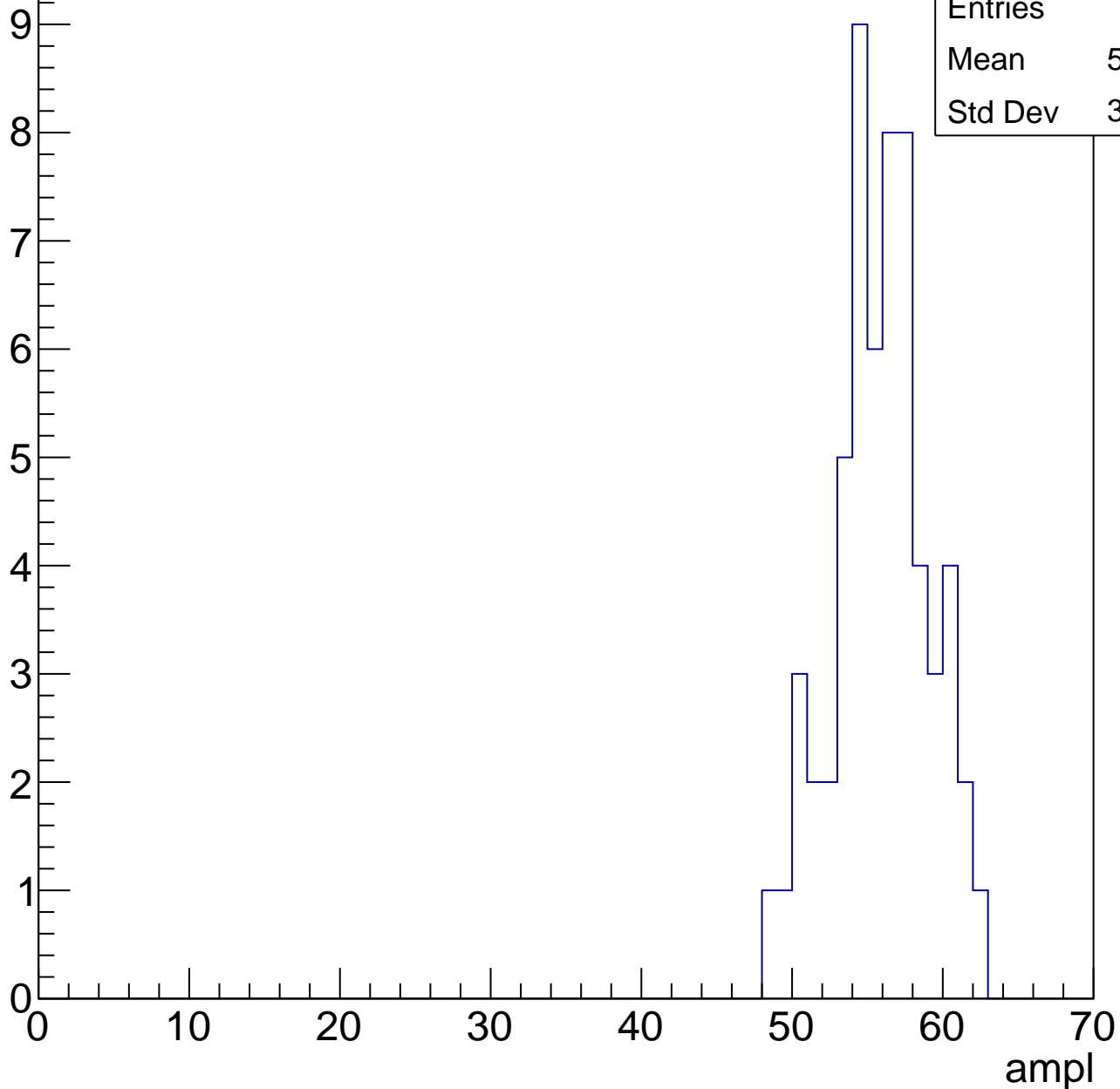
Entries	57
Mean	49.42
Std Dev	2.944



B1L003S, U20-ch91, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

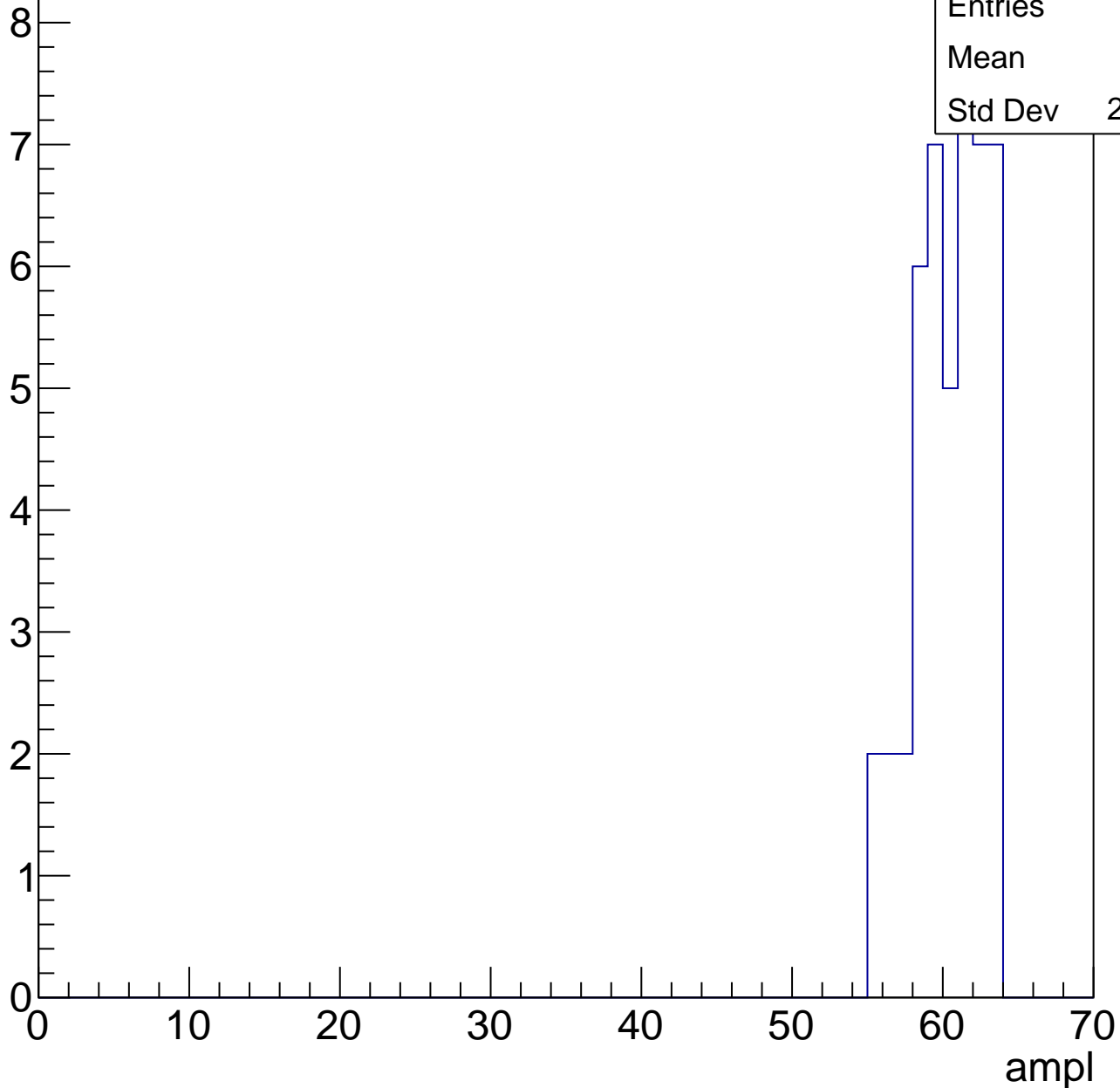


Entries	59
Mean	55.44
Std Dev	3.115

B1L003S, U20-ch91, adc5

calib_packv5_042523_0143.root, FC#13, port D2

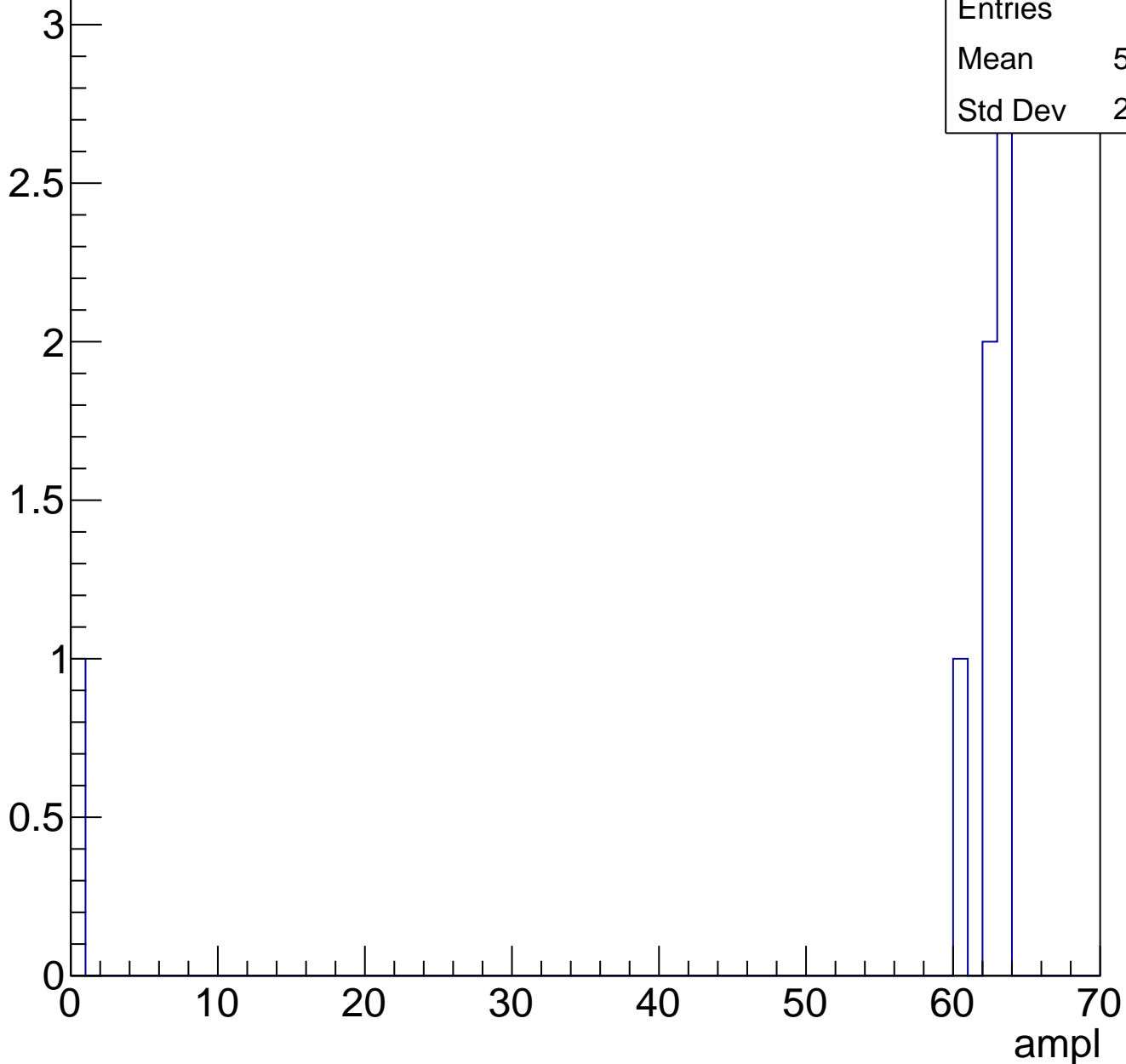
Entry



B1L003S, U20-ch91, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch91, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch92, adc0

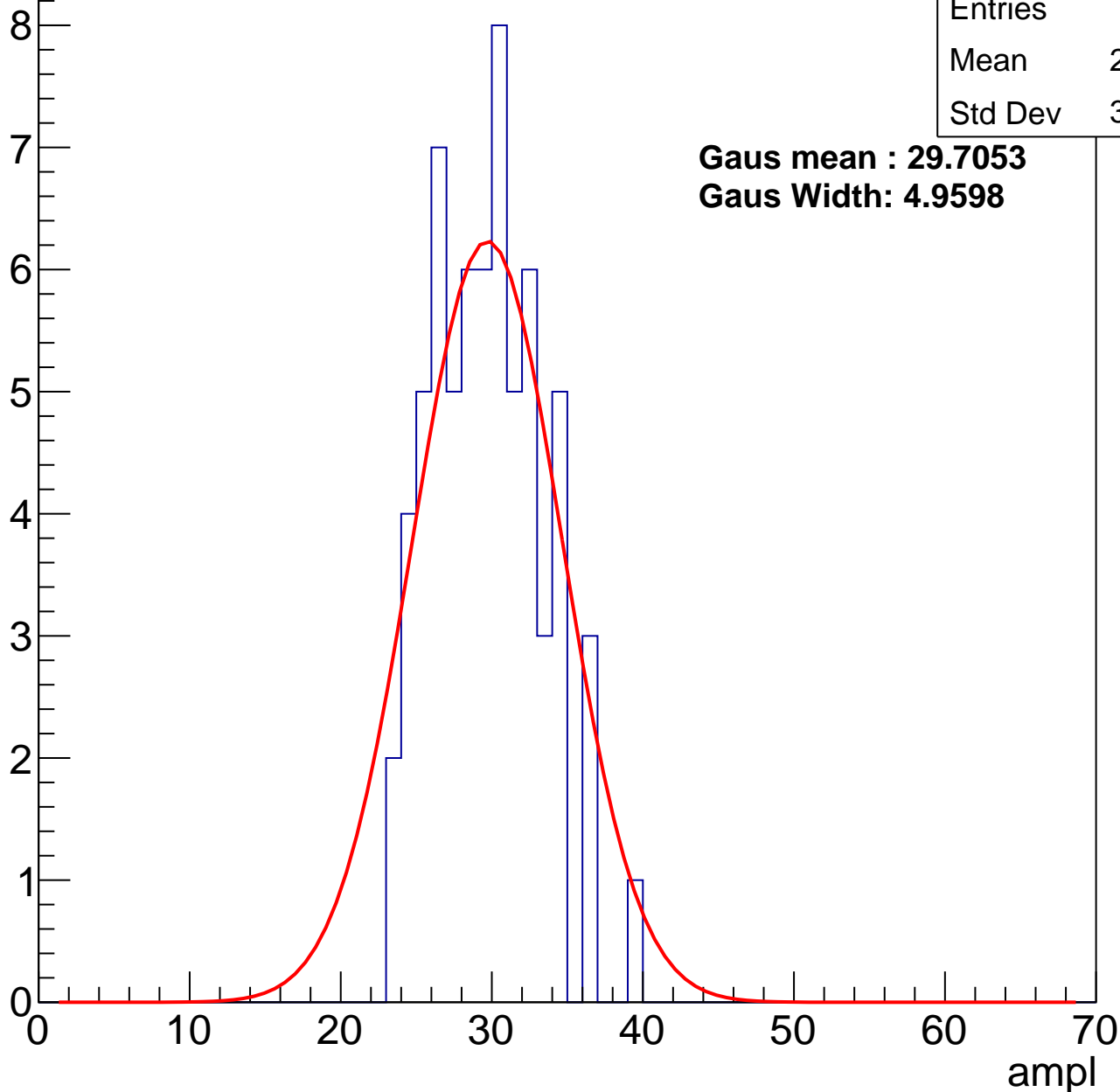
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.23
Std Dev	3.567

Gaus mean : 29.7053

Gaus Width: 4.9598



B1L003S, U20-ch92, adc1

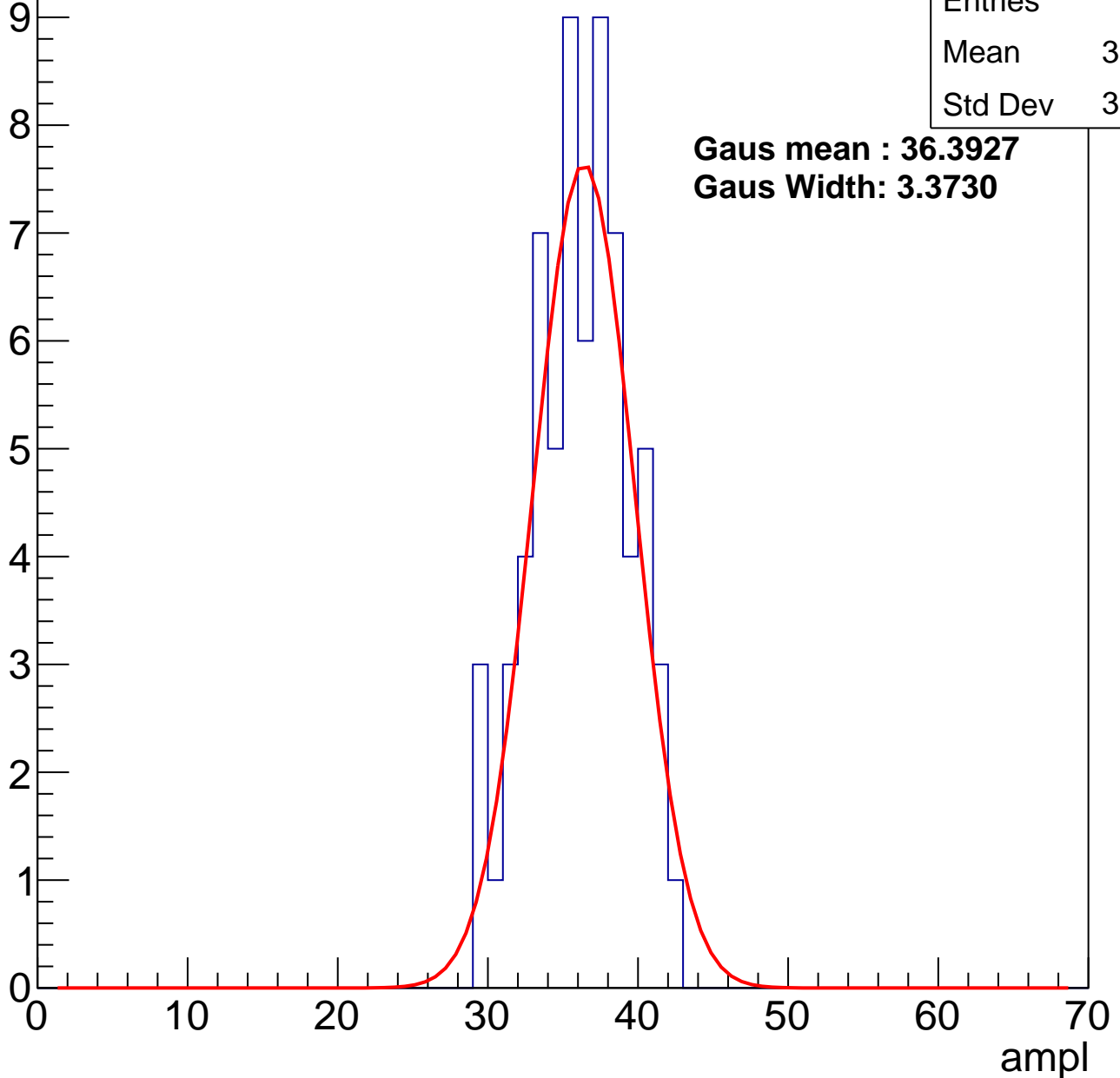
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.67
Std Dev	3.164

Gaus mean : 36.3927

Gaus Width: 3.3730



B1L003S, U20-ch92, adc2

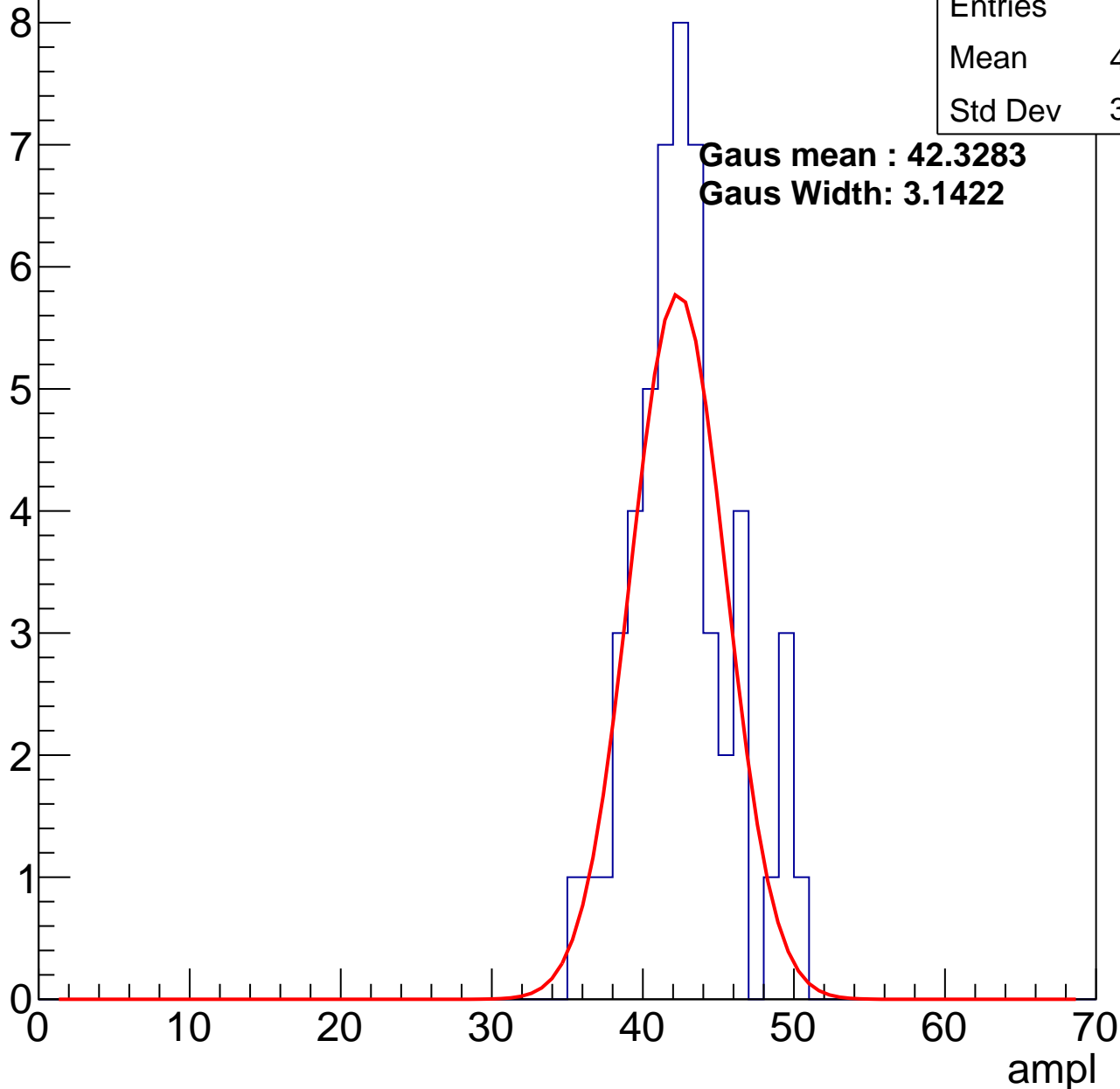
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	42.22
Std Dev	3.333

Gaus mean : 42.3283

Gaus Width: 3.1422

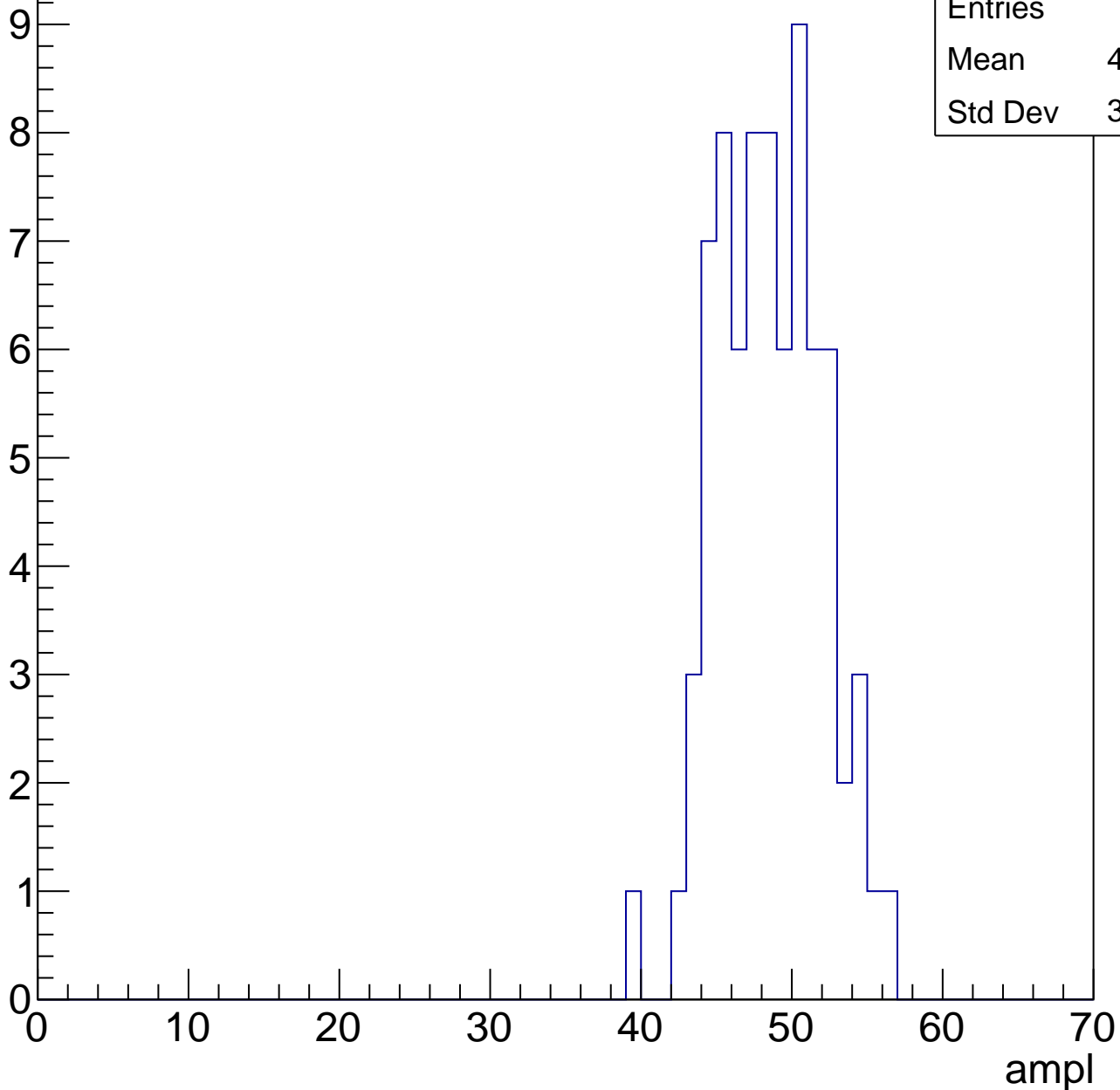


B1L003S, U20-ch92, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

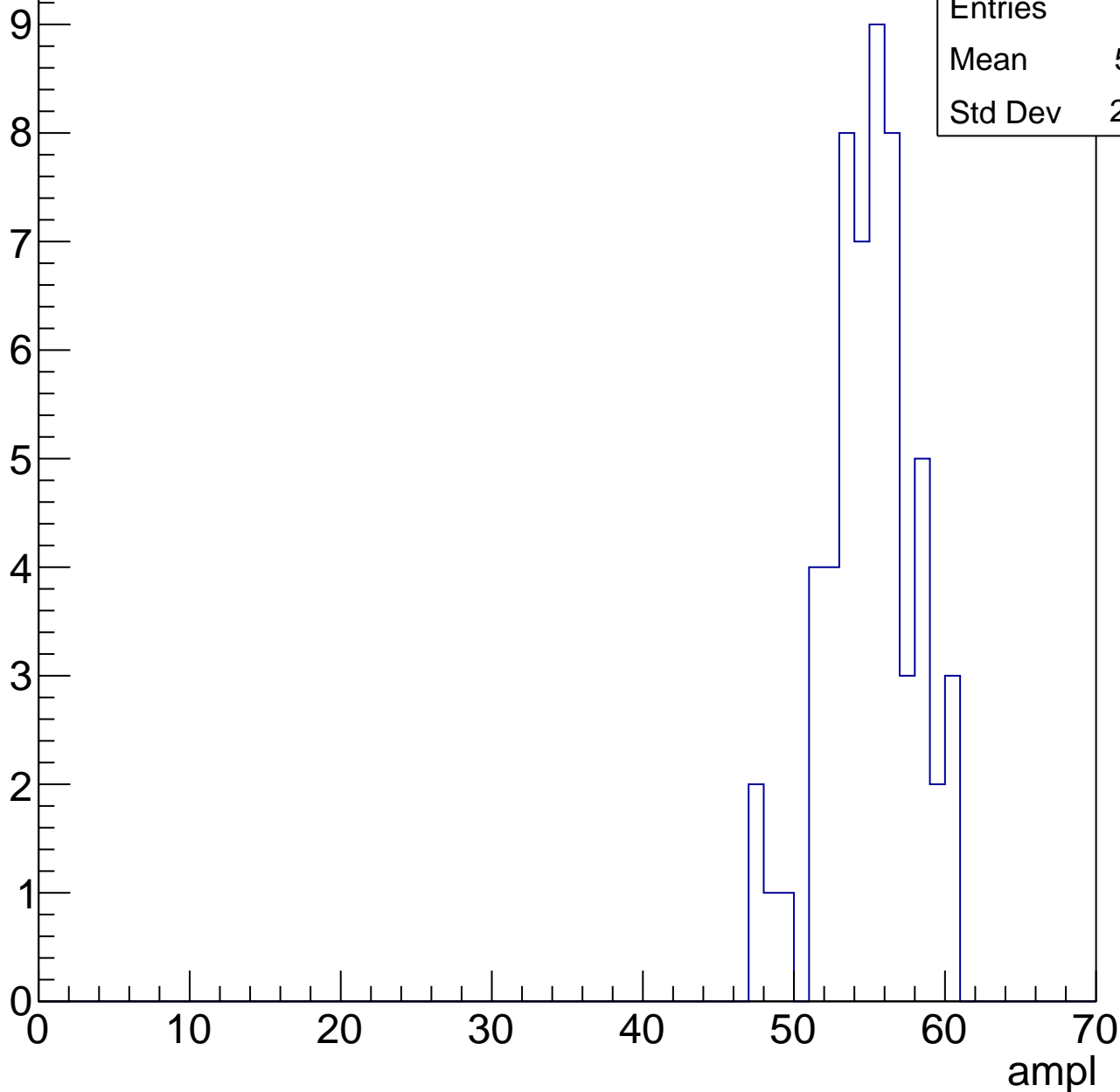
Entries	76
Mean	48.09
Std Dev	3.384



B1L003S, U20-ch92, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

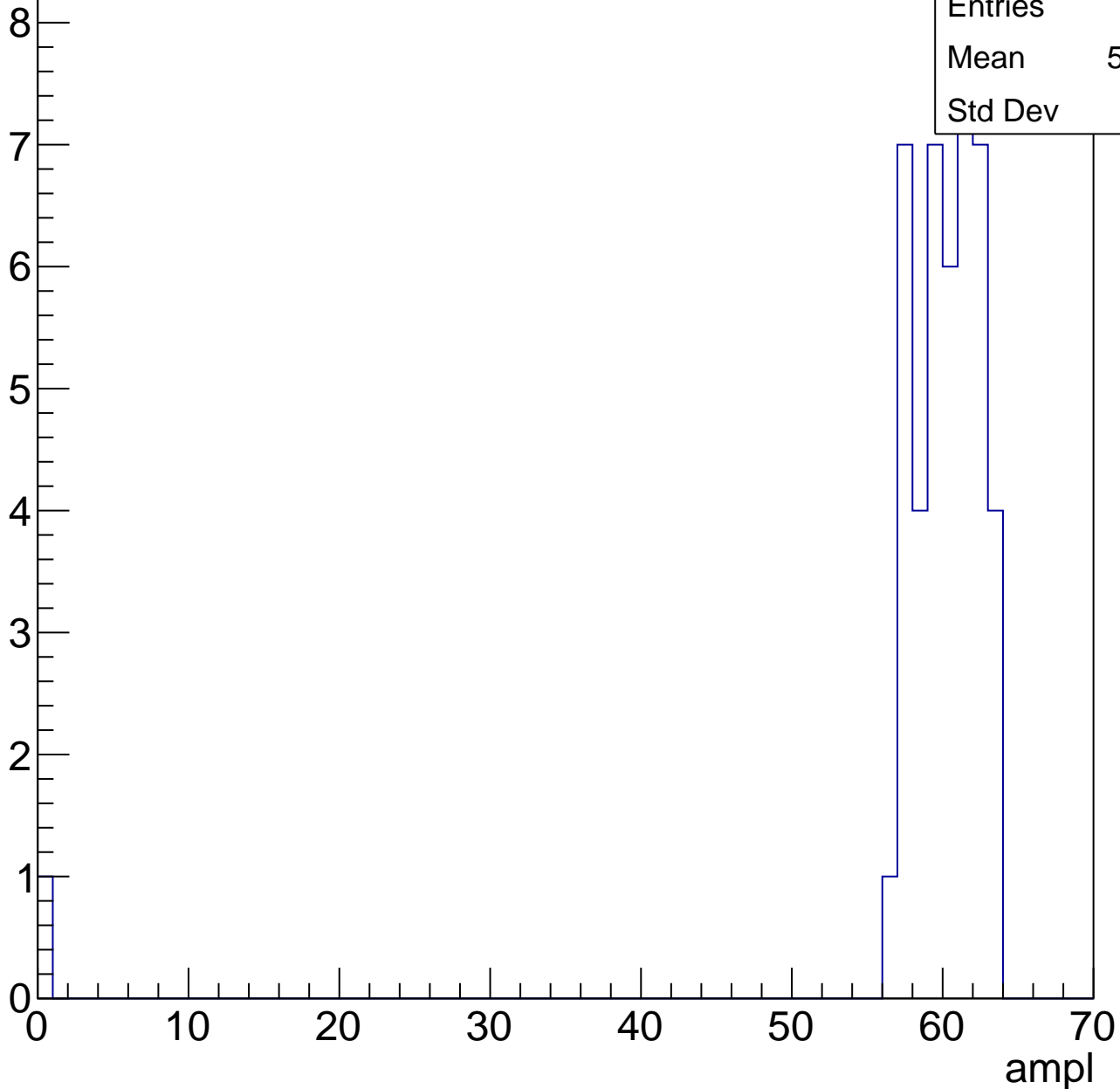


B1L003S, U20-ch92, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.53
Std Dev	9.04

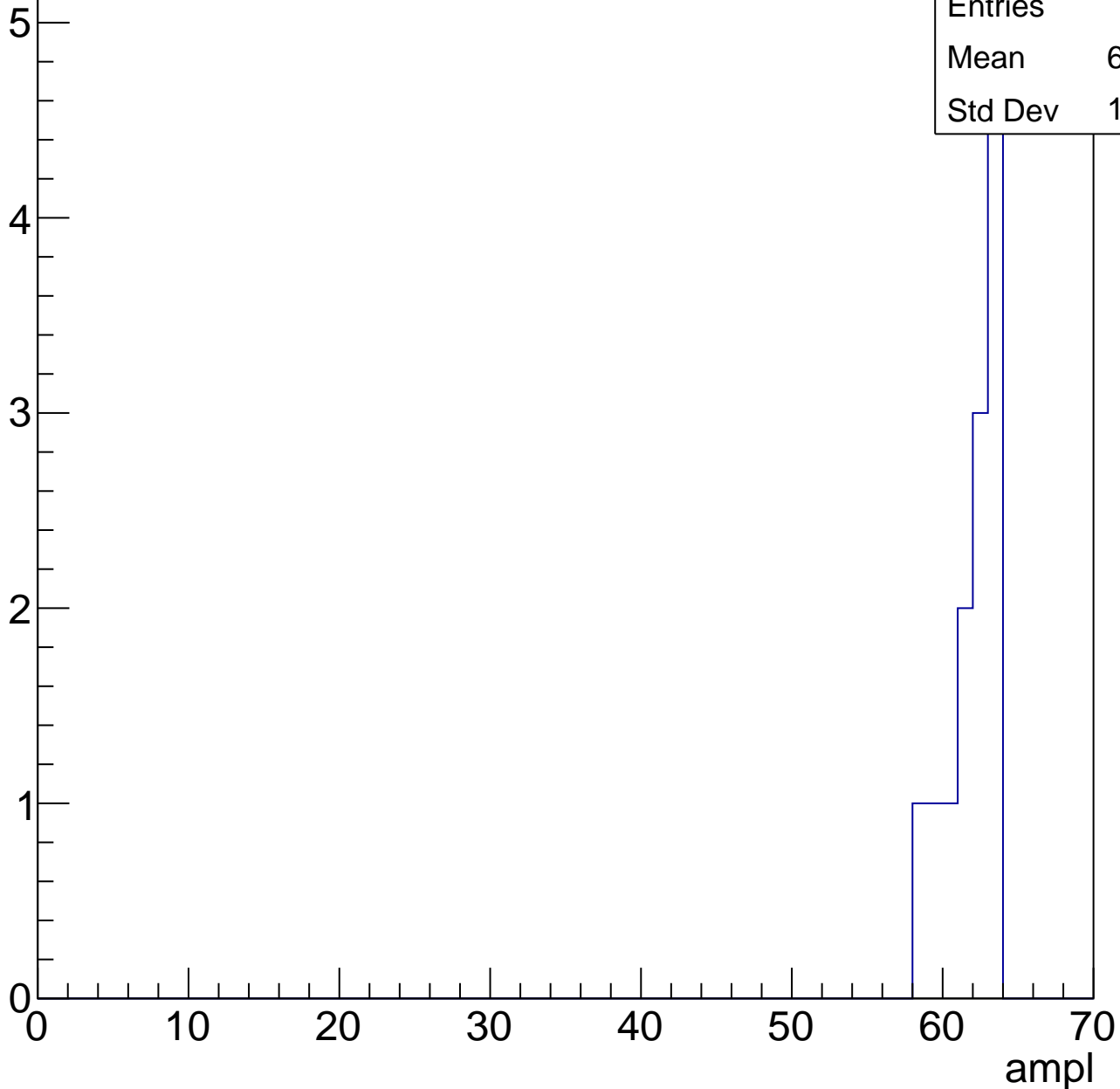


B1L003S, U20-ch92, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.54
Std Dev	1.599



B1L003S, U20-ch92, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch93, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	29.41
Std Dev	3.149

Gaus mean : 30.1544

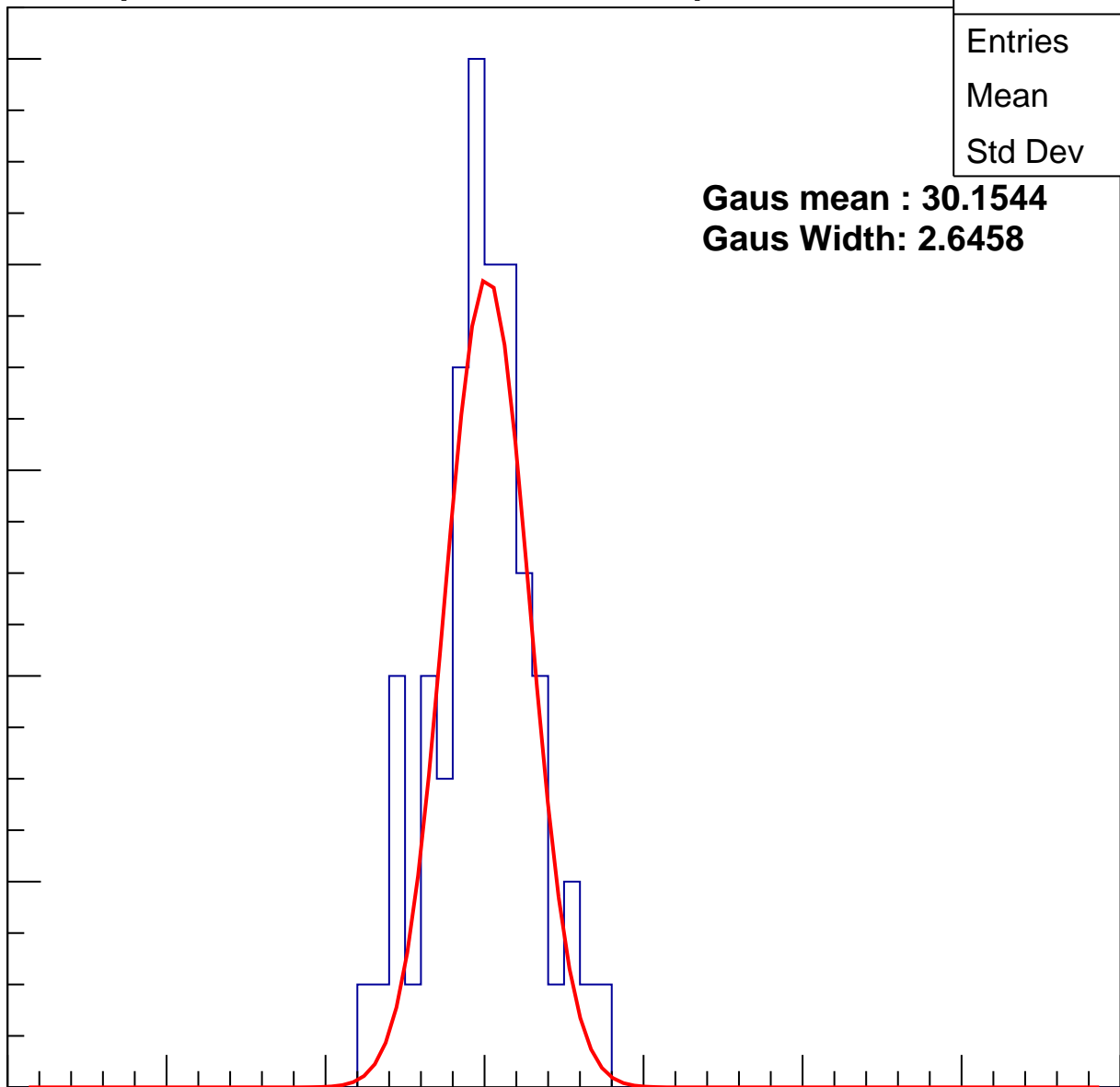
Gaus Width: 2.6458

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch93, adc1

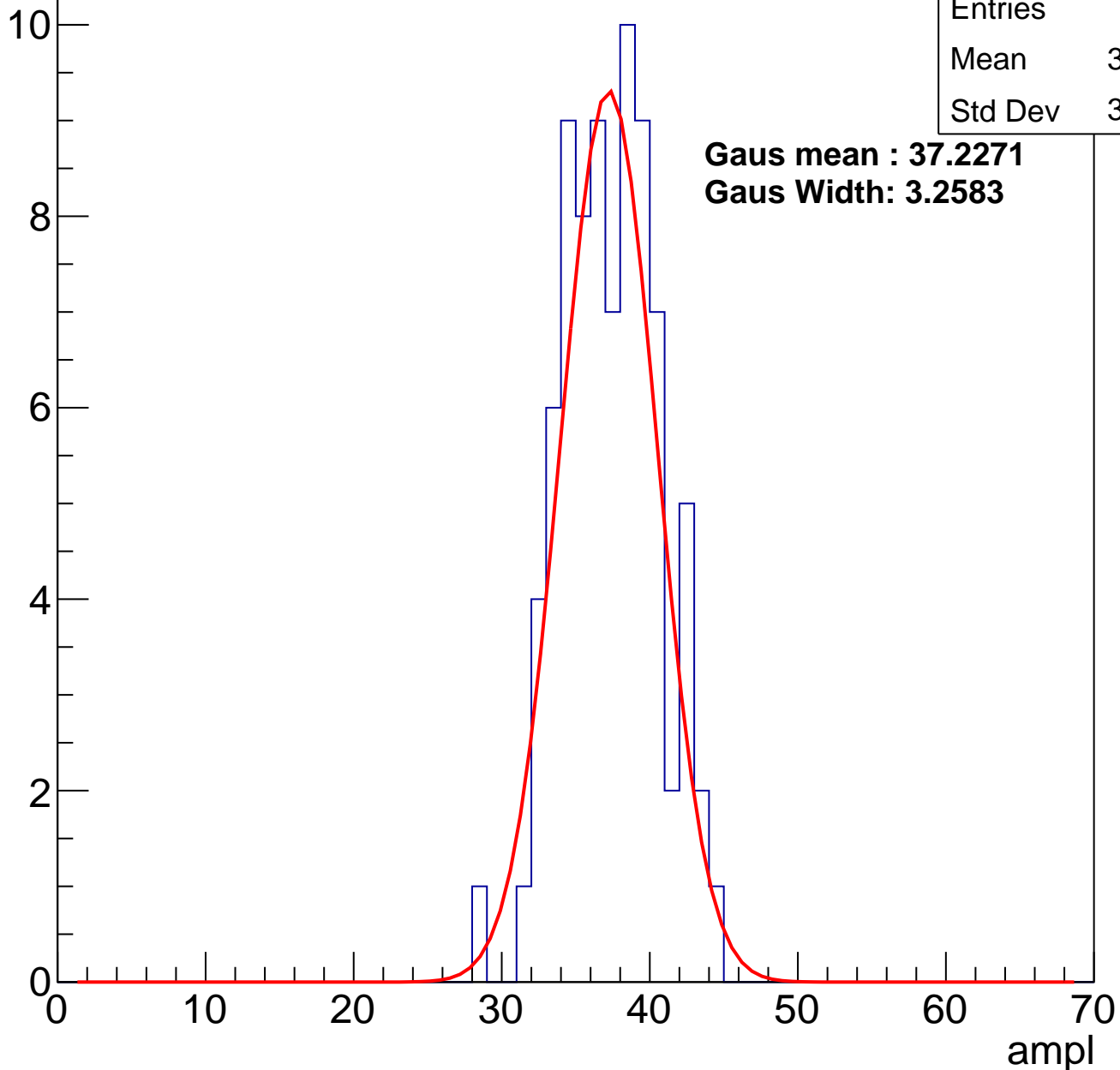
calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	36.88
Std Dev	3.183

Gaus mean : 37.2271

Gaus Width: 3.2583

Entry



B1L003S, U20-ch93, adc2

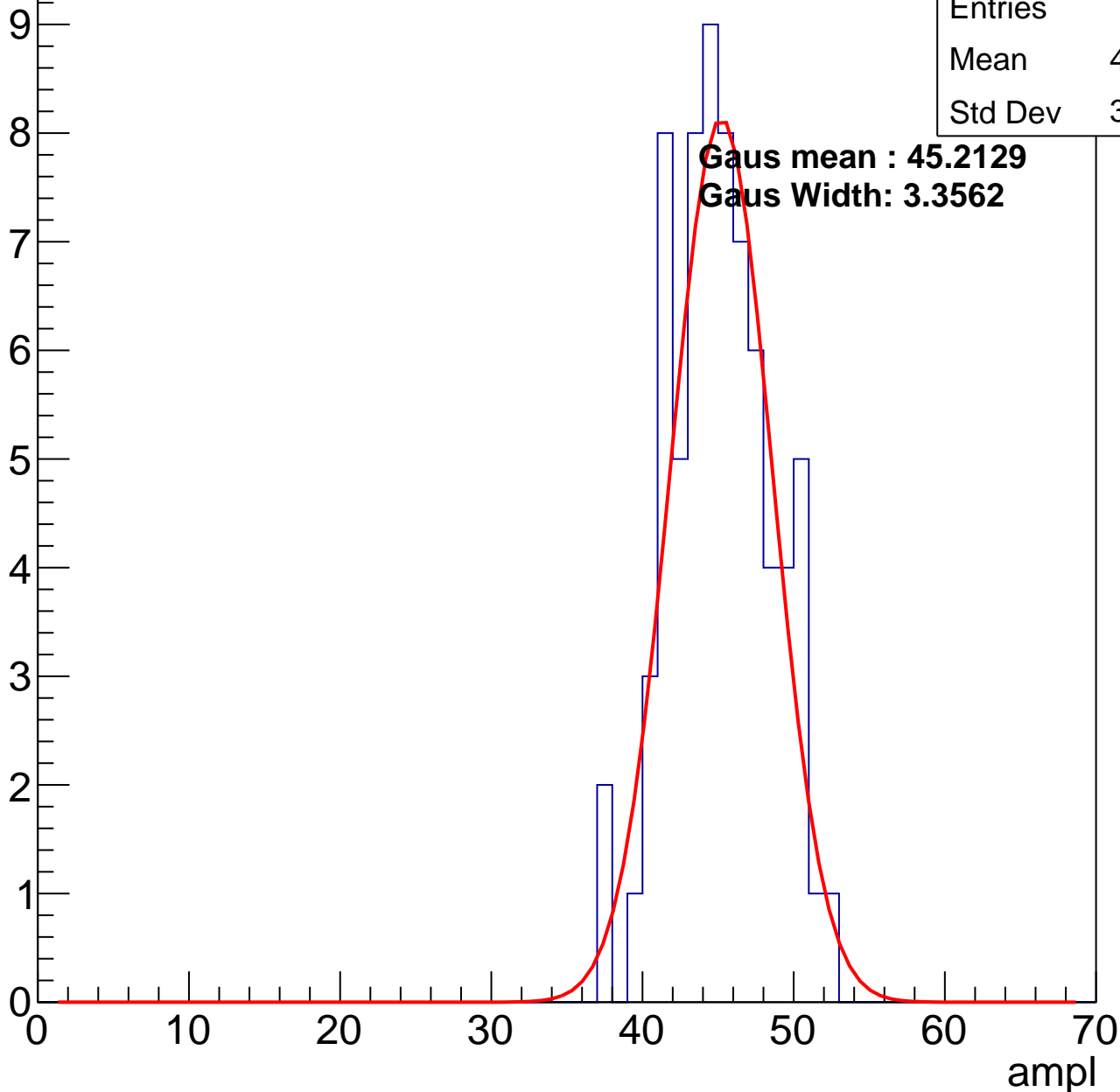
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.67
Std Dev	3.304

Gaus mean : 45.2129

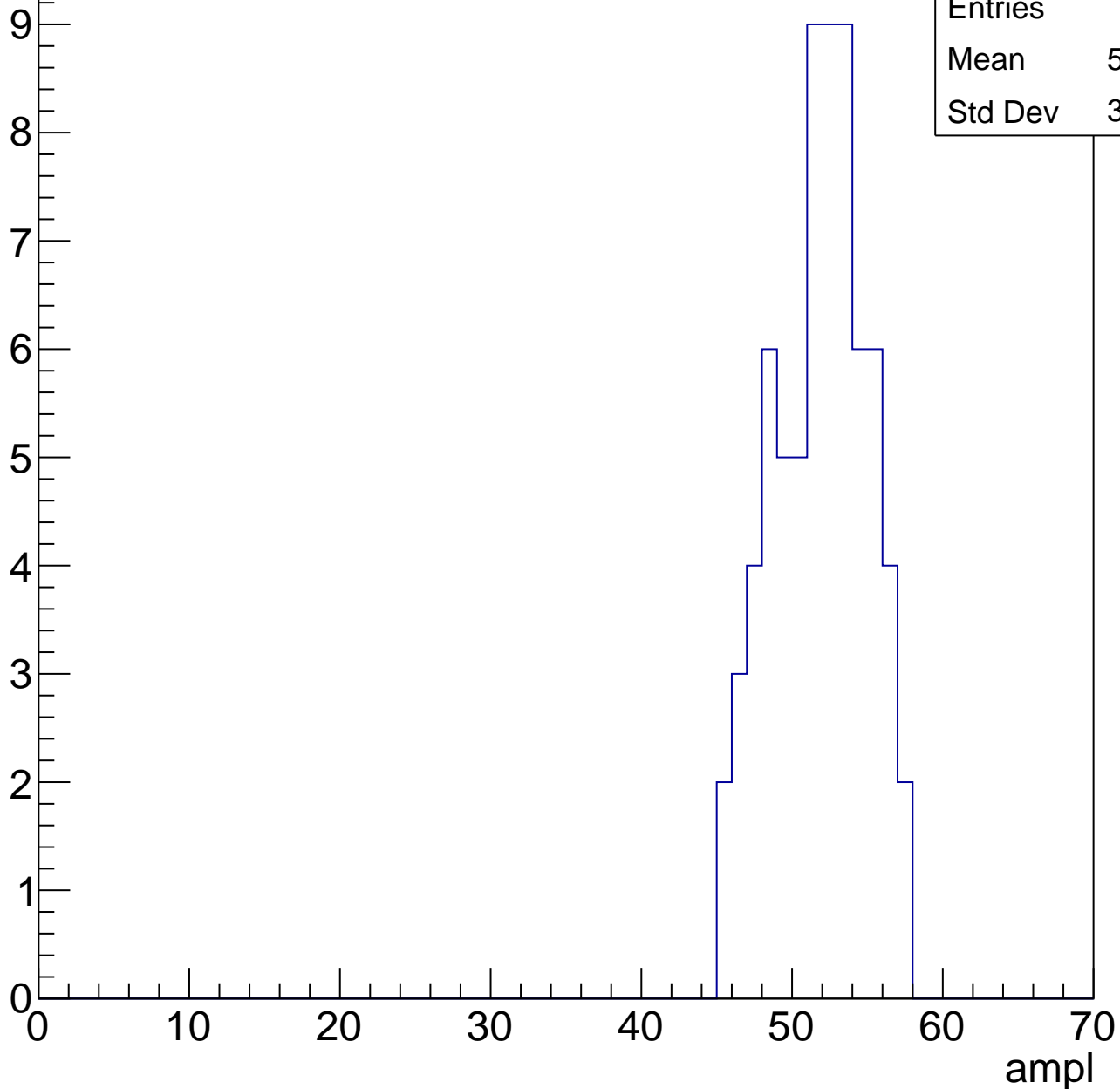
Gaus Width: 3.3562



B1L003S, U20-ch93, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch93, adc4

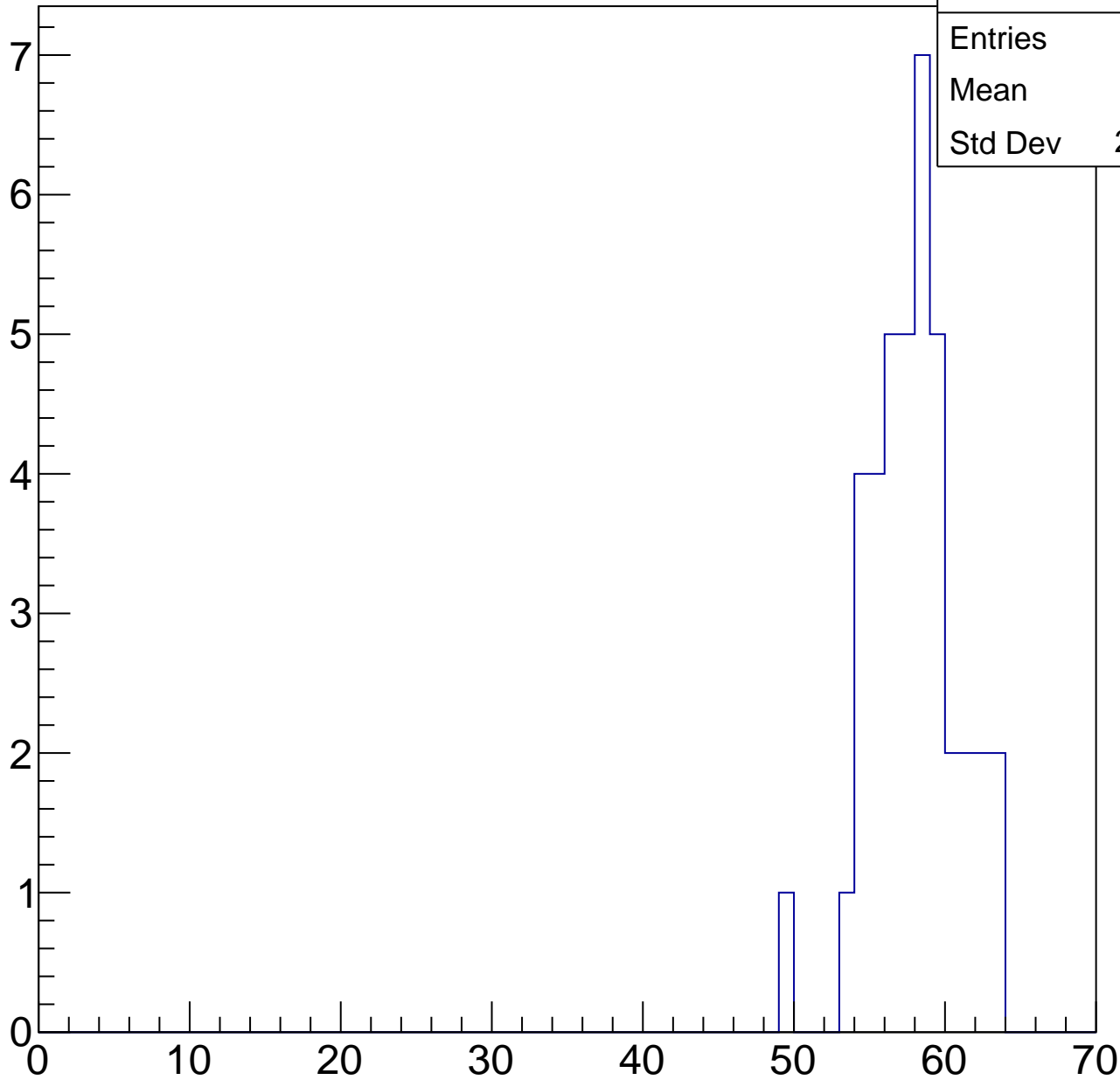
calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	40
Mean	57.4
Std Dev	2.871

ampl

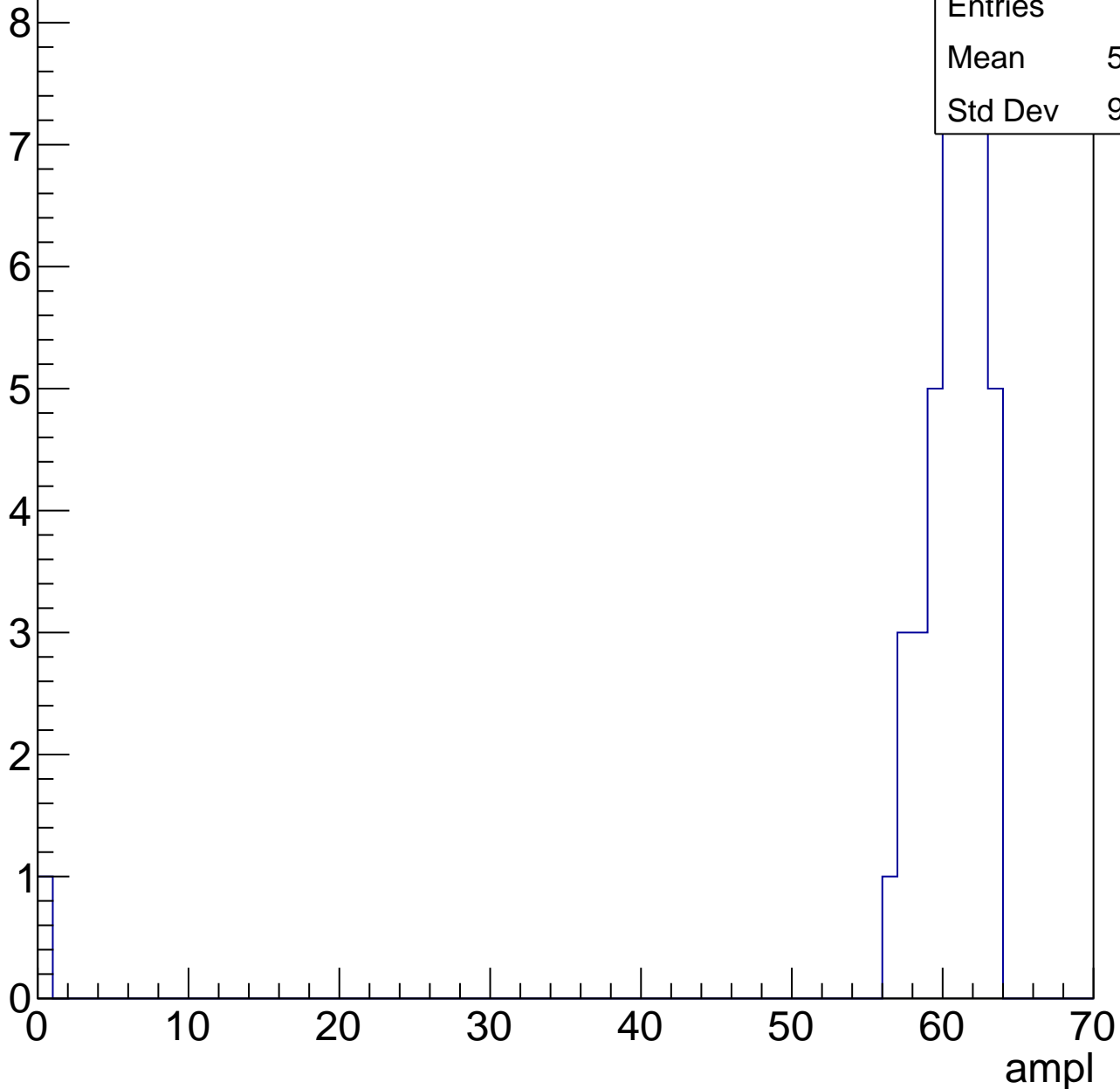


B1L003S, U20-ch93, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.93
Std Dev	9.382



B1L003S, U20-ch93, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch93, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch94, adc0

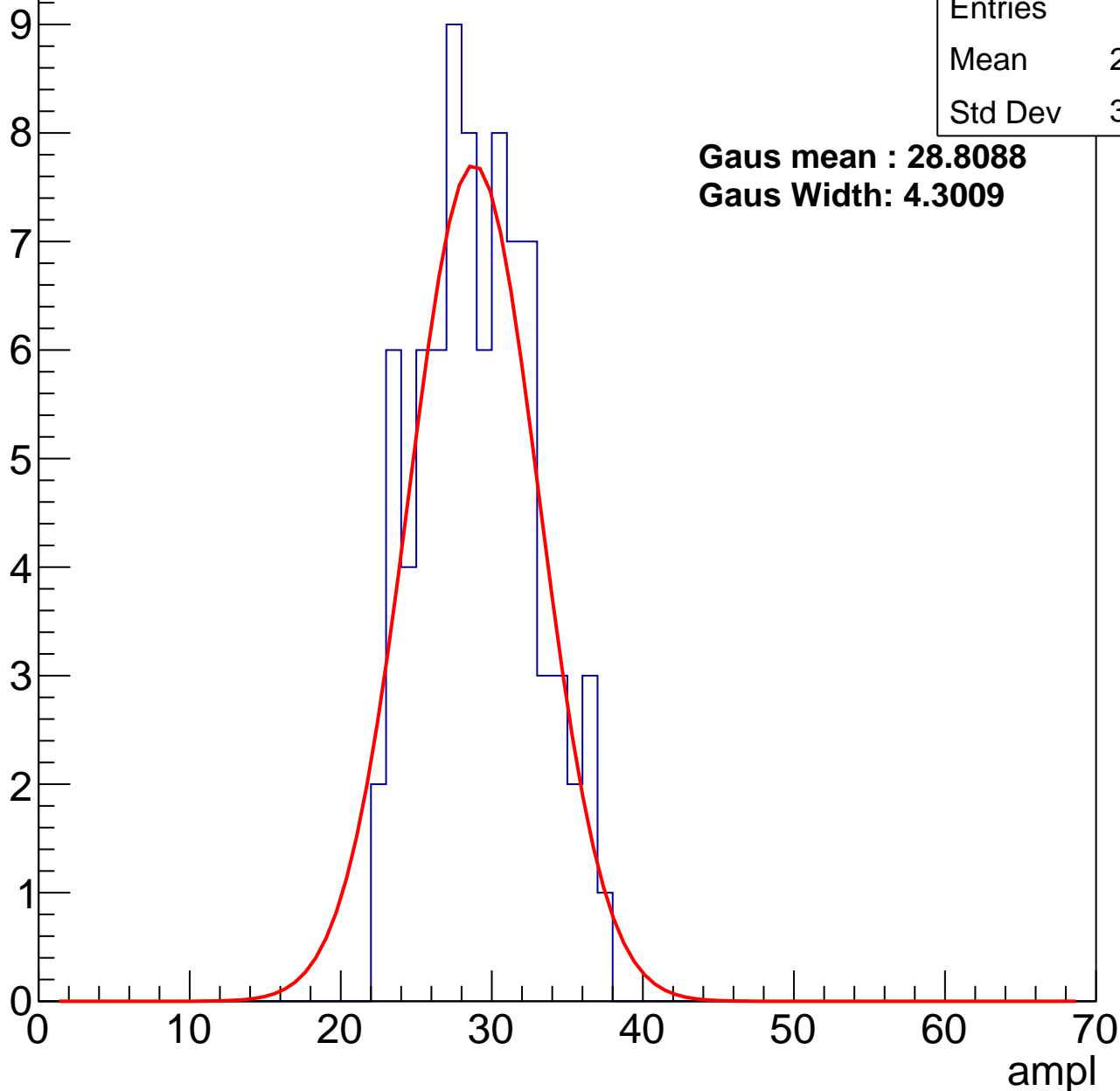
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	28.67
Std Dev	3.692

Gaus mean : 28.8088

Gaus Width: 4.3009



B1L003S, U20-ch94, adc1

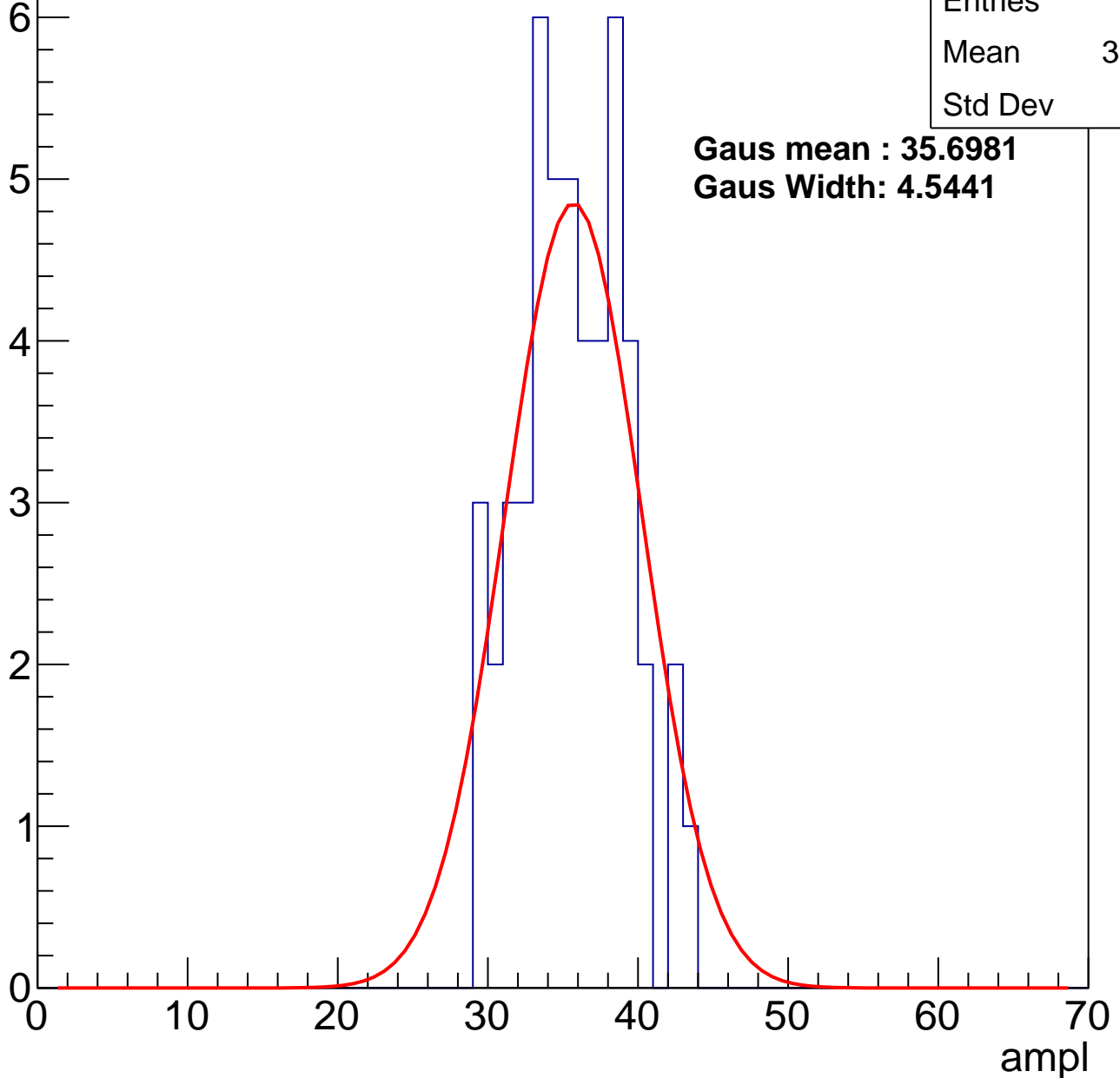
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	35.24
Std Dev	3.49

Gaus mean : 35.6981

Gaus Width: 4.5441



B1L003S, U20-ch94, adc2

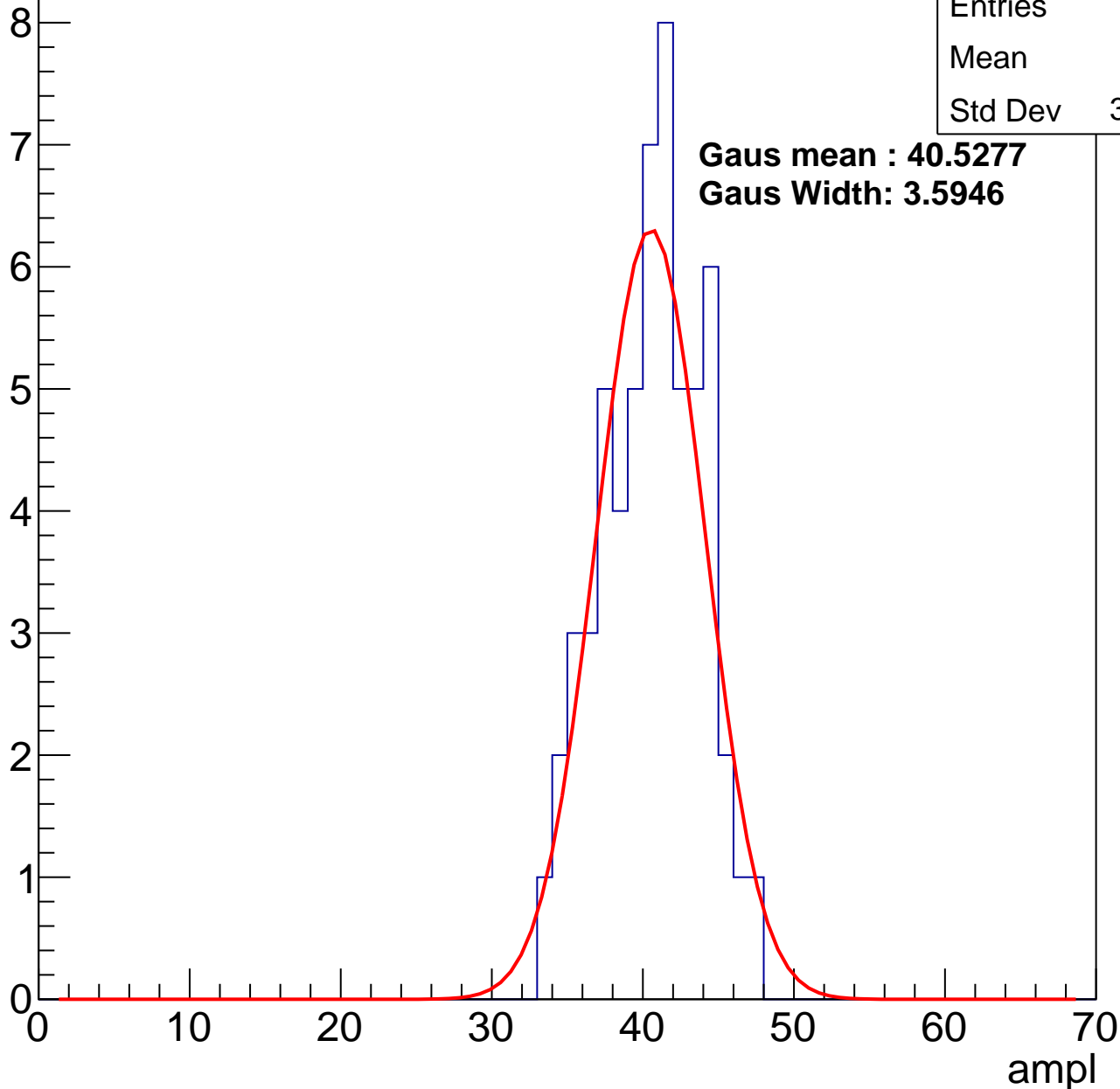
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	40.1
Std Dev	3.252

Gaus mean : 40.5277

Gaus Width: 3.5946

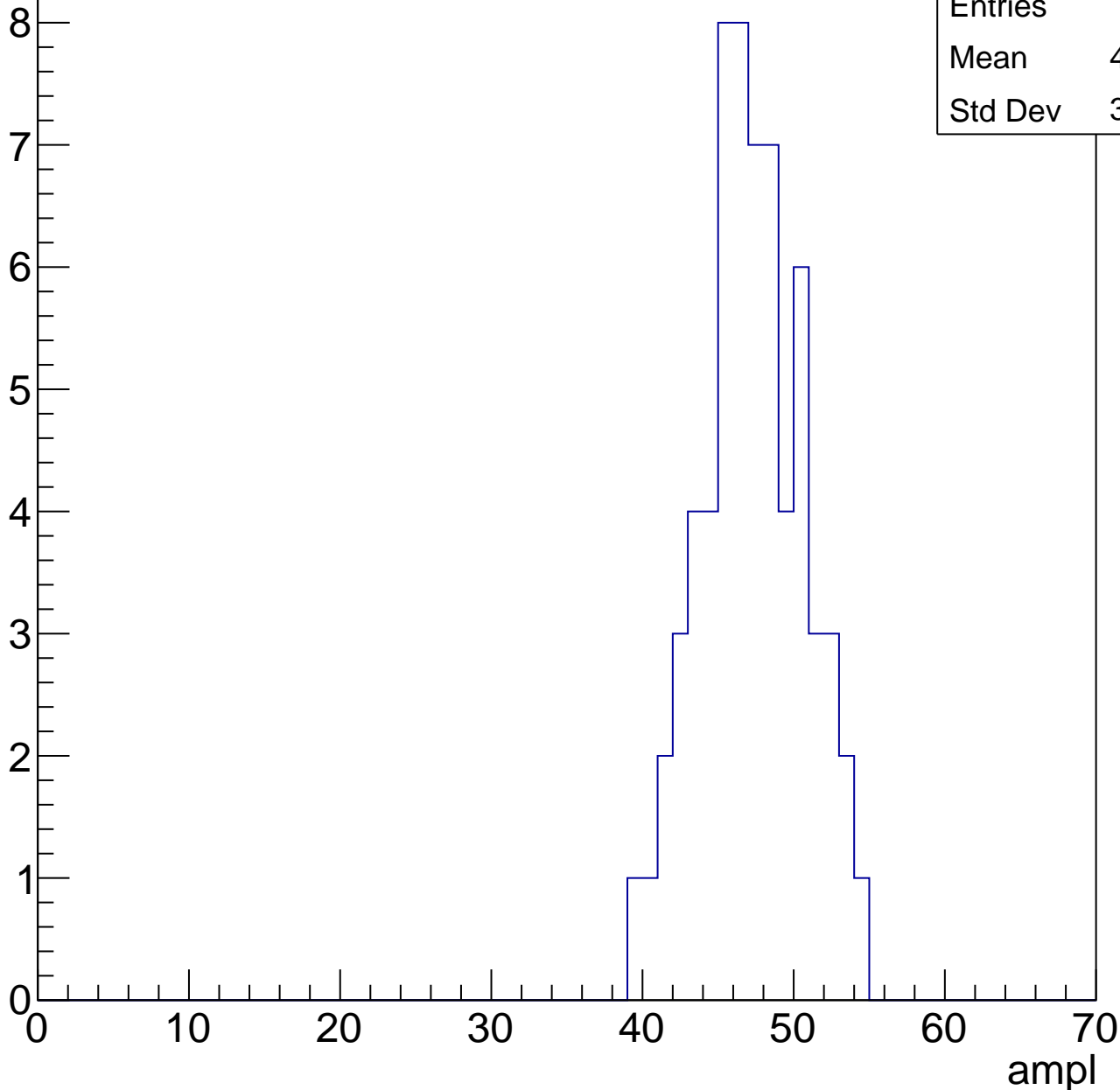


B1L003S, U20-ch94, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	46.77
Std Dev	3.348

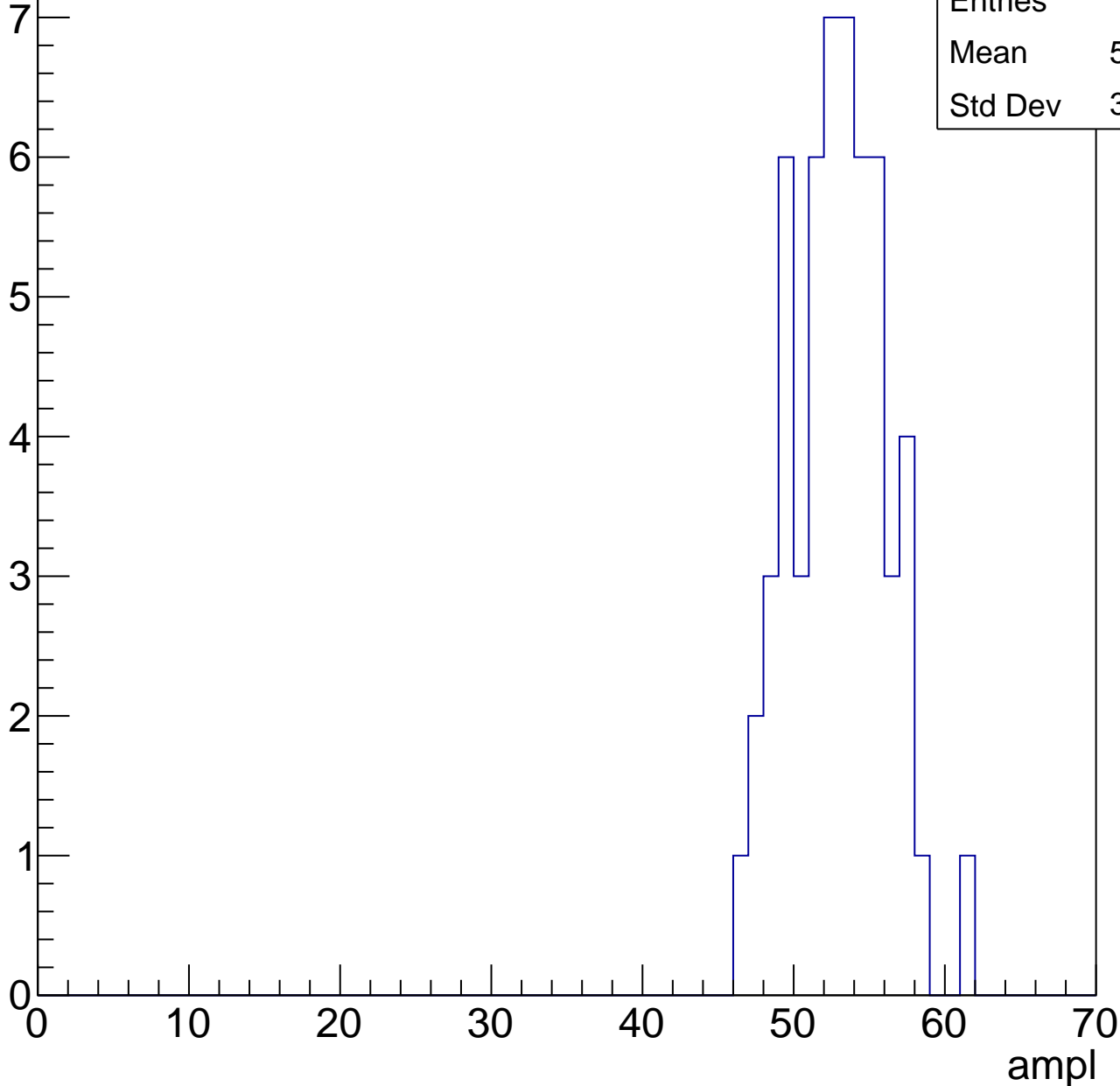


B1L003S, U20-ch94, adc4

calib_packv5_042523_0143.root, FC#13, port D2

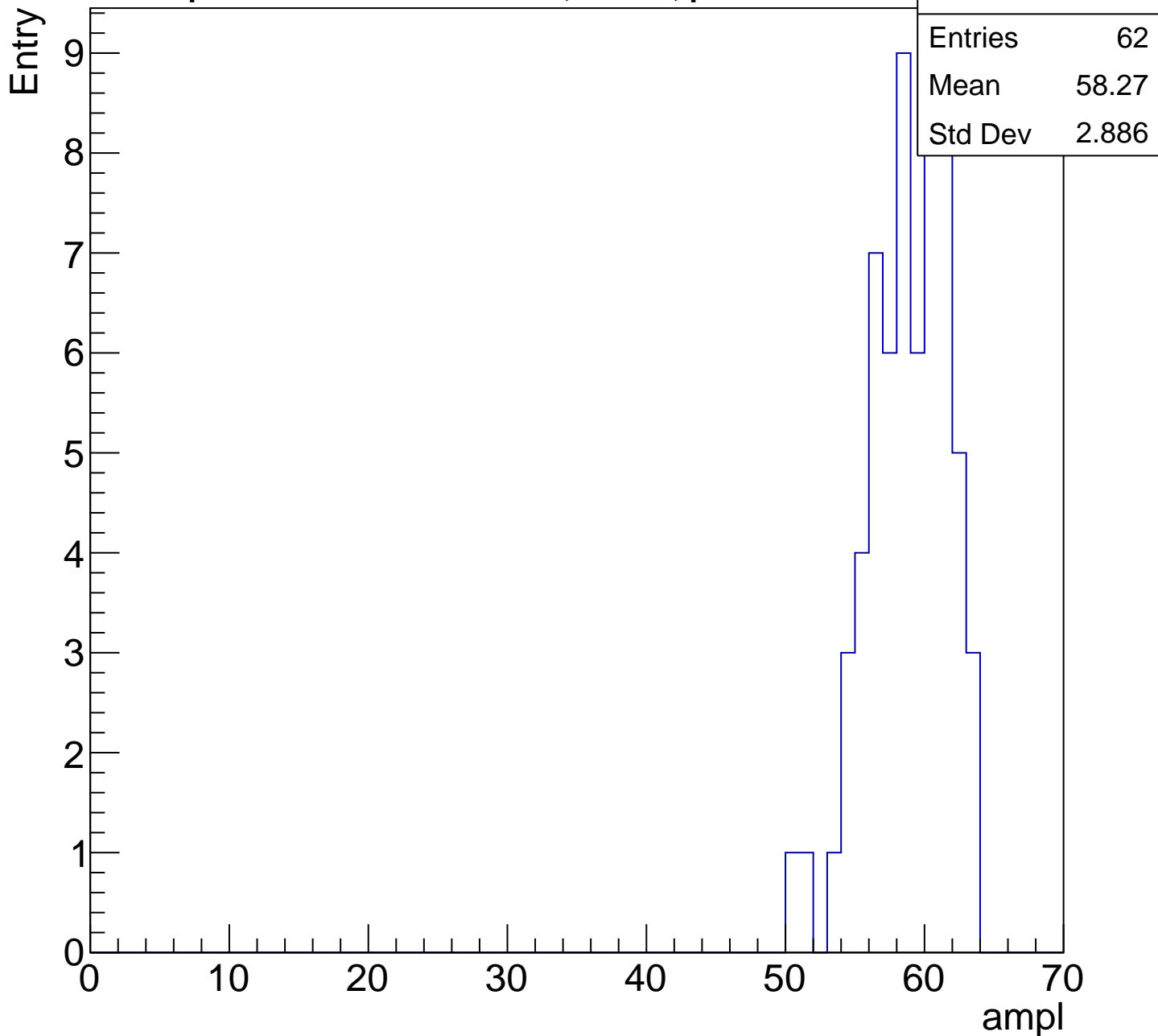
Entry

Entries	56
Mean	52.46
Std Dev	3.117



B1L003S, U20-ch94, adc5

calib_packv5_042523_0143.root, FC#13, port D2

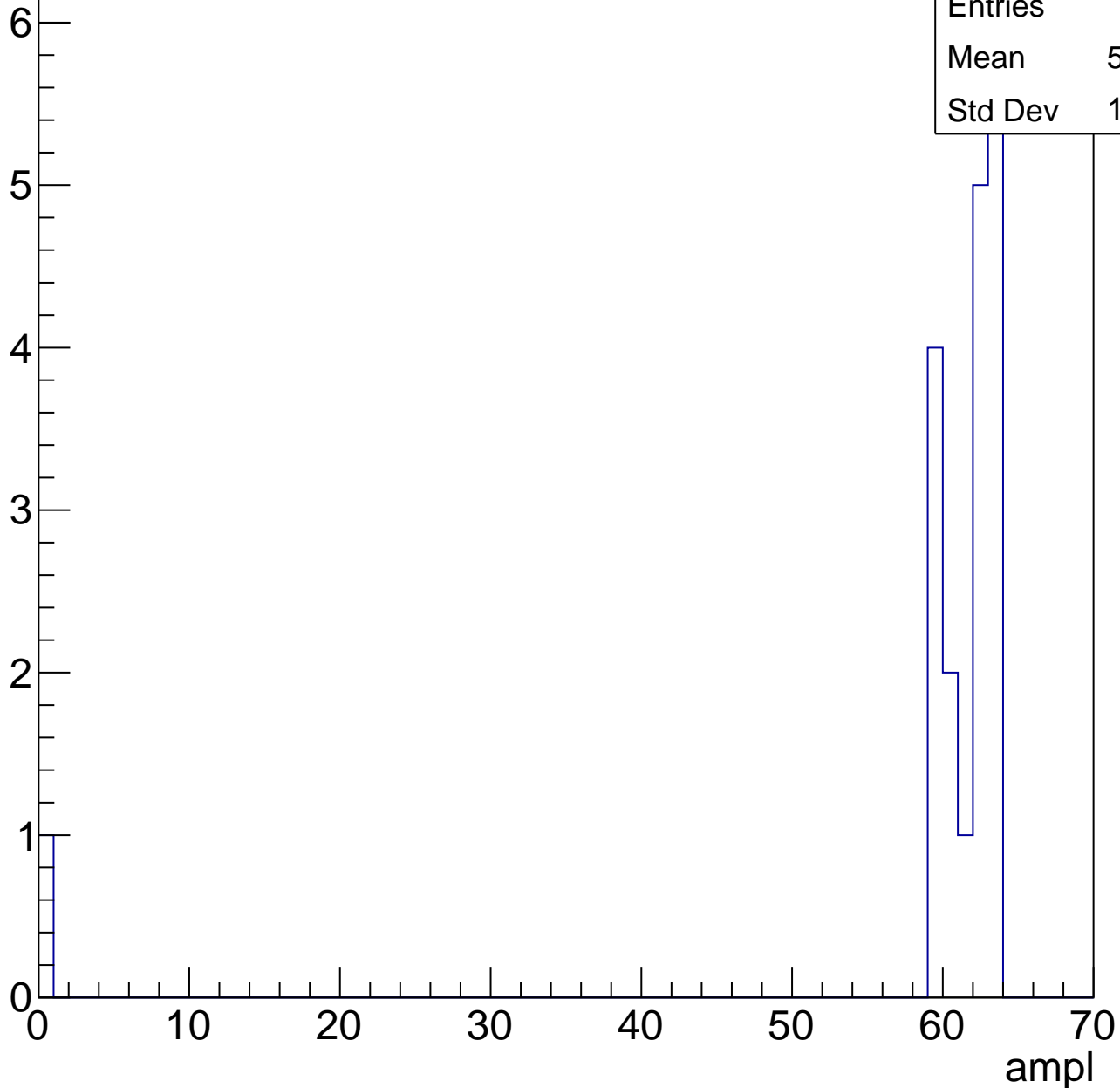


B1L003S, U20-ch94, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	58.16
Std Dev	13.79



B1L003S, U20-ch94, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch95, adc0

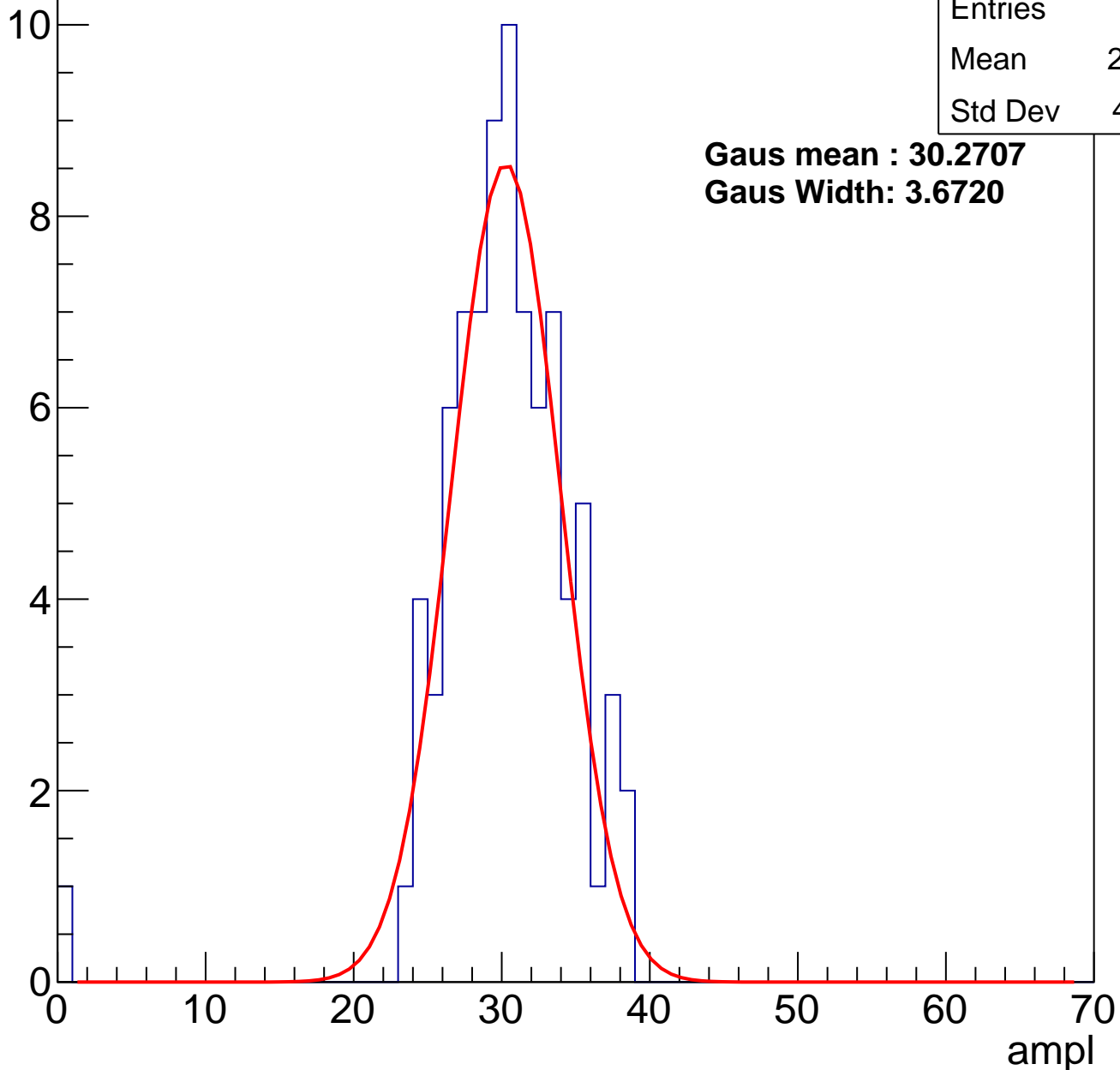
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	29.76
Std Dev	4.851

Gaus mean : 30.2707

Gaus Width: 3.6720

Entry



B1L003S, U20-ch95, adc1

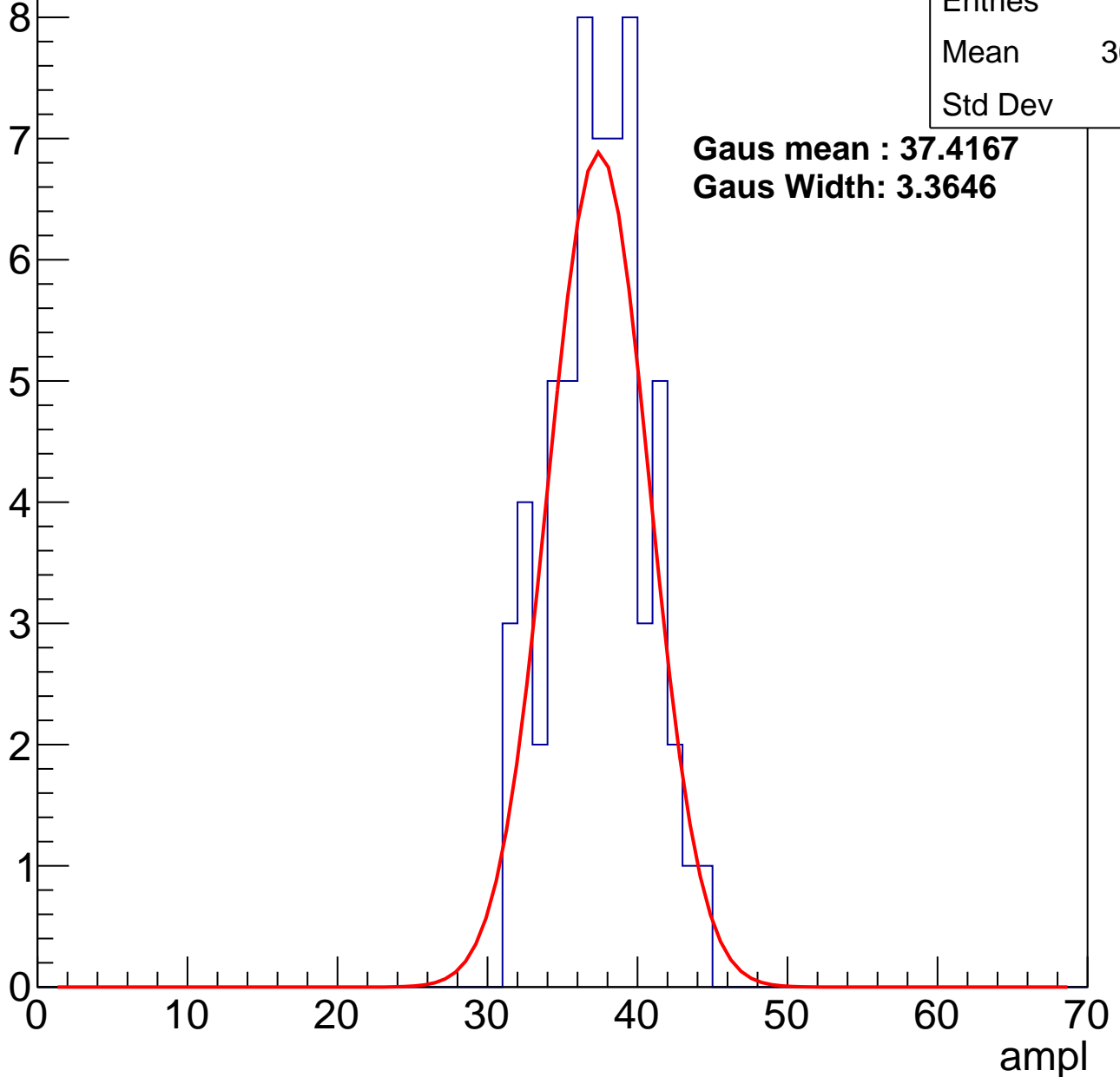
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.93
Std Dev	3.12

Gaus mean : 37.4167

Gaus Width: 3.3646



B1L003S, U20-ch95, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	43.71
Std Dev	3.061

Gaus mean : 44.0967

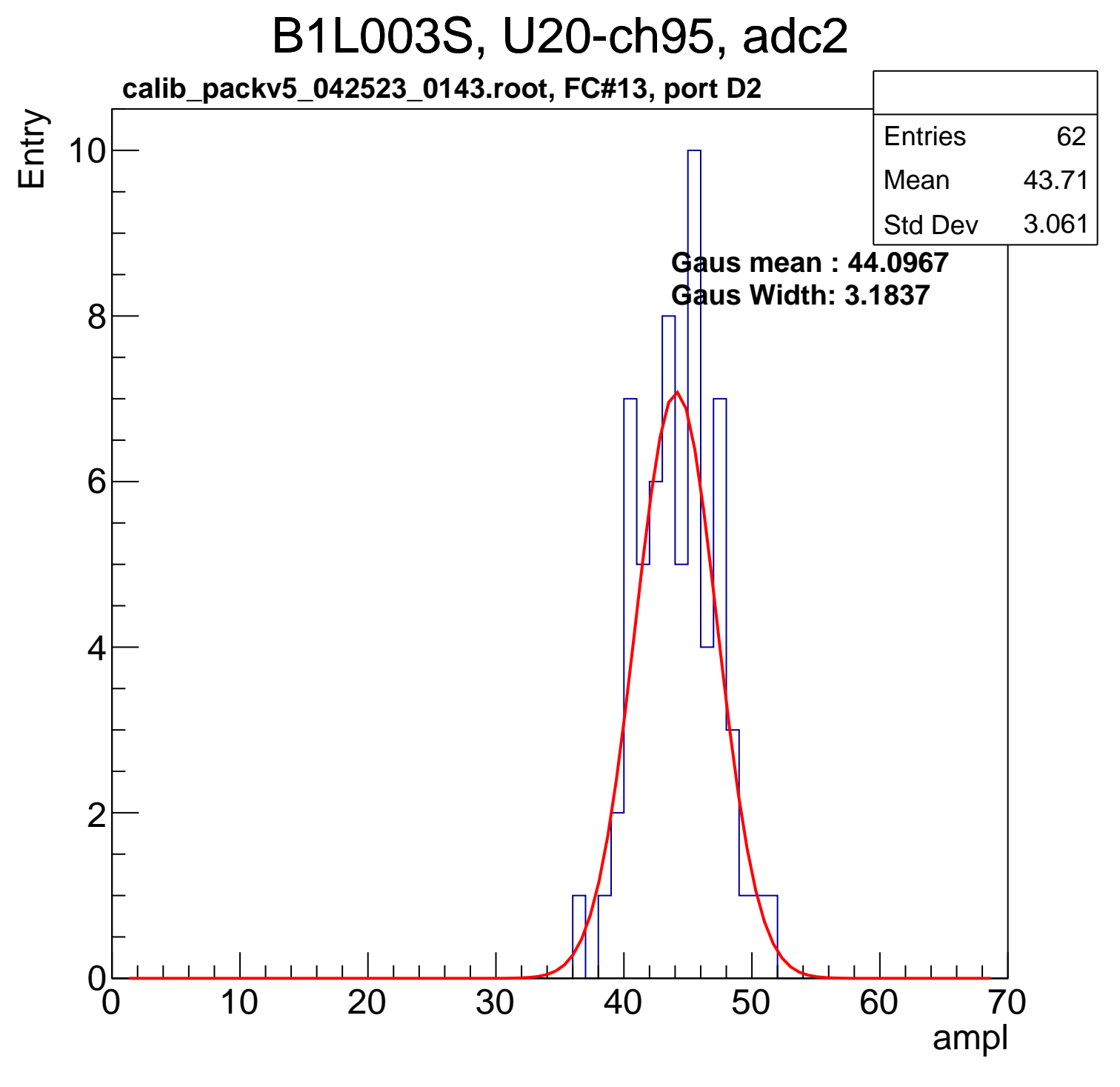
Gaus Width: 3.1837

Entry

10
8
6
4
2
0

ampl

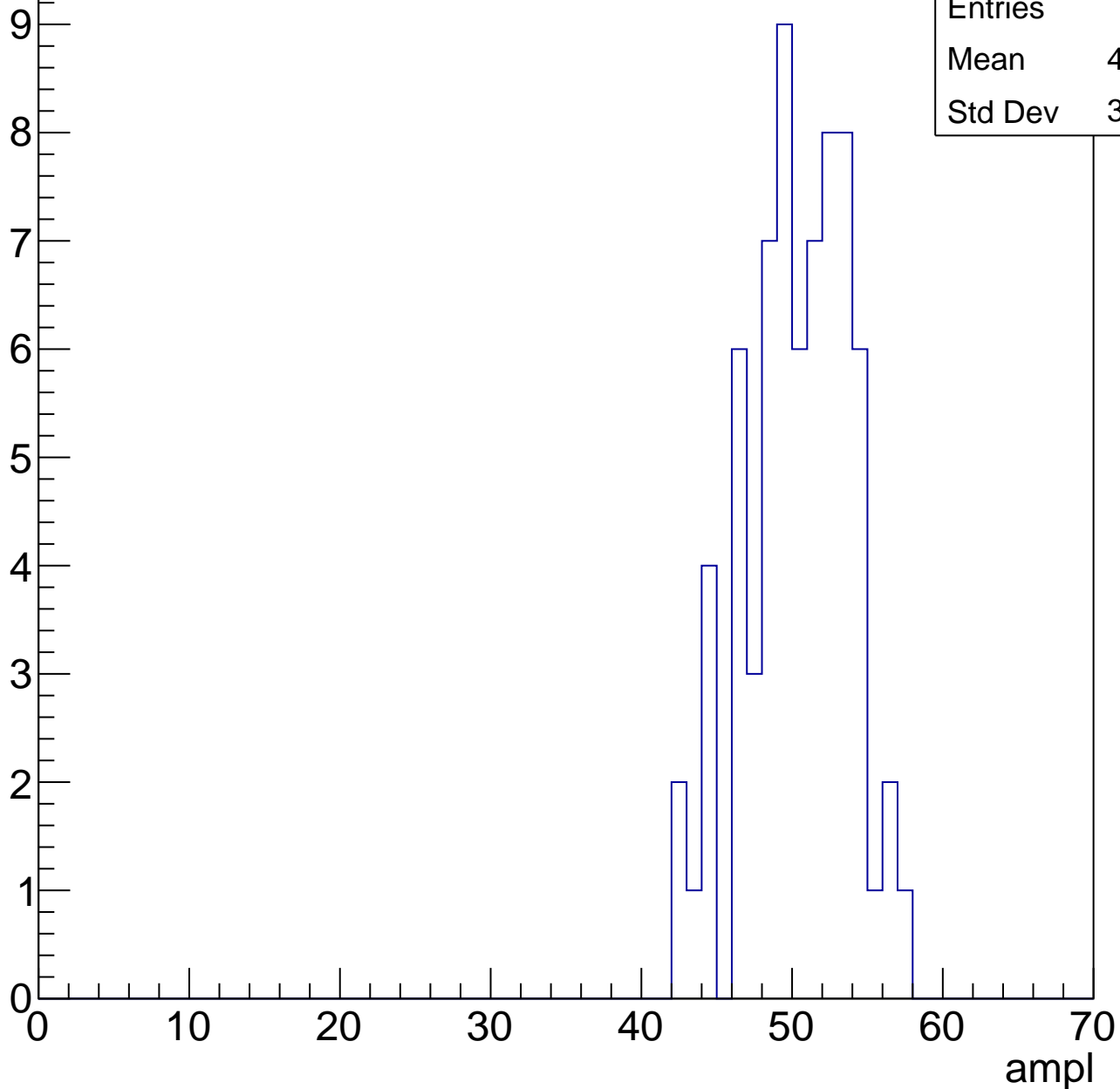
0 10 20 30 40 50 60 70



B1L003S, U20-ch95, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

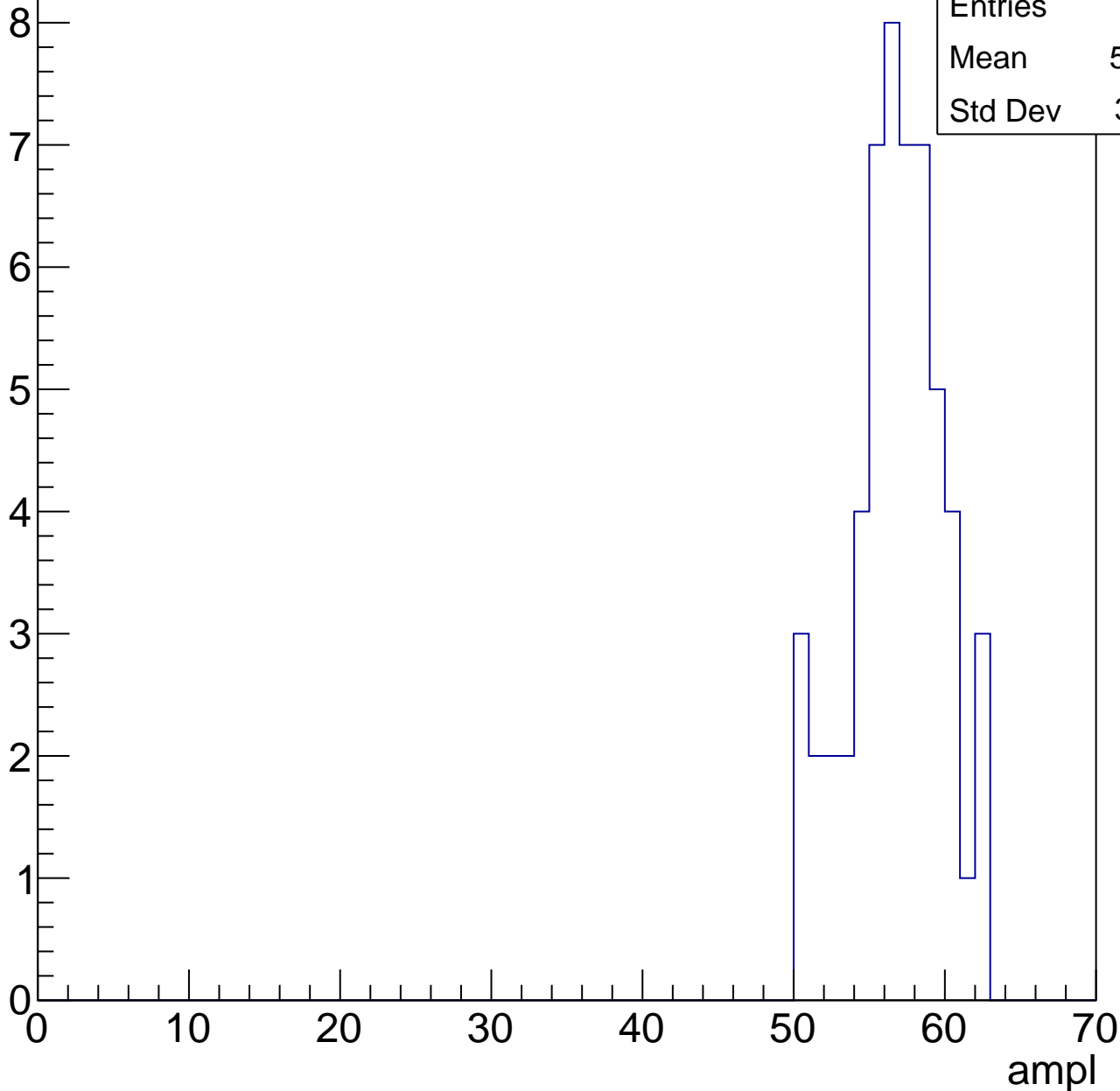


B1L003S, U20-ch95, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	56.33
Std Dev	3.021

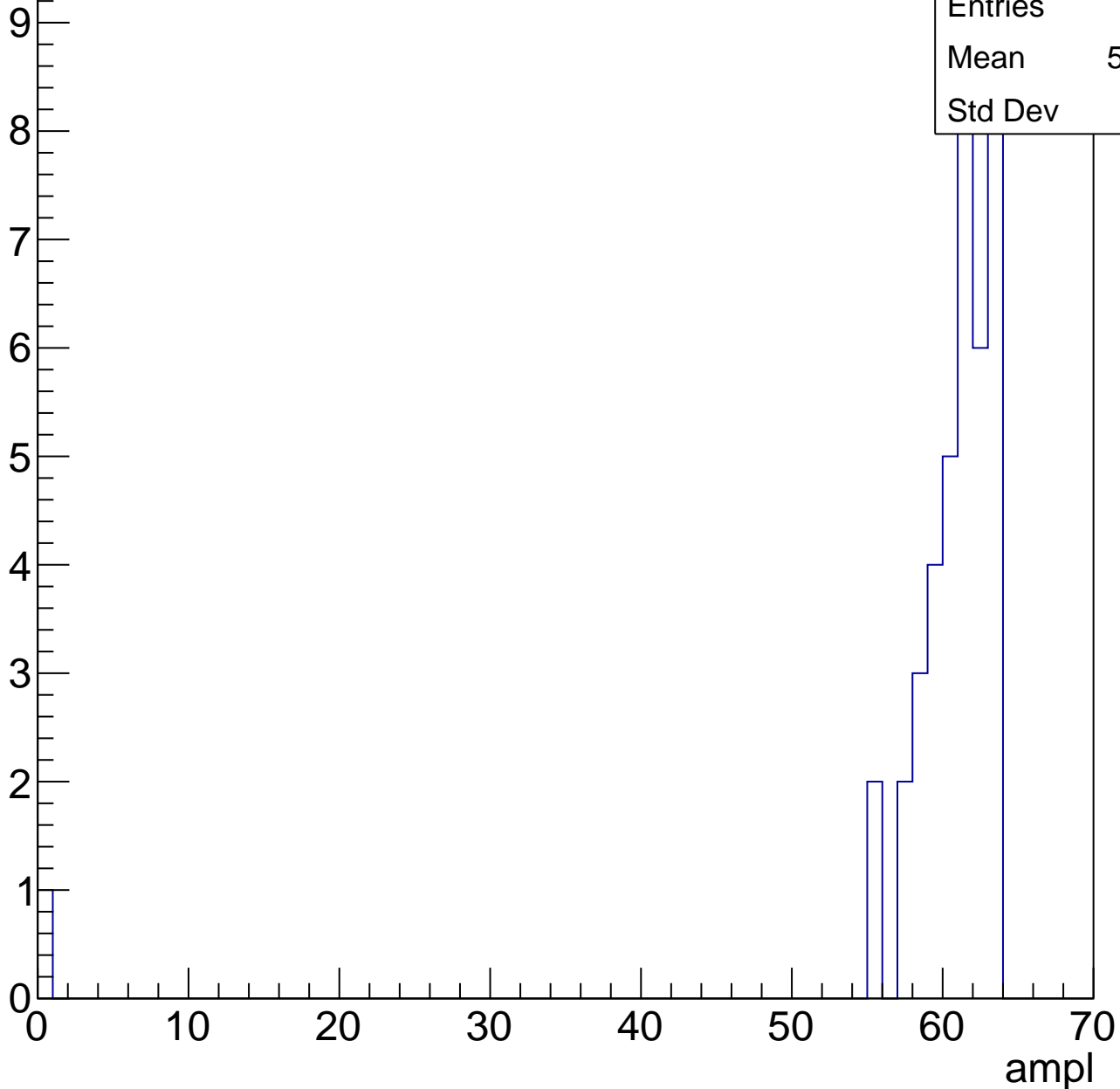


B1L003S, U20-ch95, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

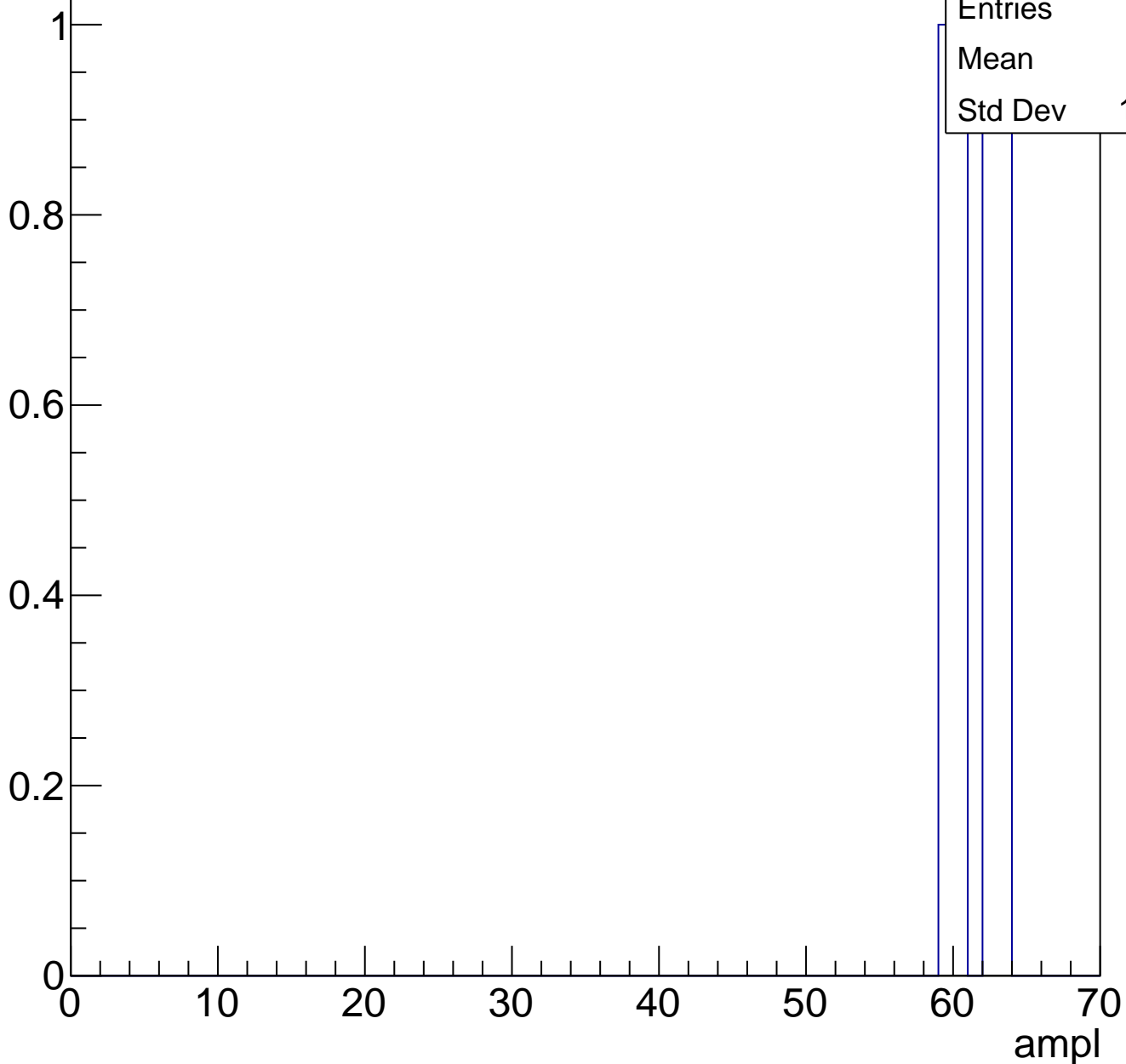
Entries	41
Mean	59.07
Std Dev	9.58



B1L003S, U20-ch95, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch95, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U20-ch96, adc0

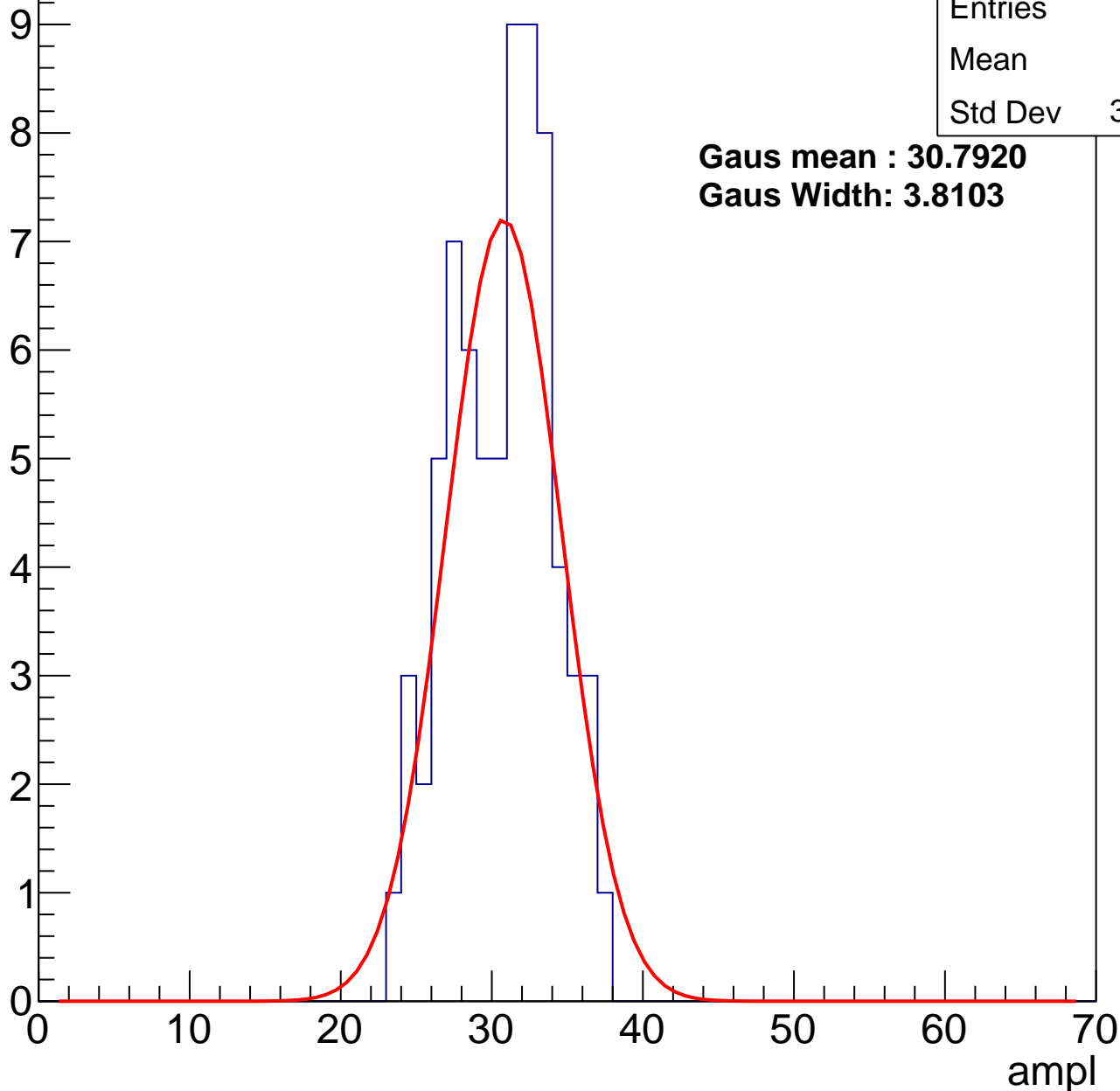
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.2
Std Dev	3.334

Gaus mean : 30.7920

Gaus Width: 3.8103



B1L003S, U20-ch96, adc1

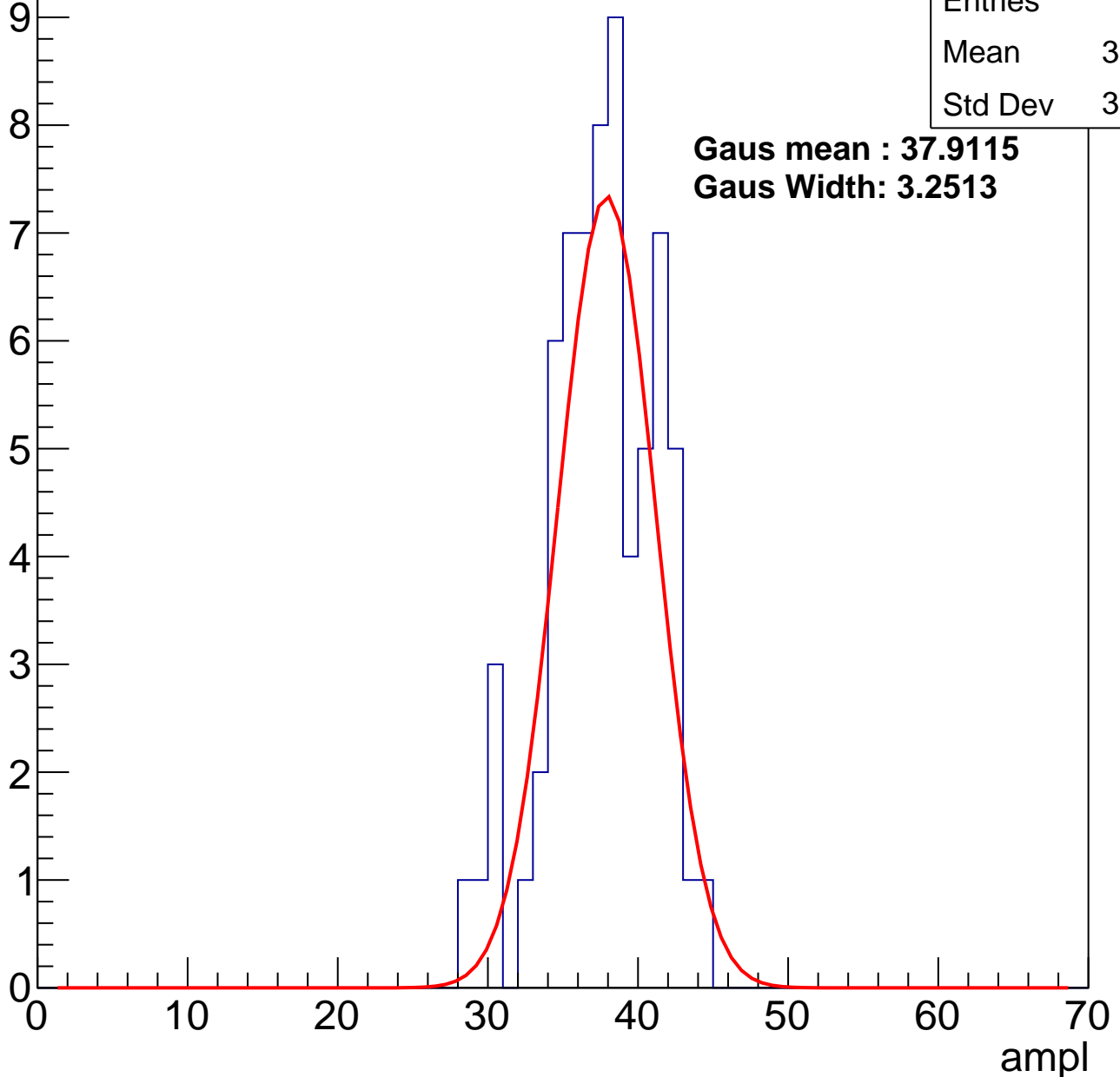
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.12
Std Dev	3.492

Gaus mean : 37.9115

Gaus Width: 3.2513



B1L003S, U20-ch96, adc2

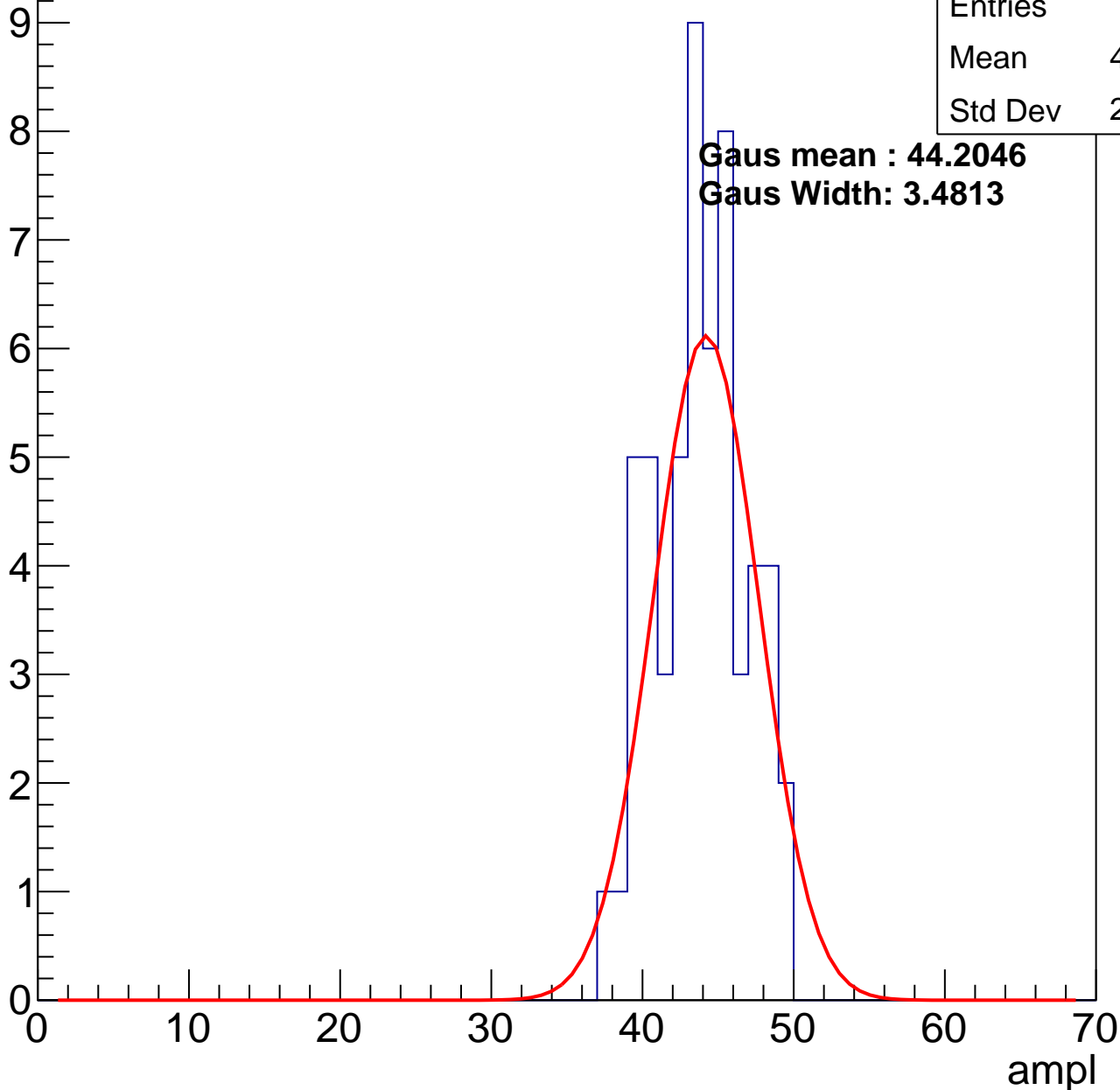
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.39
Std Dev	2.974

Gaus mean : 44.2046

Gaus Width: 3.4813

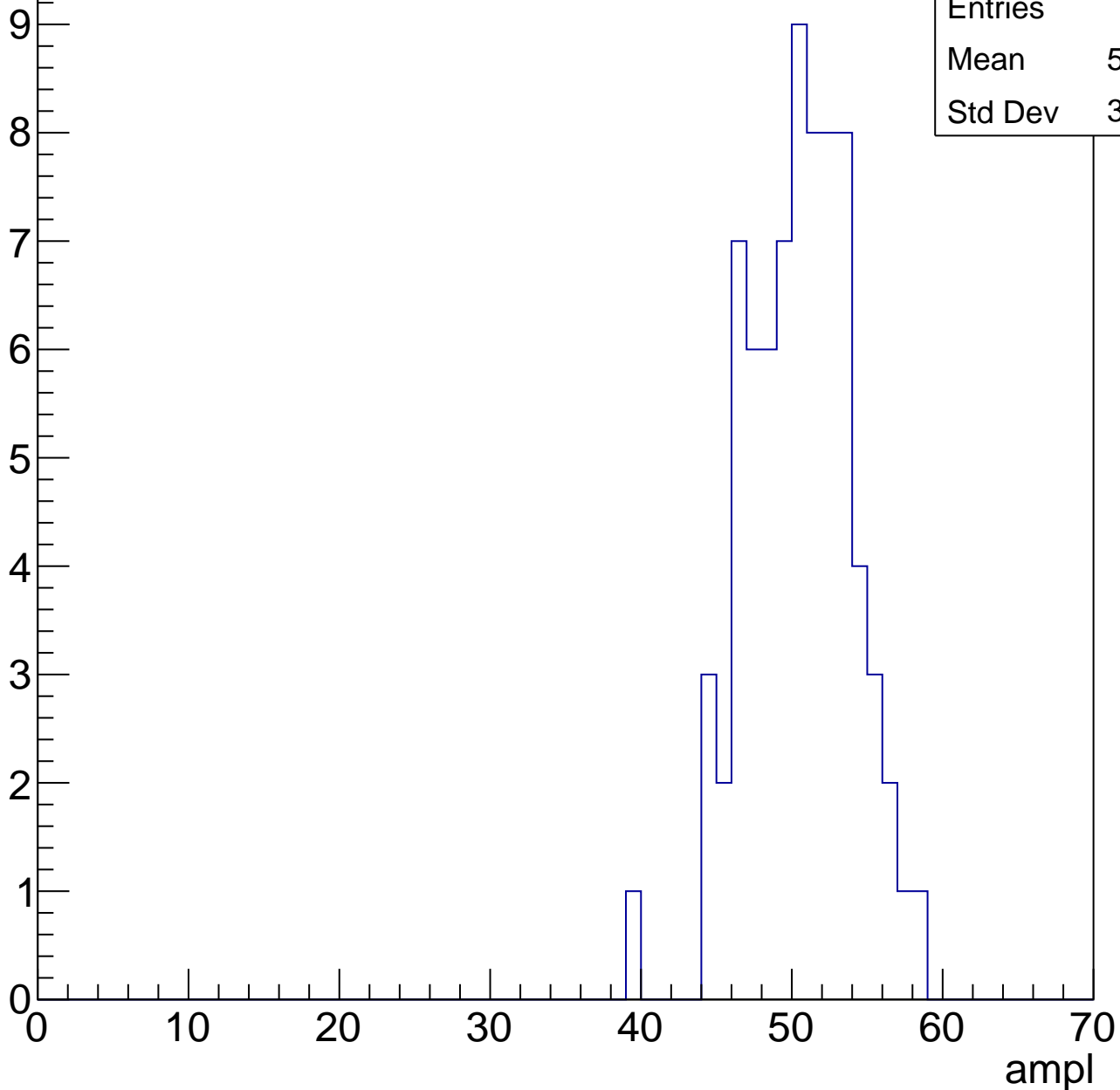


B1L003S, U20-ch96, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	50.03
Std Dev	3.464

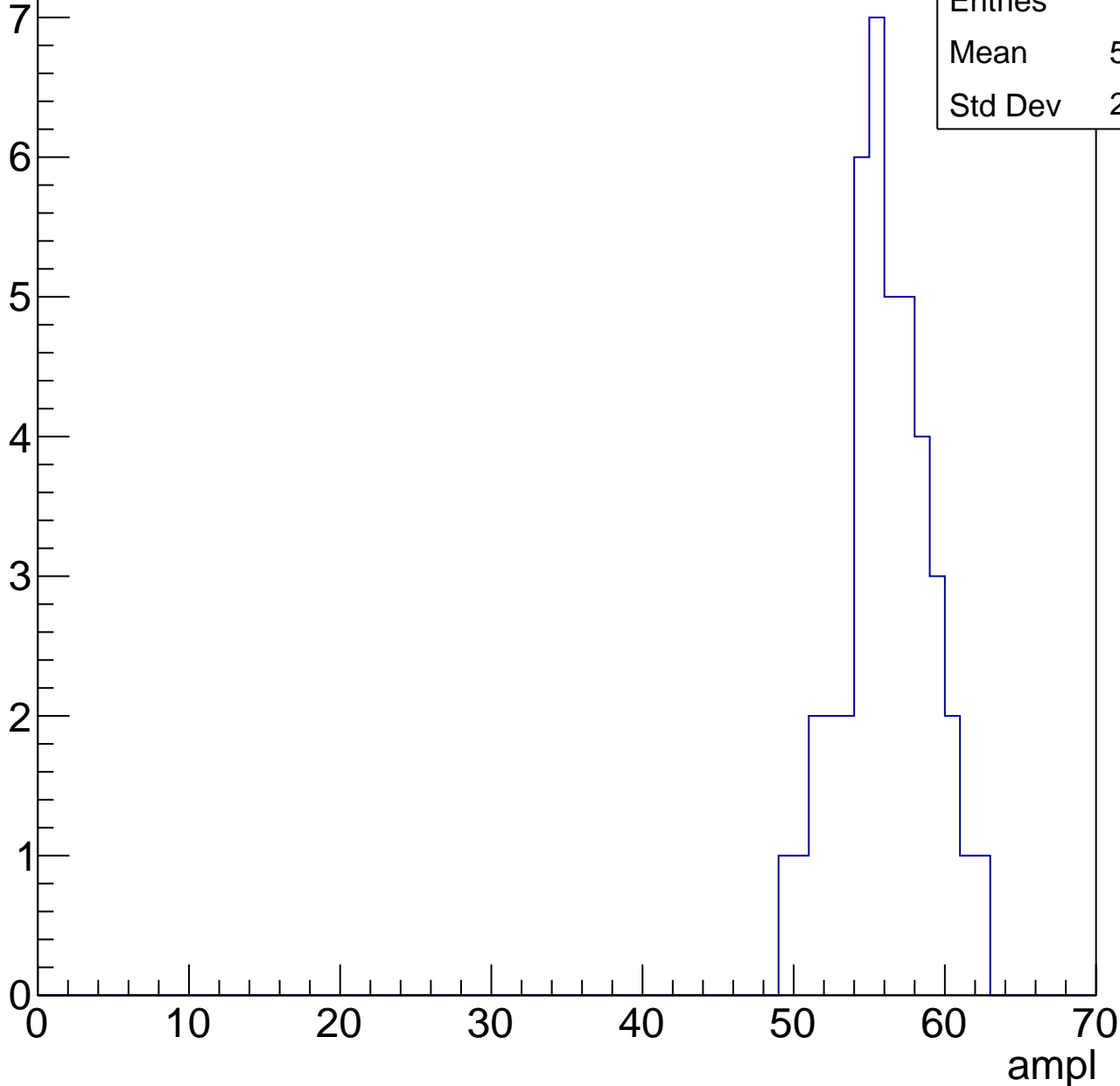


B1L003S, U20-ch96, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	55.64
Std Dev	2.894



B1L003S, U20-ch96, adc5

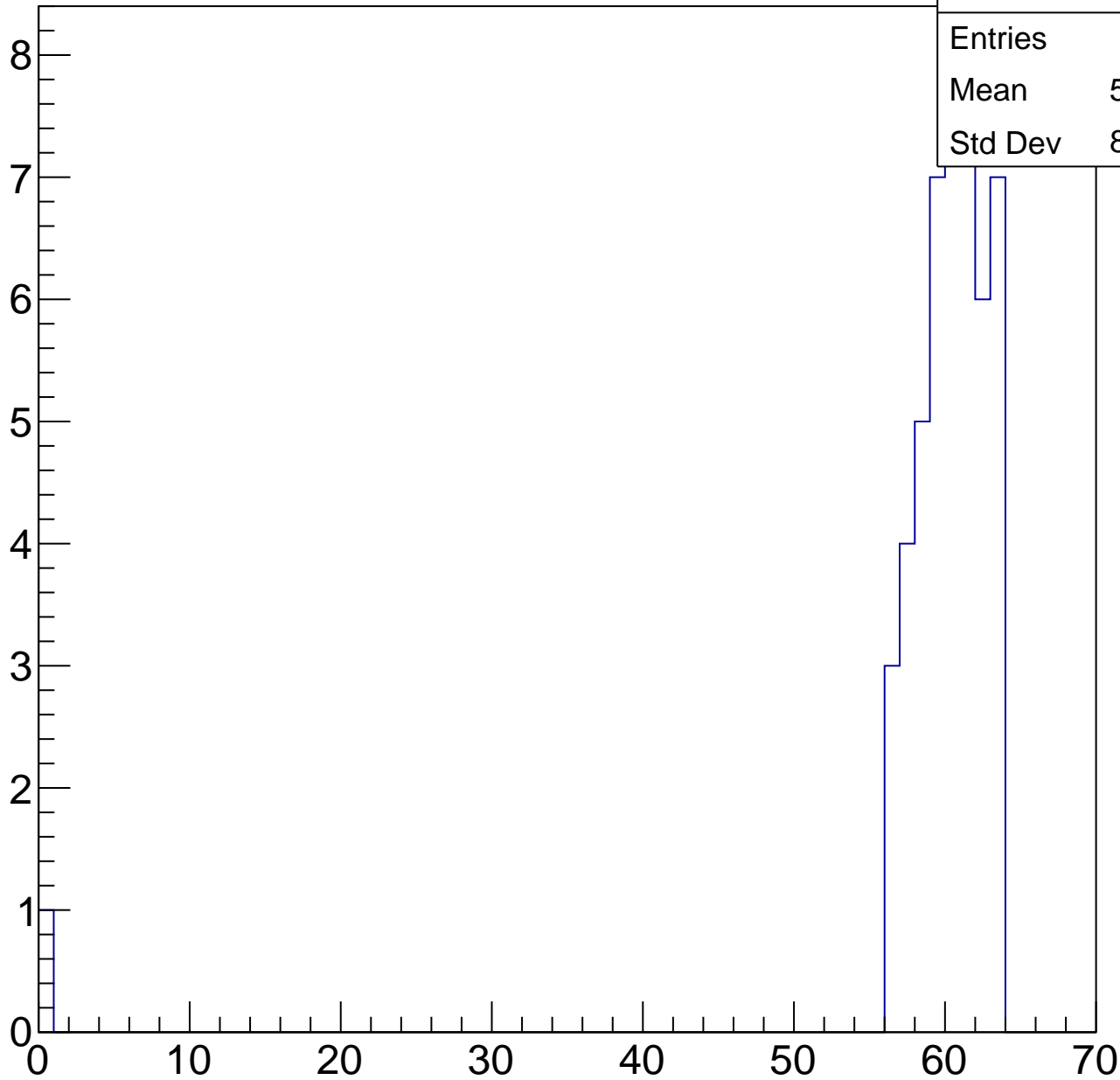
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.78
Std Dev	8.728

ampl



B1L003S, U20-ch96, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch96, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L003S, U20-ch97, adc0

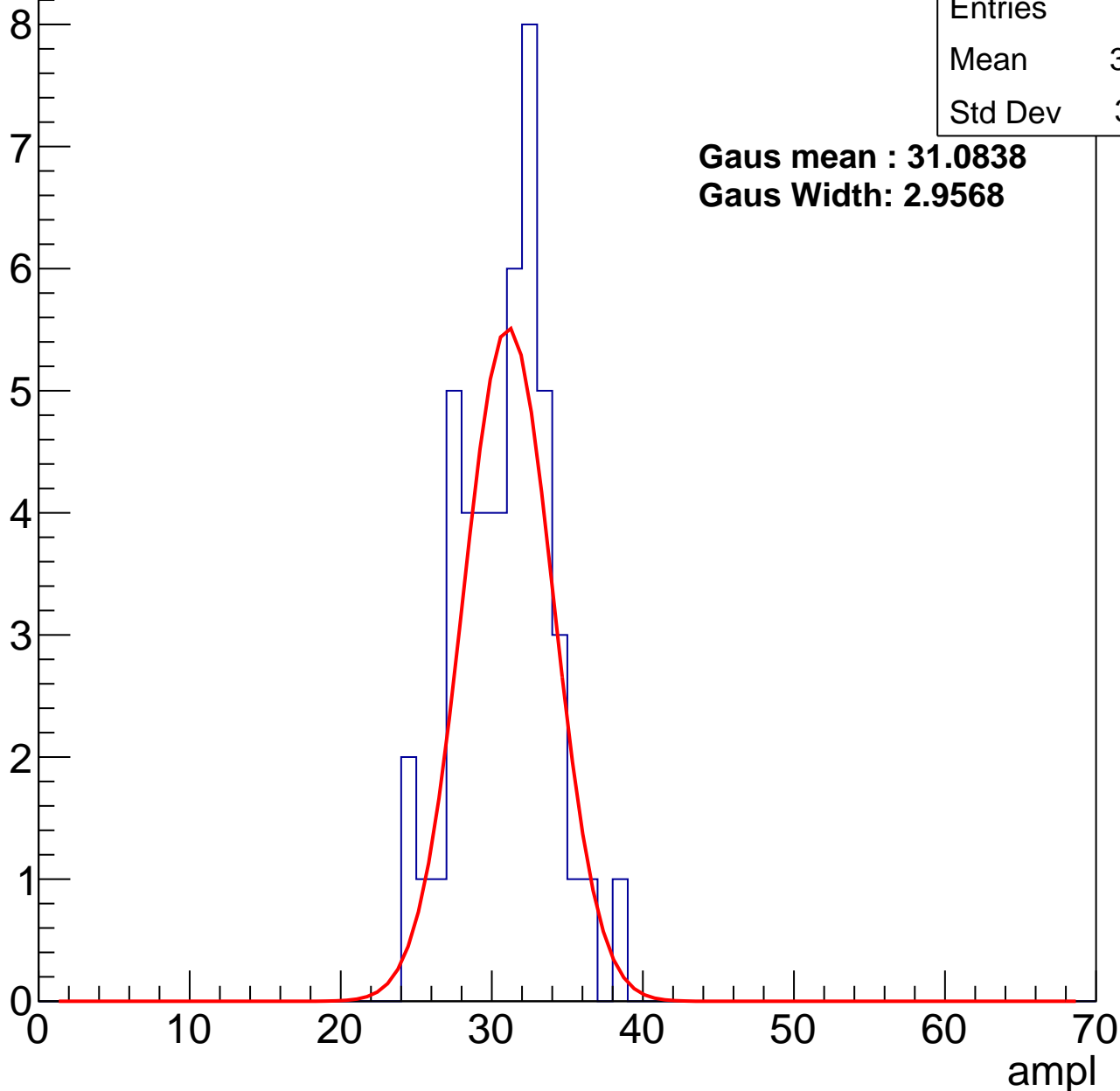
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	30.43
Std Dev	3.041

Gaus mean : 31.0838

Gaus Width: 2.9568



B1L003S, U20-ch97, adc1

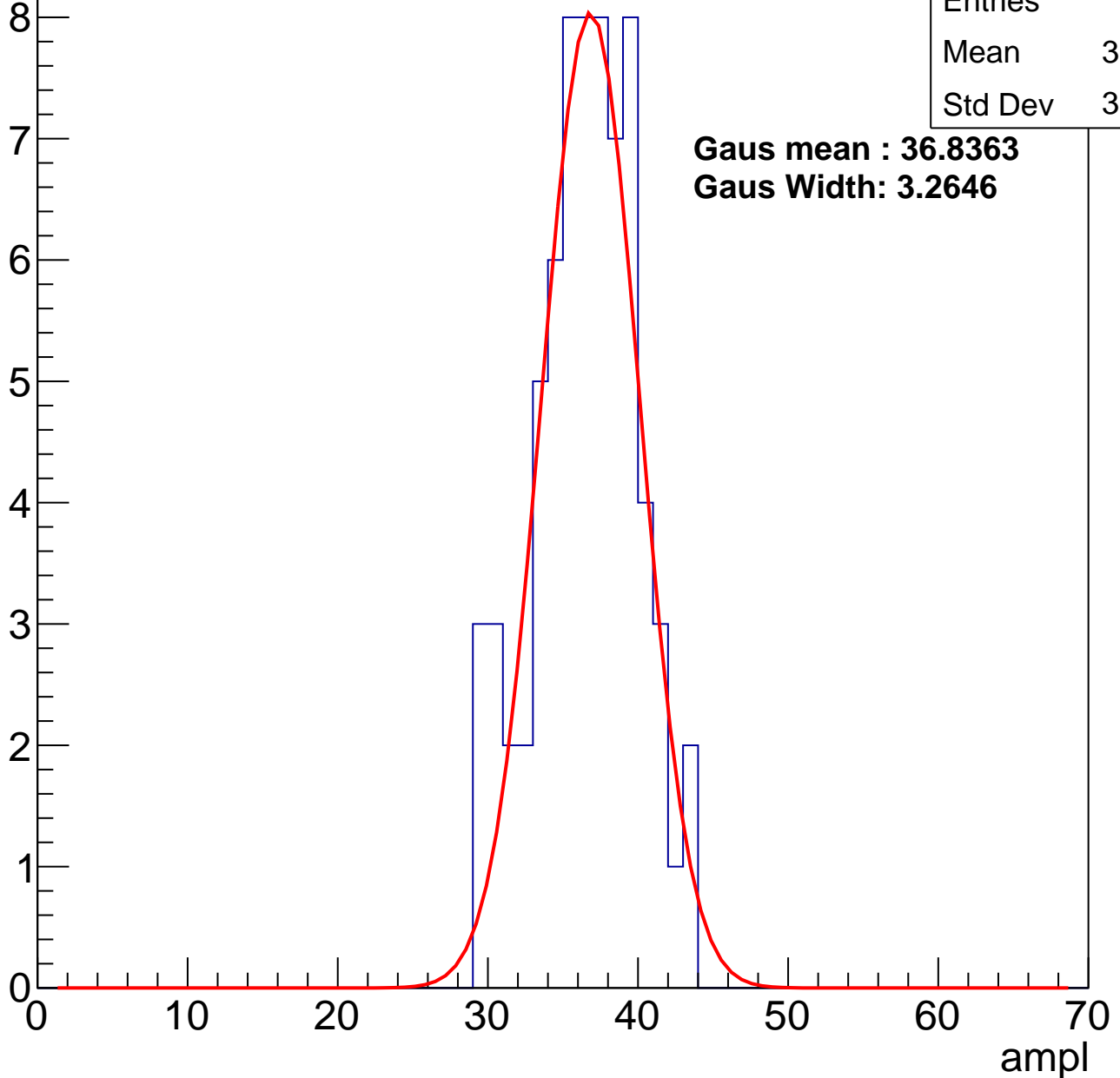
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.07
Std Dev	3.369

Gaus mean : 36.8363

Gaus Width: 3.2646



B1L003S, U20-ch97, adc2

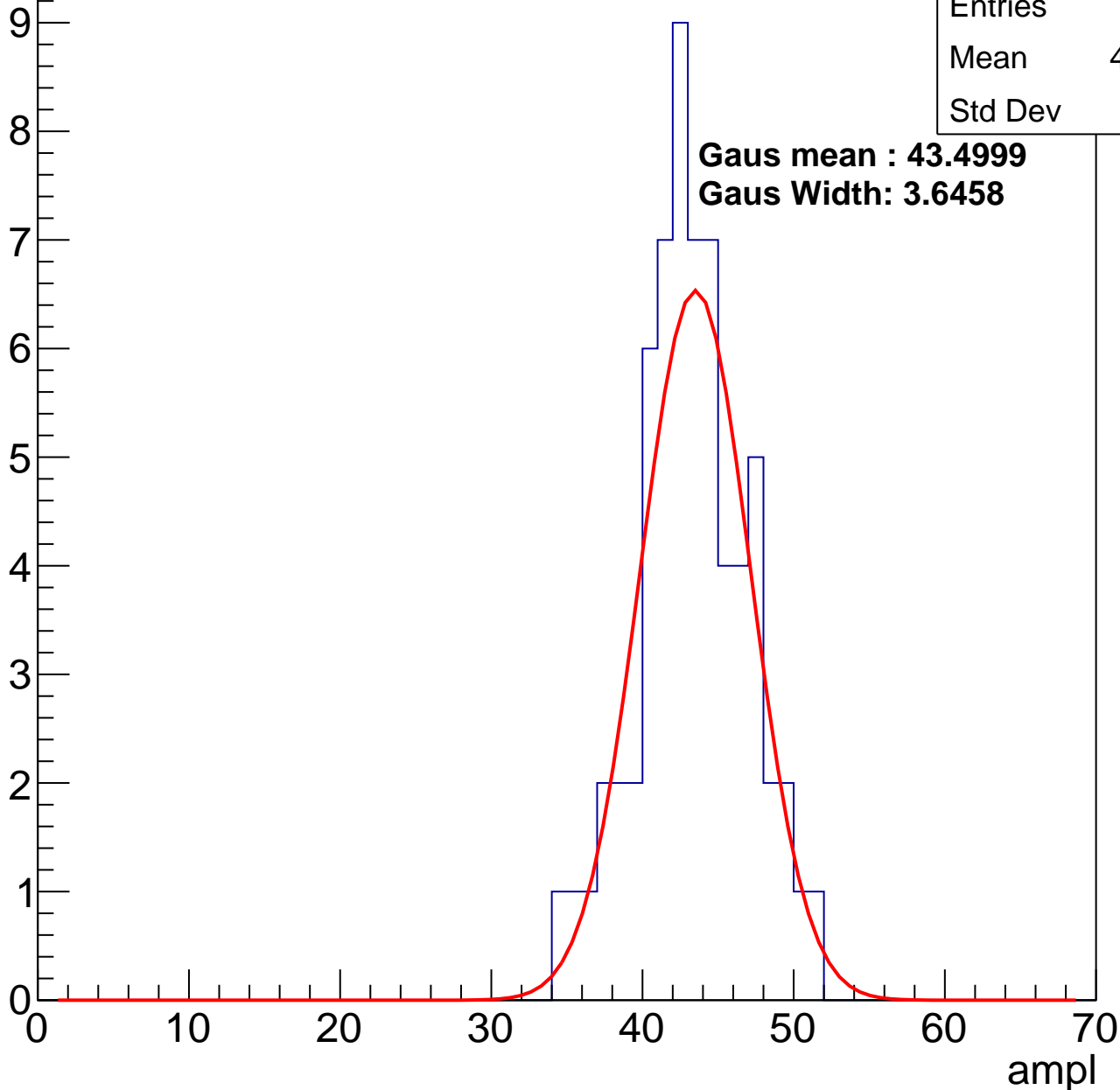
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.83
Std Dev	3.56

Gaus mean : 43.4999

Gaus Width: 3.6458

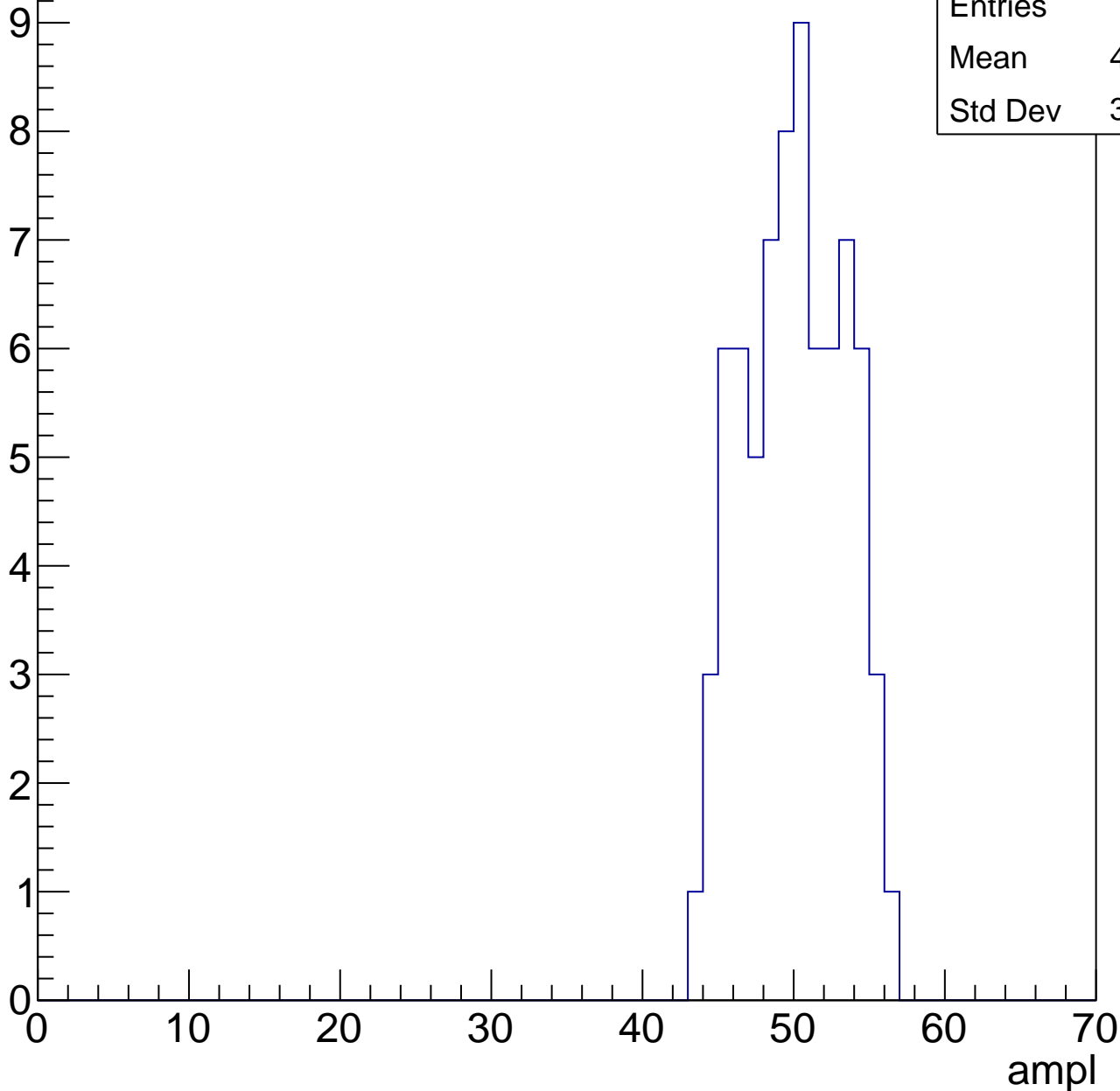


B1L003S, U20-ch97, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.57
Std Dev	3.226

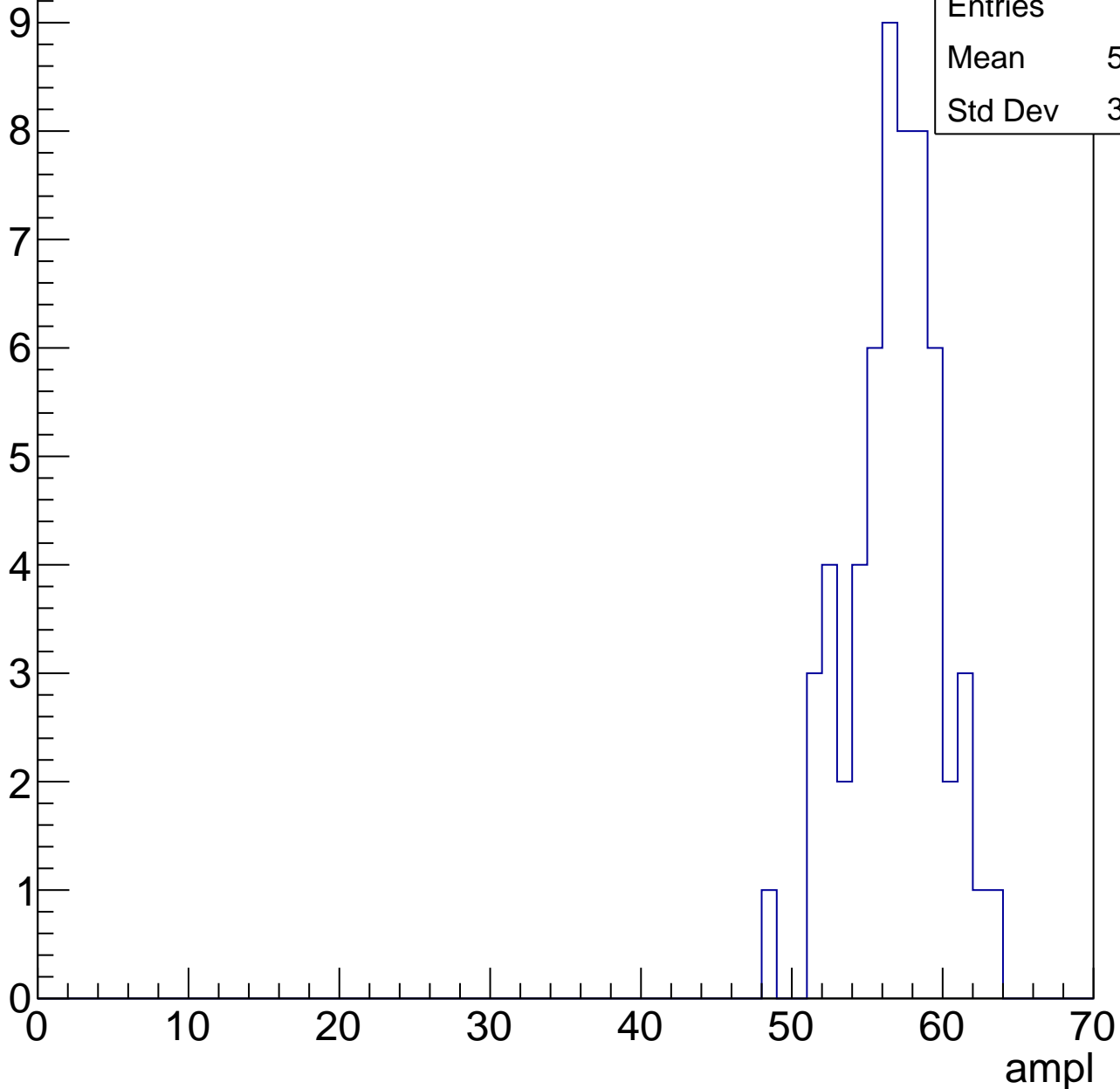


B1L003S, U20-ch97, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	56.33
Std Dev	3.002



B1L003S, U20-ch97, adc5

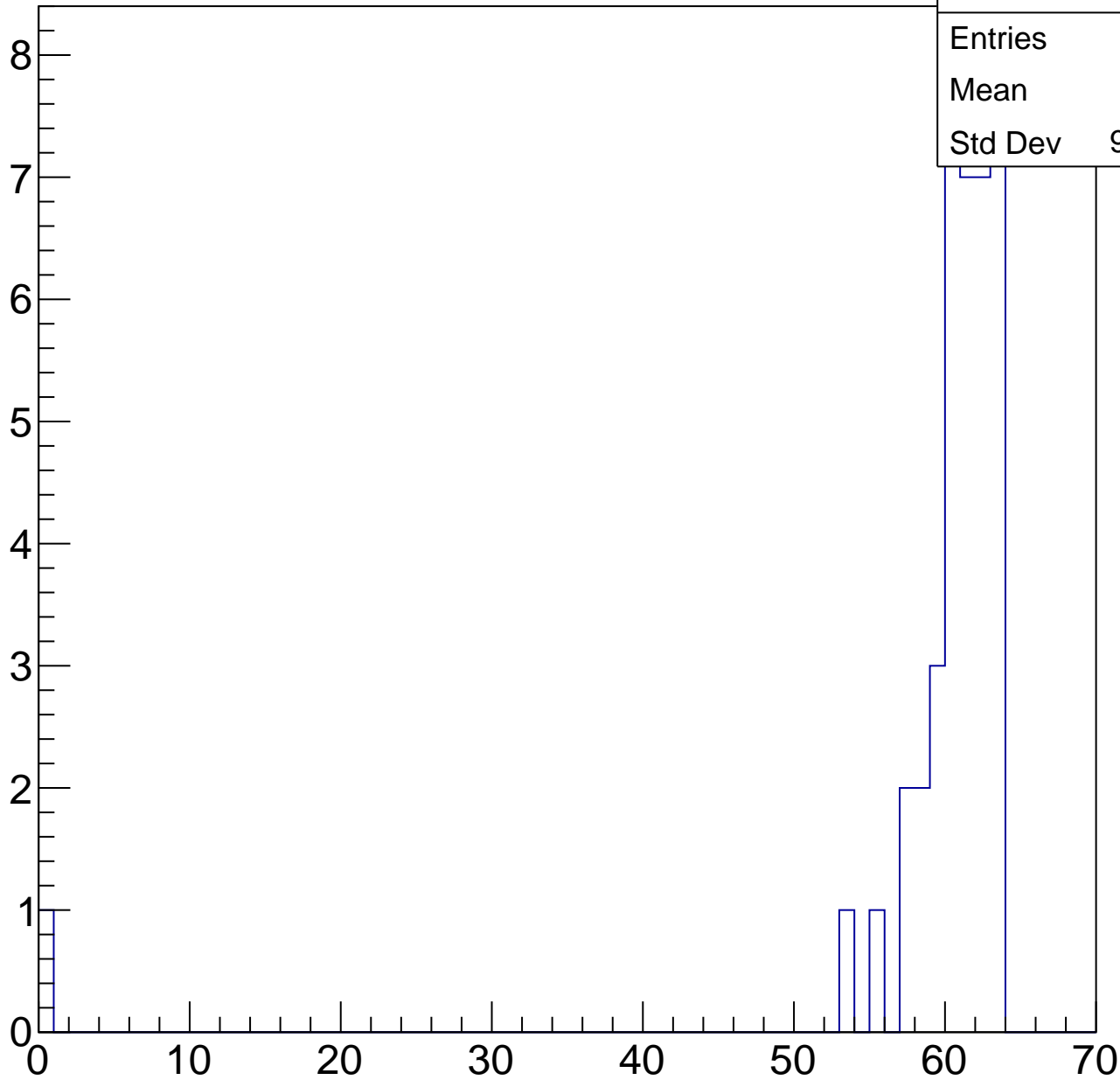
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59
Std Dev	9.708

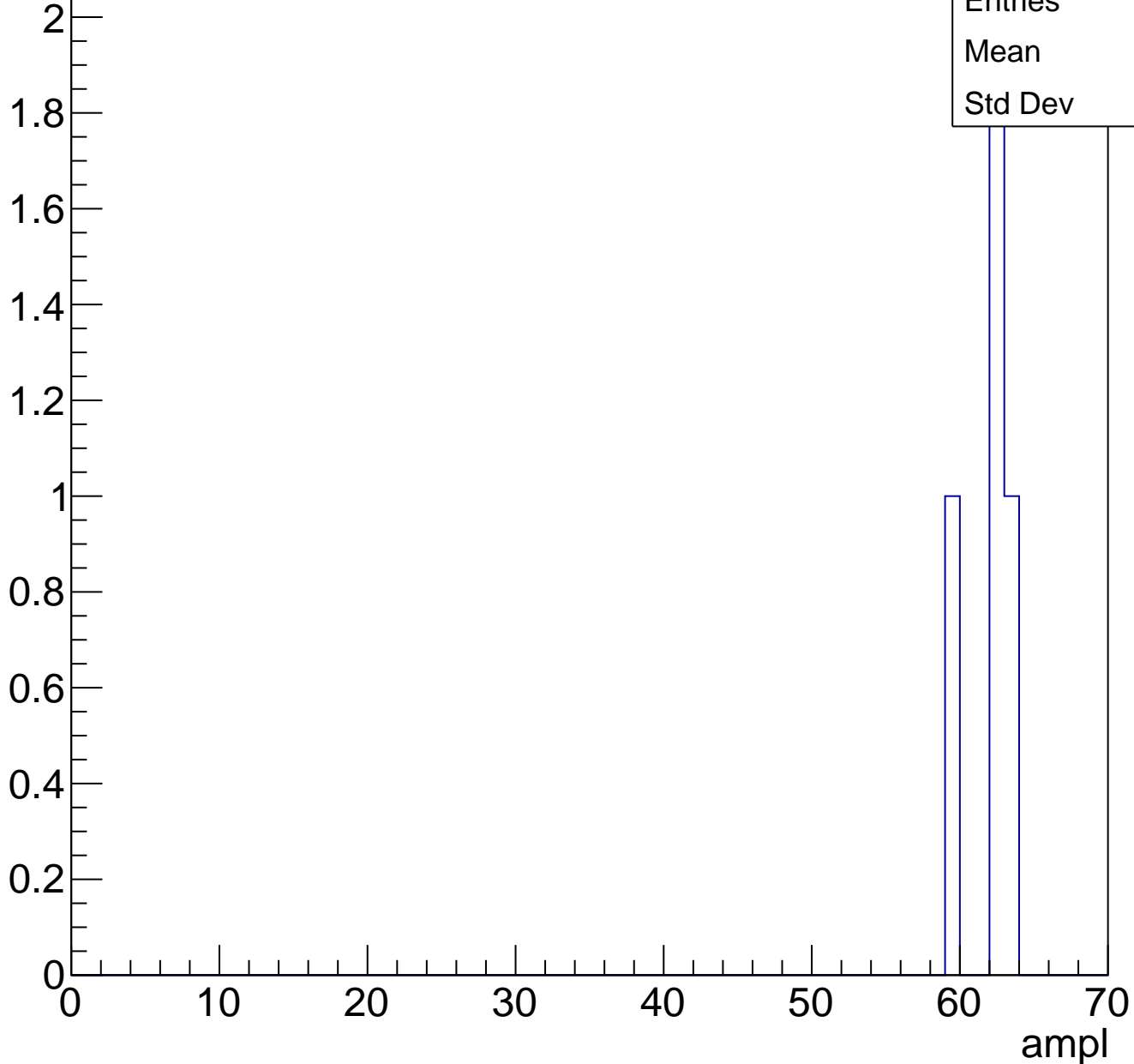
ampl



B1L003S, U20-ch97, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch97, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch98, adc0

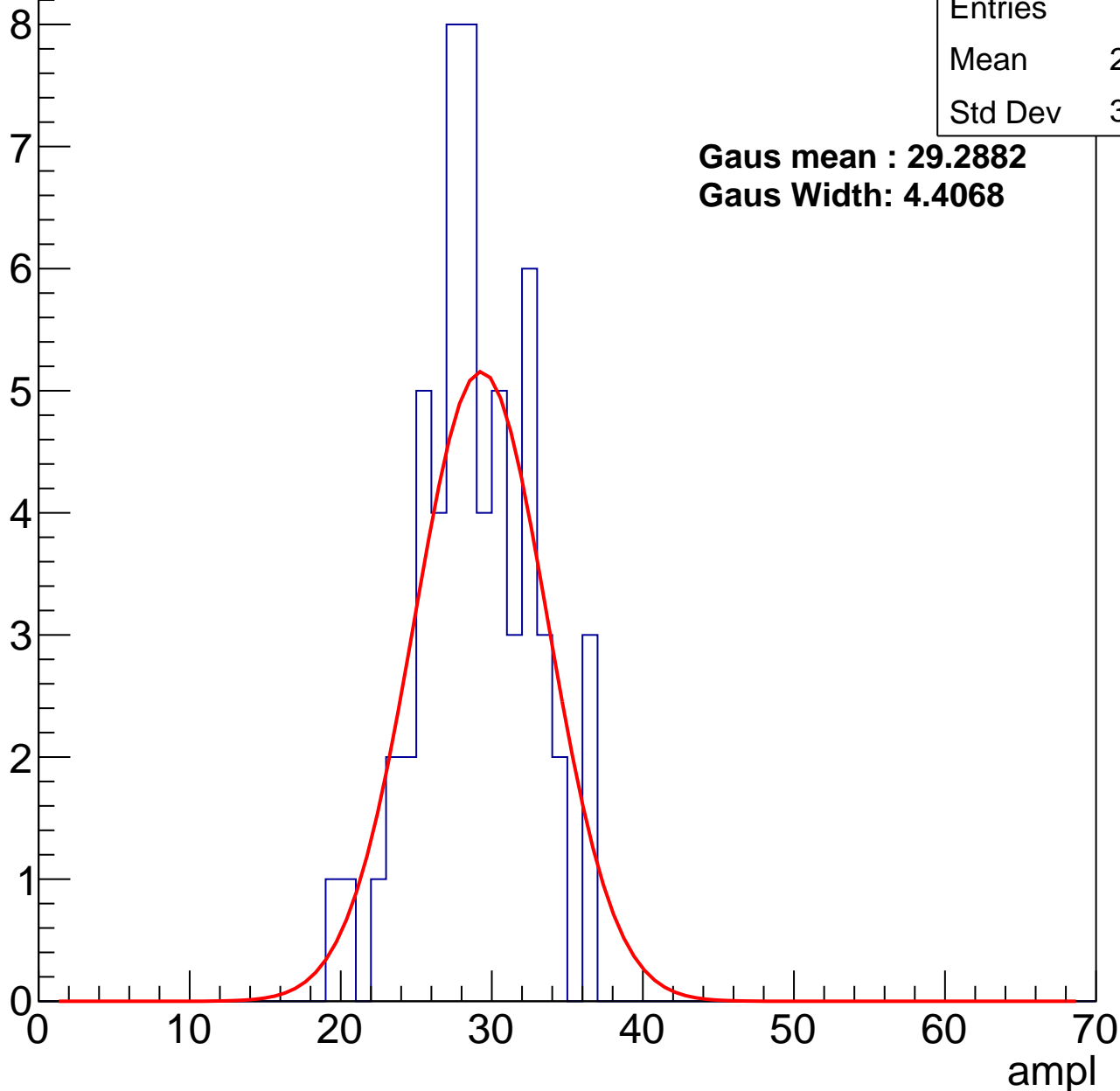
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.45
Std Dev	3.729

Gaus mean : 29.2882

Gaus Width: 4.4068



B1L003S, U20-ch98, adc1

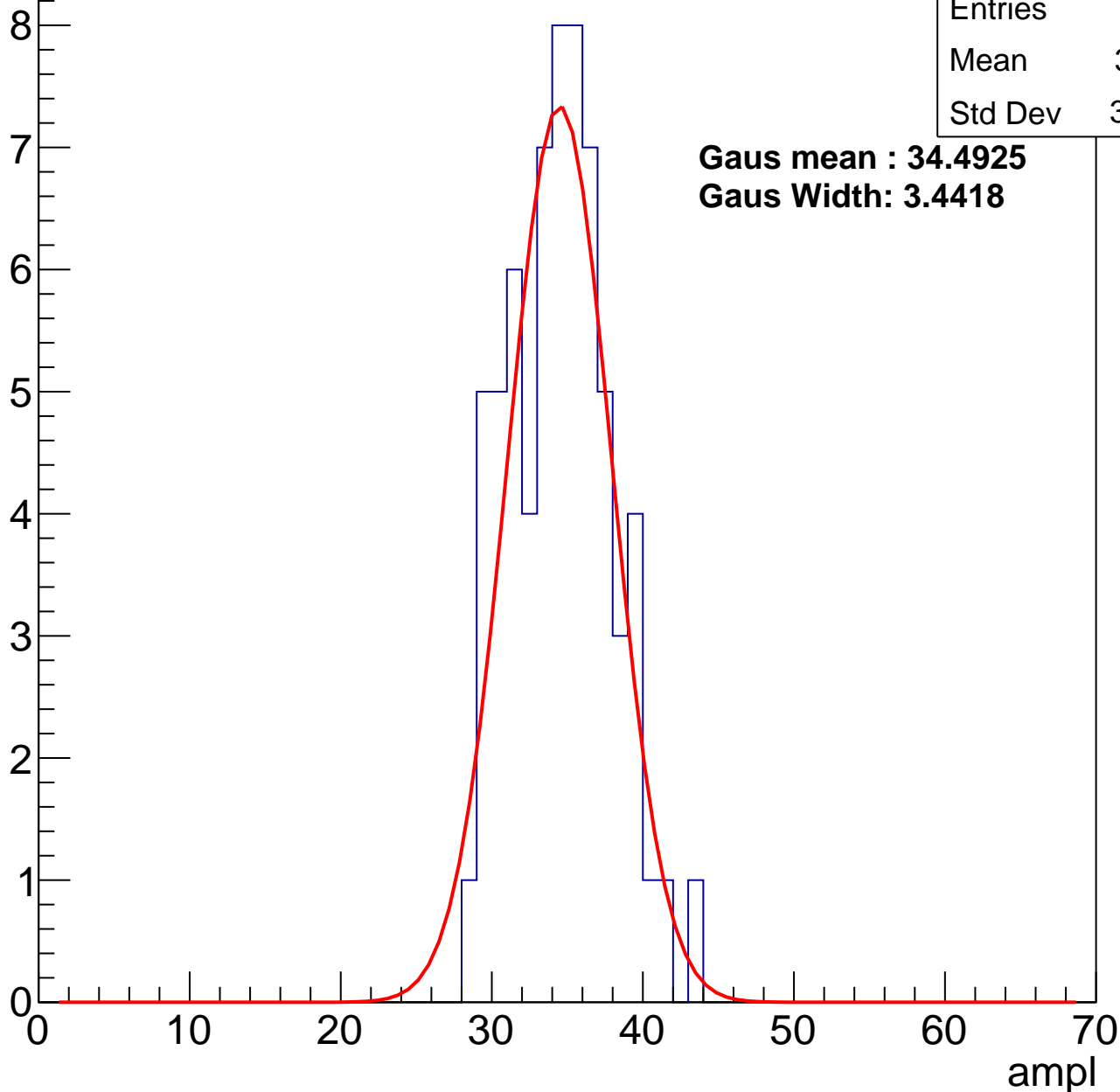
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.11
Std Dev	3.285

Gaus mean : 34.4925

Gaus Width: 3.4418



B1L003S, U20-ch98, adc2

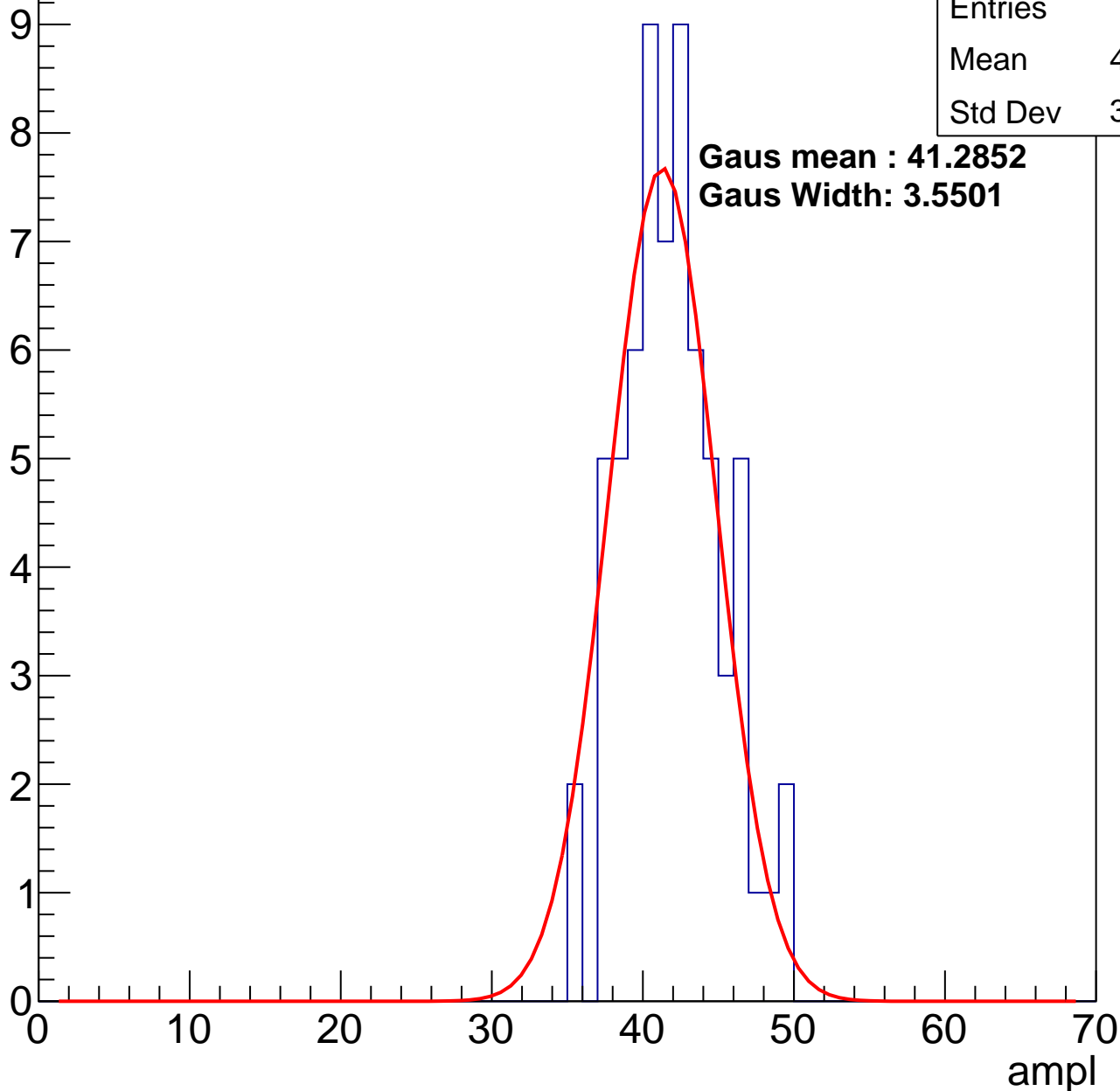
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.52
Std Dev	3.202

Gaus mean : 41.2852

Gaus Width: 3.5501

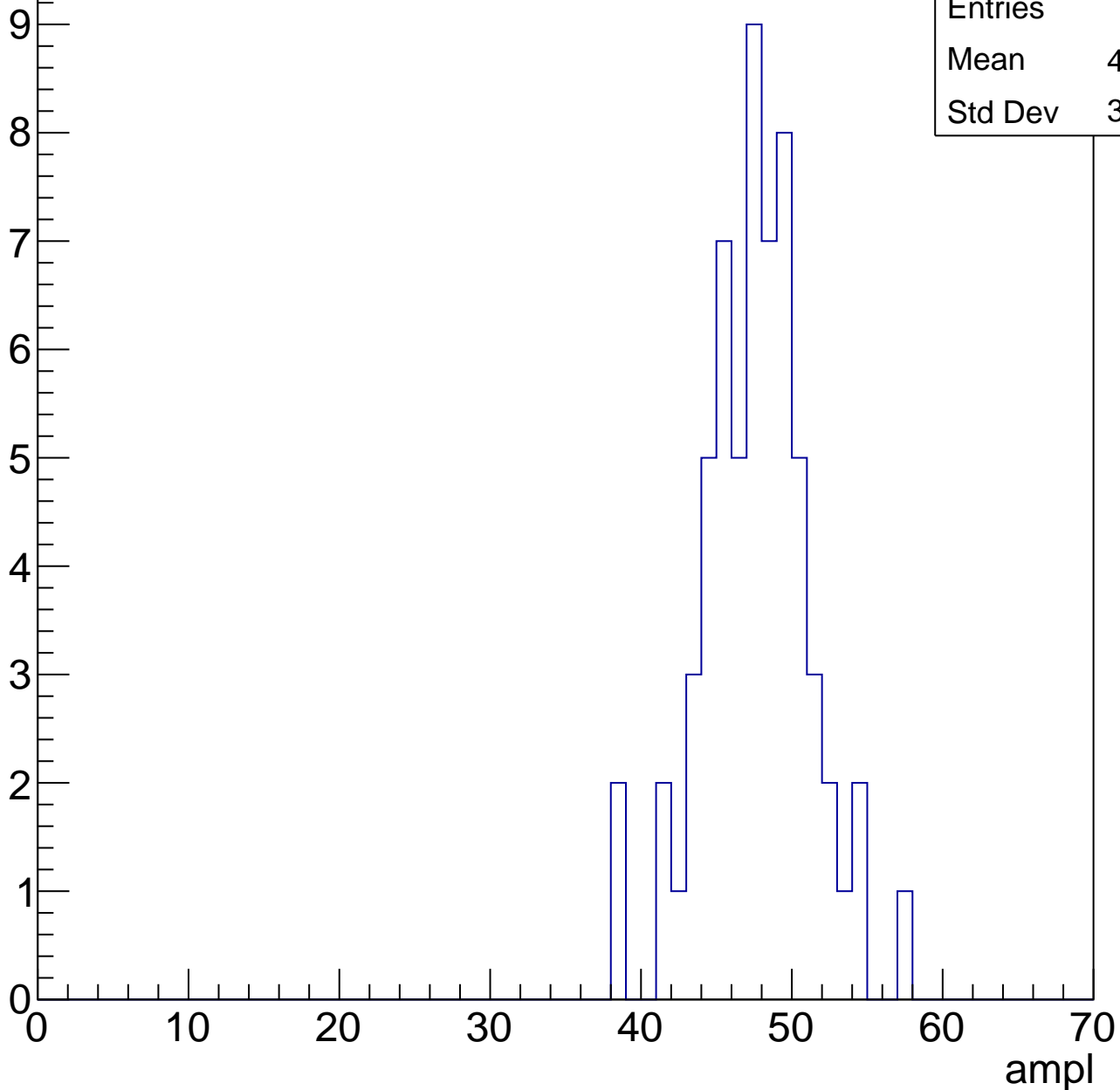


B1L003S, U20-ch98, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	47.14
Std Dev	3.563

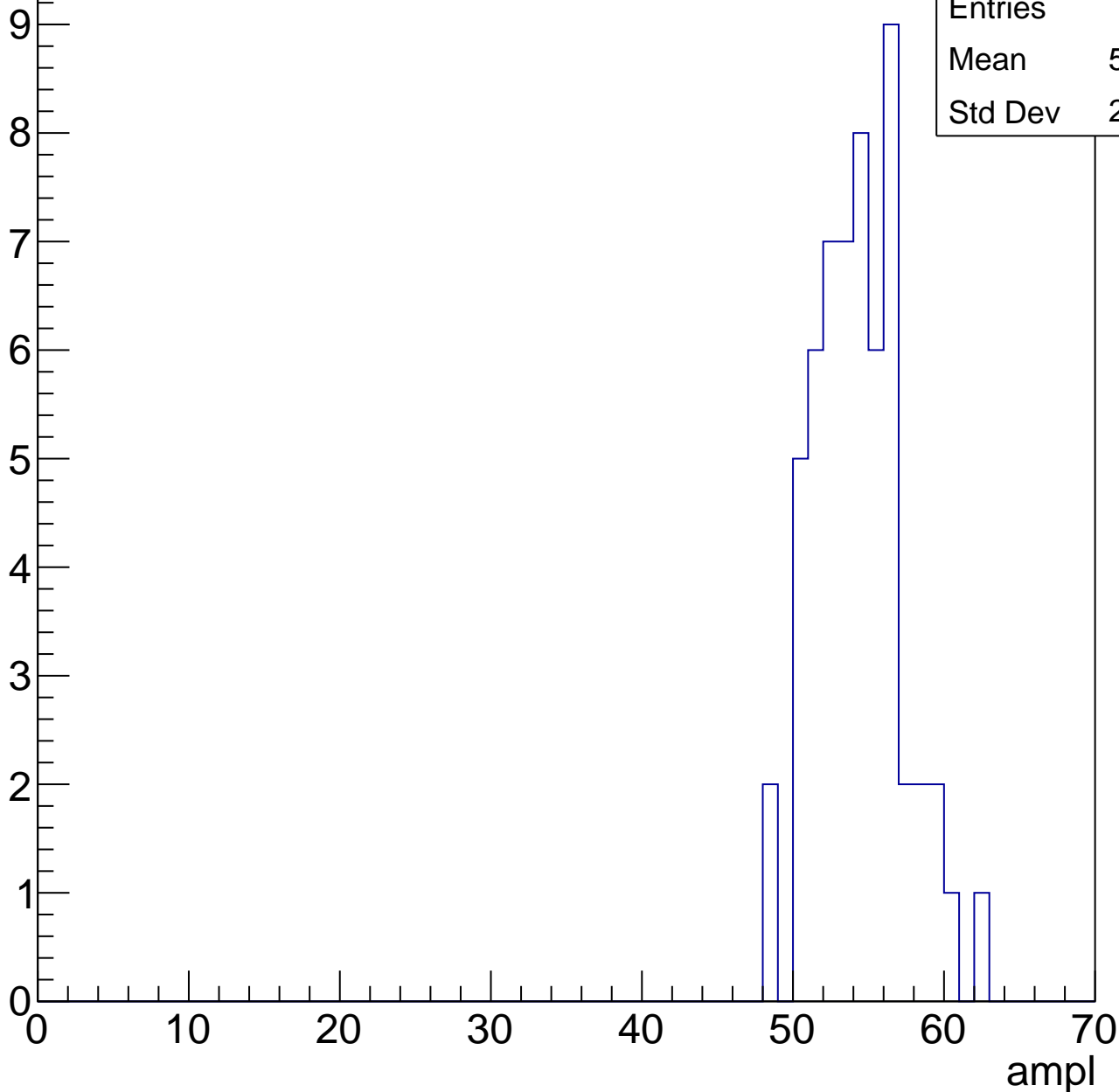


B1L003S, U20-ch98, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.84
Std Dev	2.882

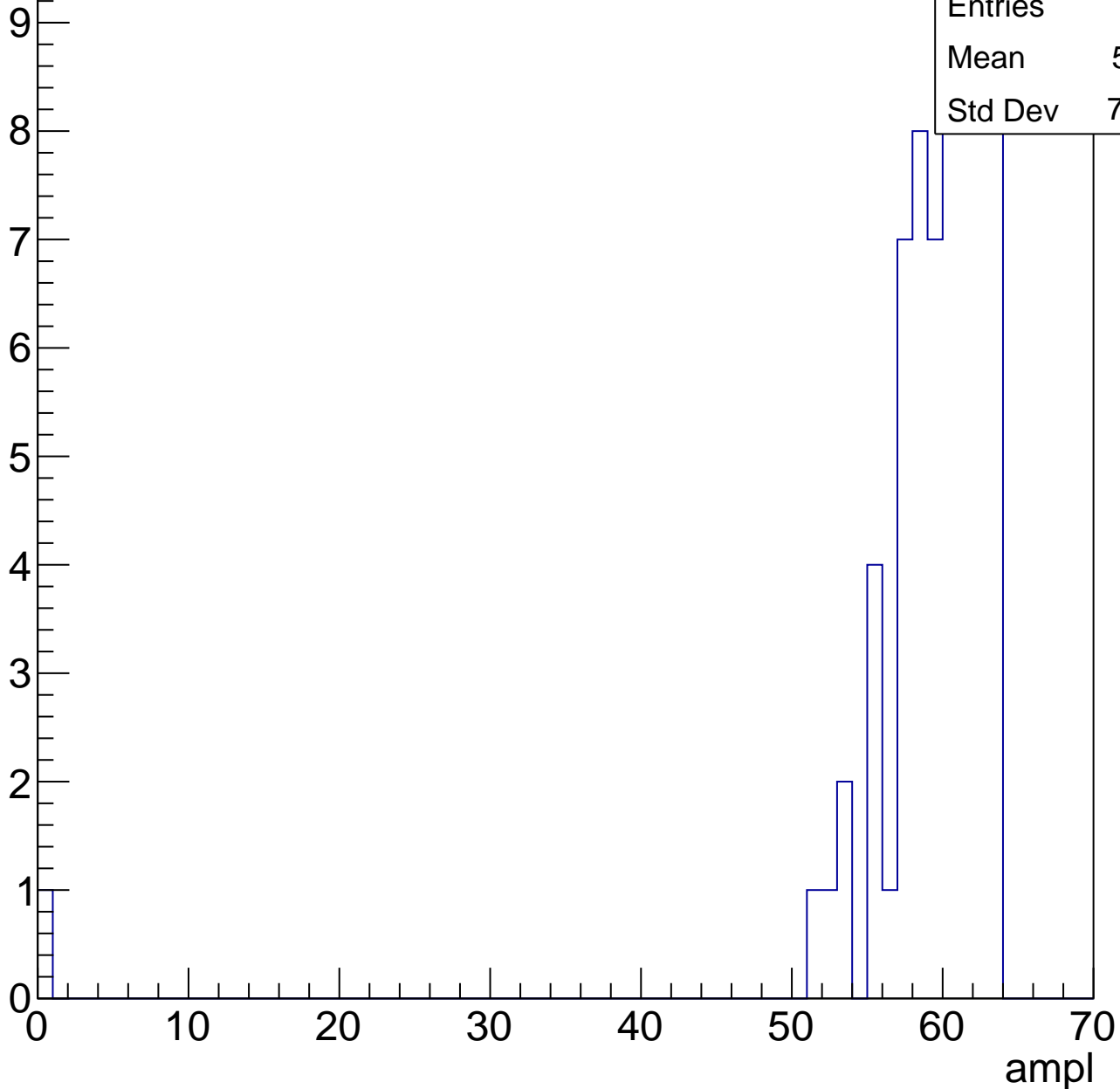


B1L003S, U20-ch98, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	58.31
Std Dev	7.833



B1L003S, U20-ch98, adc6

calib_packv5_042523_0143.root, FC#13, port D2

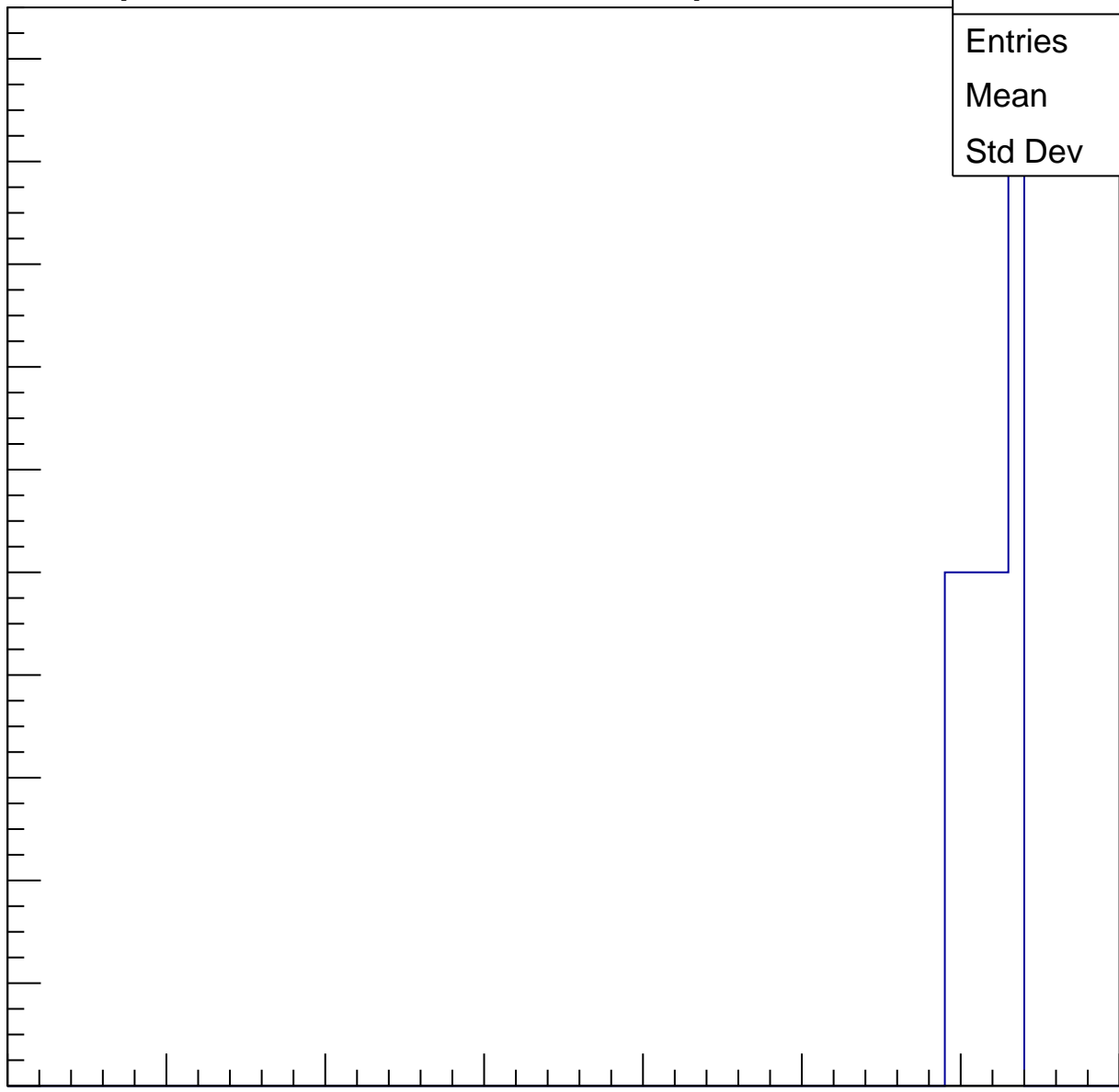
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch98, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch99, adc0

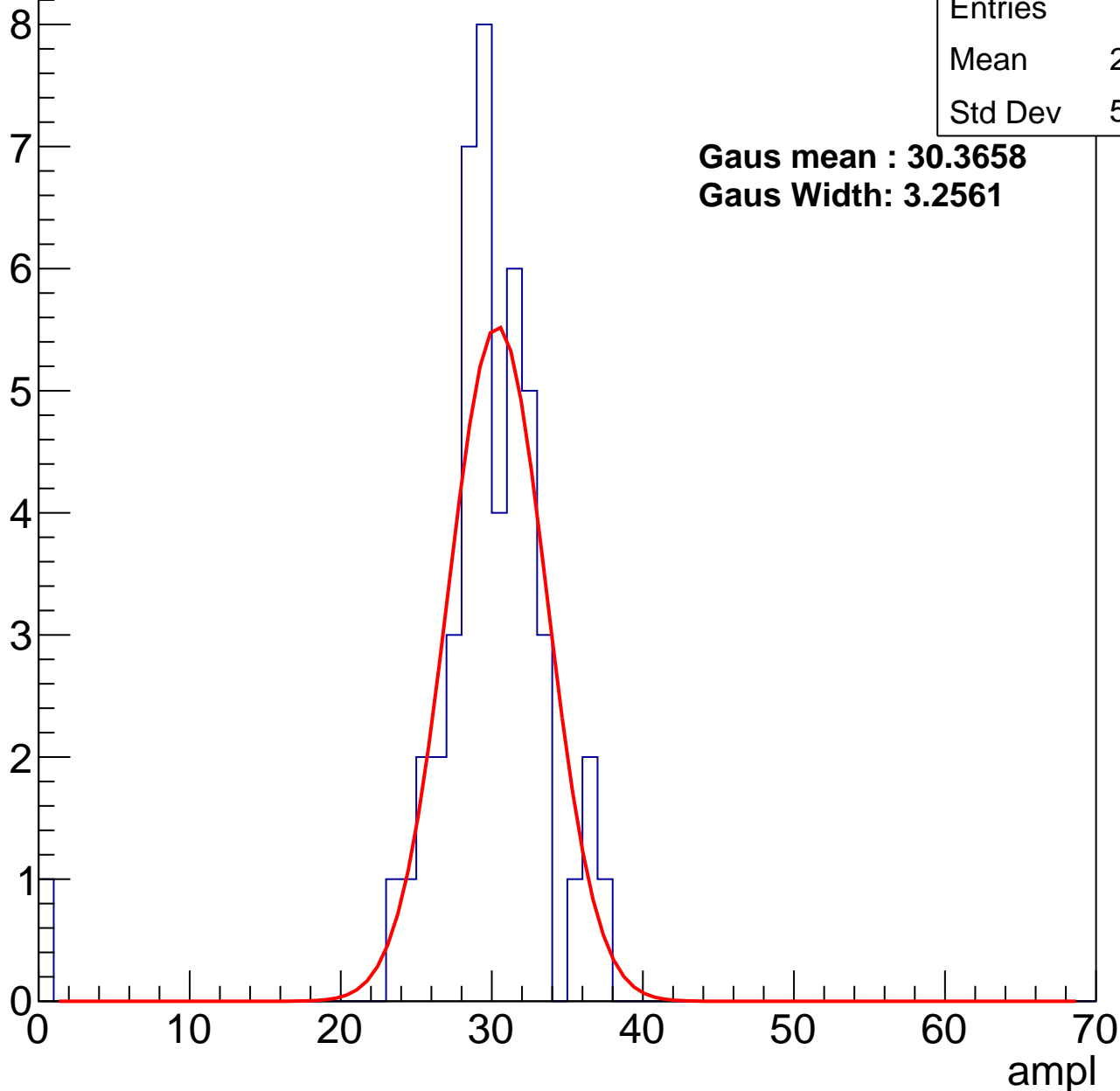
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	29.09
Std Dev	5.238

Gaus mean : 30.3658

Gaus Width: 3.2561



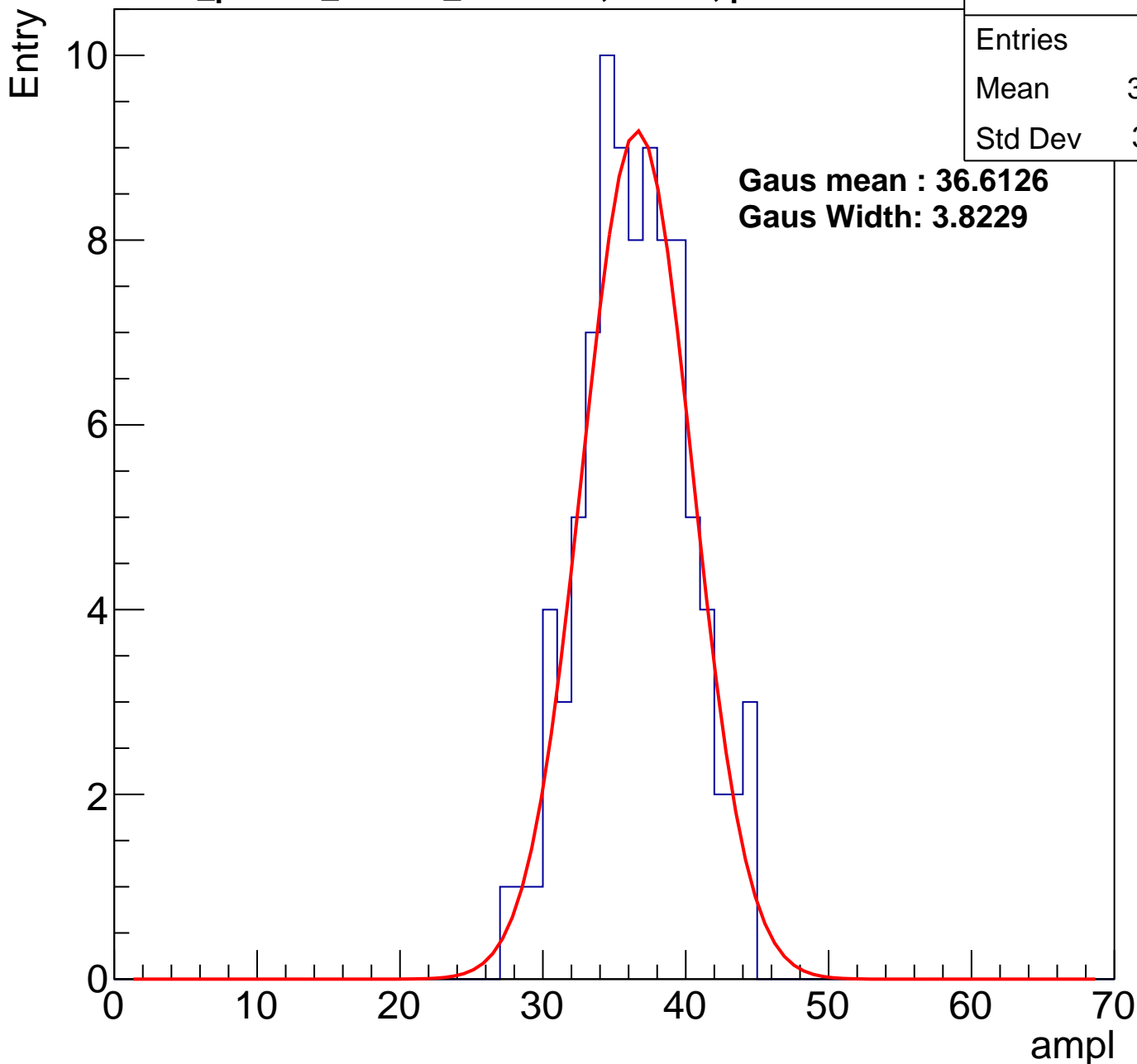
B1L003S, U20-ch99, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	90
Mean	36.07
Std Dev	3.741

Gaus mean : 36.6126

Gaus Width: 3.8229



B1L003S, U20-ch99, adc2

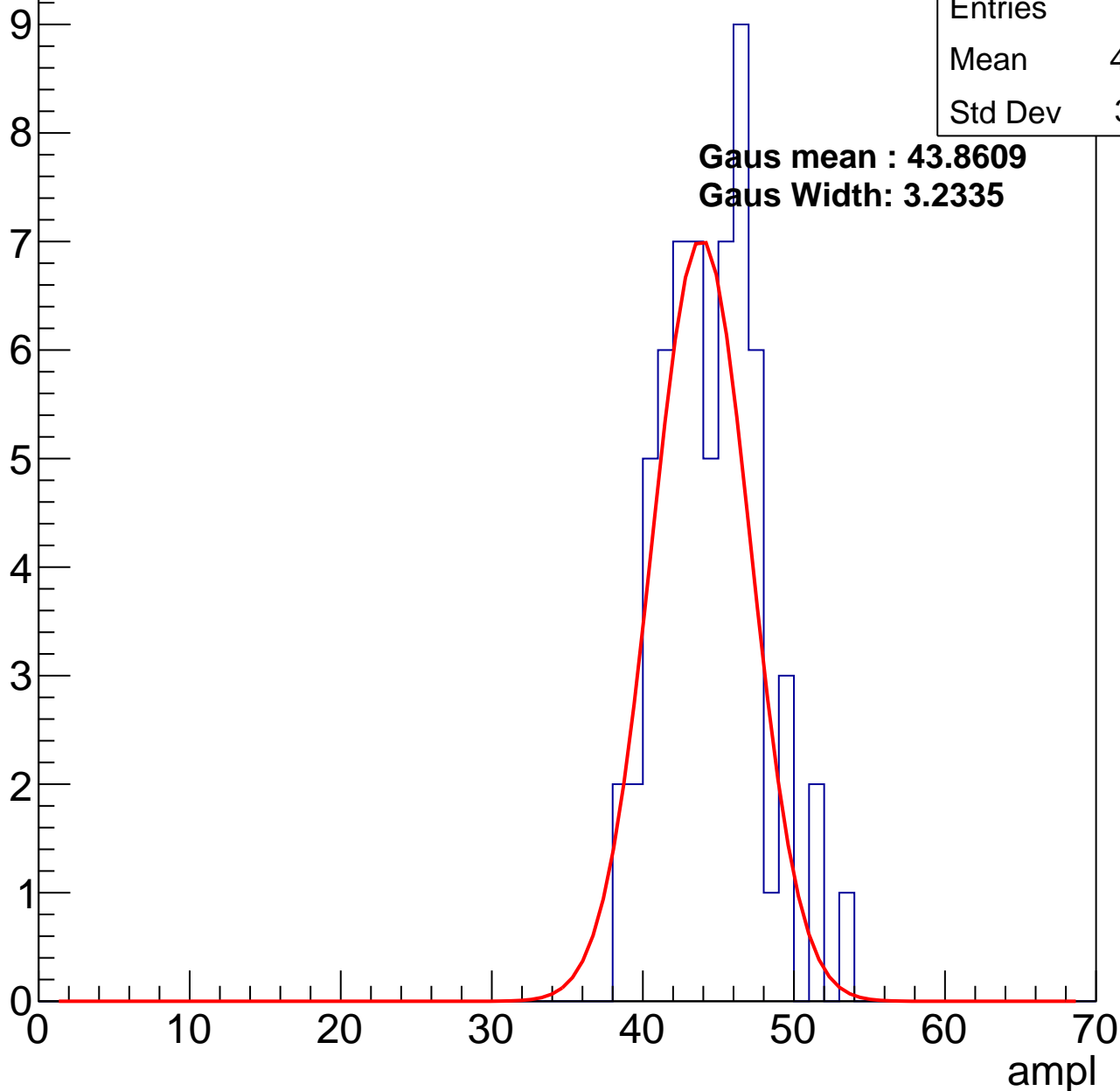
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	44.06
Std Dev	3.231

Gaus mean : 43.8609

Gaus Width: 3.2335



B1L003S, U20-ch99, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	50.12
Std Dev	3.299

Entry

10

8

6

4

2

0

0

10

20

30

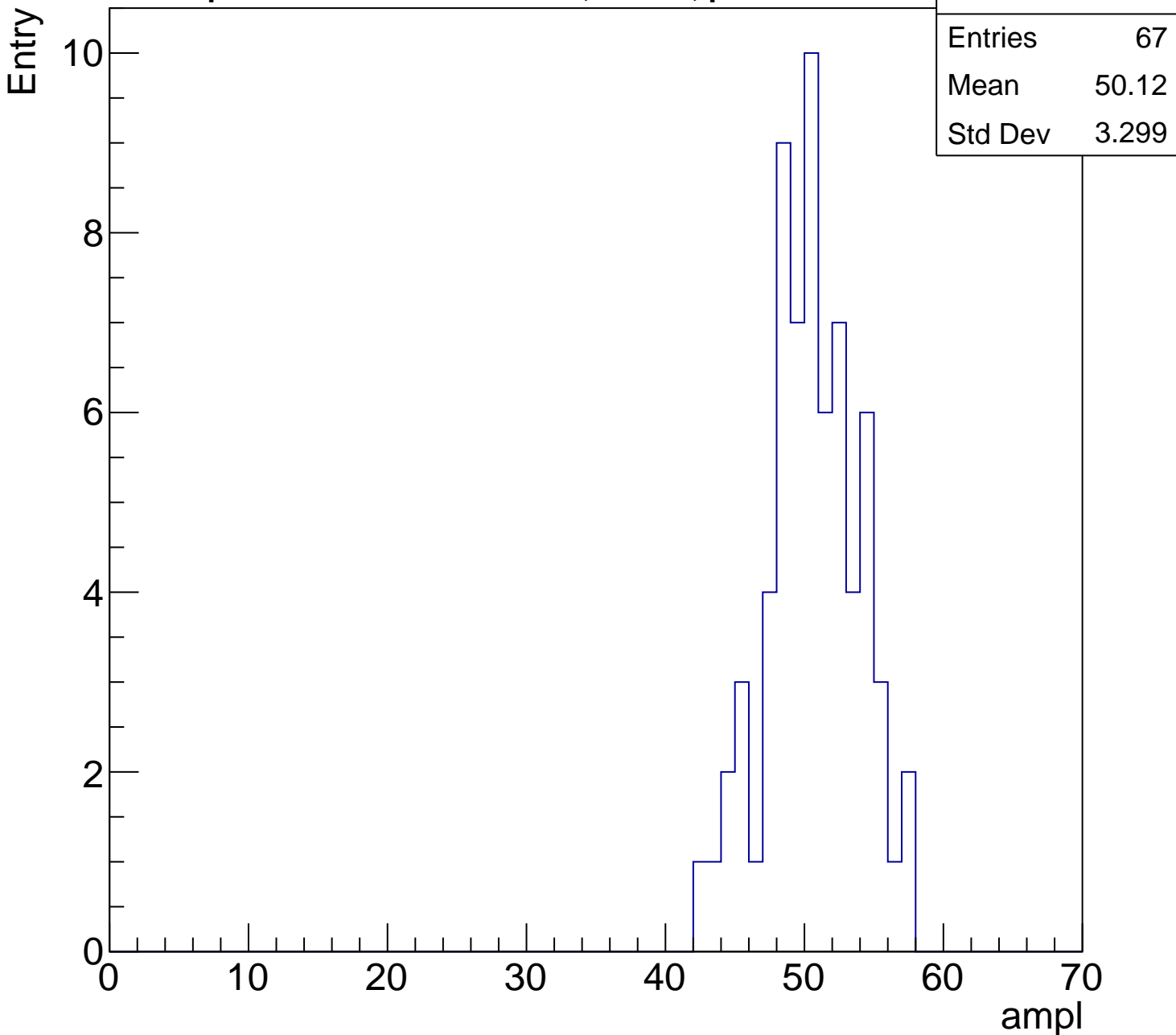
40

50

60

70

ampl



B1L003S, U20-ch99, adc4

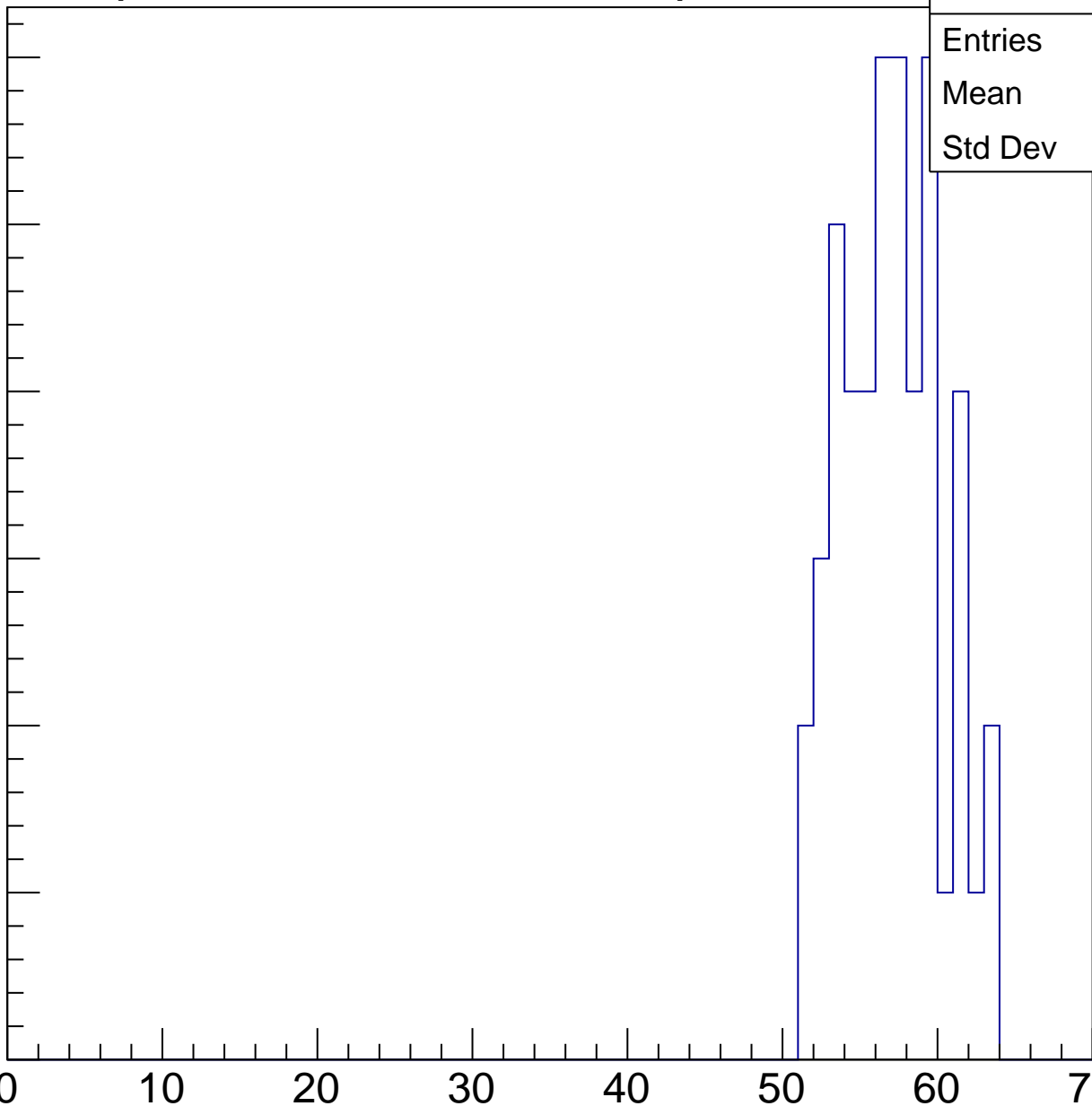
calib_packv5_042523_0143.root, FC#13, port D2

Entry

6
5
4
3
2
1
0

Entries	48
Mean	56.56
Std Dev	3.142

ampl

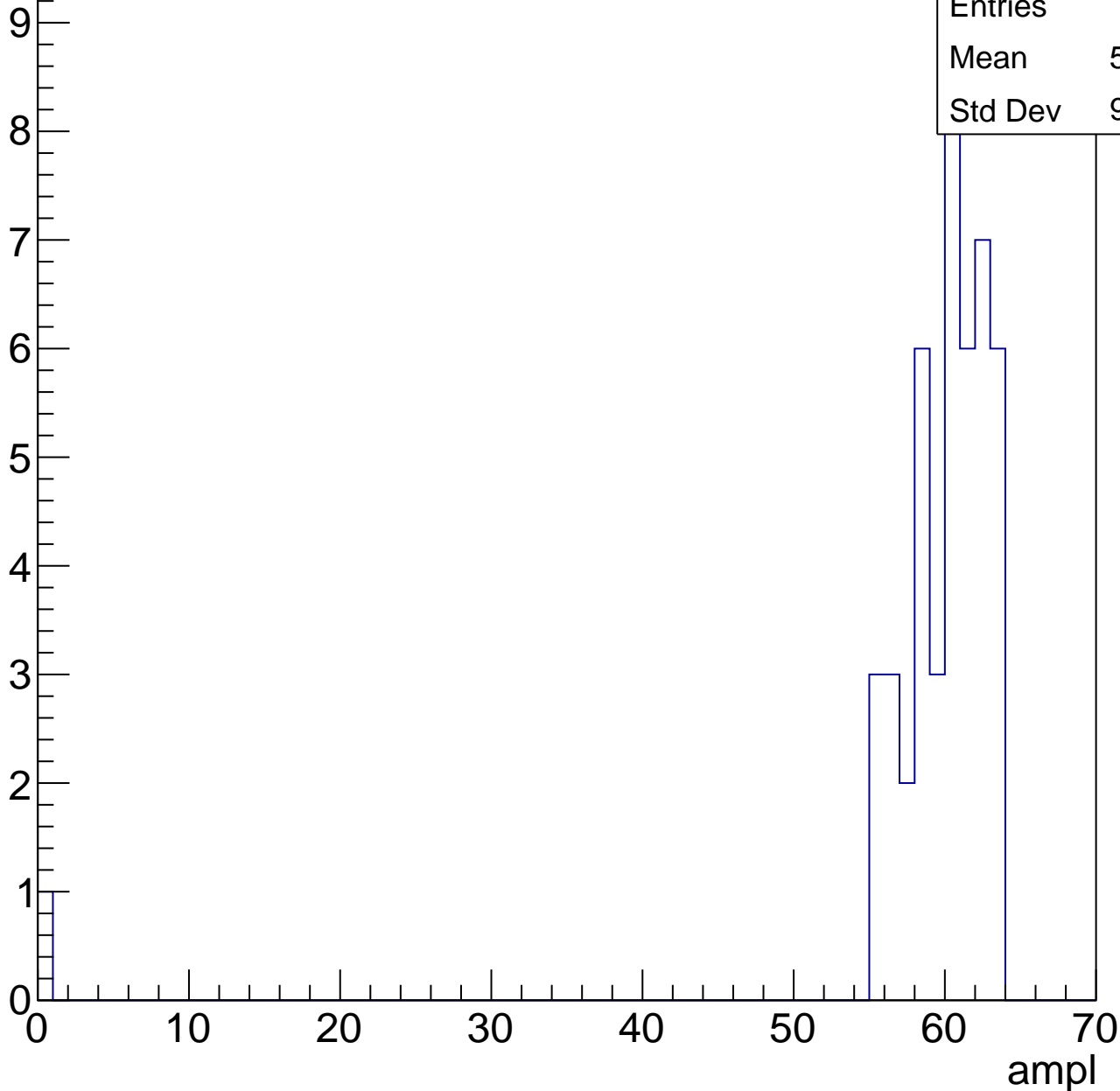


B1L003S, U20-ch99, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.48
Std Dev	9.028



B1L003S, U20-ch99, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch99, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch100, adc0

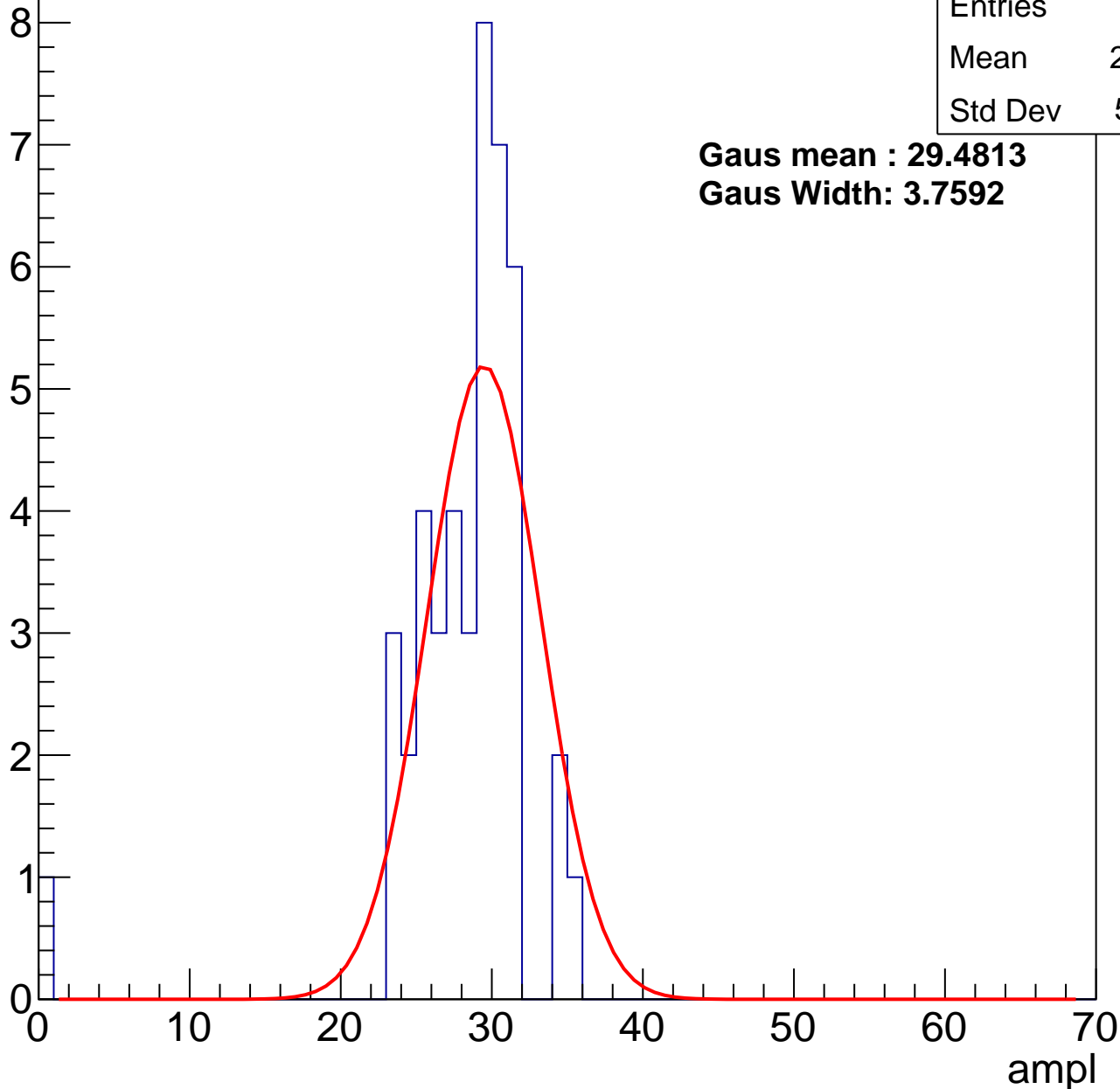
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	27.68
Std Dev	5.111

Gaus mean : 29.4813

Gaus Width: 3.7592



B1L003S, U20-ch100, adc1

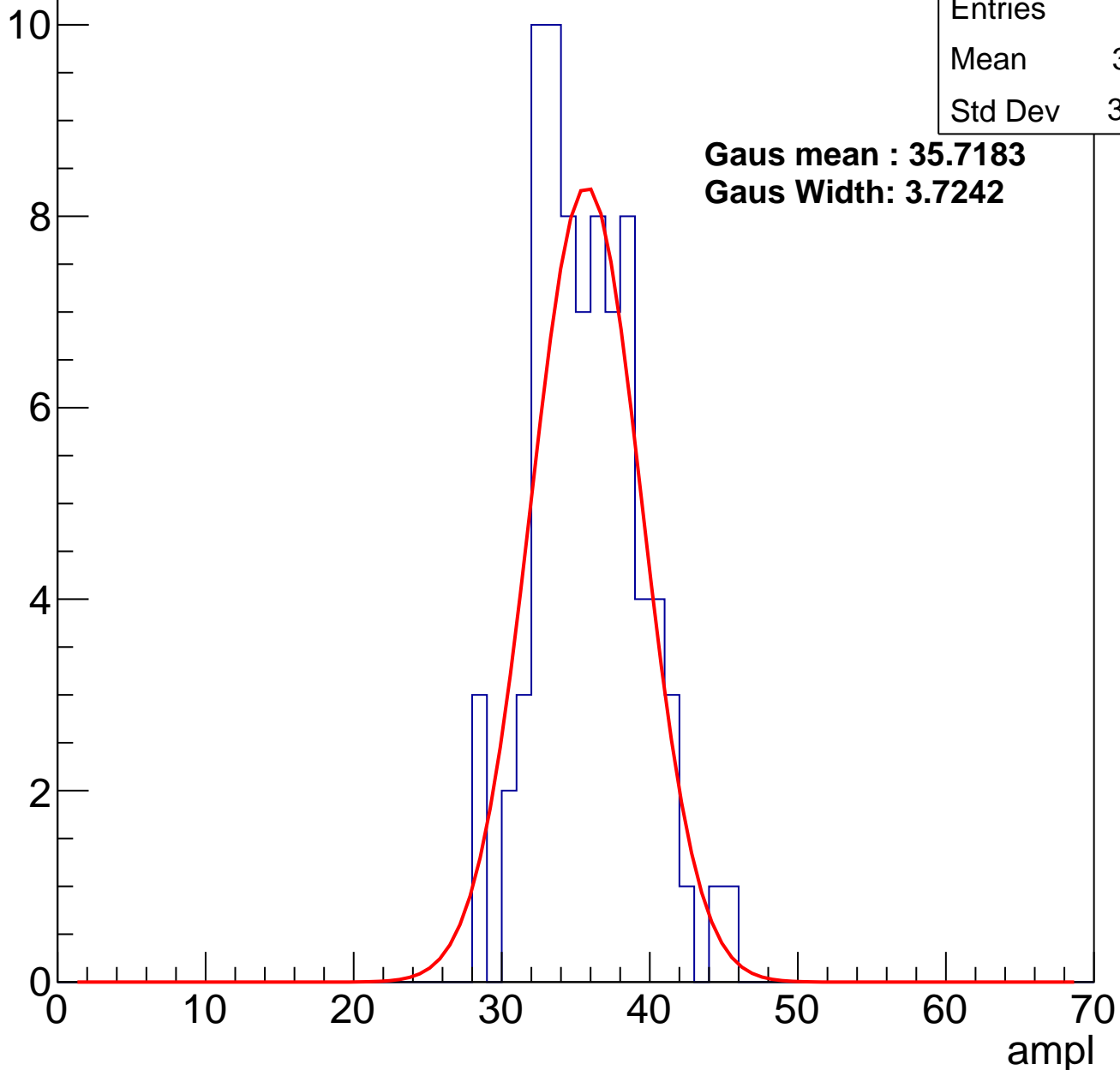
calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	35.31
Std Dev	3.509

Gaus mean : 35.7183

Gaus Width: 3.7242

Entry



B1L003S, U20-ch100, adc2

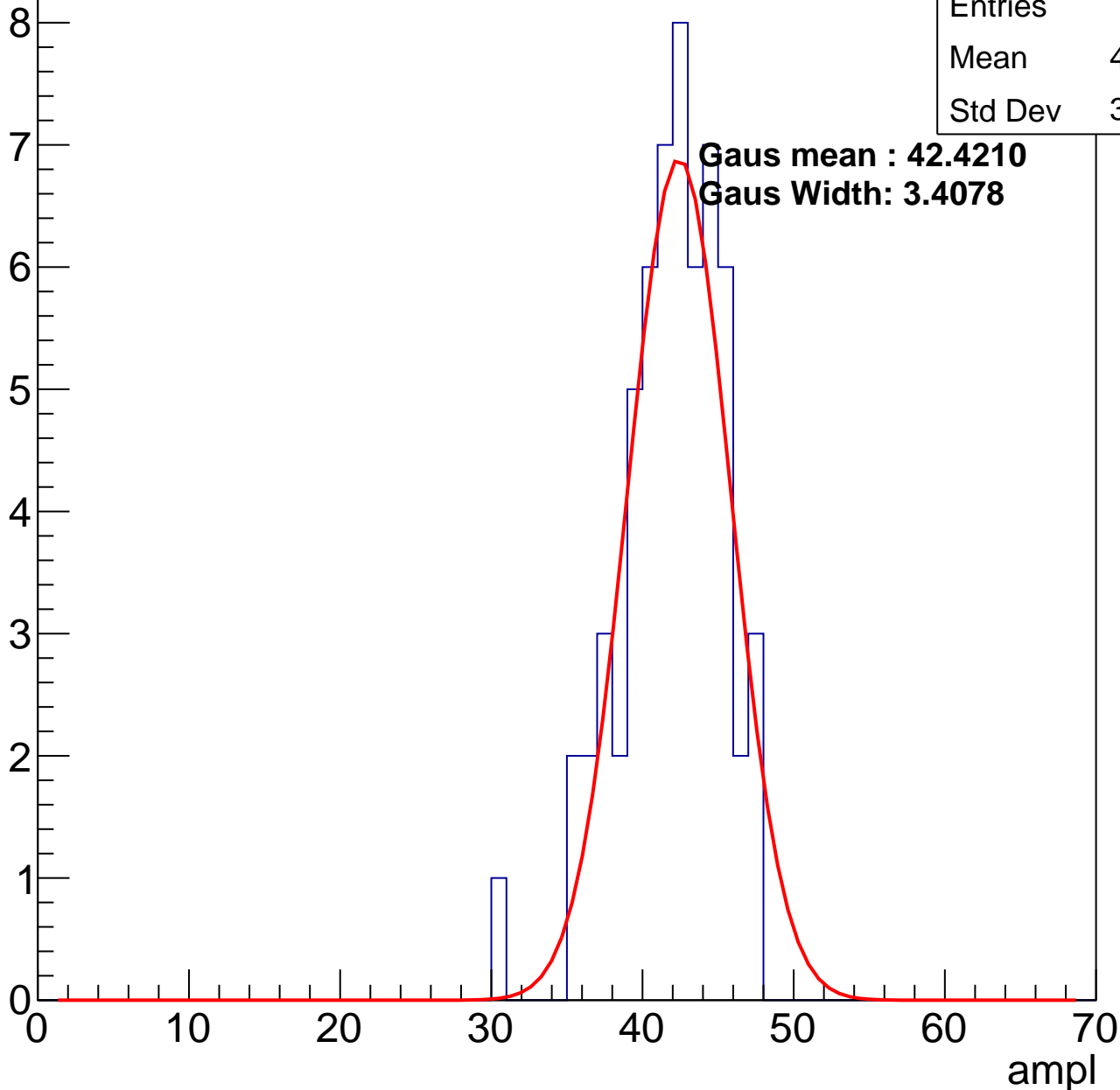
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	41.43
Std Dev	3.348

Gaus mean : 42.4210

Gaus Width: 3.4078



B1L003S, U20-ch100, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	48.3
Std Dev	3.055

Entry

10

8

6

4

2

0

0

10

20

30

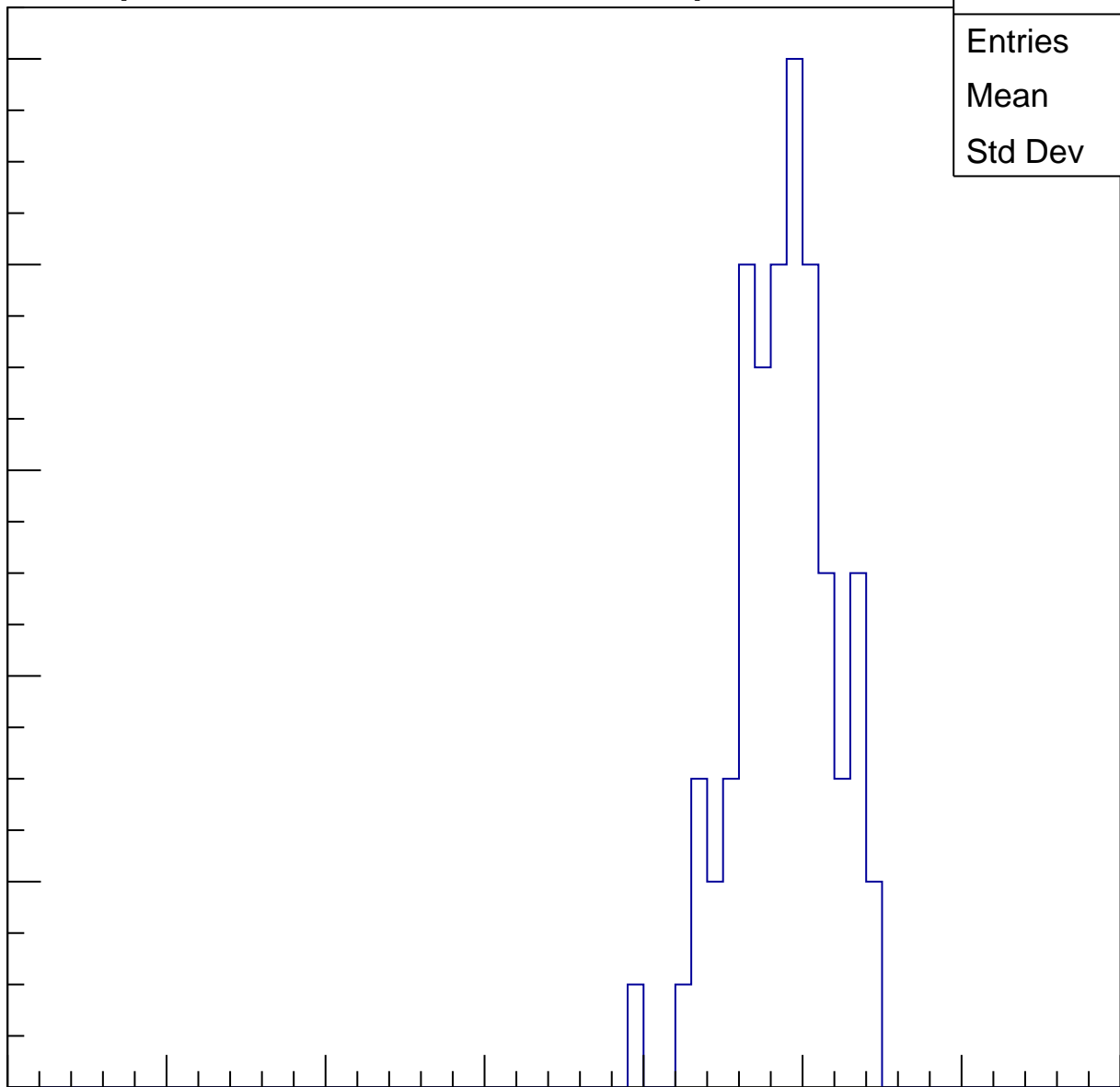
40

50

60

70

ampl

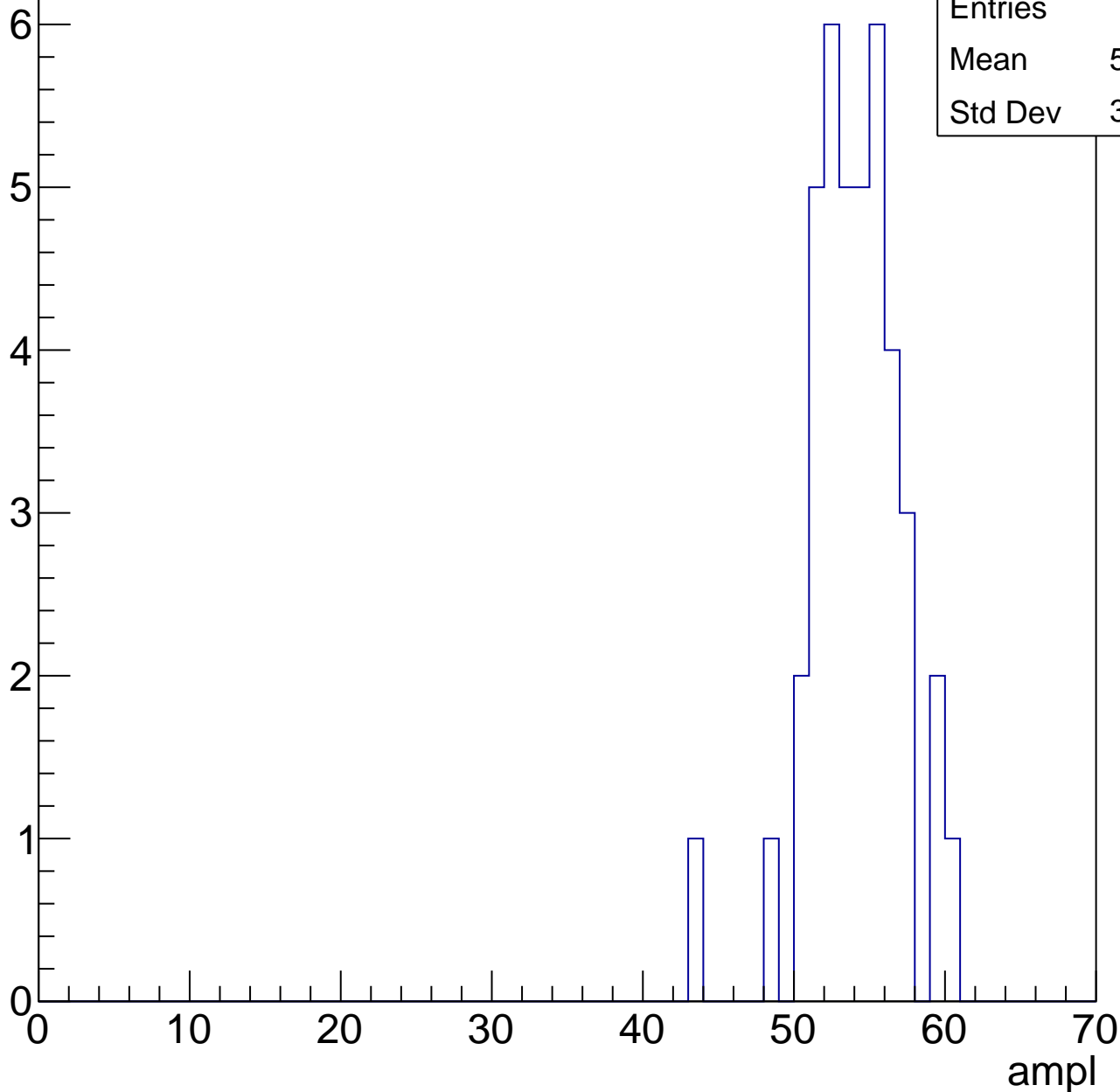


B1L003S, U20-ch100, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	53.56
Std Dev	3.085



B1L003S, U20-ch100, adc5

calib_packv5_042523_0143.root, FC#13, port D2

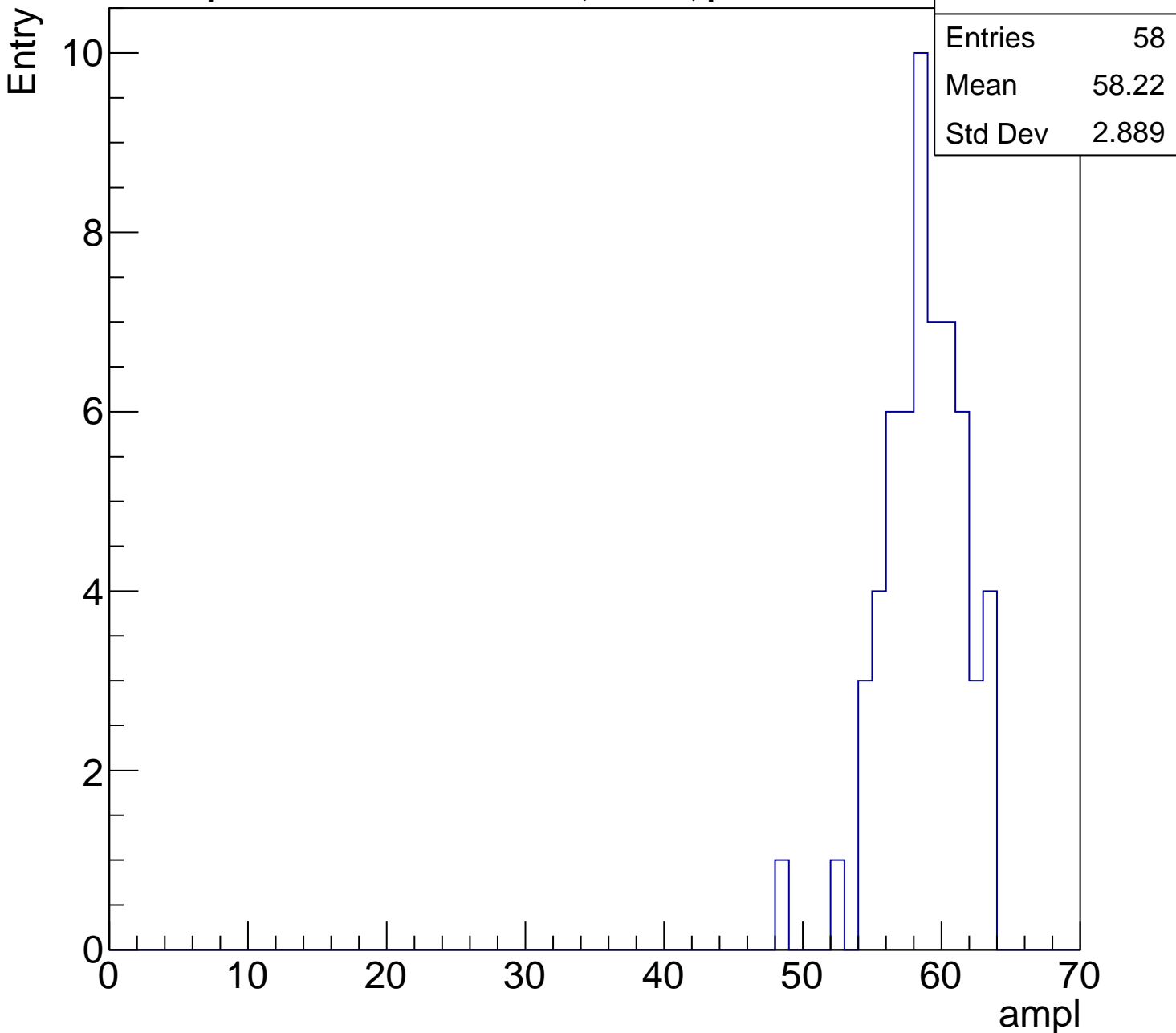
Entries	58
Mean	58.22
Std Dev	2.889

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

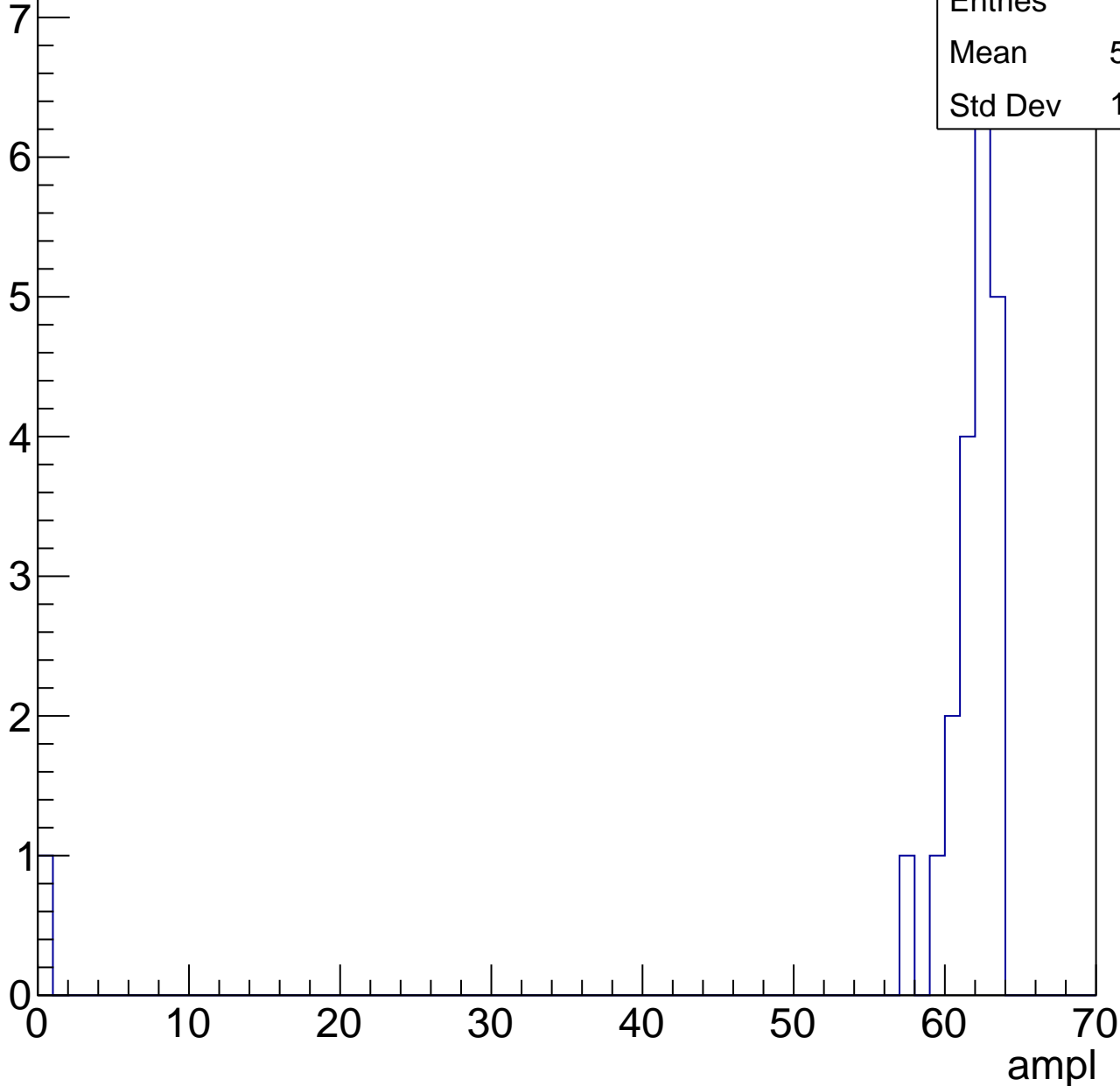


B1L003S, U20-ch100, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.52
Std Dev	13.17



B1L003S, U20-ch100, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch101, adc0

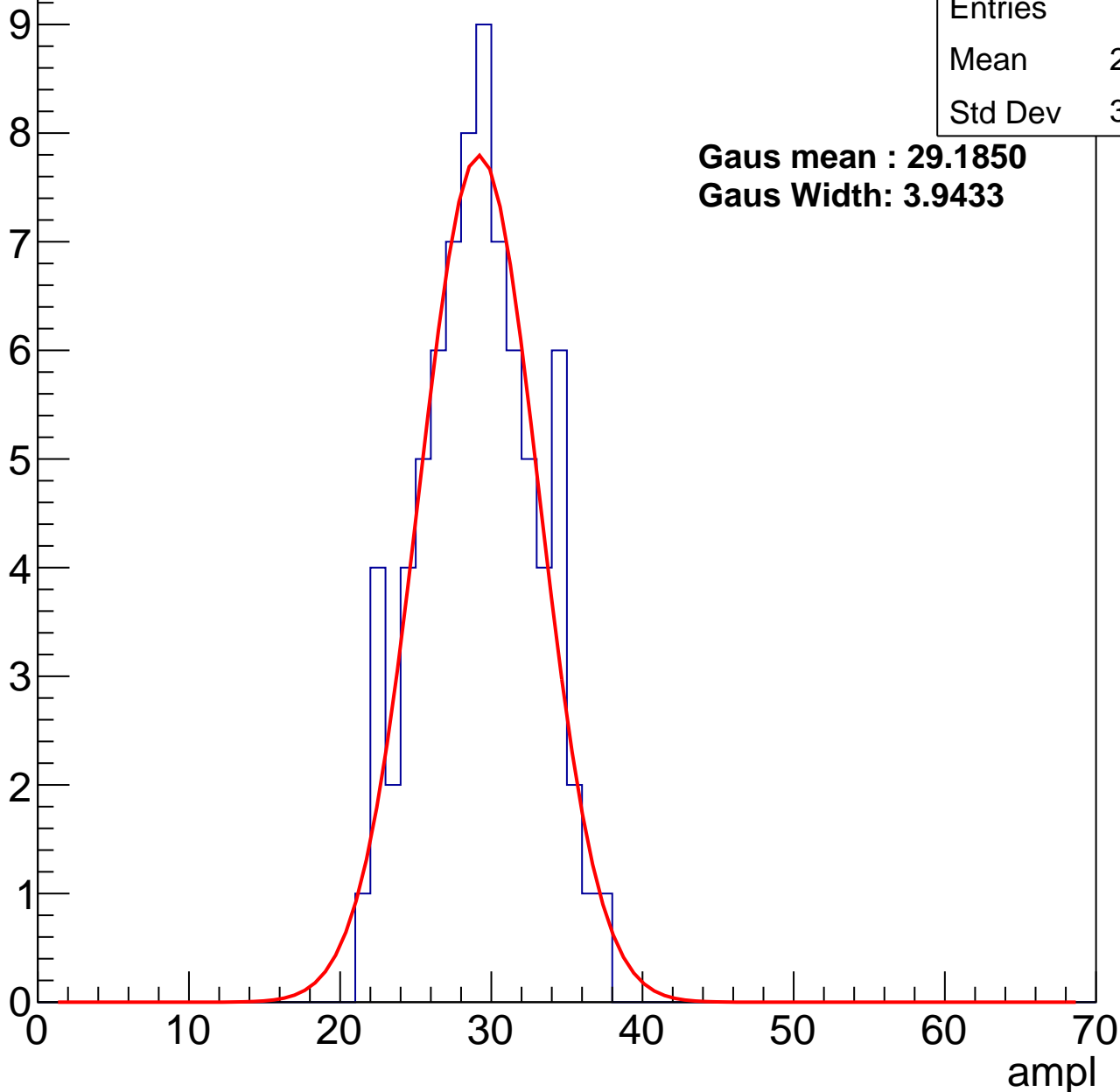
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	28.73
Std Dev	3.706

Gaus mean : 29.1850

Gaus Width: 3.9433



B1L003S, U20-ch101, adc1

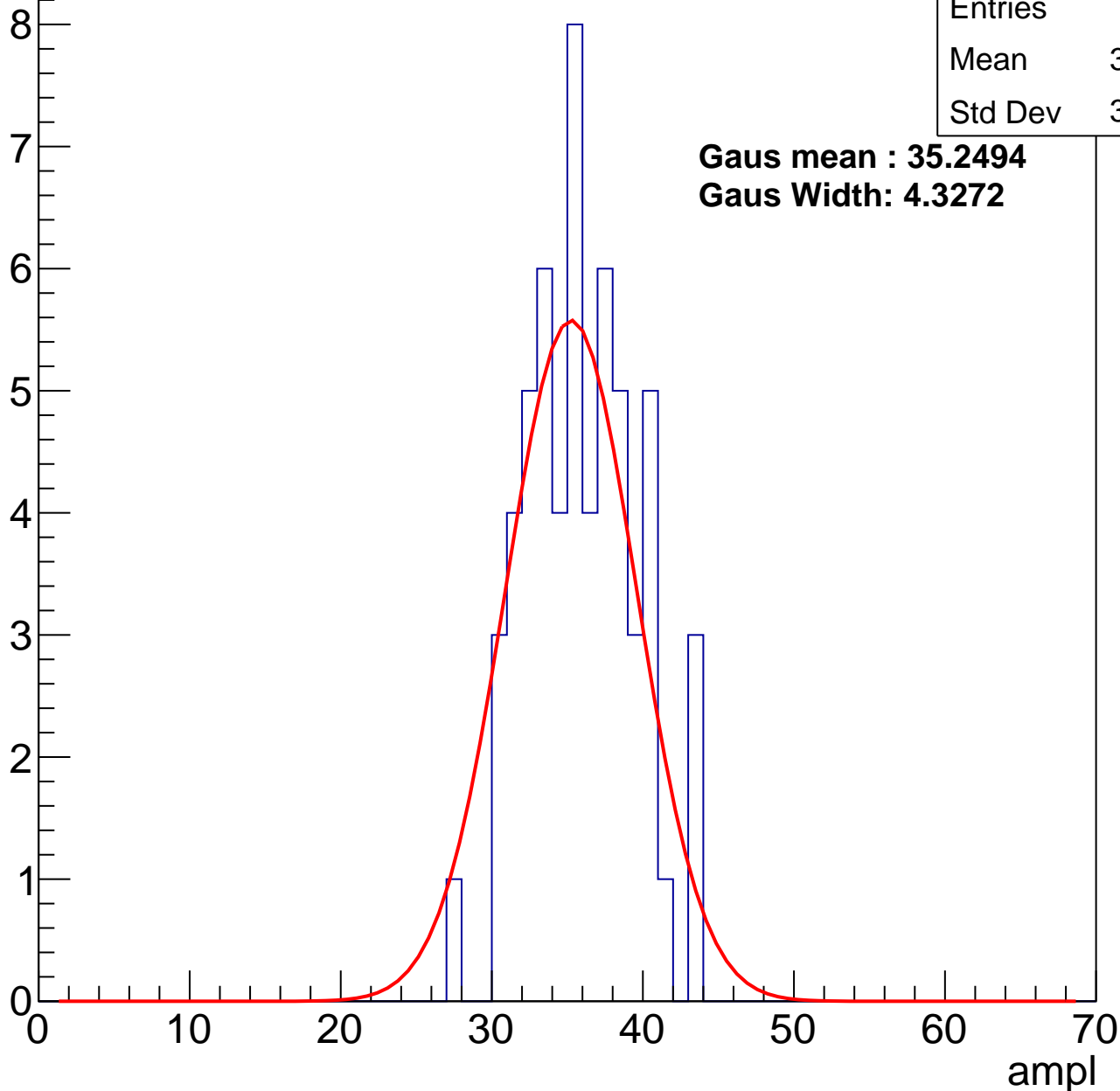
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	35.48
Std Dev	3.563

Gaus mean : 35.2494

Gaus Width: 4.3272



B1L003S, U20-ch101, adc2

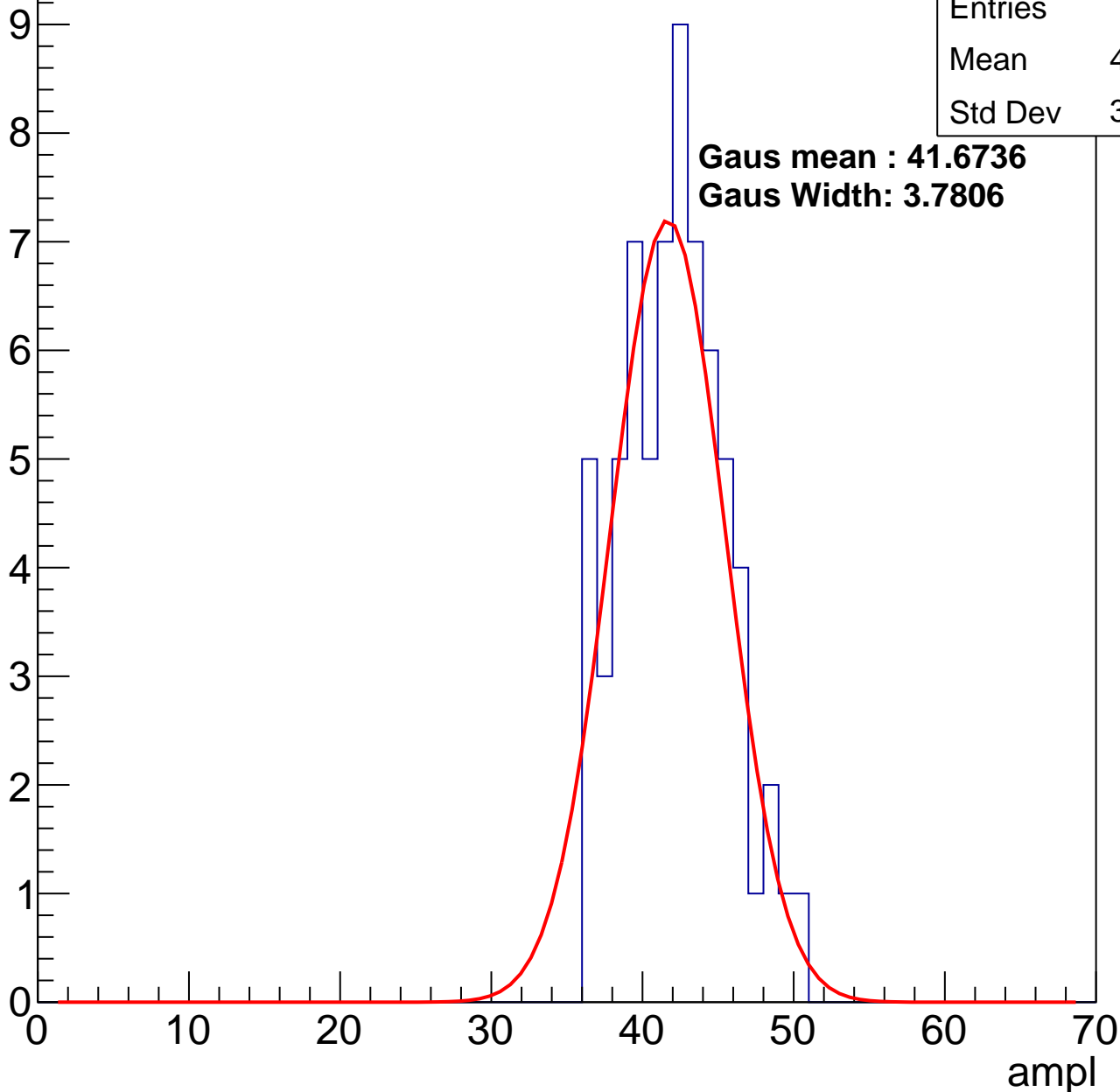
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.69
Std Dev	3.362

Gaus mean : 41.6736

Gaus Width: 3.7806

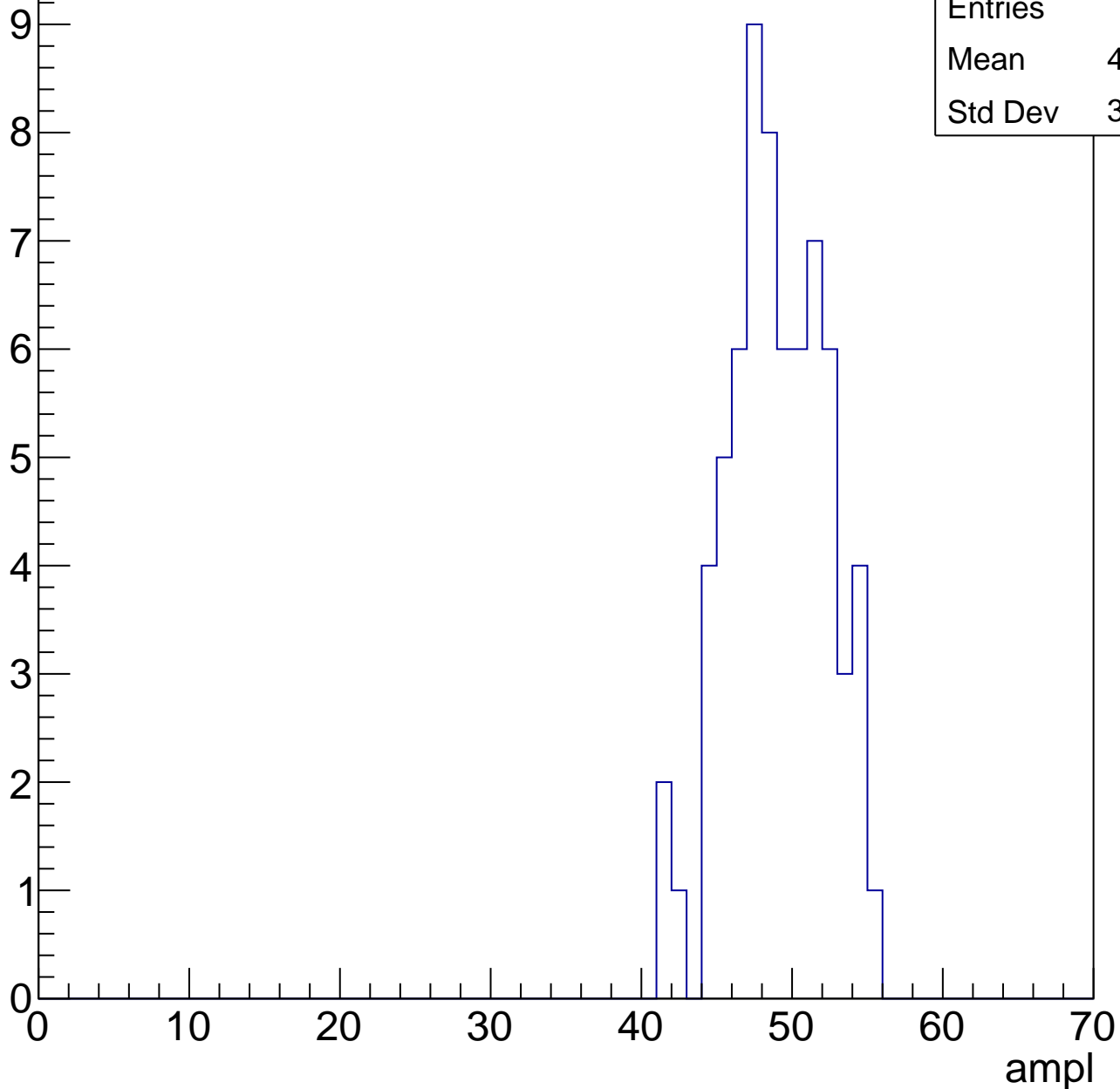


B1L003S, U20-ch101, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	48.54
Std Dev	3.238

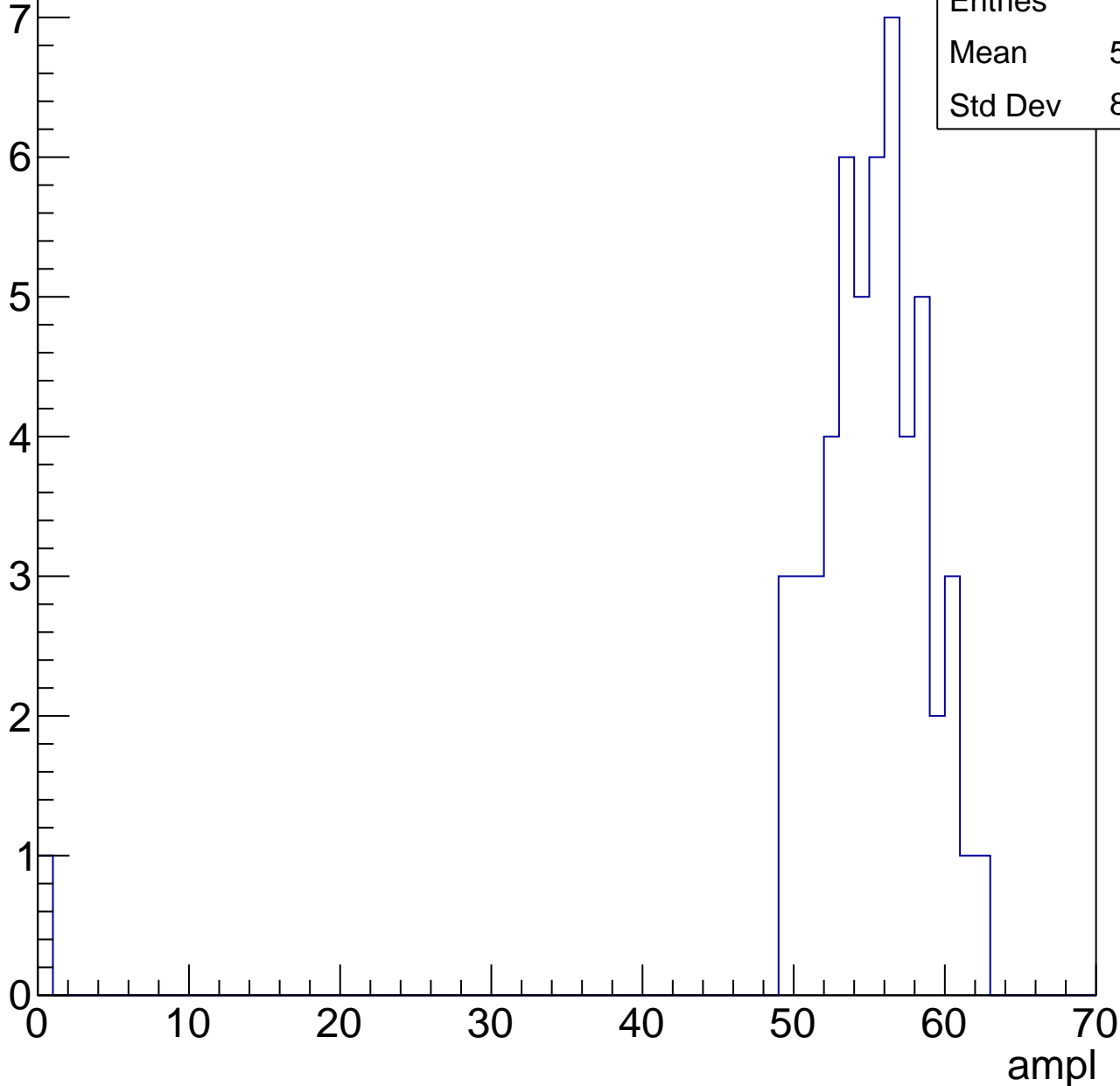


B1L003S, U20-ch101, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	53.83
Std Dev	8.059

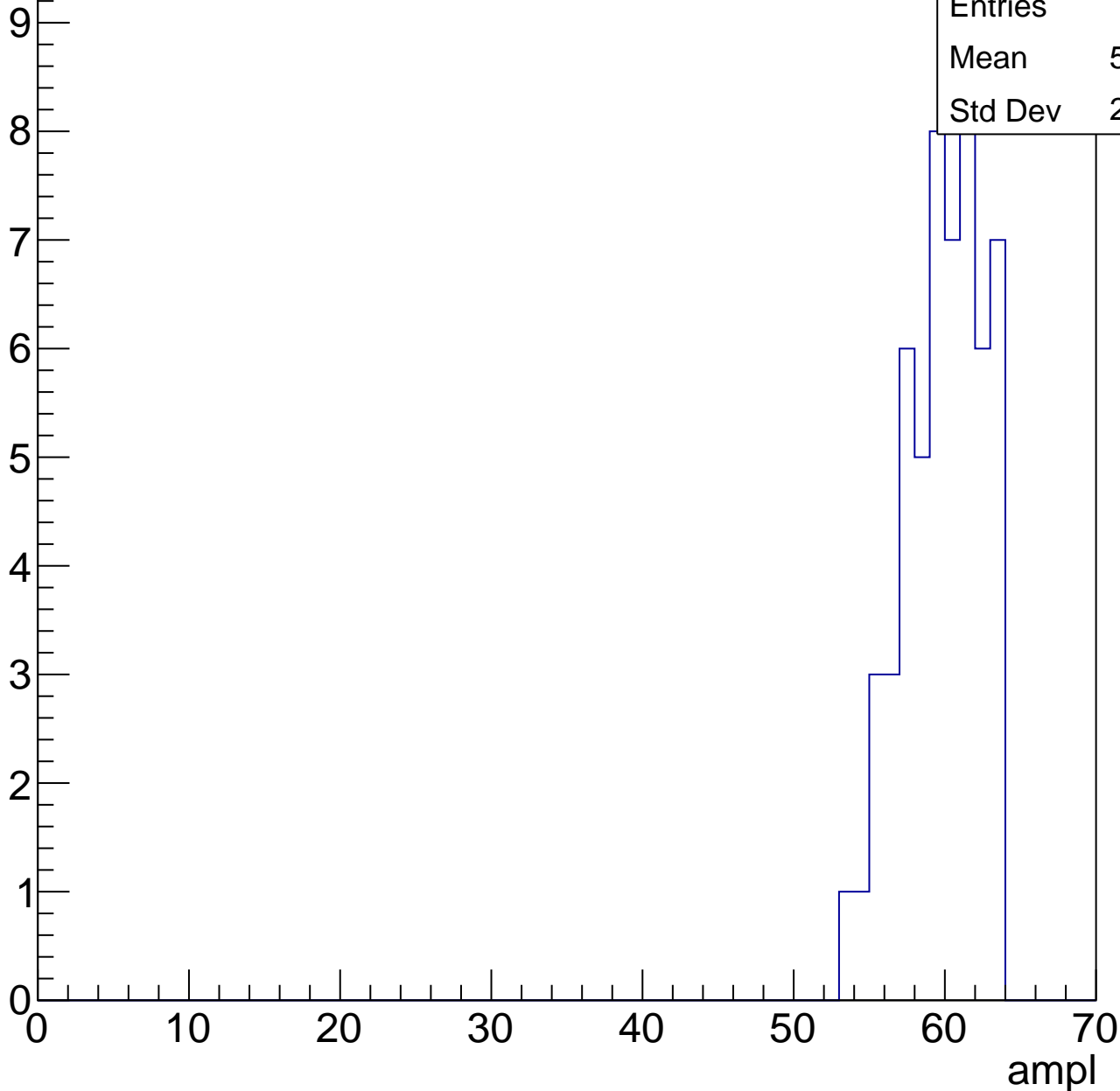


B1L003S, U20-ch101, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	59.39
Std Dev	2.554



B1L003S, U20-ch101, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch101, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch102, adc0

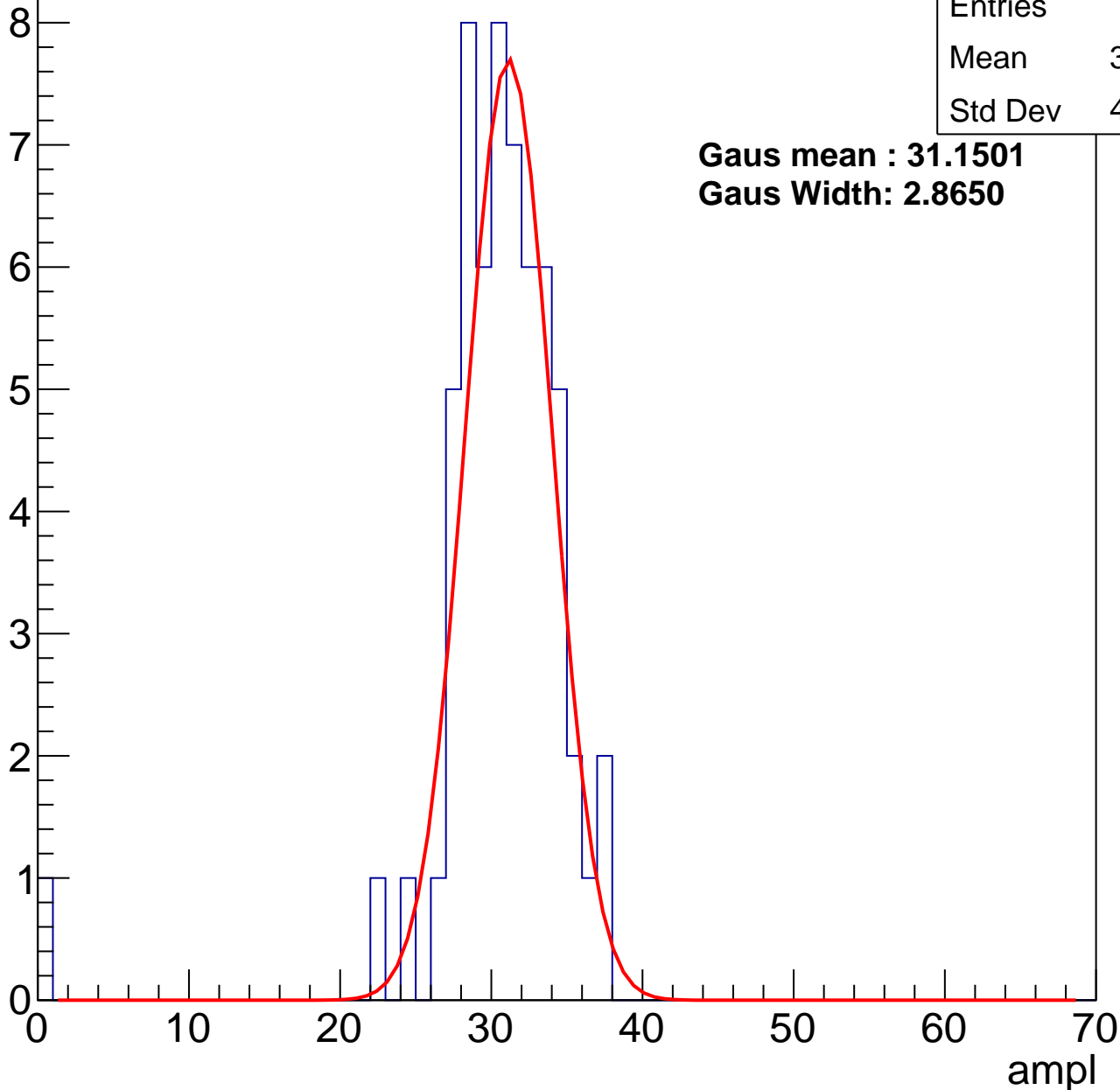
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	30.03
Std Dev	4.912

Gaus mean : 31.1501

Gaus Width: 2.8650



B1L003S, U20-ch102, adc1

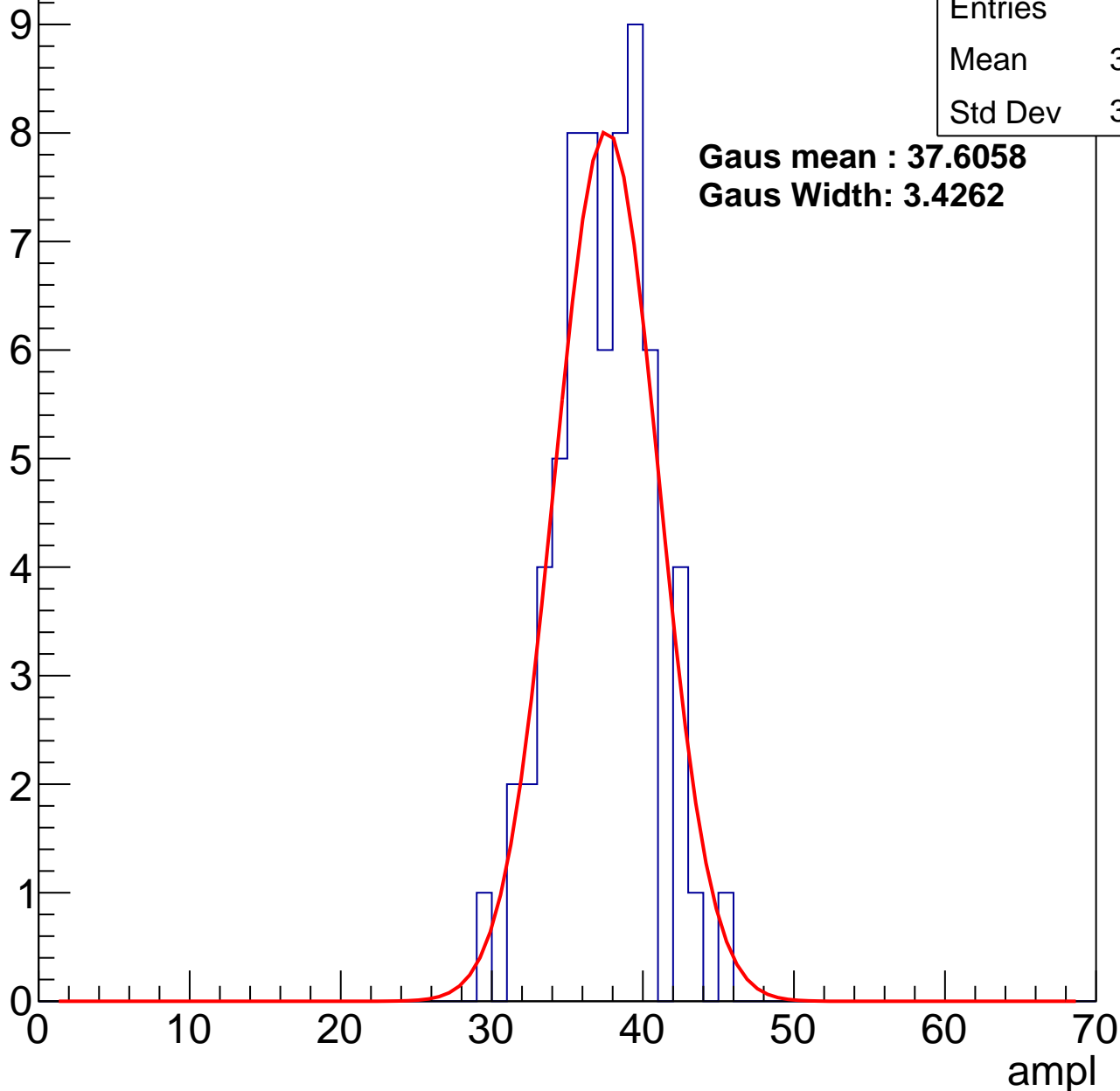
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.89
Std Dev	3.119

Gaus mean : 37.6058

Gaus Width: 3.4262



B1L003S, U20-ch102, adc2

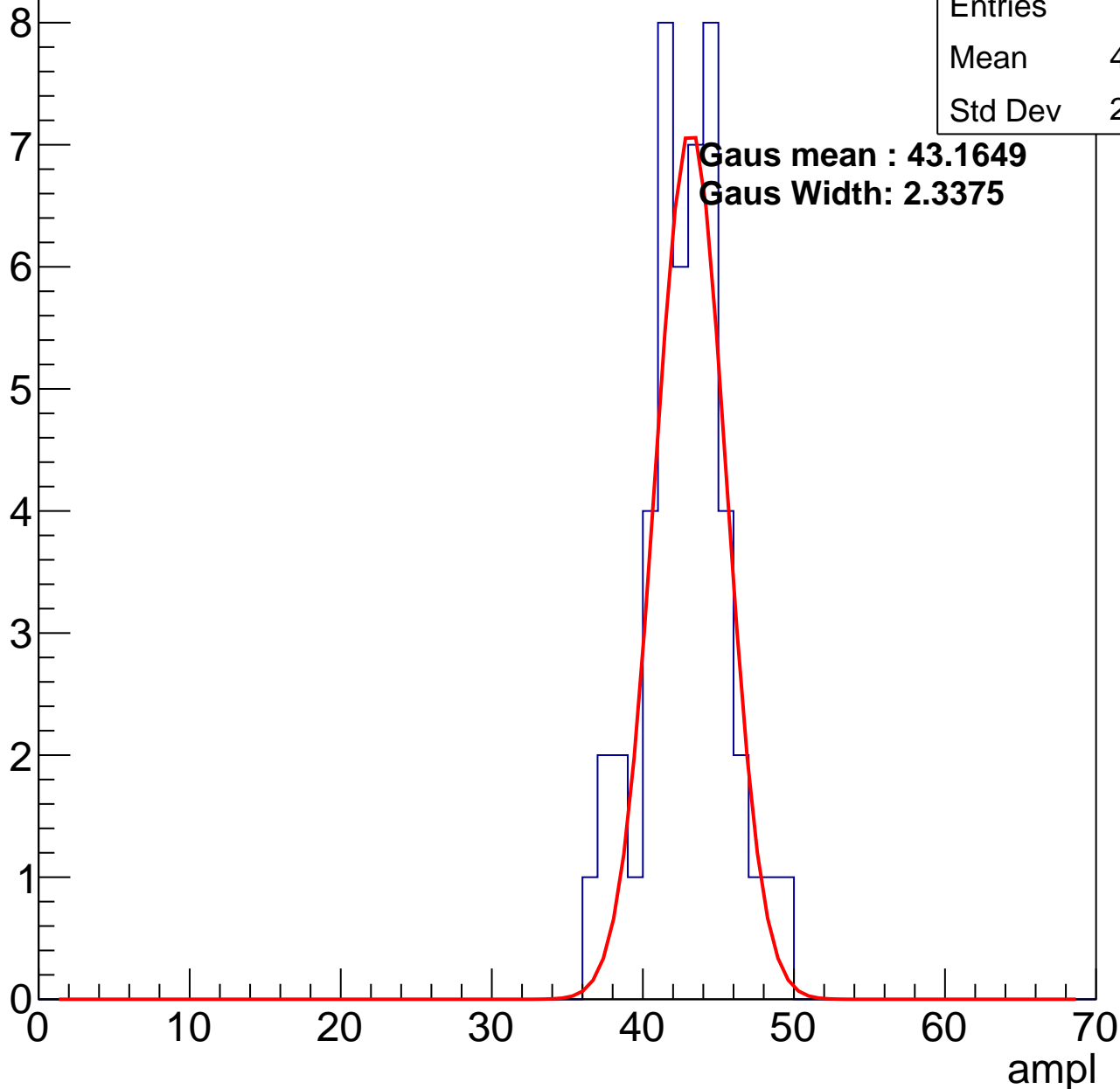
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	42.38
Std Dev	2.743

Gaus mean : 43.1649

Gaus Width: 2.3375

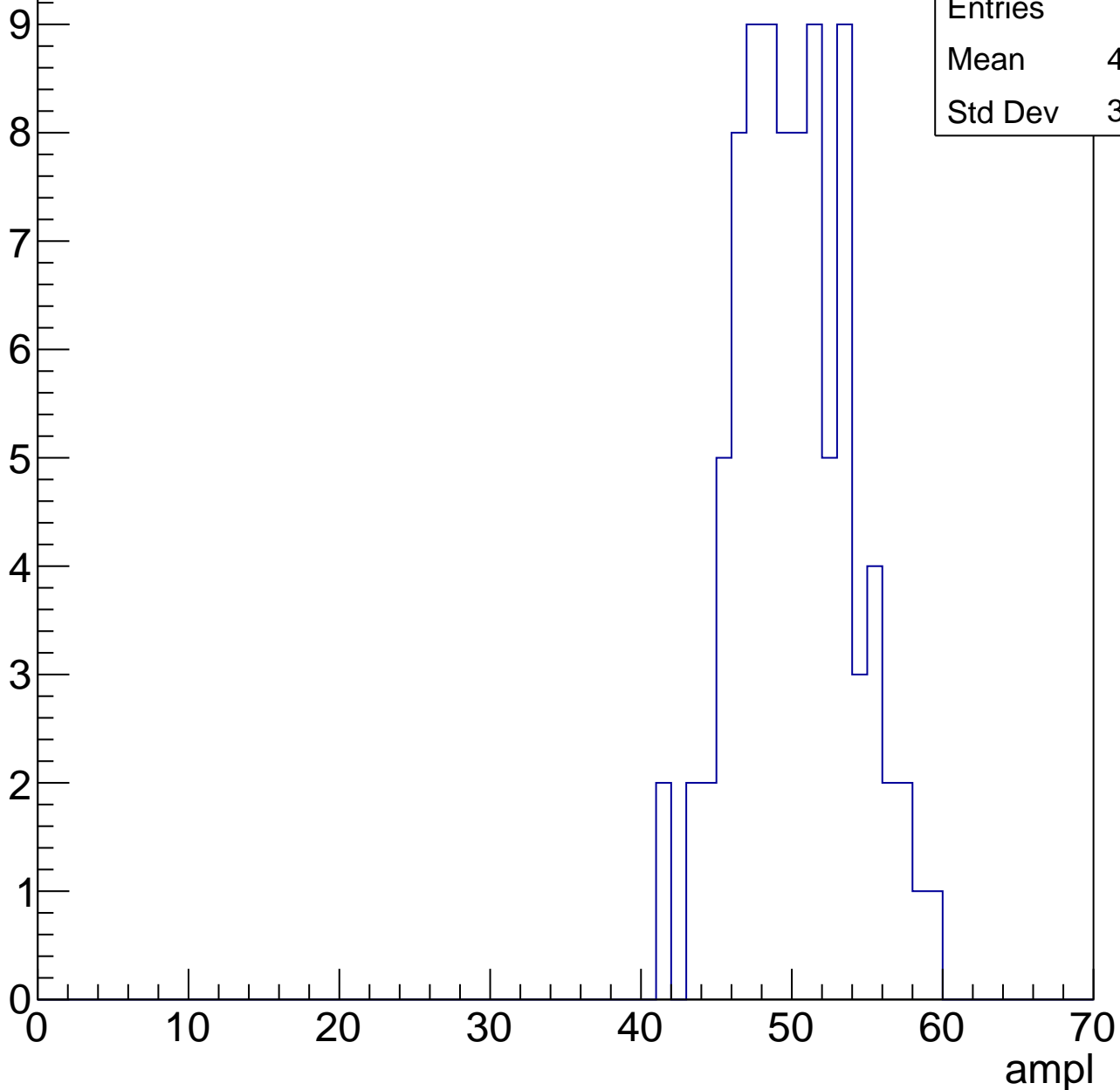


B1L003S, U20-ch102, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	89
Mean	49.63
Std Dev	3.779

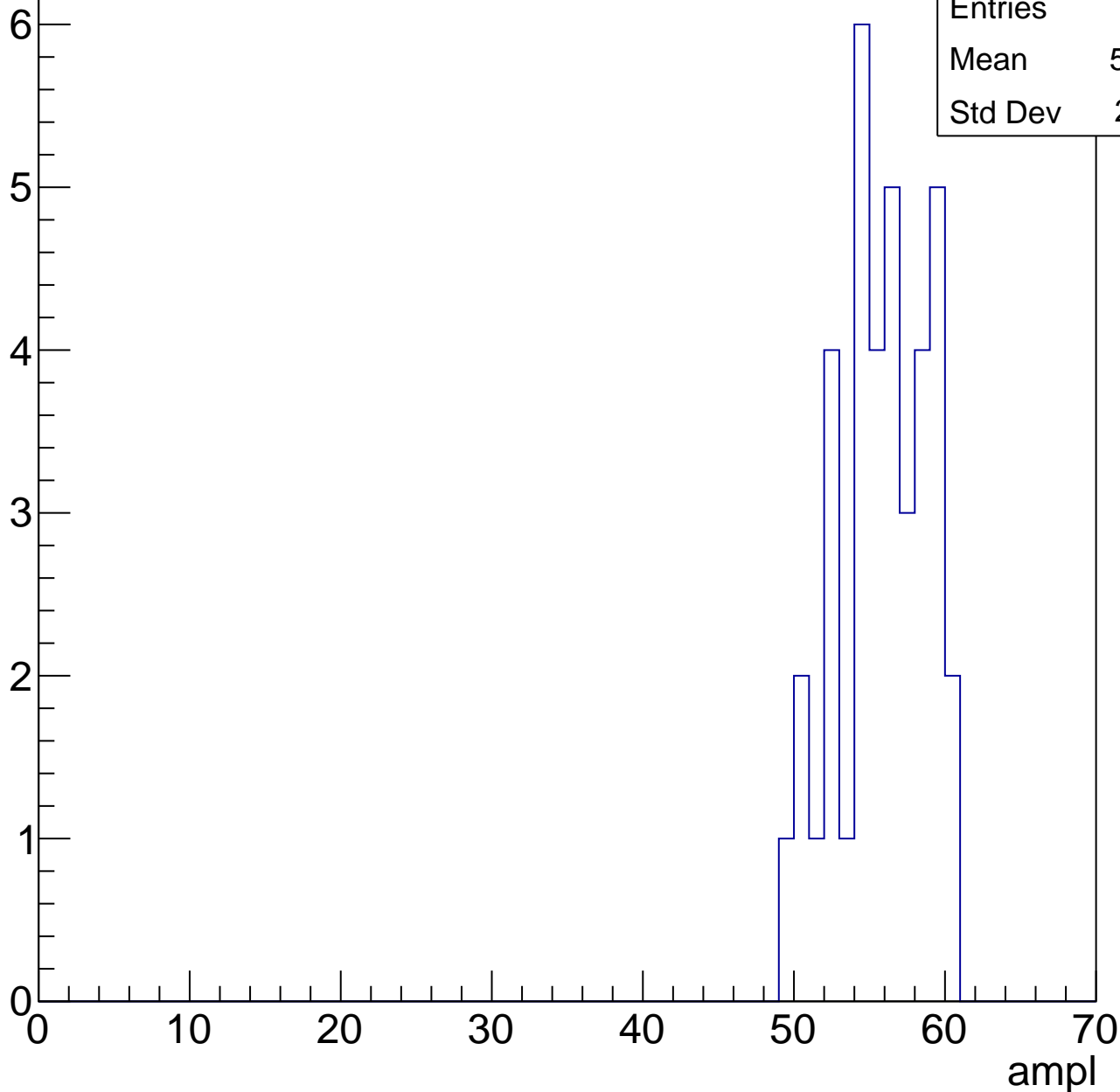


B1L003S, U20-ch102, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	55.34
Std Dev	2.931

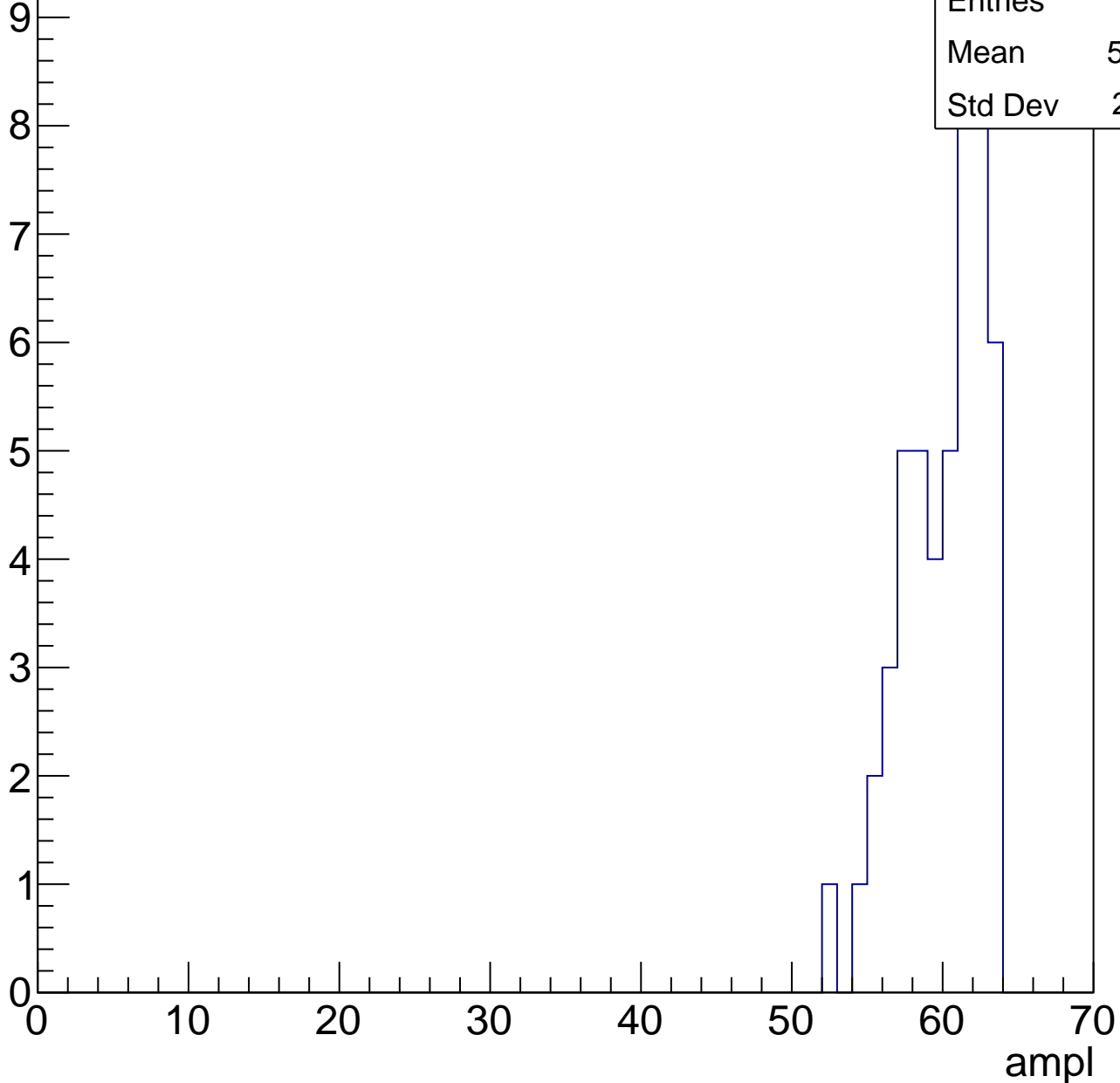


B1L003S, U20-ch102, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

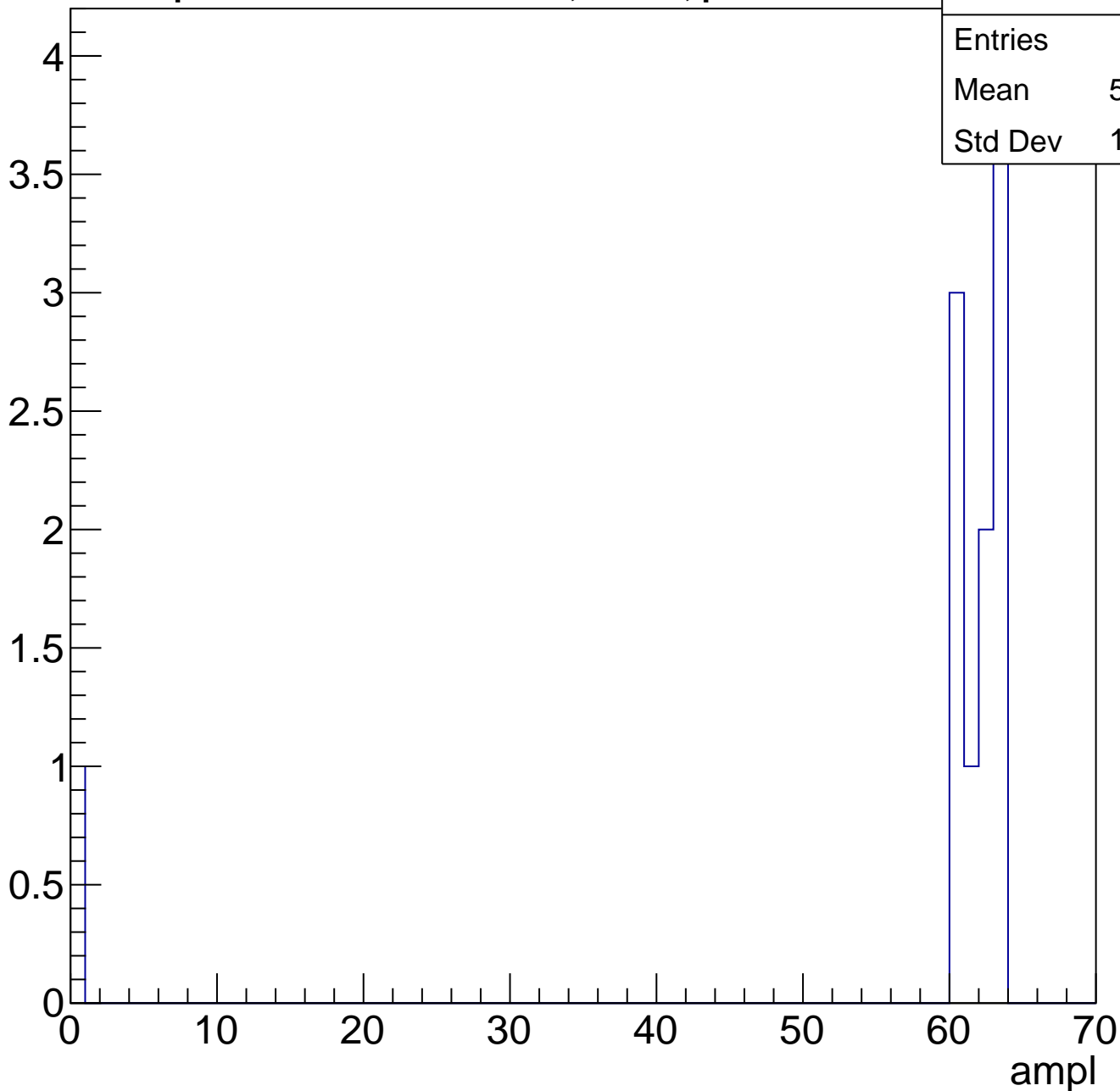
Entries	49
Mean	59.55
Std Dev	2.681



B1L003S, U20-ch102, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch102, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch103, adc0

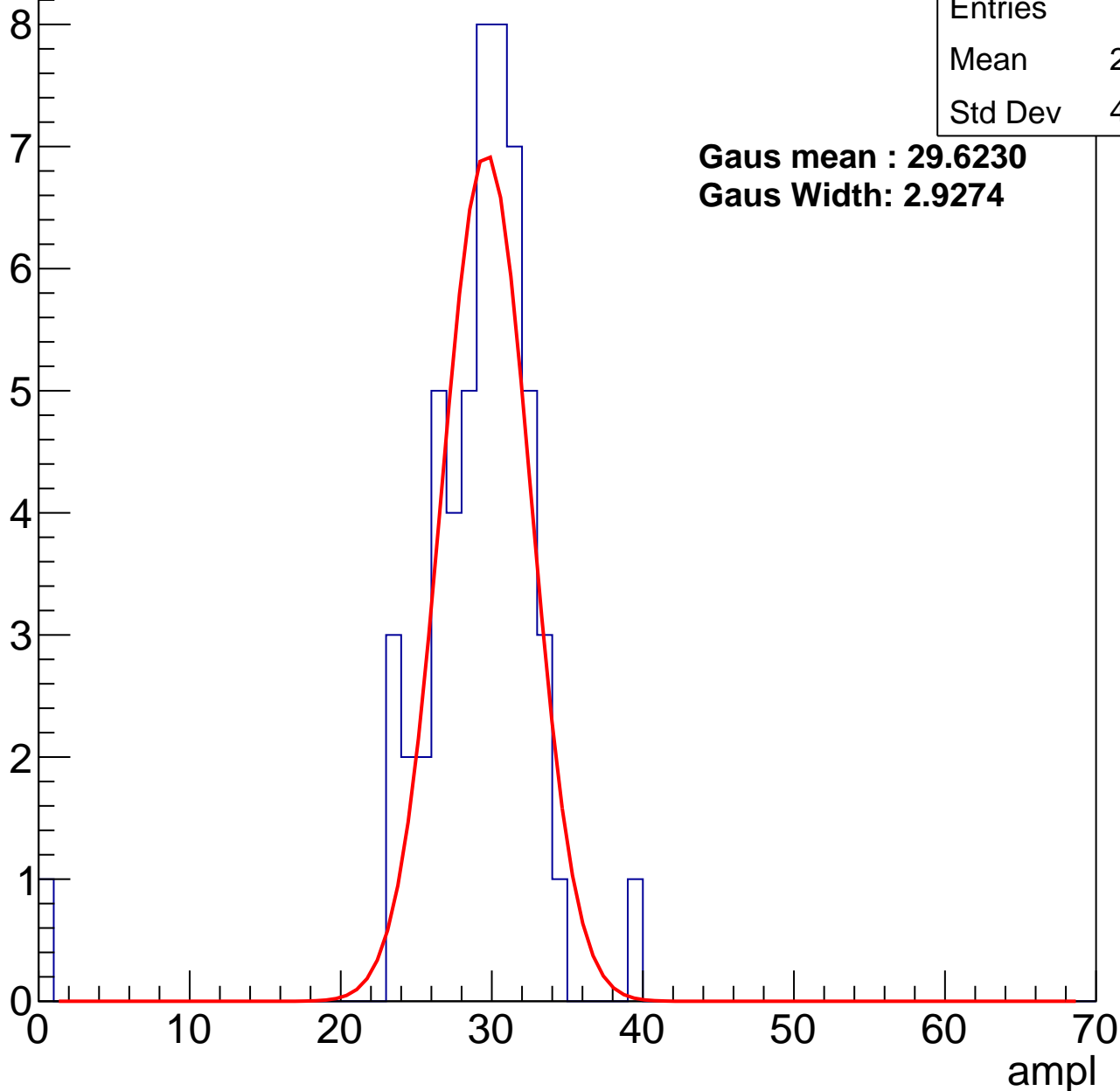
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.47
Std Dev	4.928

Gaus mean : 29.6230

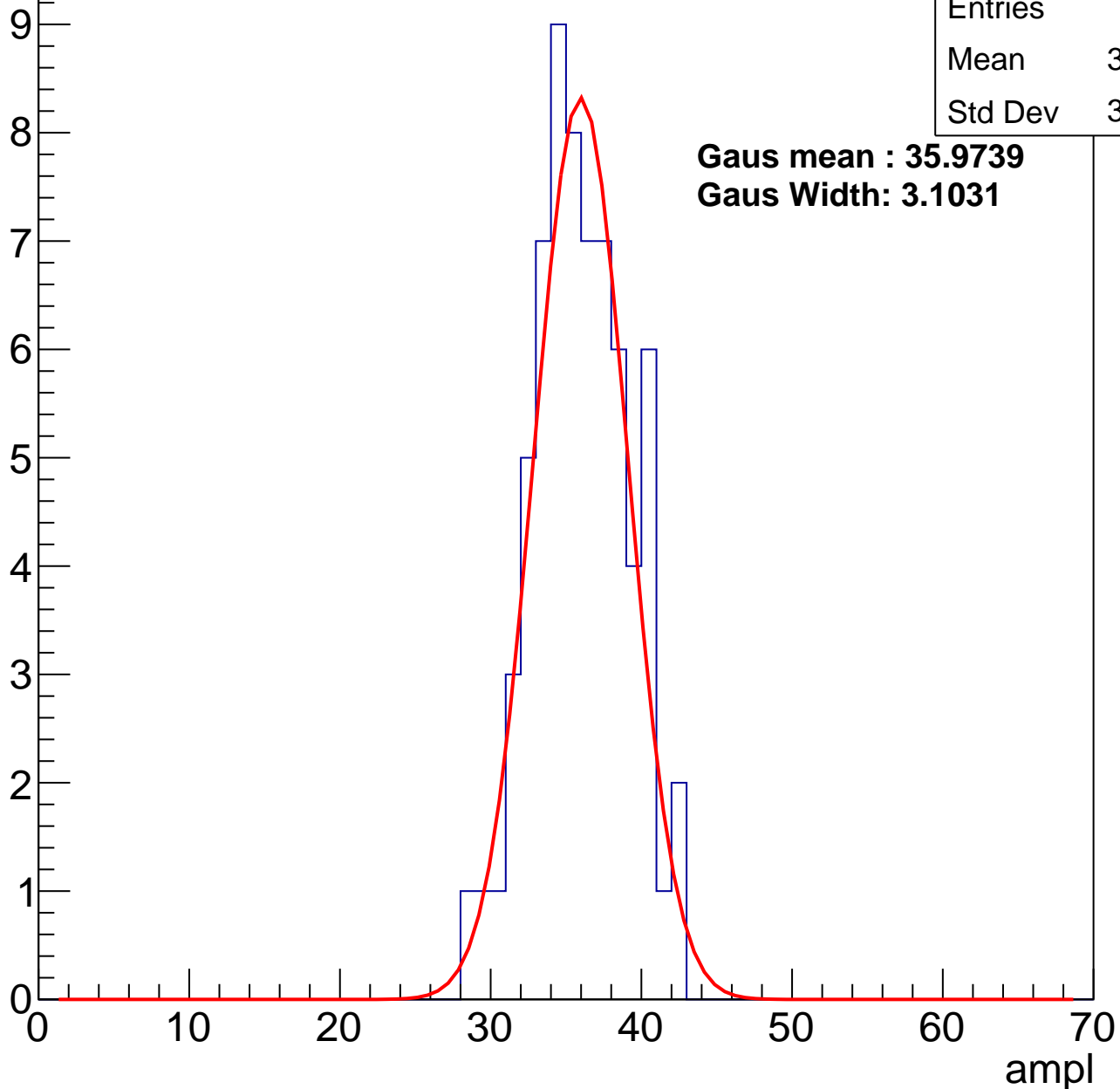
Gaus Width: 2.9274



B1L003S, U20-ch103, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	68
Mean	35.54
Std Dev	3.108

B1L003S, U20-ch103, adc2

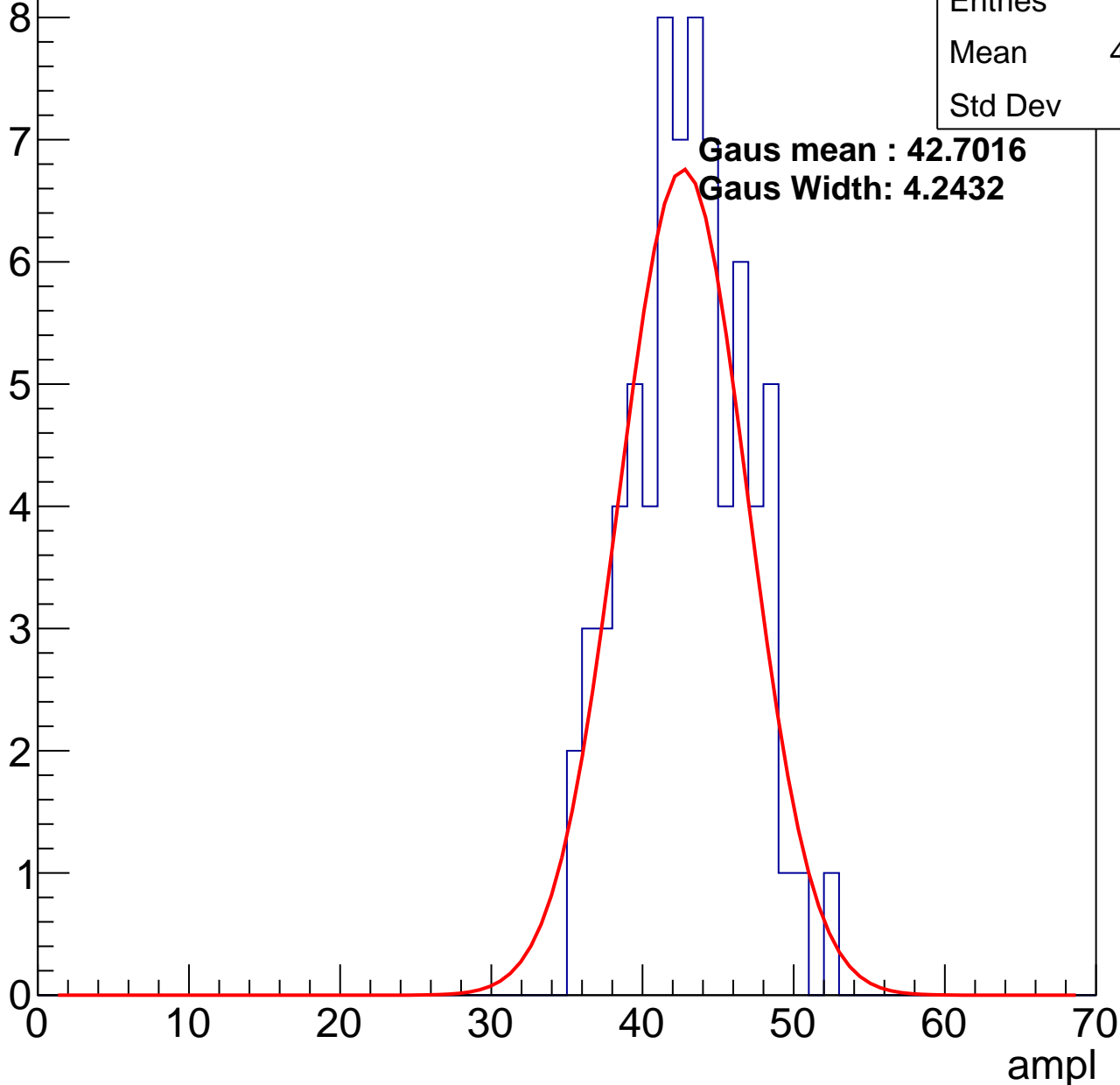
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.53
Std Dev	3.8

Gaus mean : 42.7016

Gaus Width: 4.2432

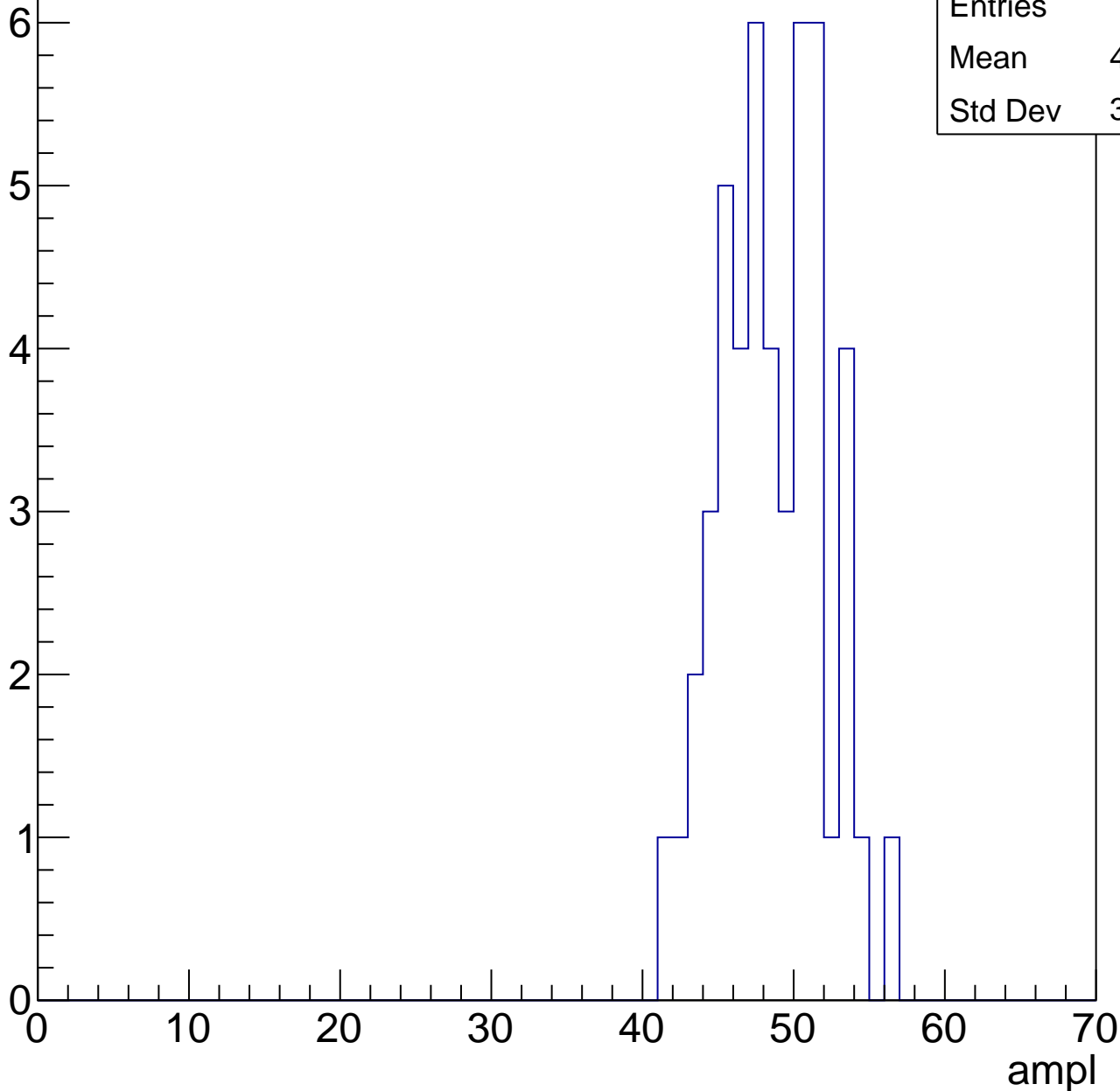


B1L003S, U20-ch103, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	48.15
Std Dev	3.373

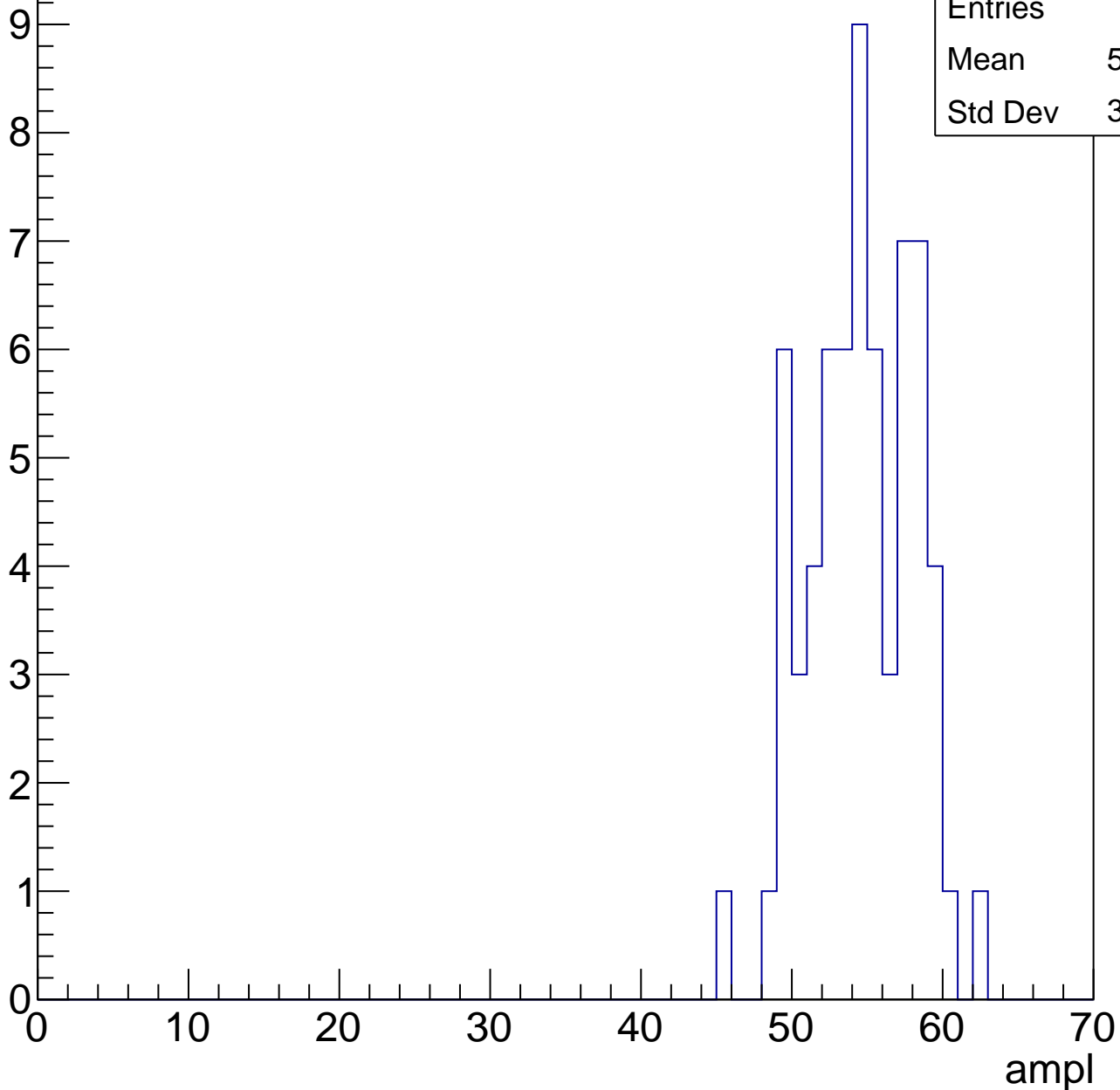


B1L003S, U20-ch103, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.12
Std Dev	3.449

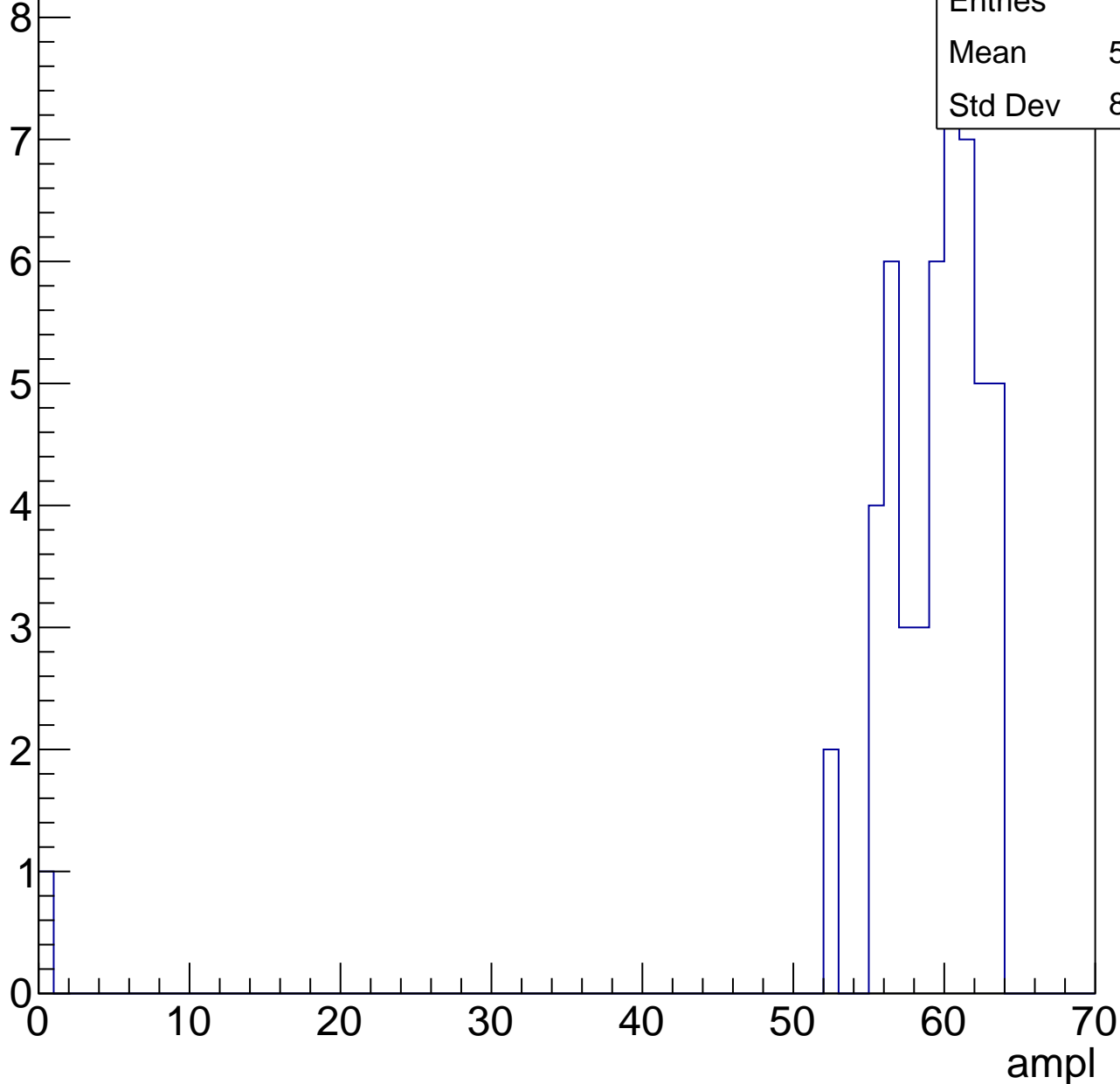


B1L003S, U20-ch103, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	57.82
Std Dev	8.722

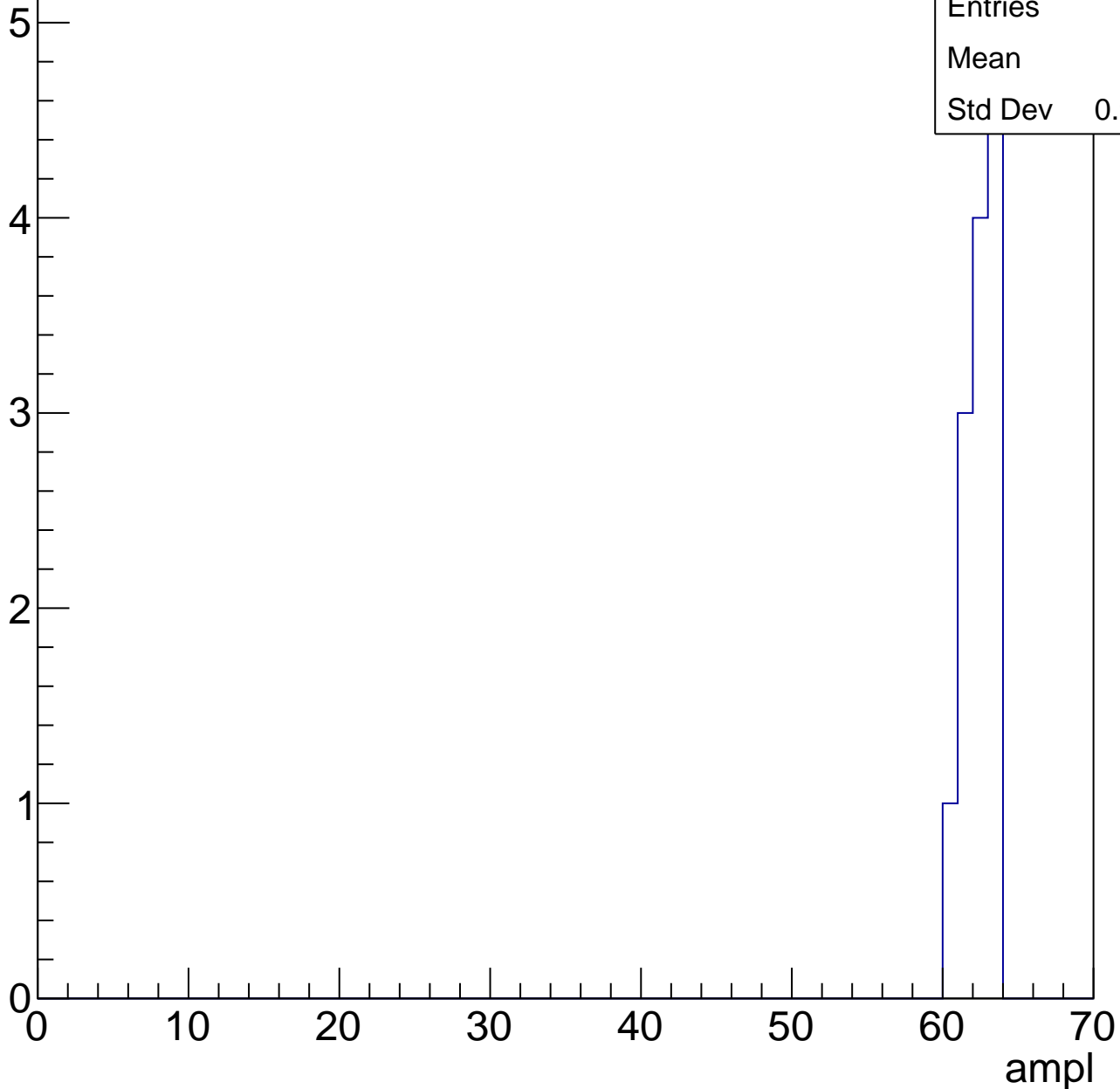


B1L003S, U20-ch103, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

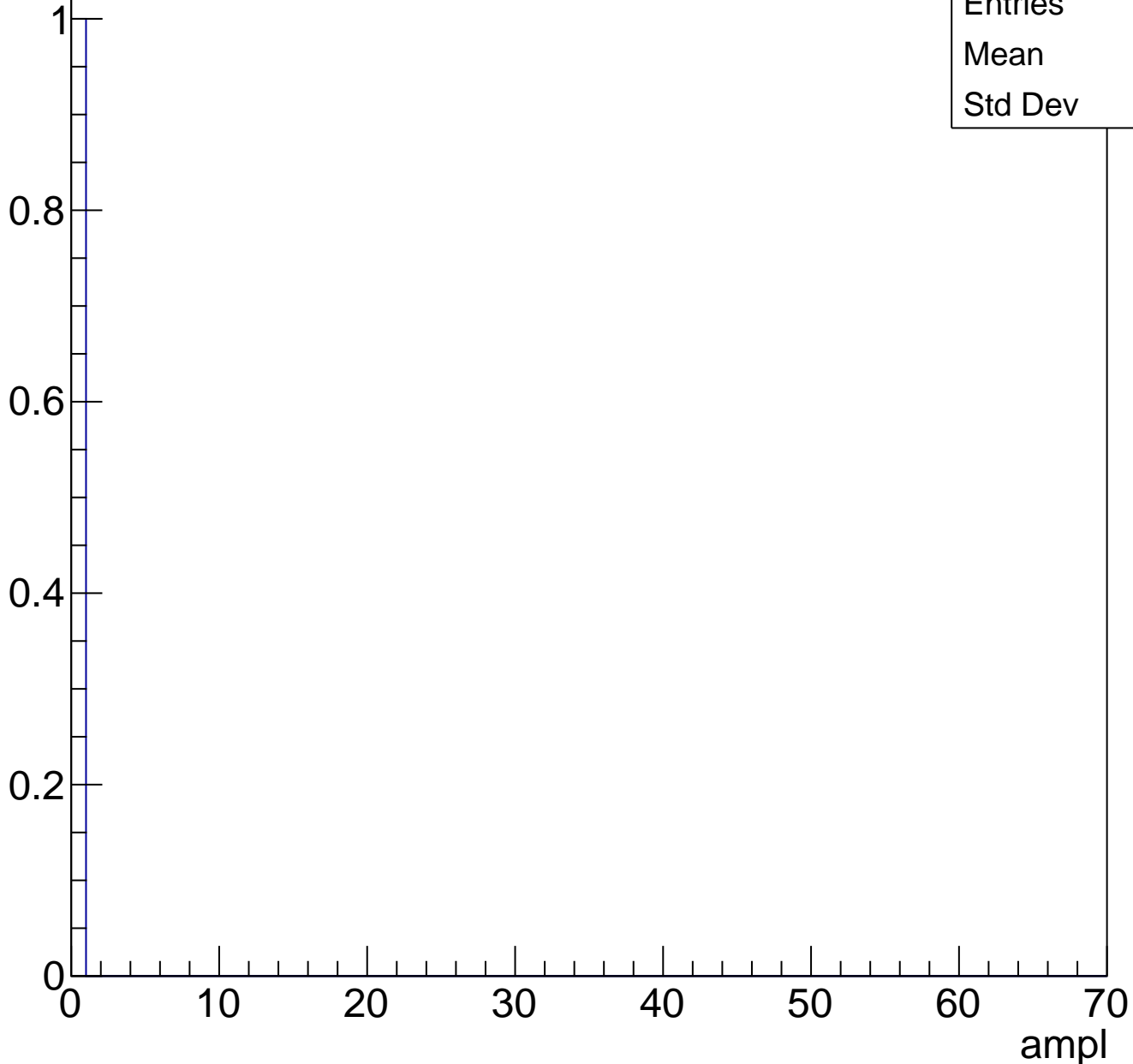
Entries	13
Mean	62
Std Dev	0.9608



B1L003S, U20-ch103, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch104, adc0

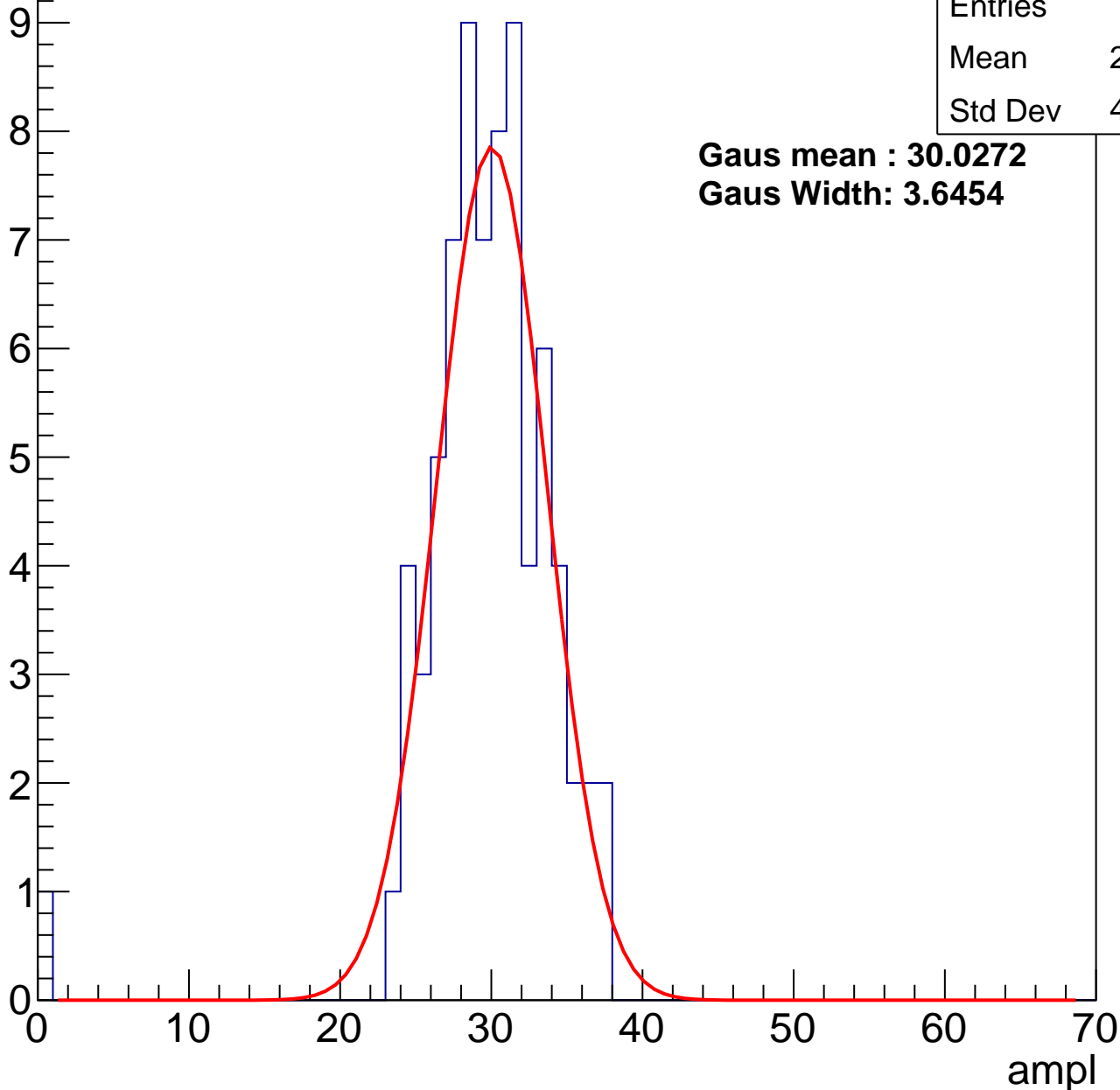
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.26
Std Dev	4.759

Gaus mean : 30.0272

Gaus Width: 3.6454



B1L003S, U20-ch104, adc1

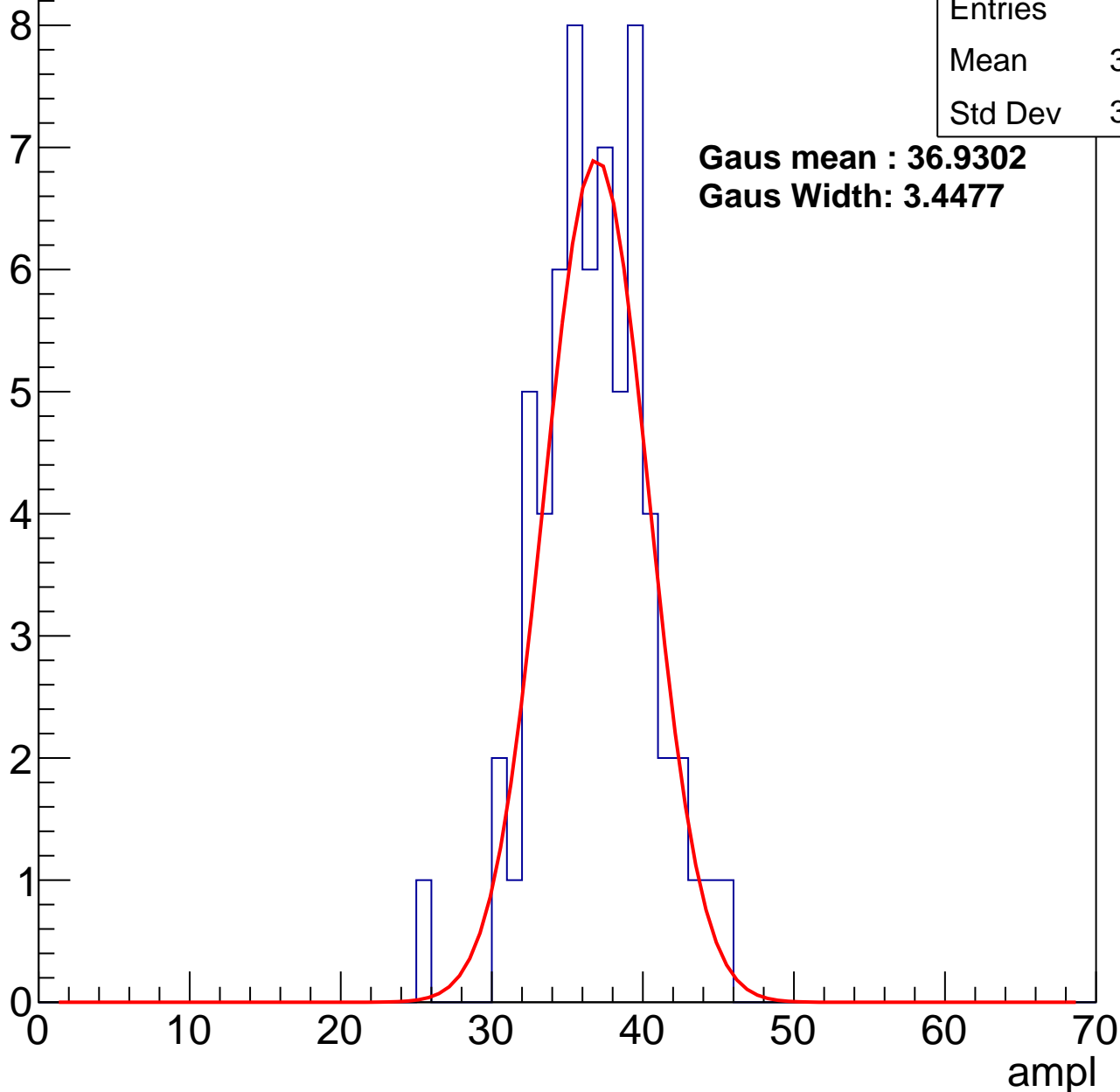
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.36
Std Dev	3.616

Gaus mean : 36.9302

Gaus Width: 3.4477



B1L003S, U20-ch104, adc2

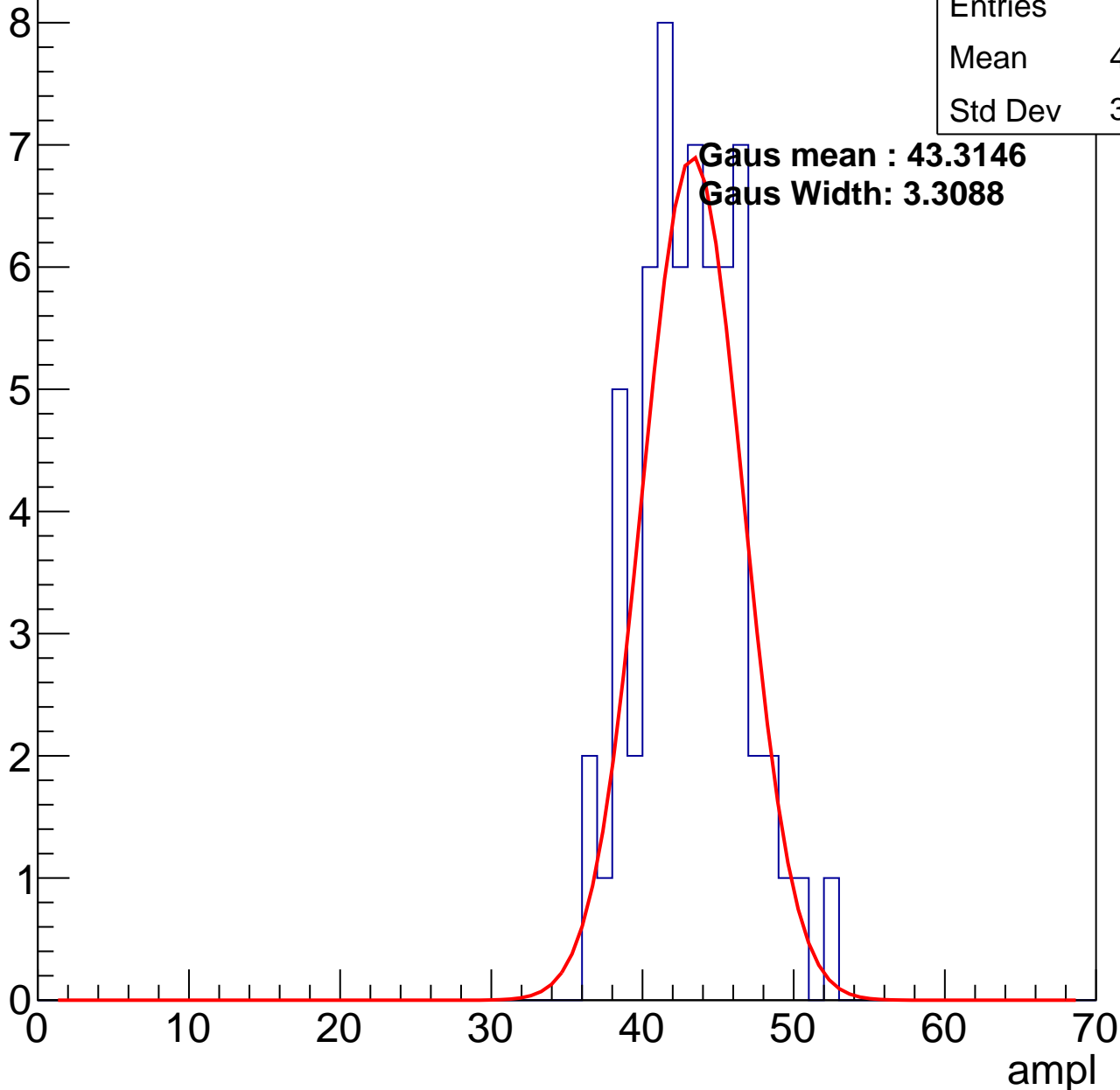
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.78
Std Dev	3.383

Gaus mean : 43.3146

Gaus Width: 3.3088

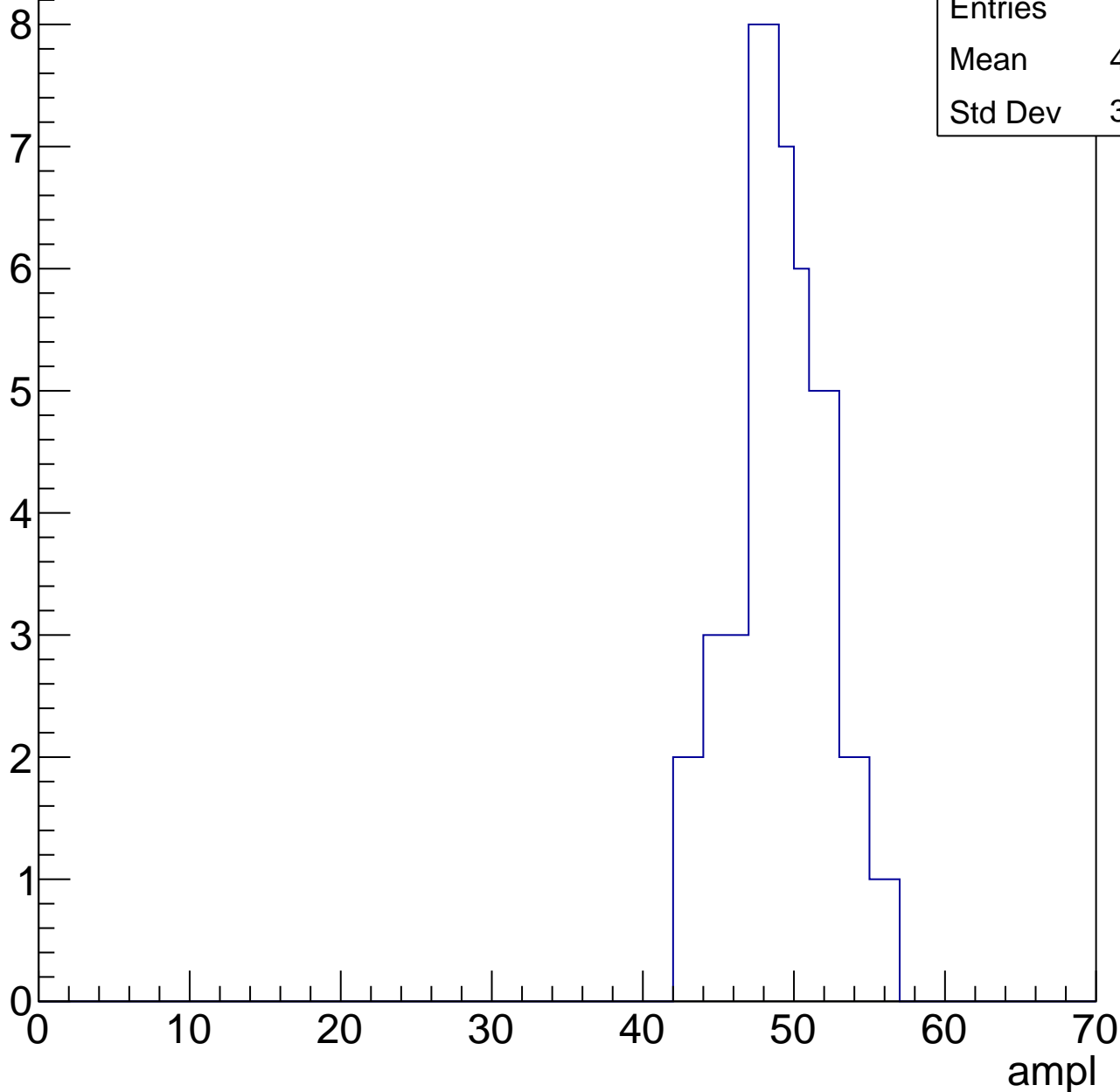


B1L003S, U20-ch104, adc3

calib_packv5_042523_0143.root, FC#13, port D2

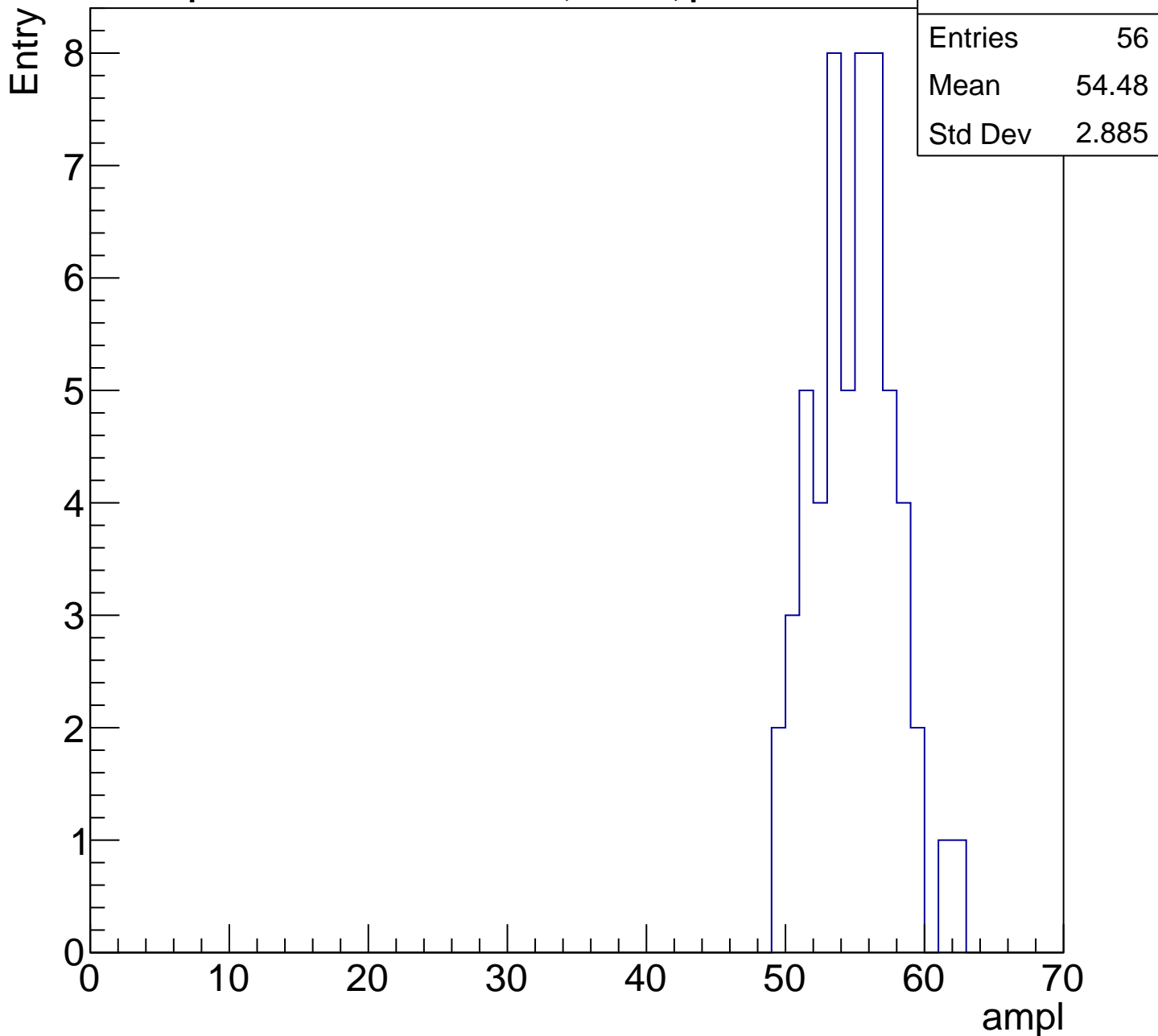
Entry

Entries	58
Mean	48.59
Std Dev	3.184



B1L003S, U20-ch104, adc4

calib_packv5_042523_0143.root, FC#13, port D2

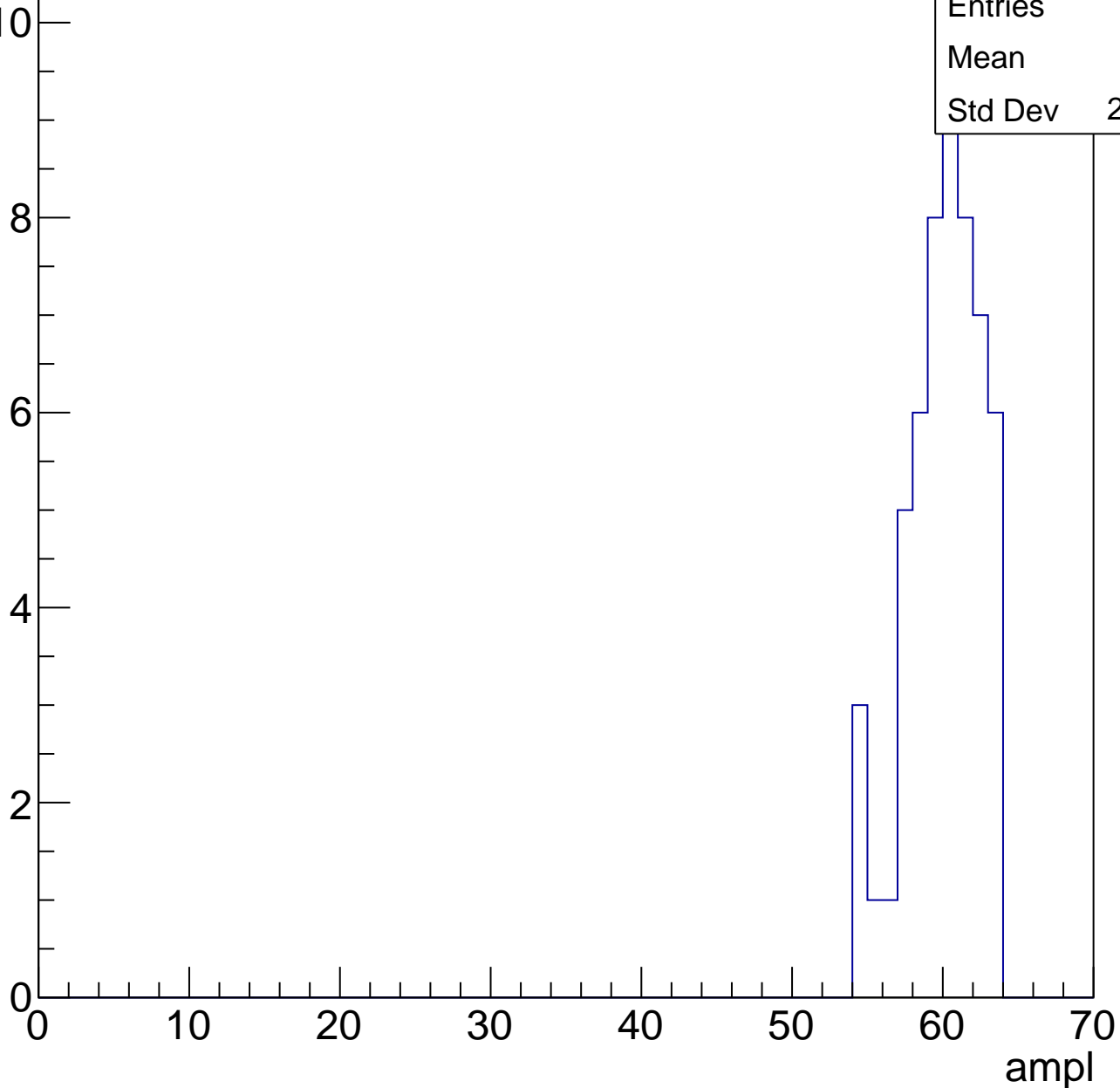


B1L003S, U20-ch104, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

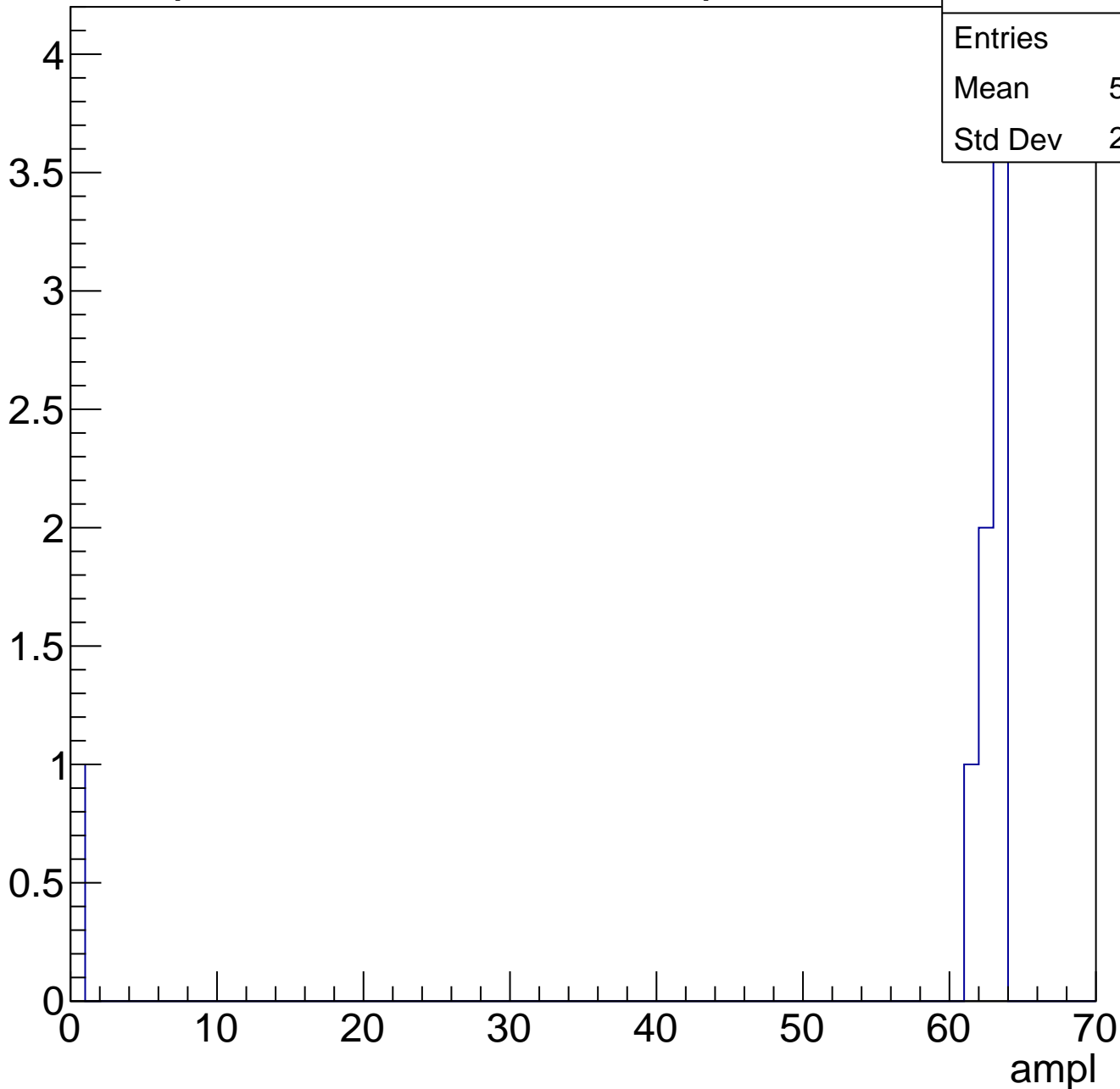
Entries	55
Mean	59.6
Std Dev	2.363



B1L003S, U20-ch104, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch104, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch105, adc0

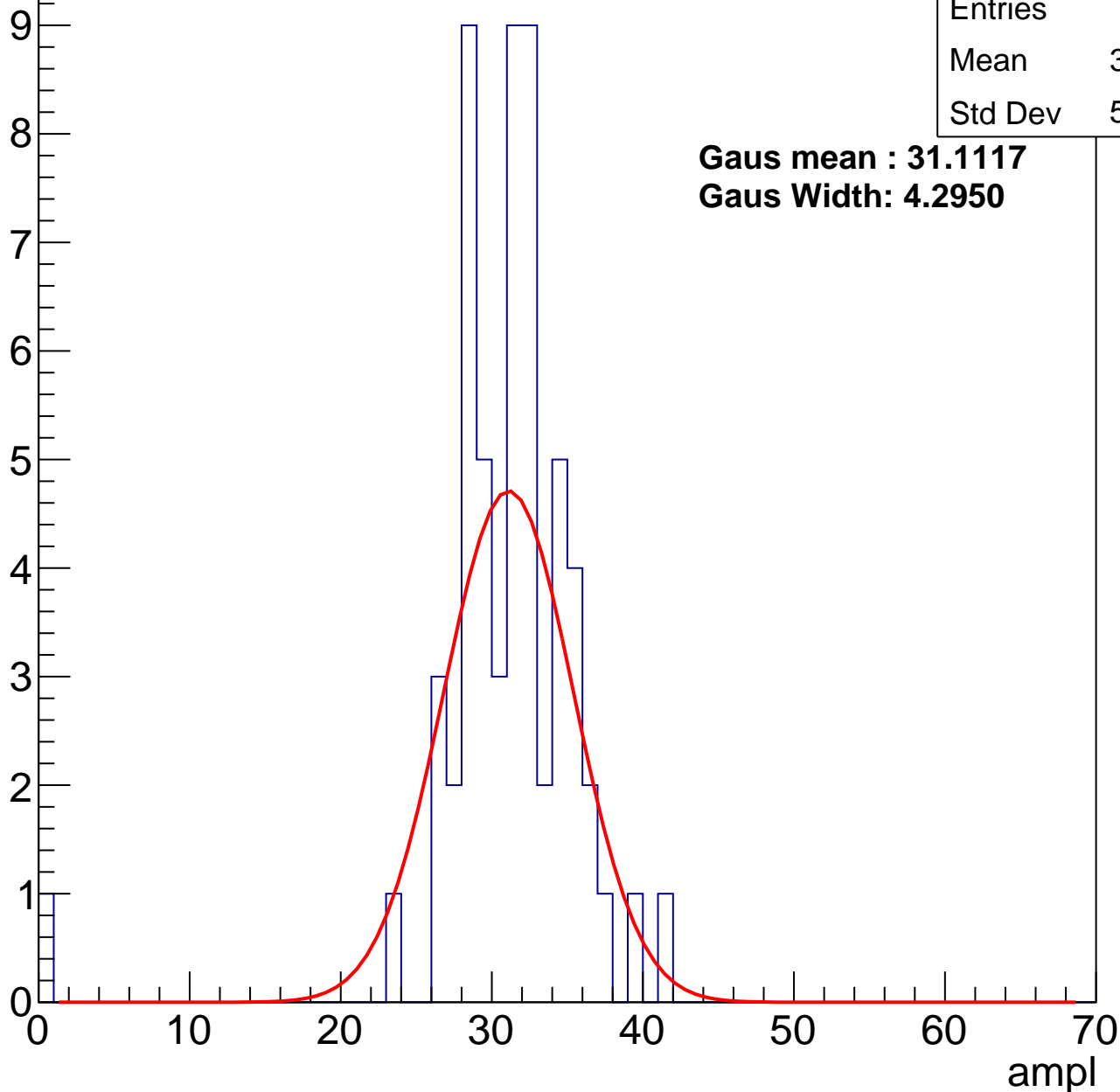
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.59
Std Dev	5.256

Gaus mean : 31.1117

Gaus Width: 4.2950



B1L003S, U20-ch105, adc1

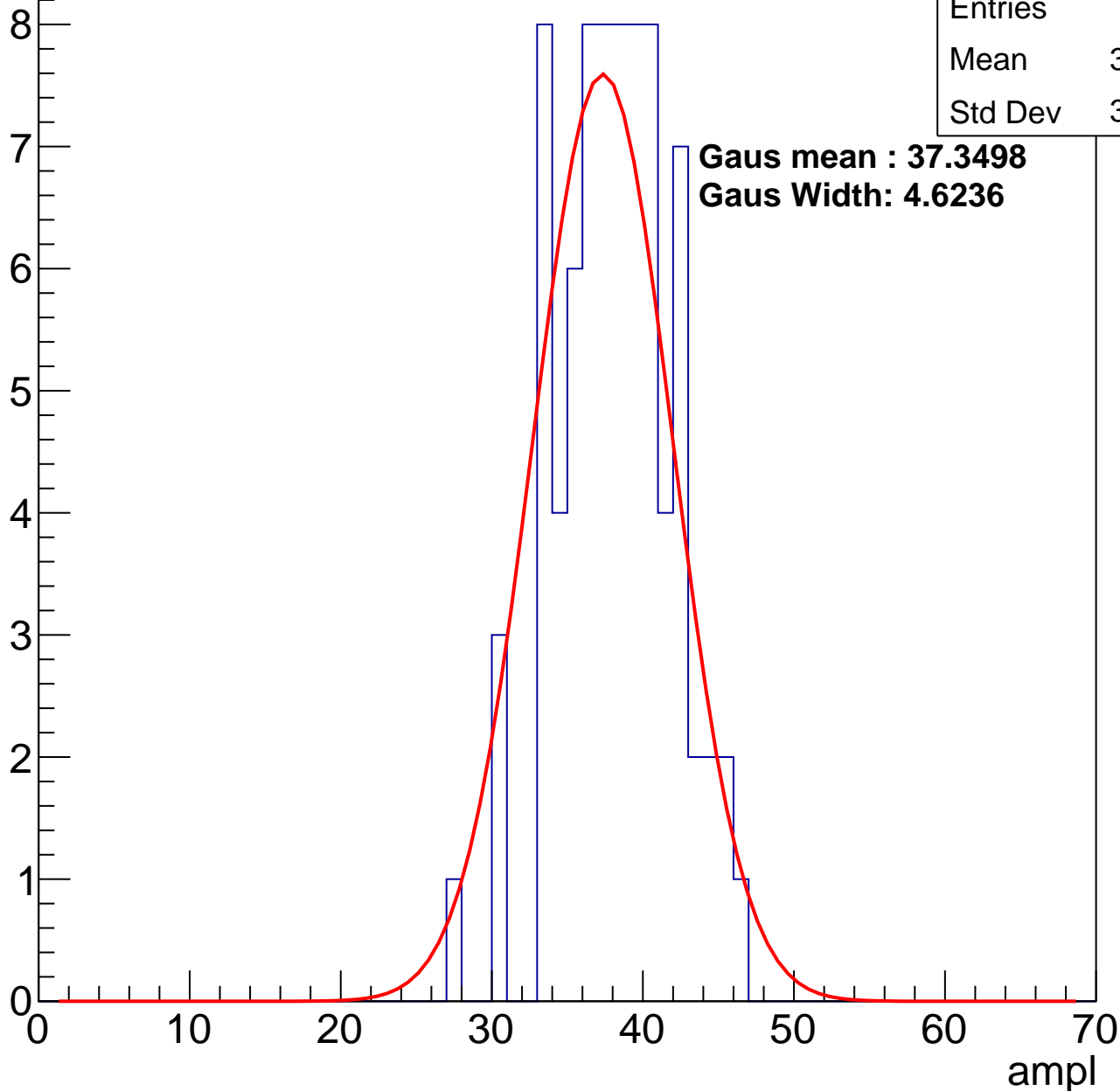
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	37.69
Std Dev	3.767

Gaus mean : 37.3498

Gaus Width: 4.6236



B1L003S, U20-ch105, adc2

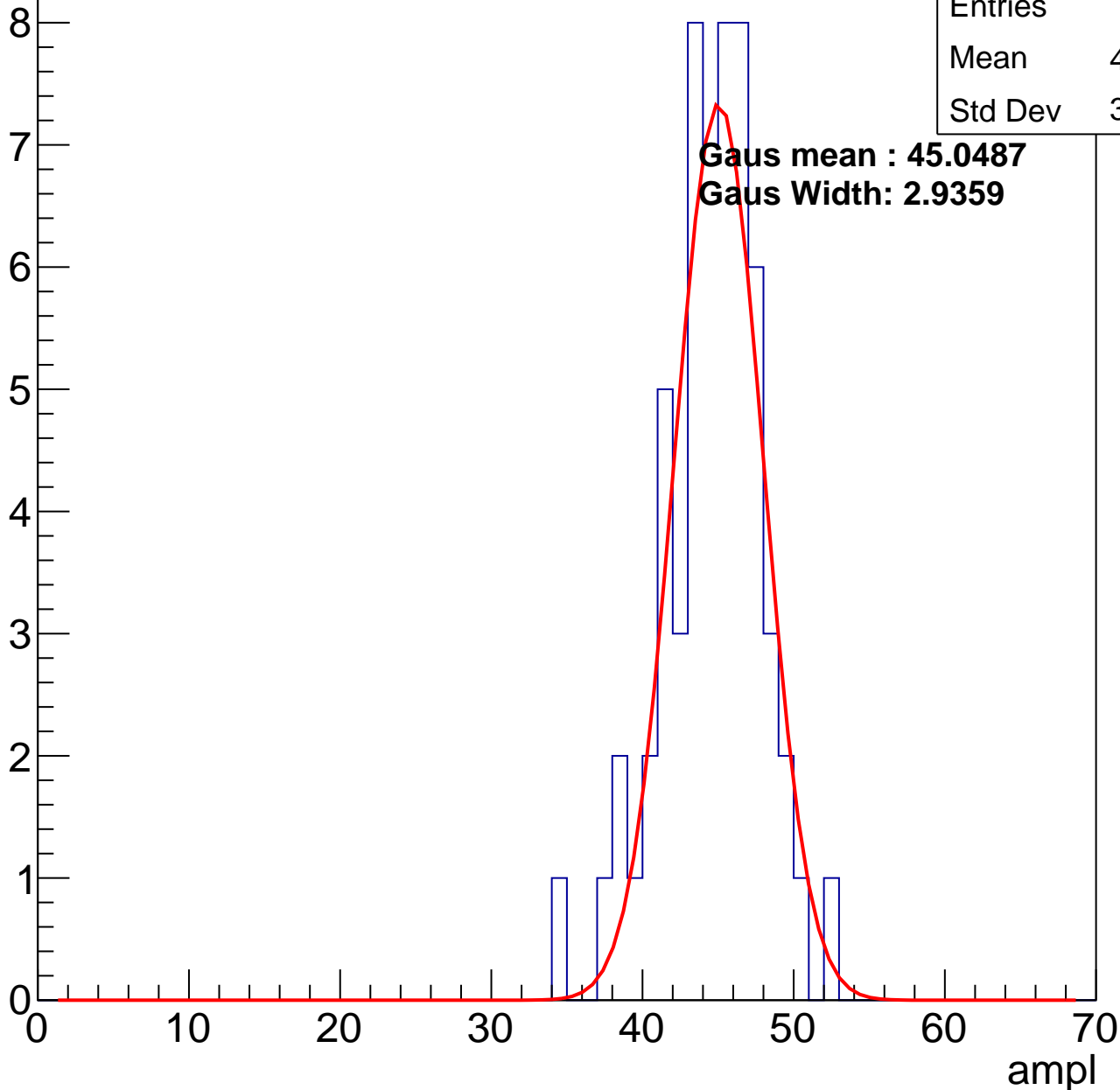
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.12
Std Dev	3.279

Gaus mean : 45.0487

Gaus Width: 2.9359

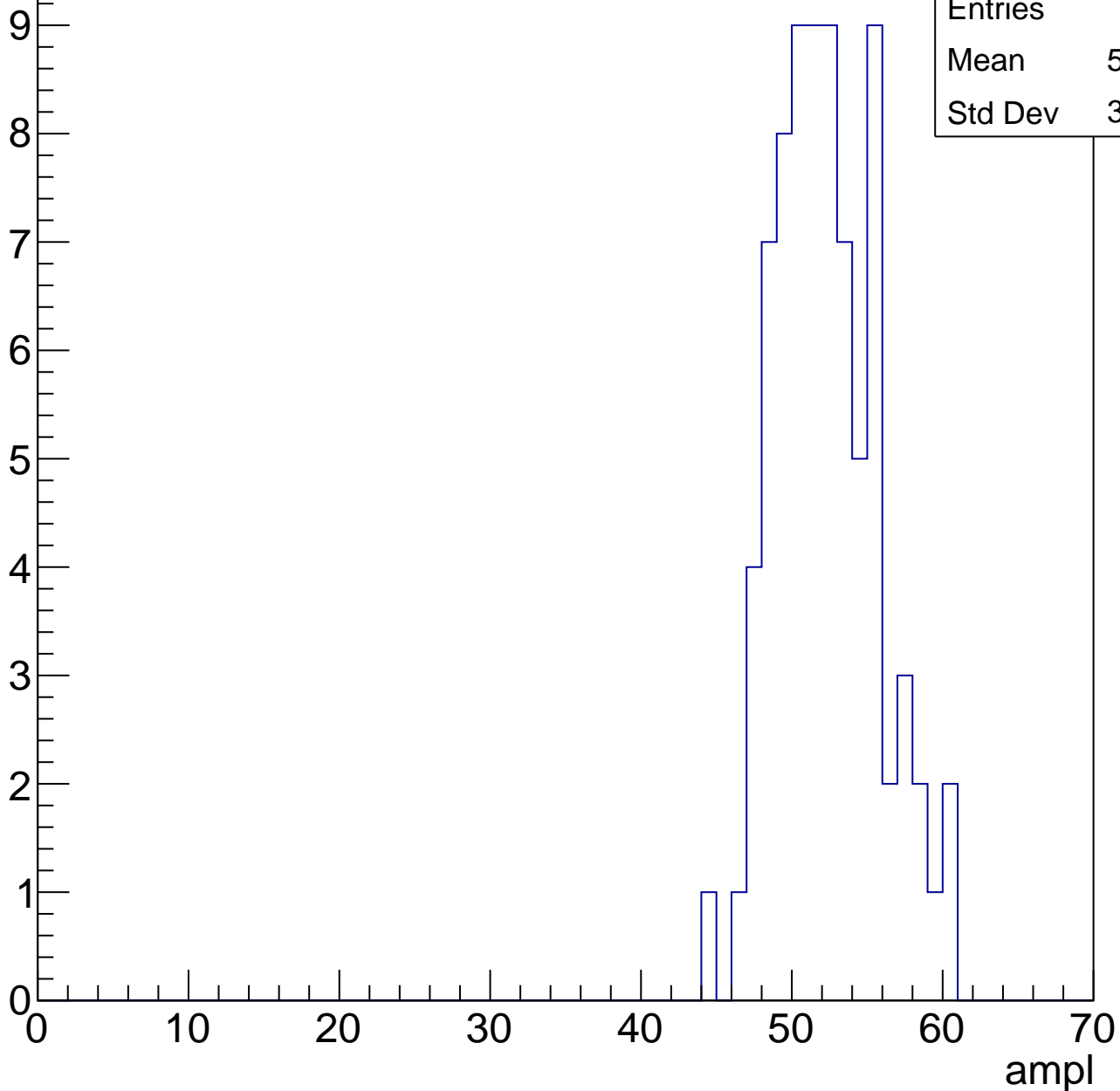


B1L003S, U20-ch105, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	51.86
Std Dev	3.359

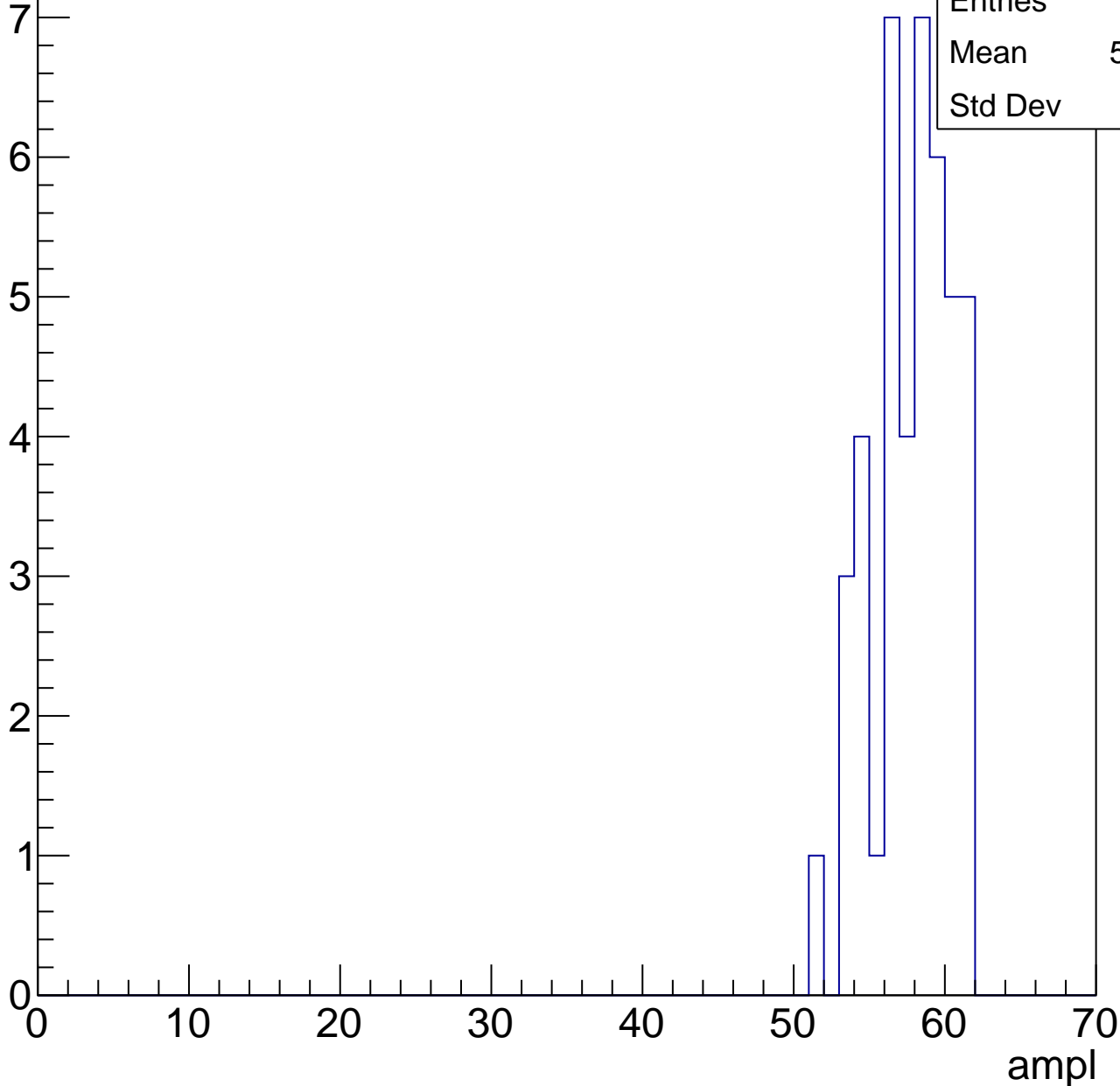


B1L003S, U20-ch105, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	57.35
Std Dev	2.56

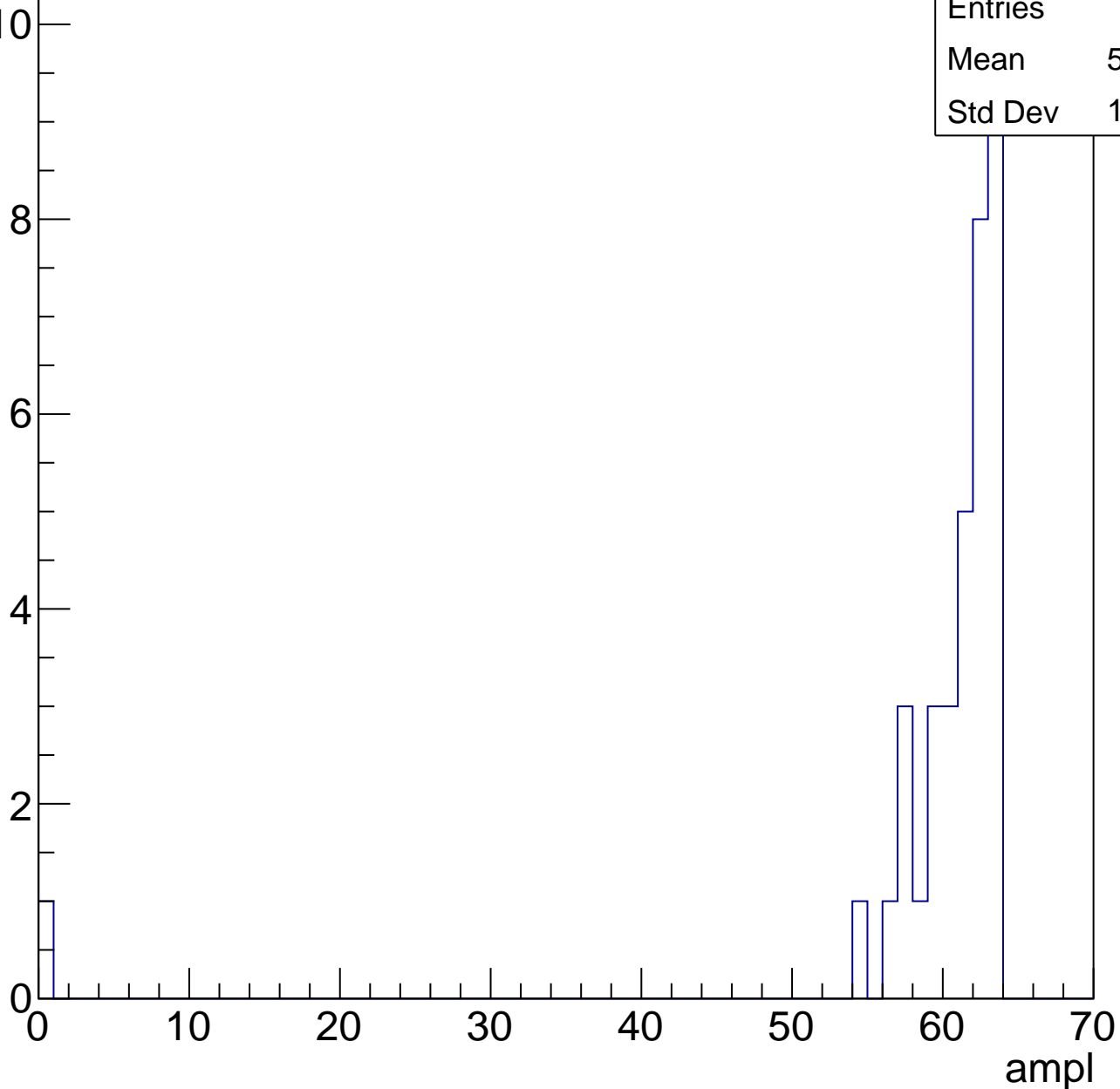


B1L003S, U20-ch105, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.08
Std Dev	10.25



B1L003S, U20-ch105, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch105, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch106, adc0

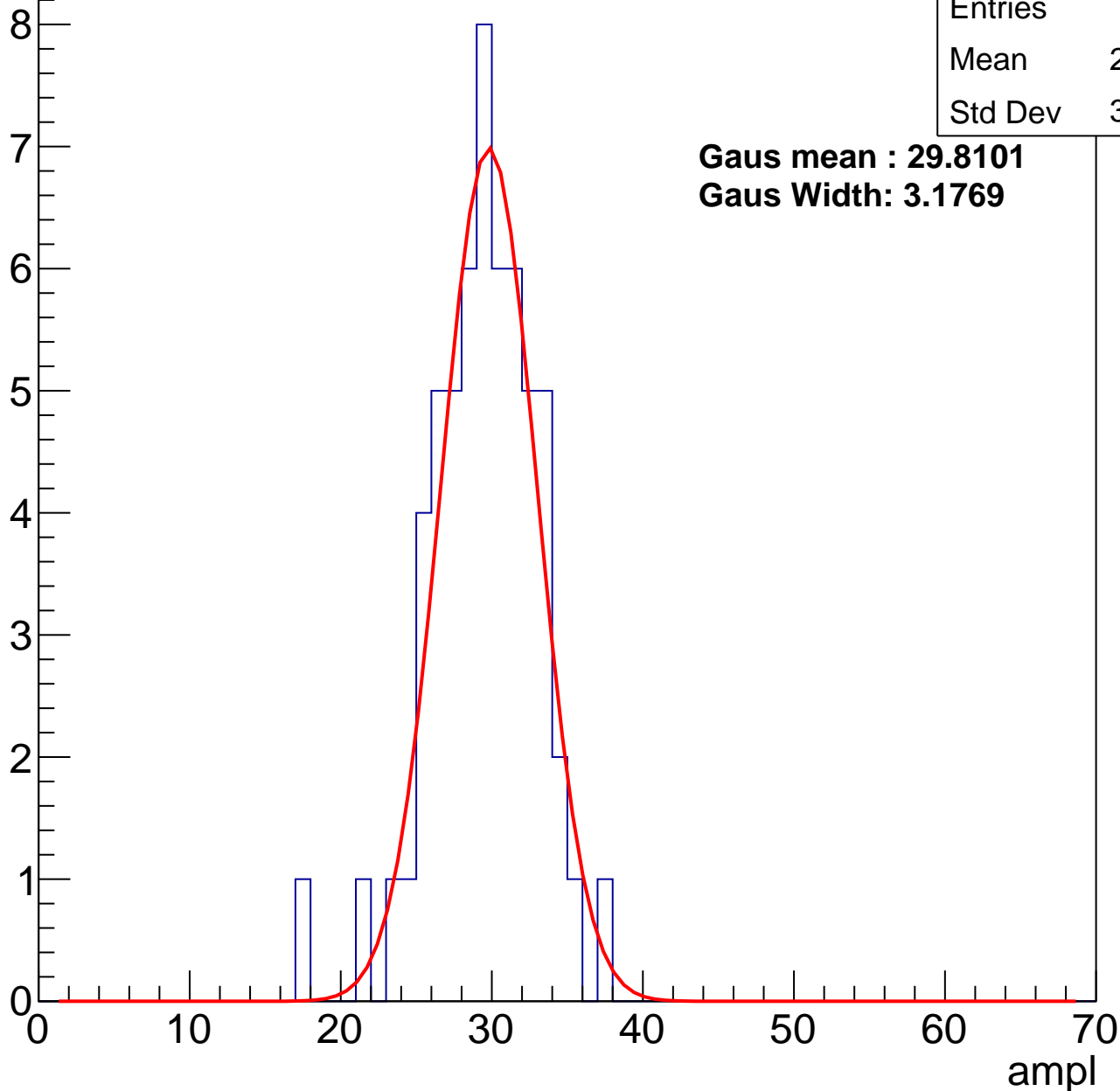
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.98
Std Dev	3.496

Gaus mean : 29.8101

Gaus Width: 3.1769



B1L003S, U20-ch106, adc1

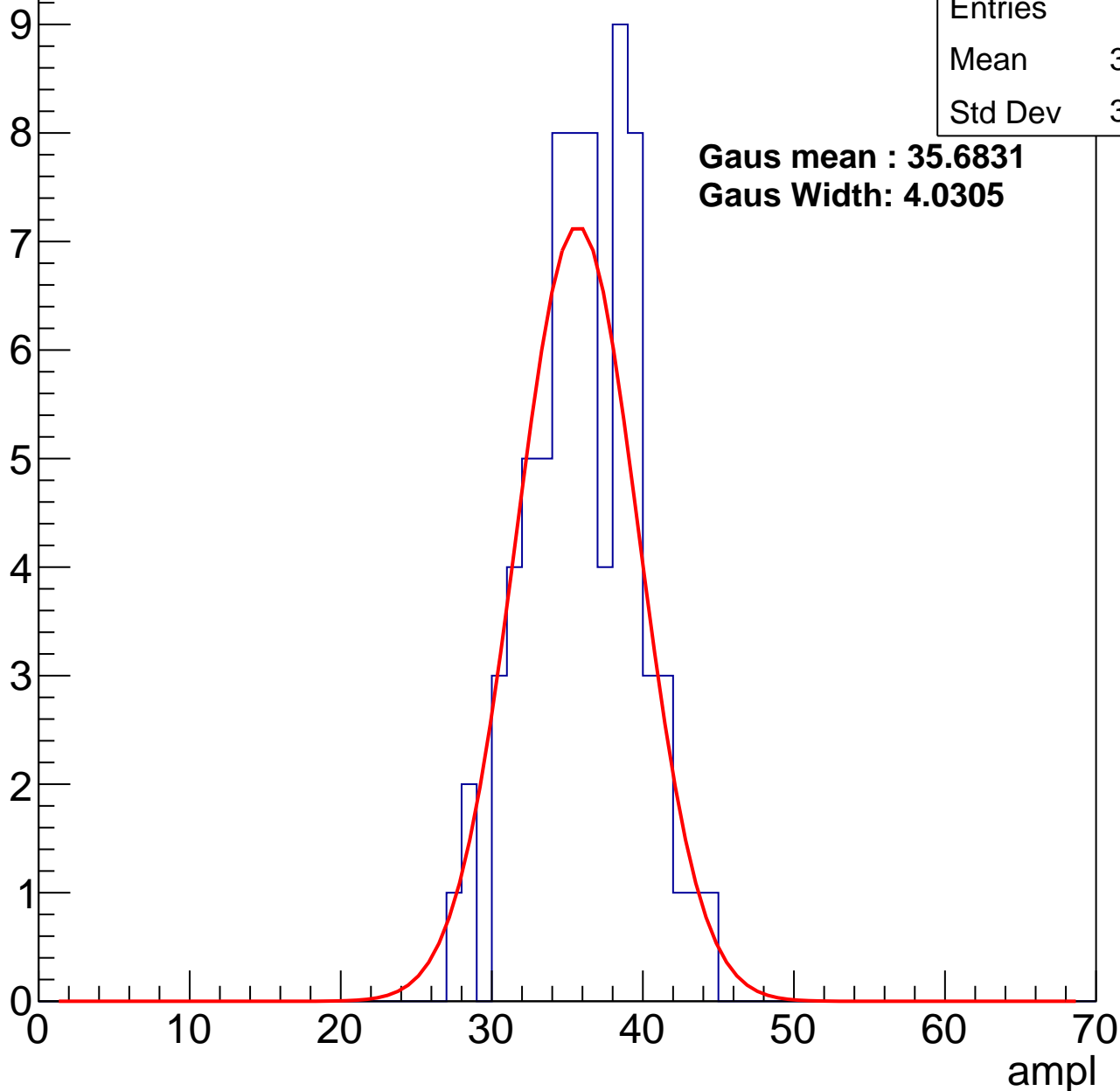
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.62
Std Dev	3.593

Gaus mean : 35.6831

Gaus Width: 4.0305



B1L003S, U20-ch106, adc2

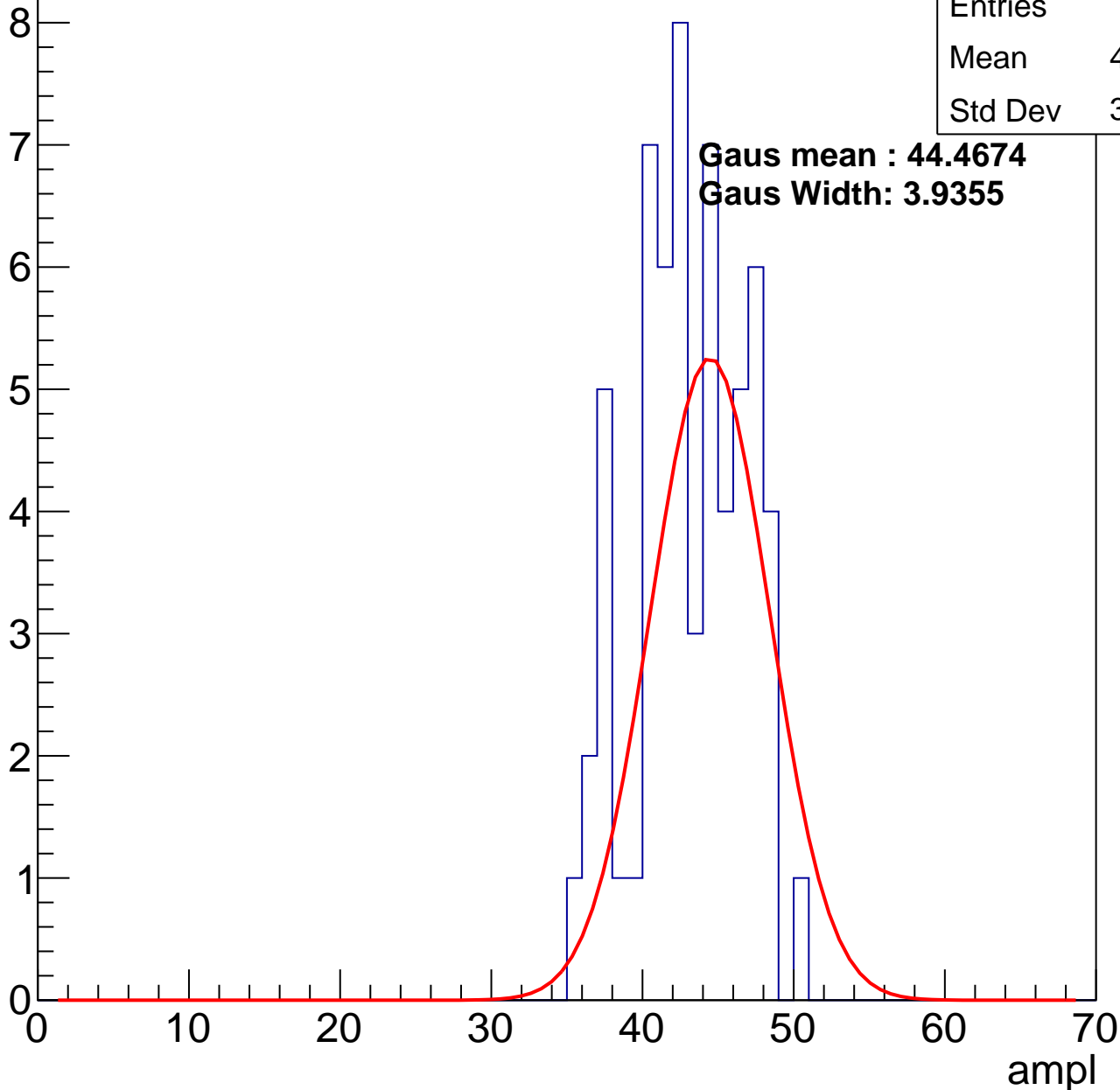
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.66
Std Dev	3.585

Gaus mean : 44.4674

Gaus Width: 3.9355

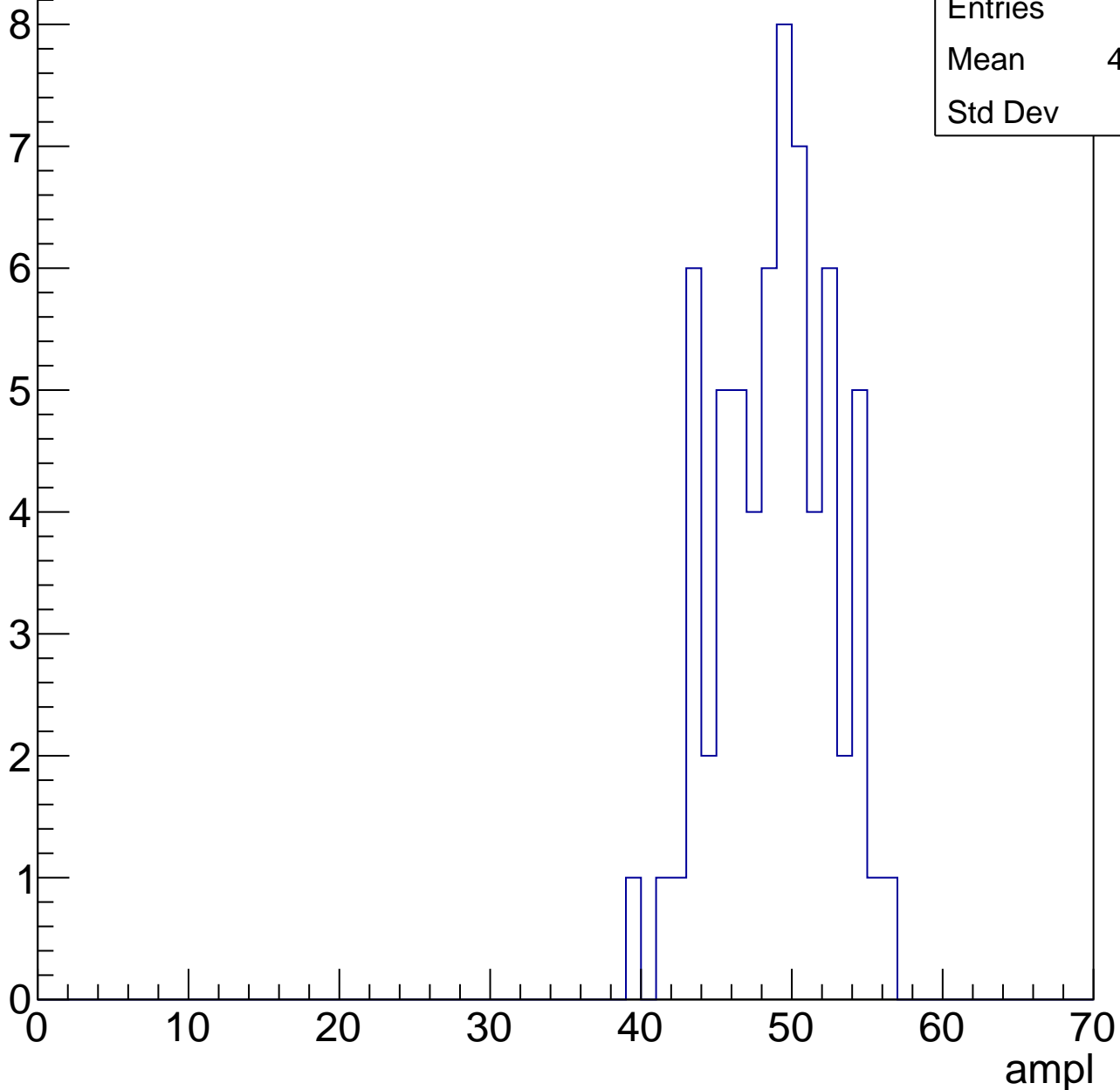


B1L003S, U20-ch106, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	48.37
Std Dev	3.76

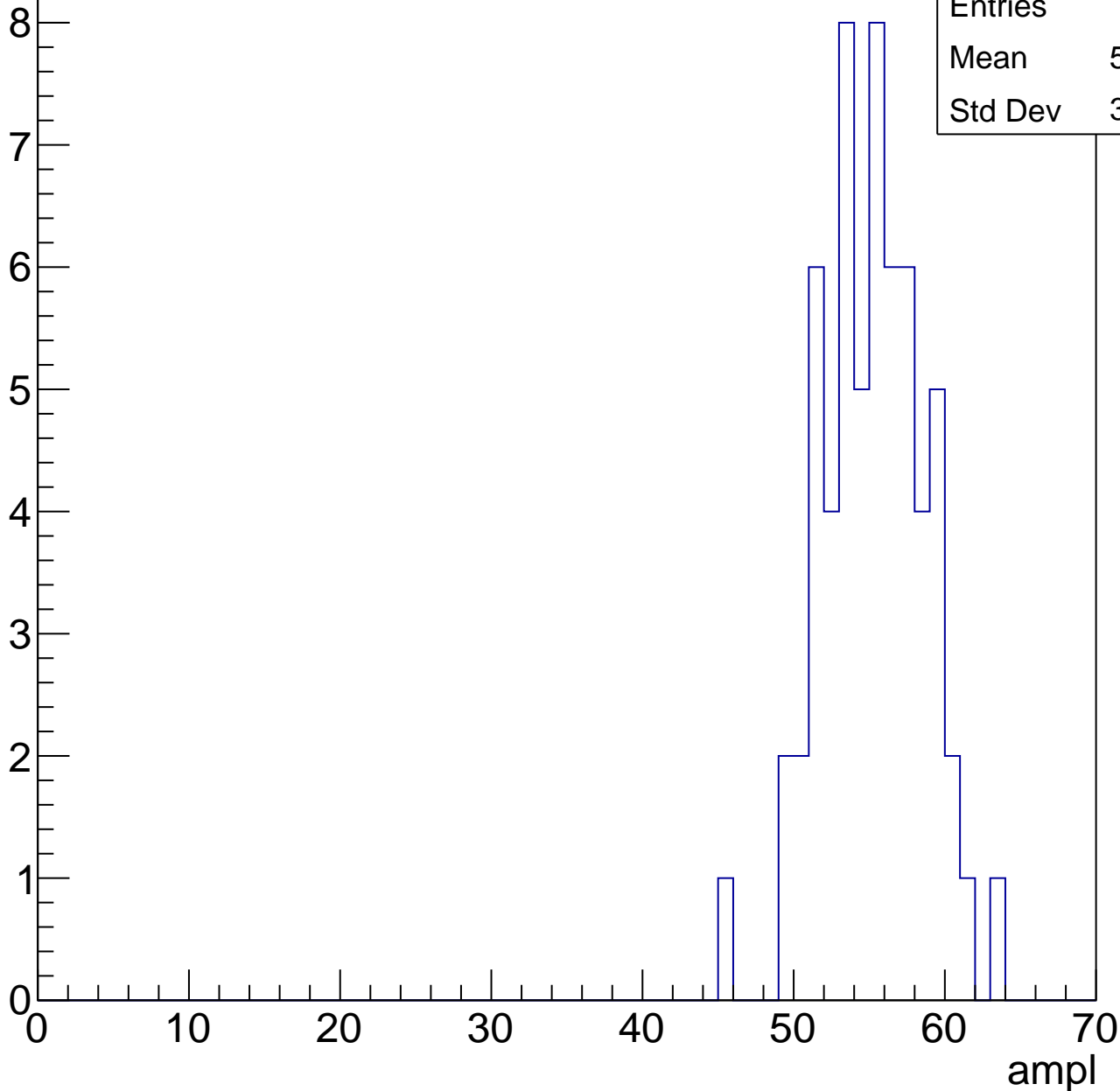


B1L003S, U20-ch106, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	54.75
Std Dev	3.347



B1L003S, U20-ch106, adc5

calib_packv5_042523_0143.root, FC#13, port D2

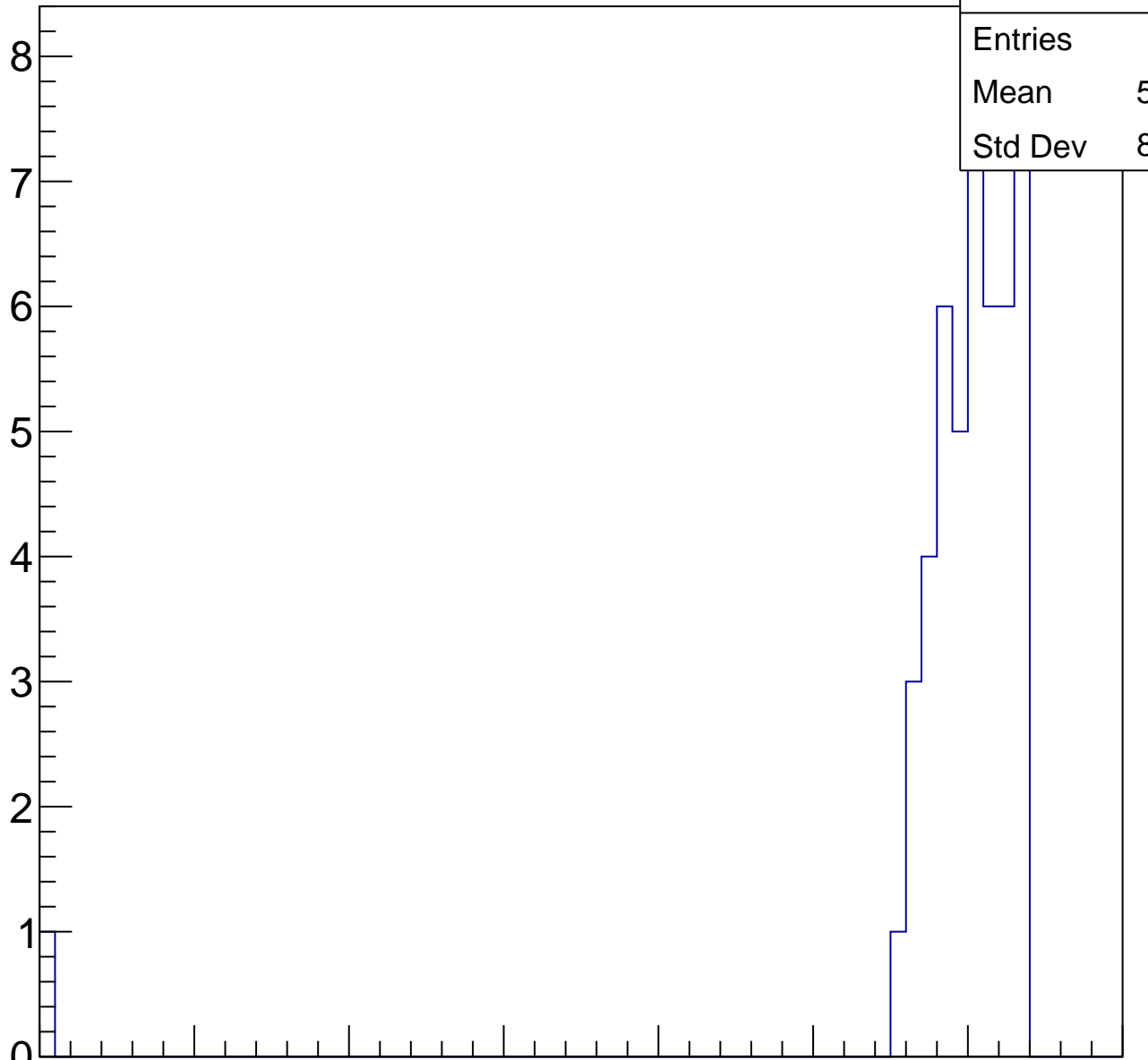
Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.67
Std Dev	8.844

ampl

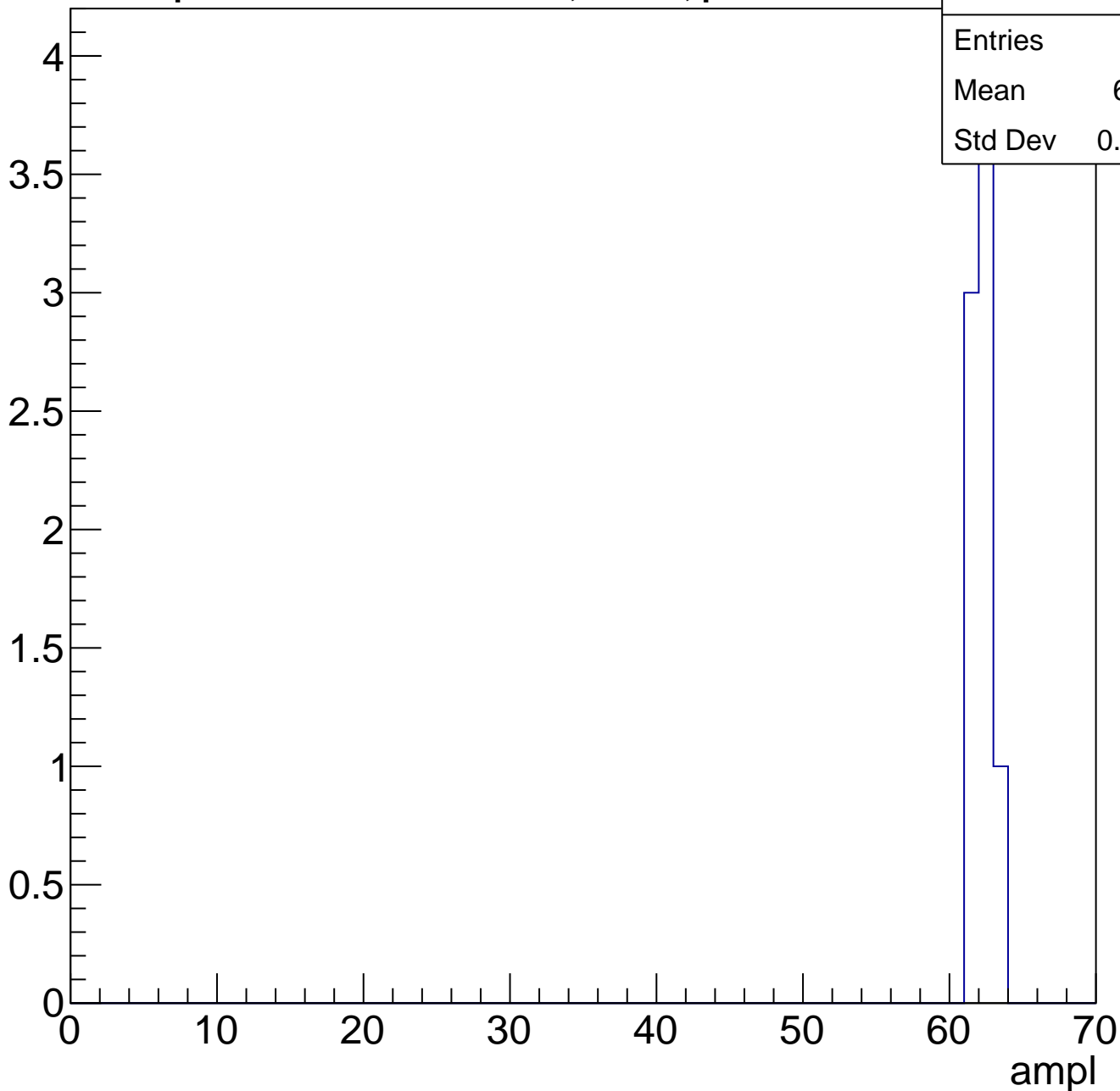
0 10 20 30 40 50 60 70



B1L003S, U20-ch106, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch106, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch107, adc0

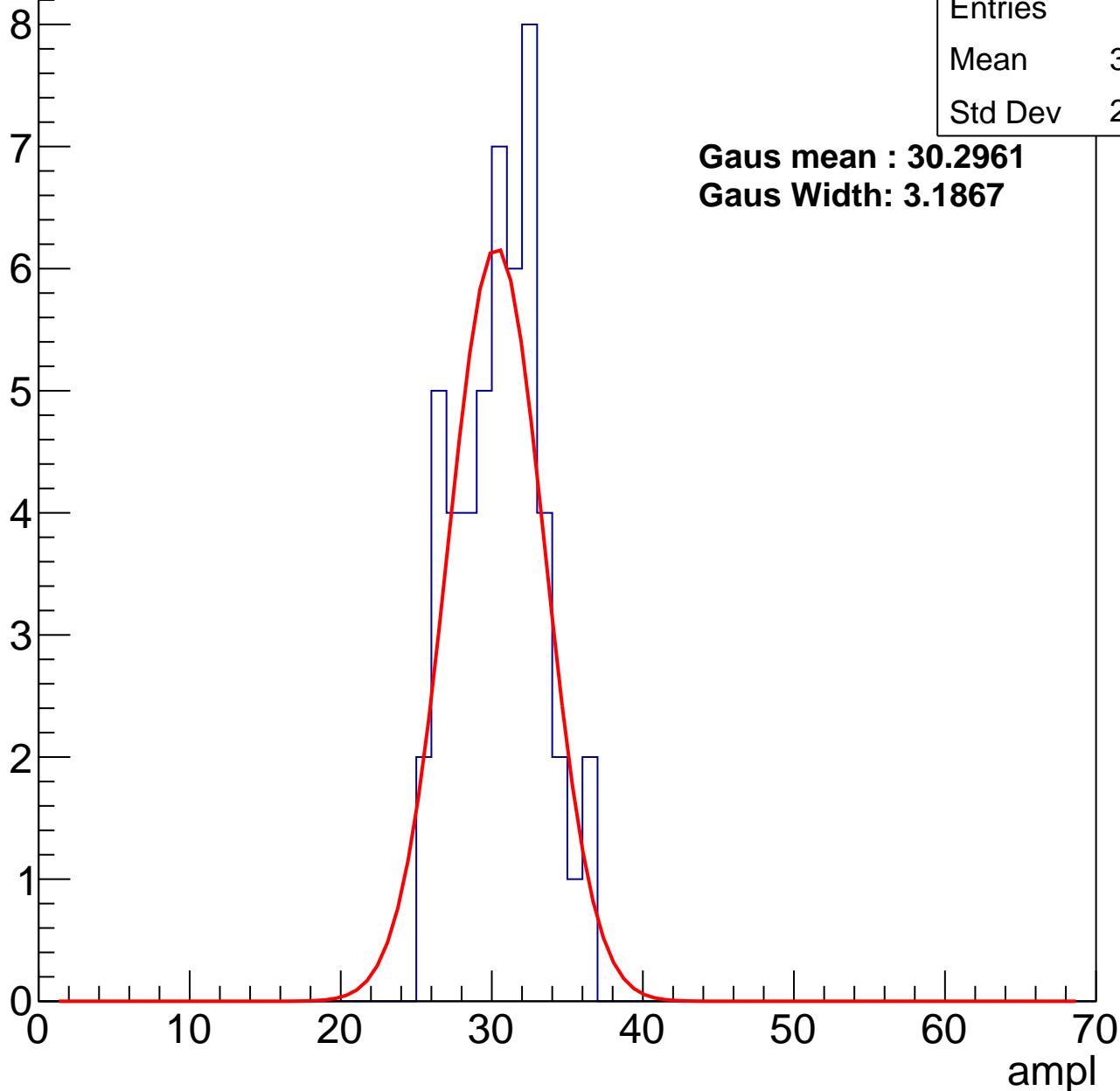
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	30.08
Std Dev	2.792

Gaus mean : 30.2961

Gaus Width: 3.1867



B1L003S, U20-ch107, adc1

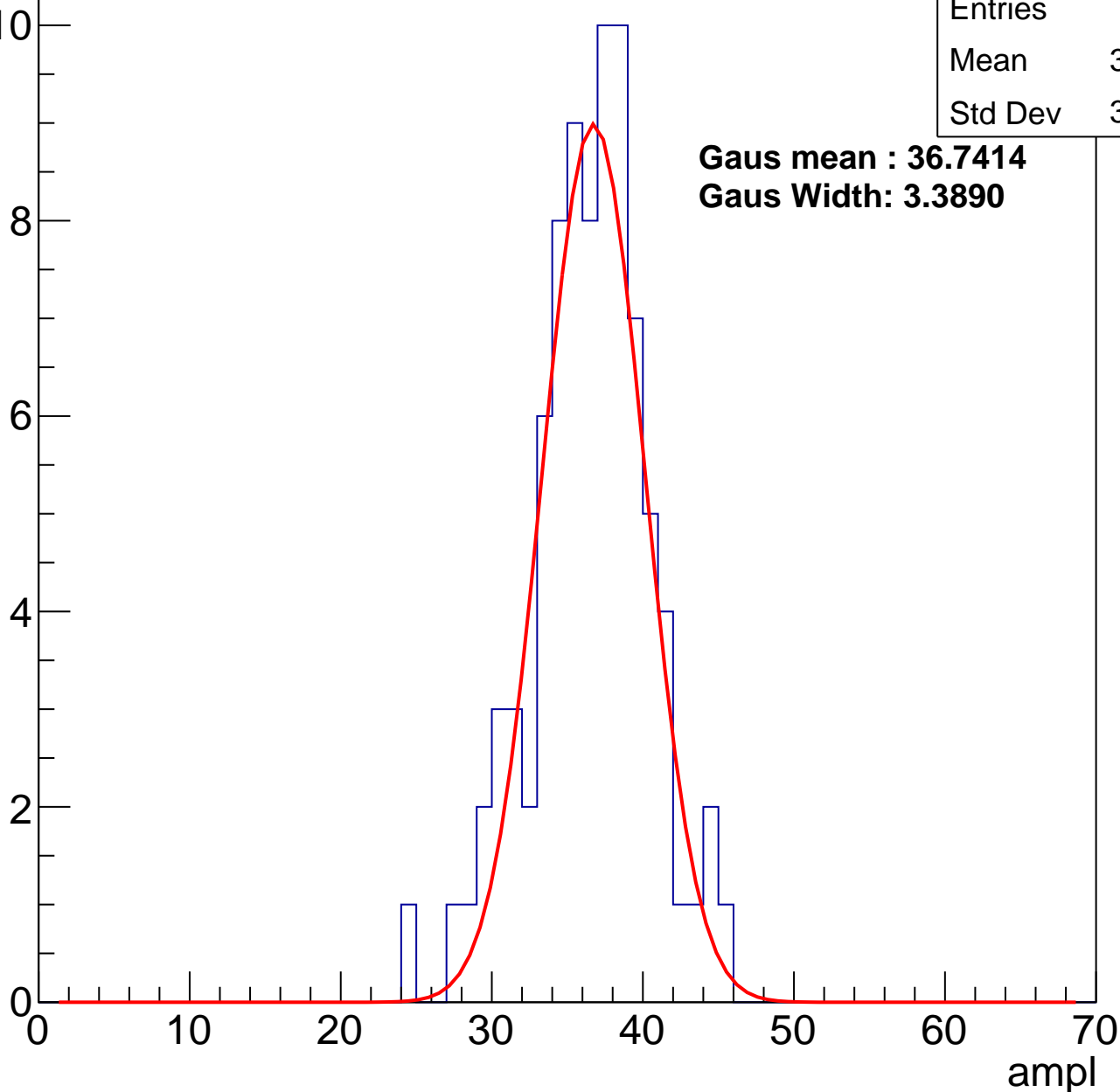
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	36.02
Std Dev	3.887

Gaus mean : 36.7414

Gaus Width: 3.3890



B1L003S, U20-ch107, adc2

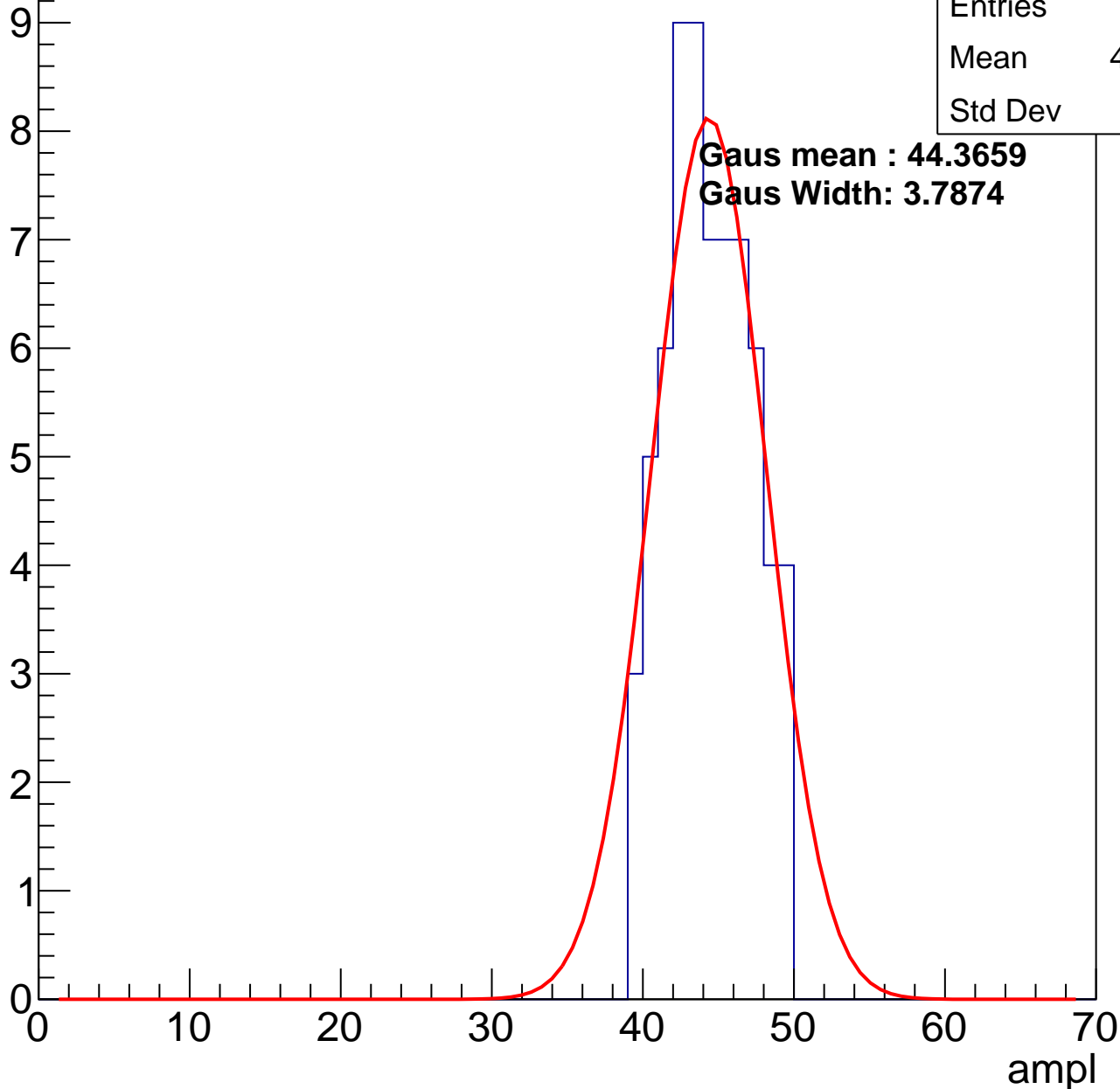
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.93
Std Dev	2.75

Gaus mean : 44.3659

Gaus Width: 3.7874

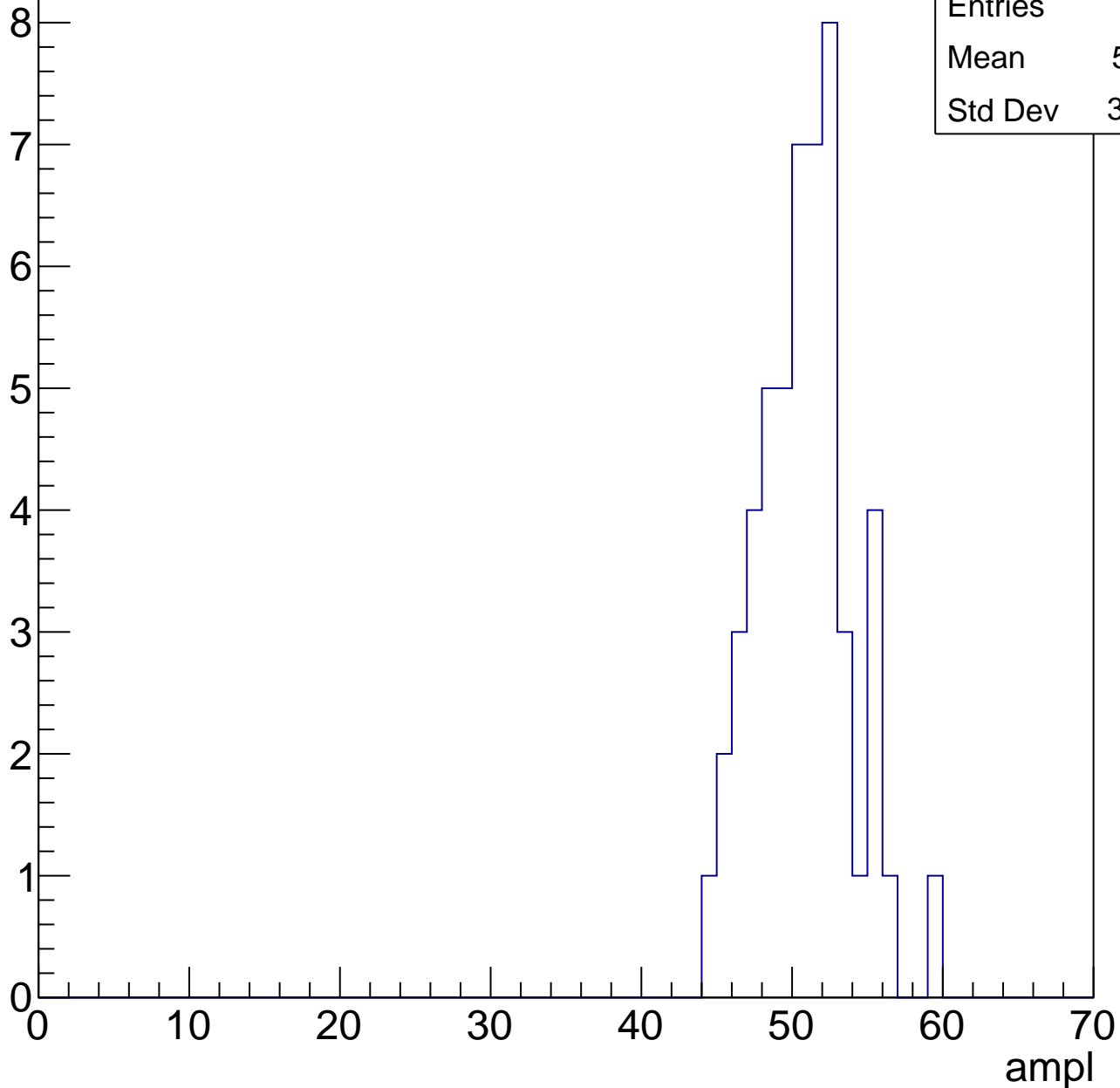


B1L003S, U20-ch107, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	50.31
Std Dev	3.067

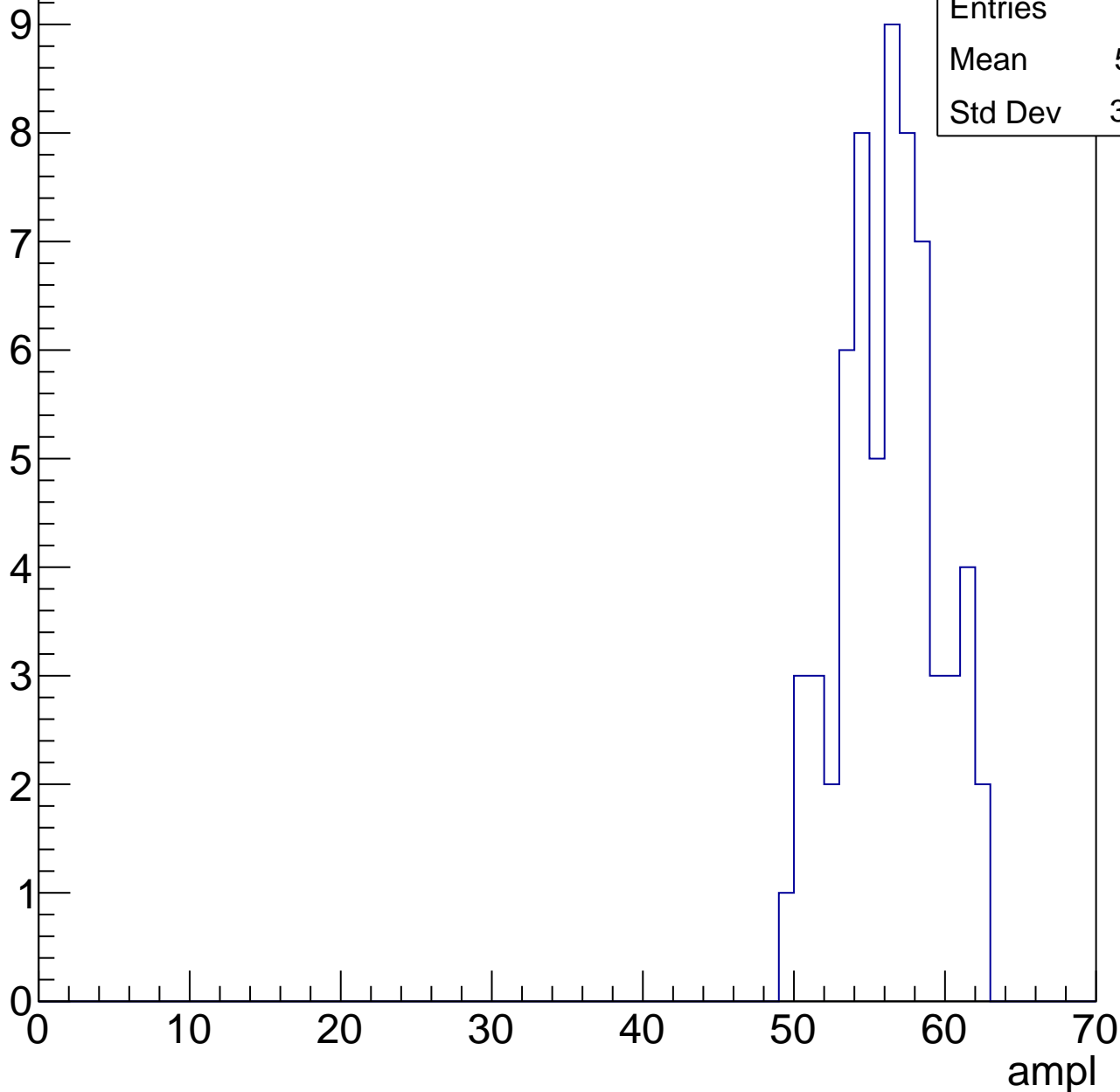


B1L003S, U20-ch107, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.81
Std Dev	3.152

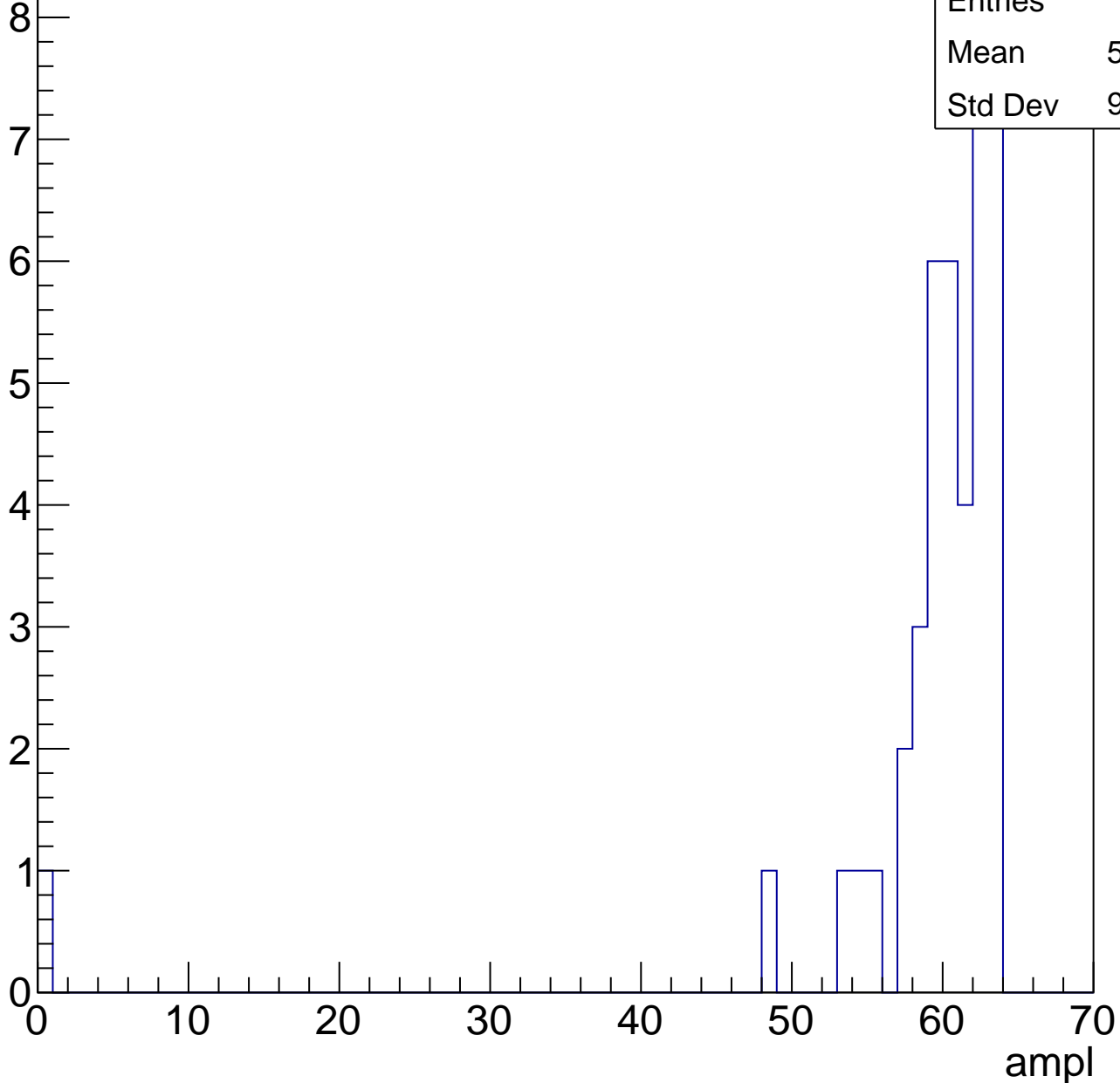


B1L003S, U20-ch107, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.48
Std Dev	9.637



B1L003S, U20-ch107, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.2

ampl

0 10 20 30 40 50 60 70

B1L003S, U20-ch107, adc7

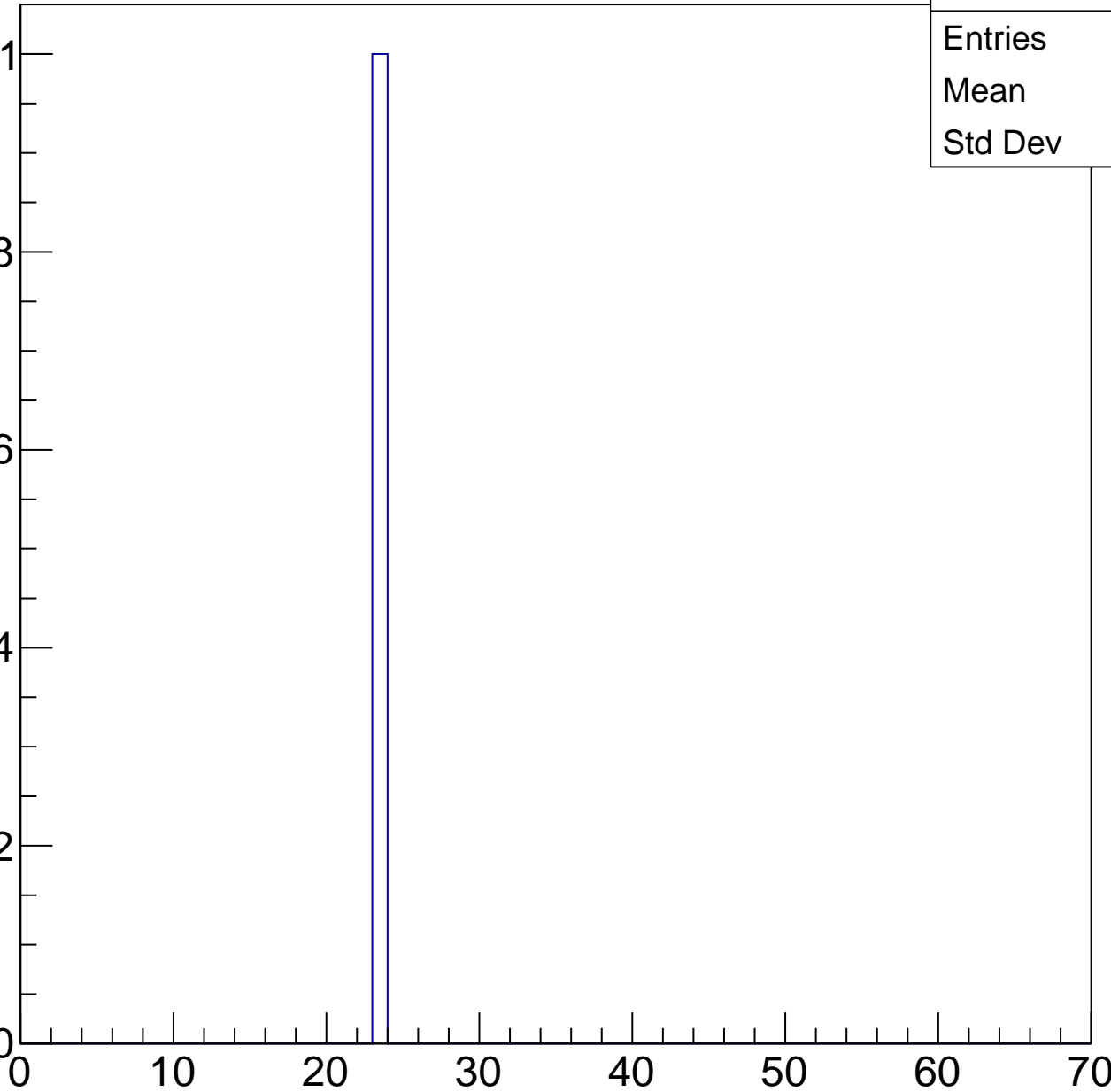
calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L003S, U20-ch108, adc0

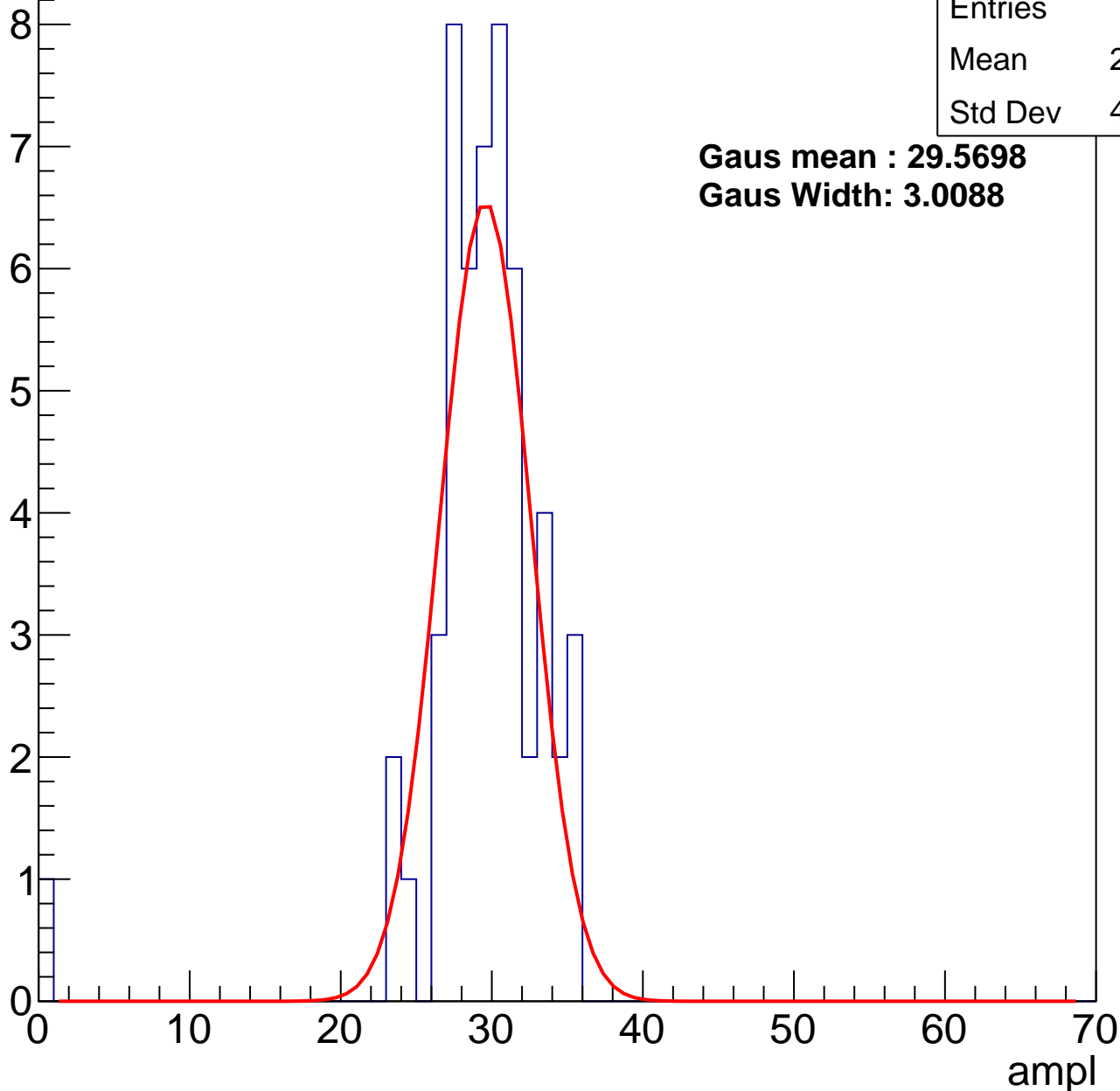
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	28.87
Std Dev	4.903

Gaus mean : 29.5698

Gaus Width: 3.0088



B1L003S, U20-ch108, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	36.35
Std Dev	3.504

Gaus mean : 36.9552

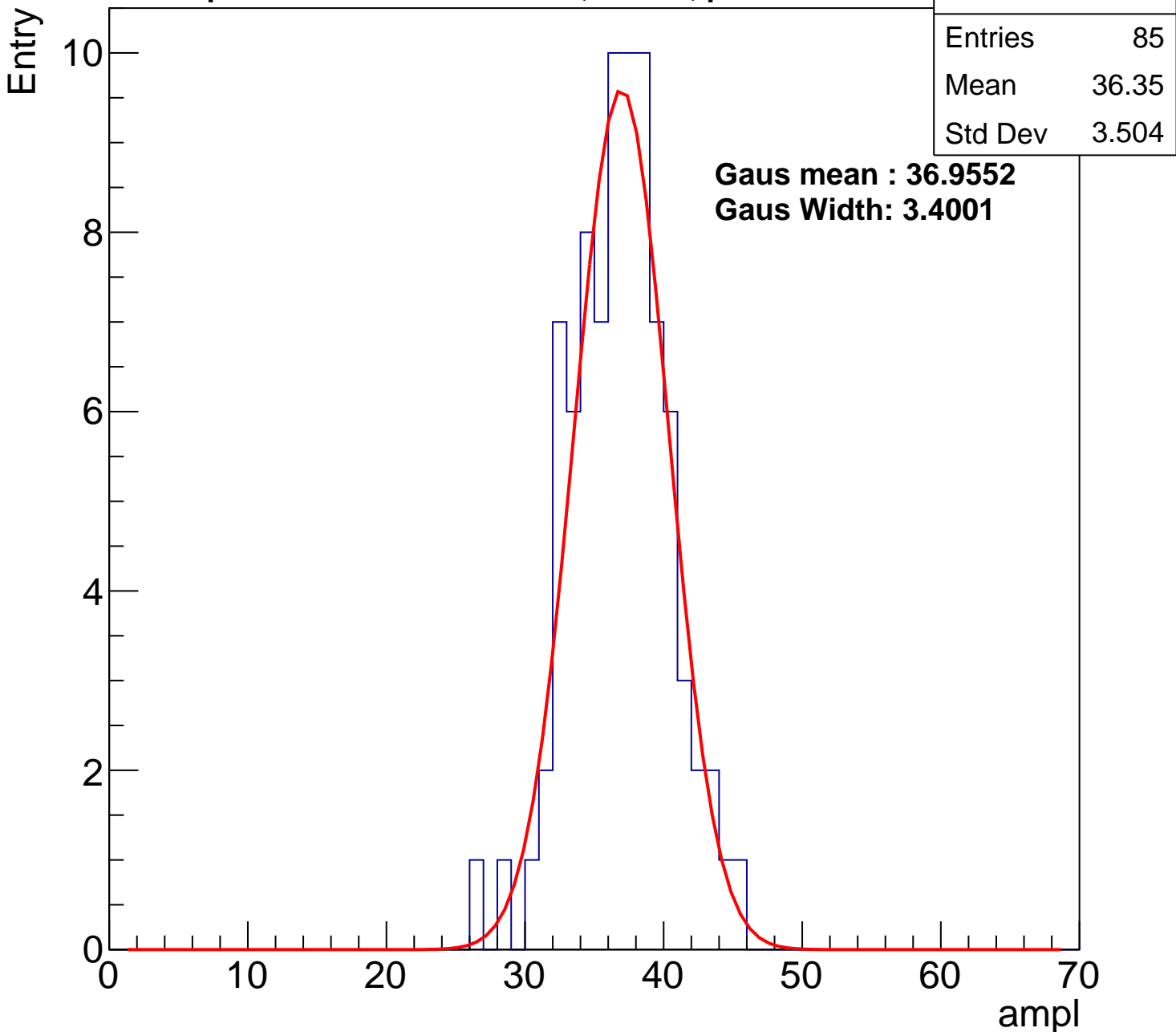
Gaus Width: 3.4001

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch108, adc2

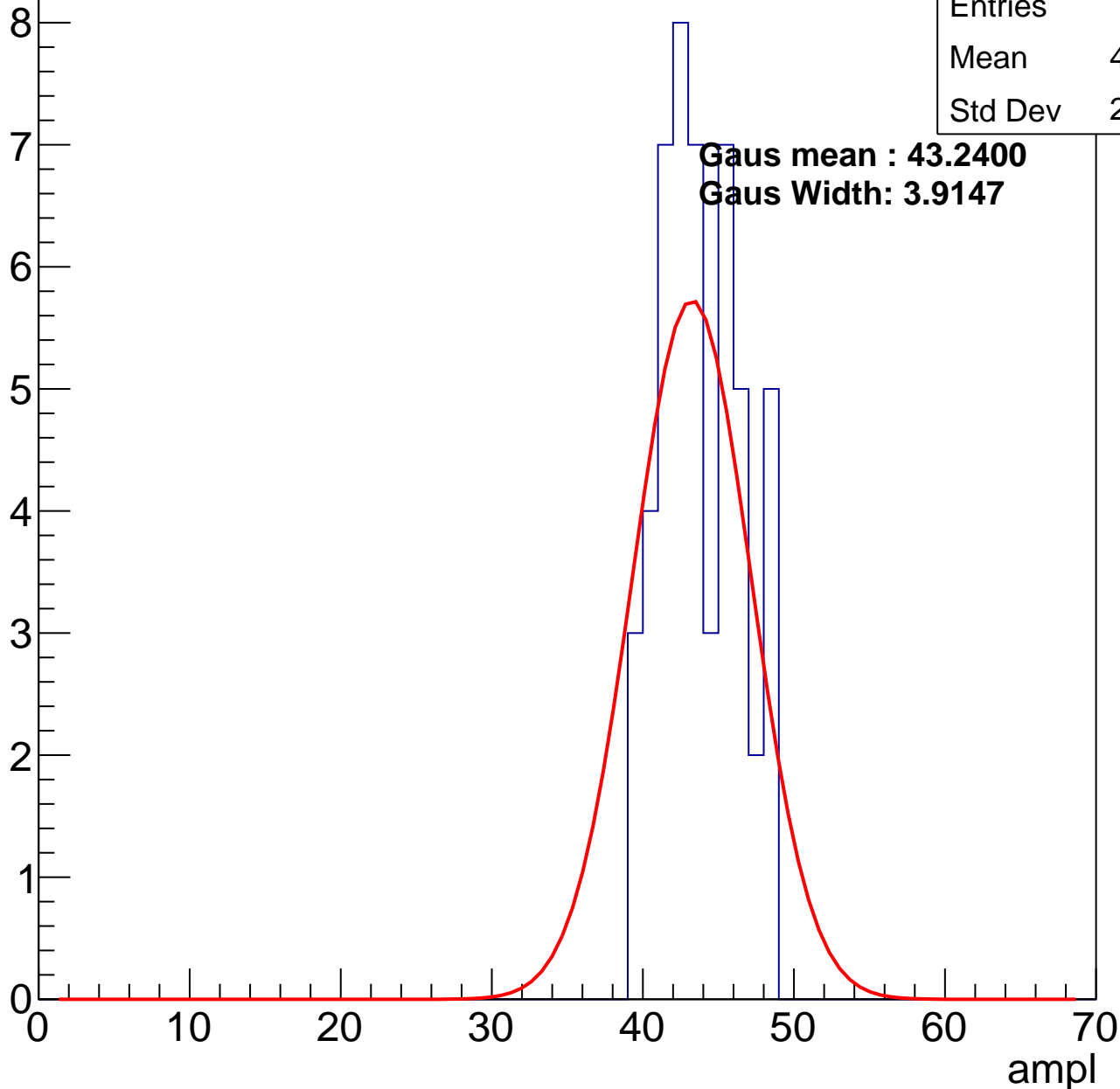
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.37
Std Dev	2.604

Gaus mean : 43.2400

Gaus Width: 3.9147

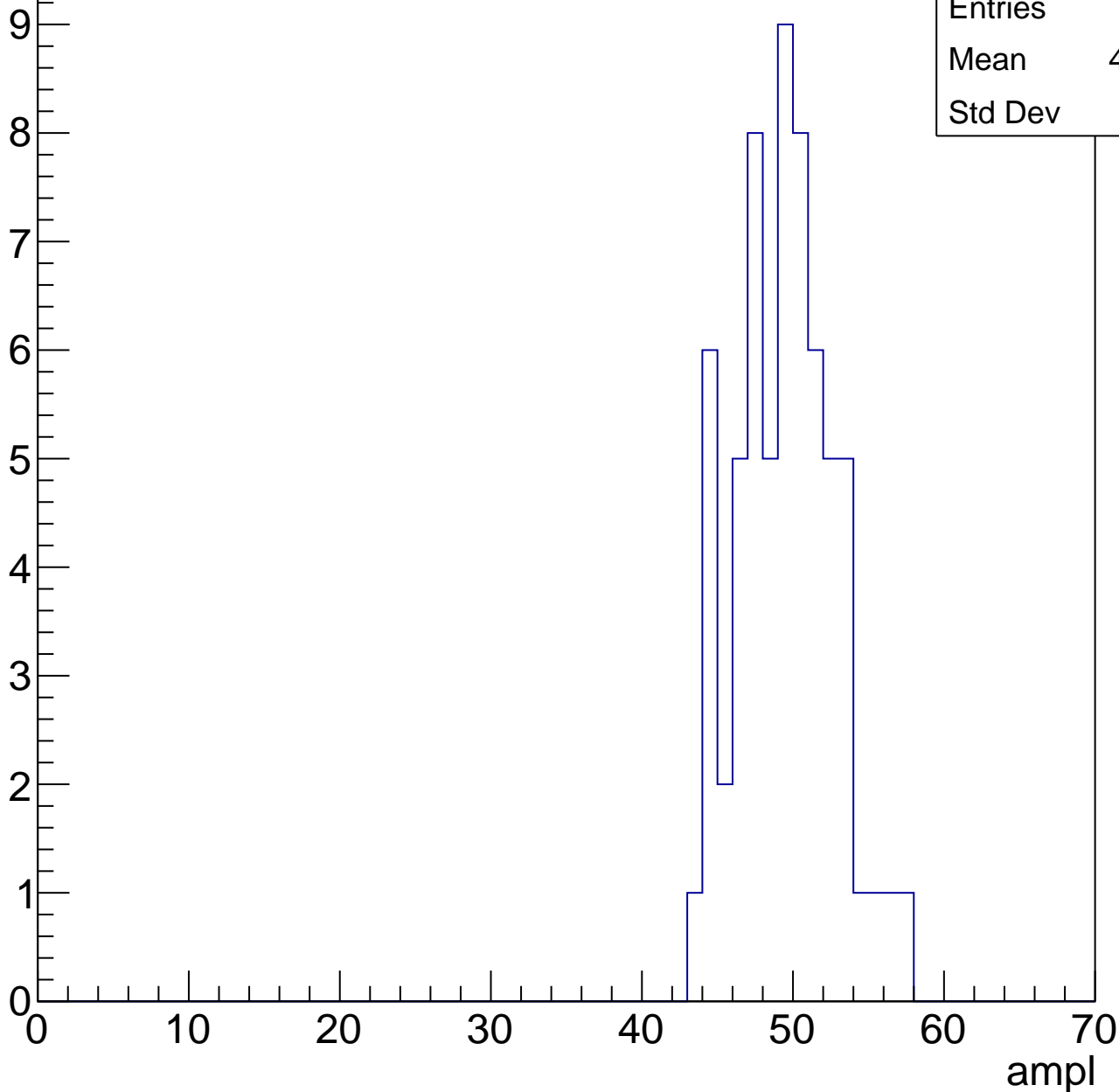


B1L003S, U20-ch108, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.02
Std Dev	3.14

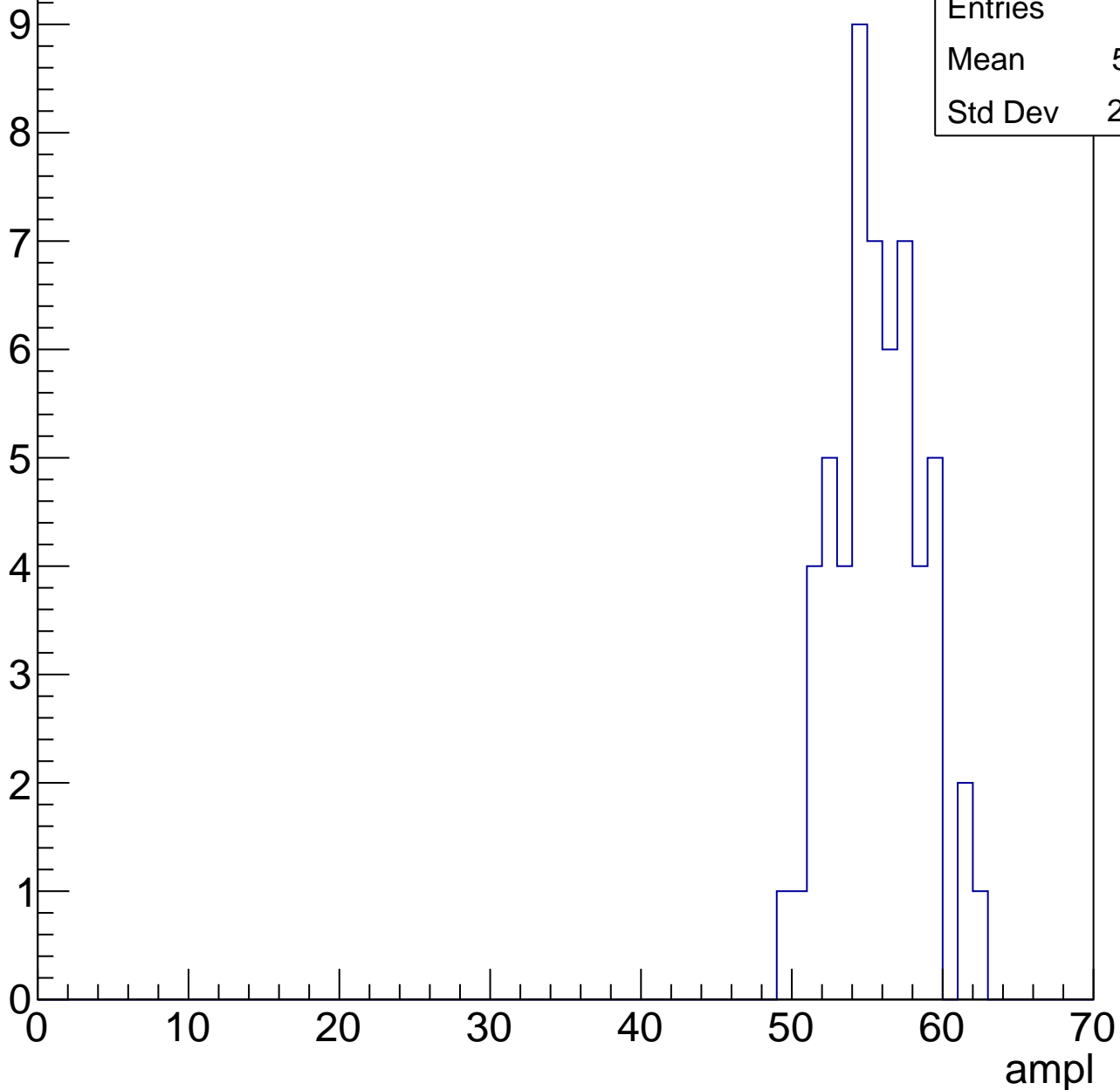


B1L003S, U20-ch108, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.21
Std Dev	2.877

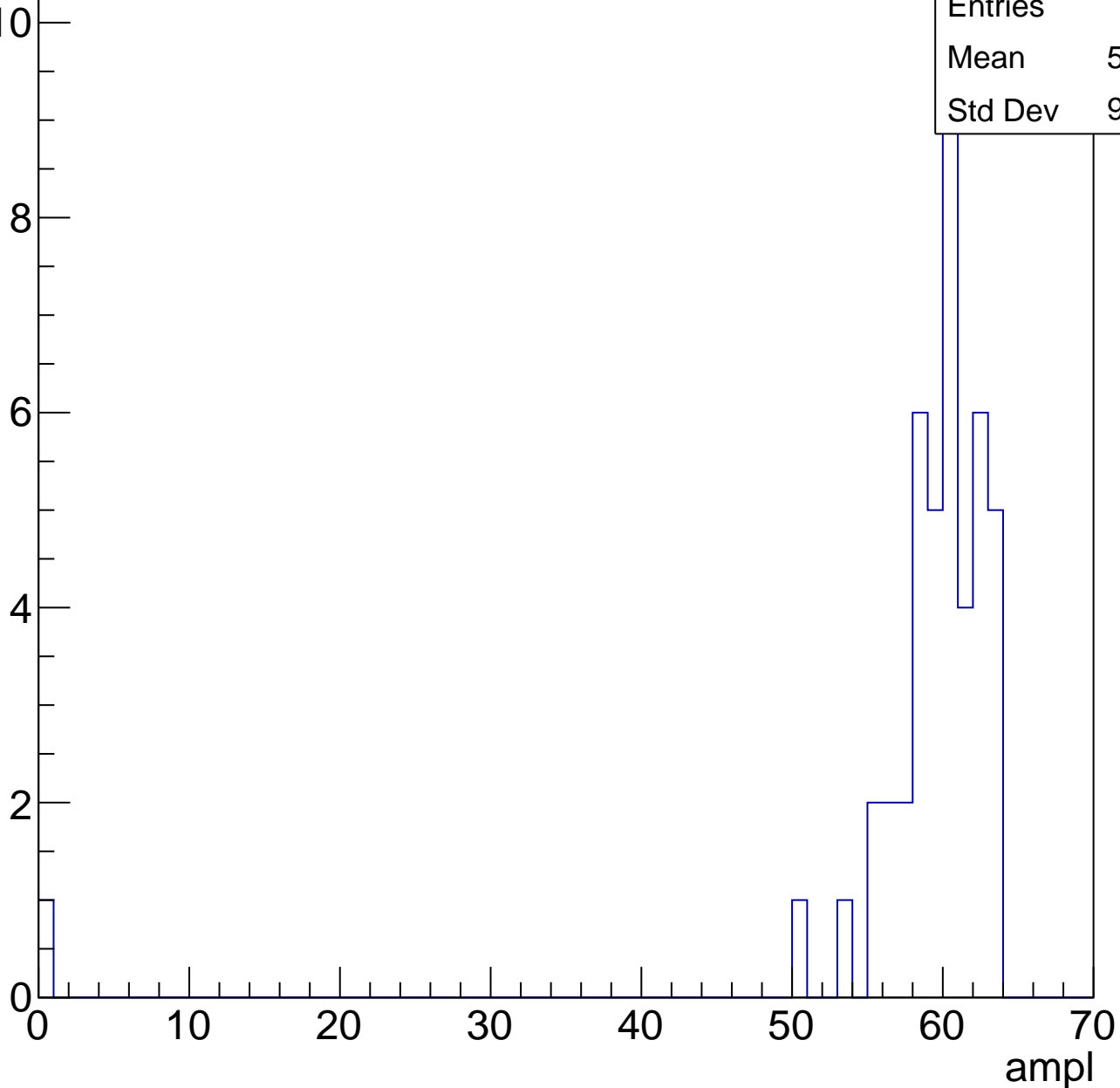


B1L003S, U20-ch108, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.07
Std Dev	9.169

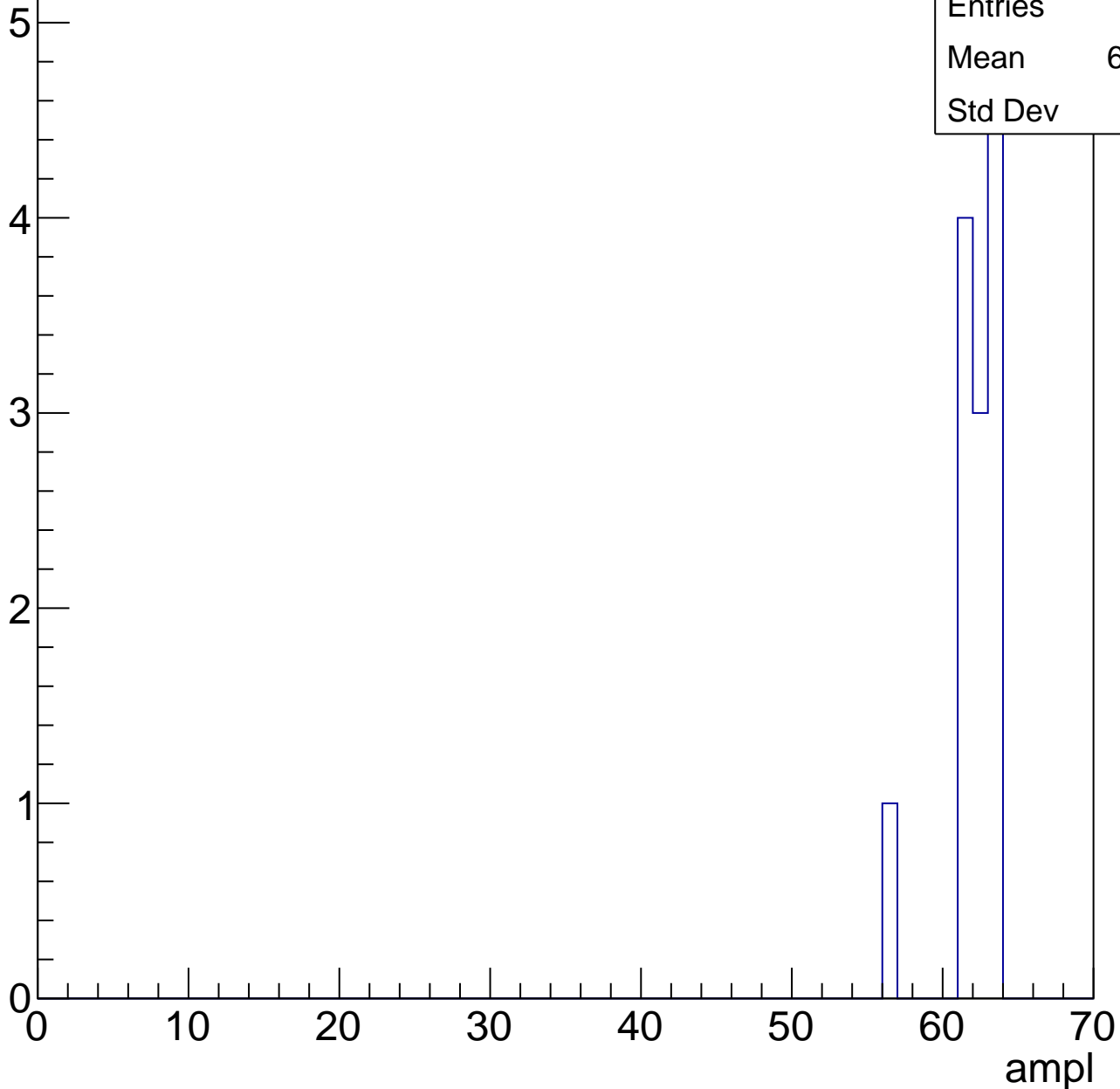


B1L003S, U20-ch108, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.62
Std Dev	1.82



B1L003S, U20-ch108, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch109, adc0

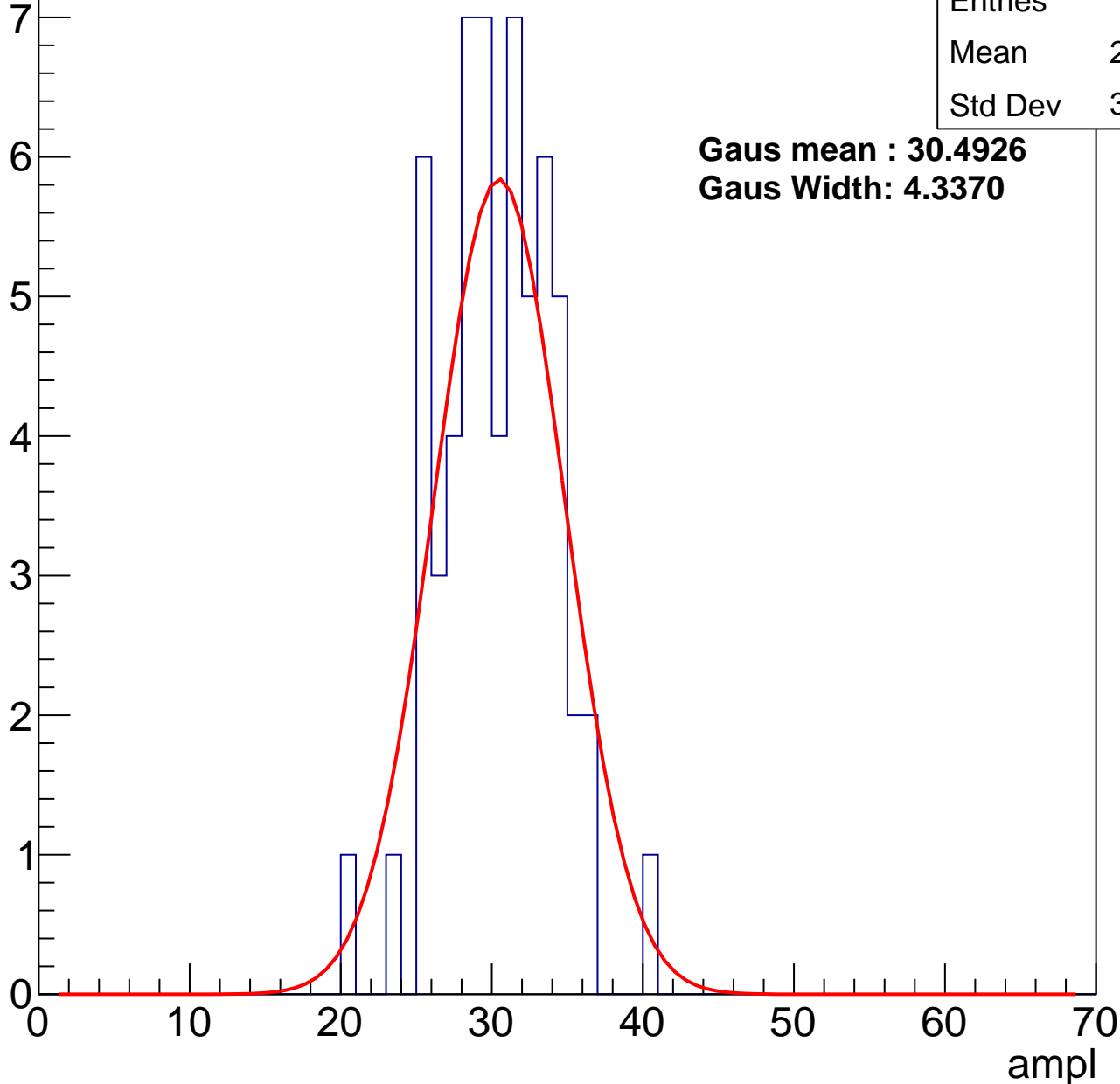
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.92
Std Dev	3.623

Gaus mean : 30.4926

Gaus Width: 4.3370



B1L003S, U20-ch109, adc1

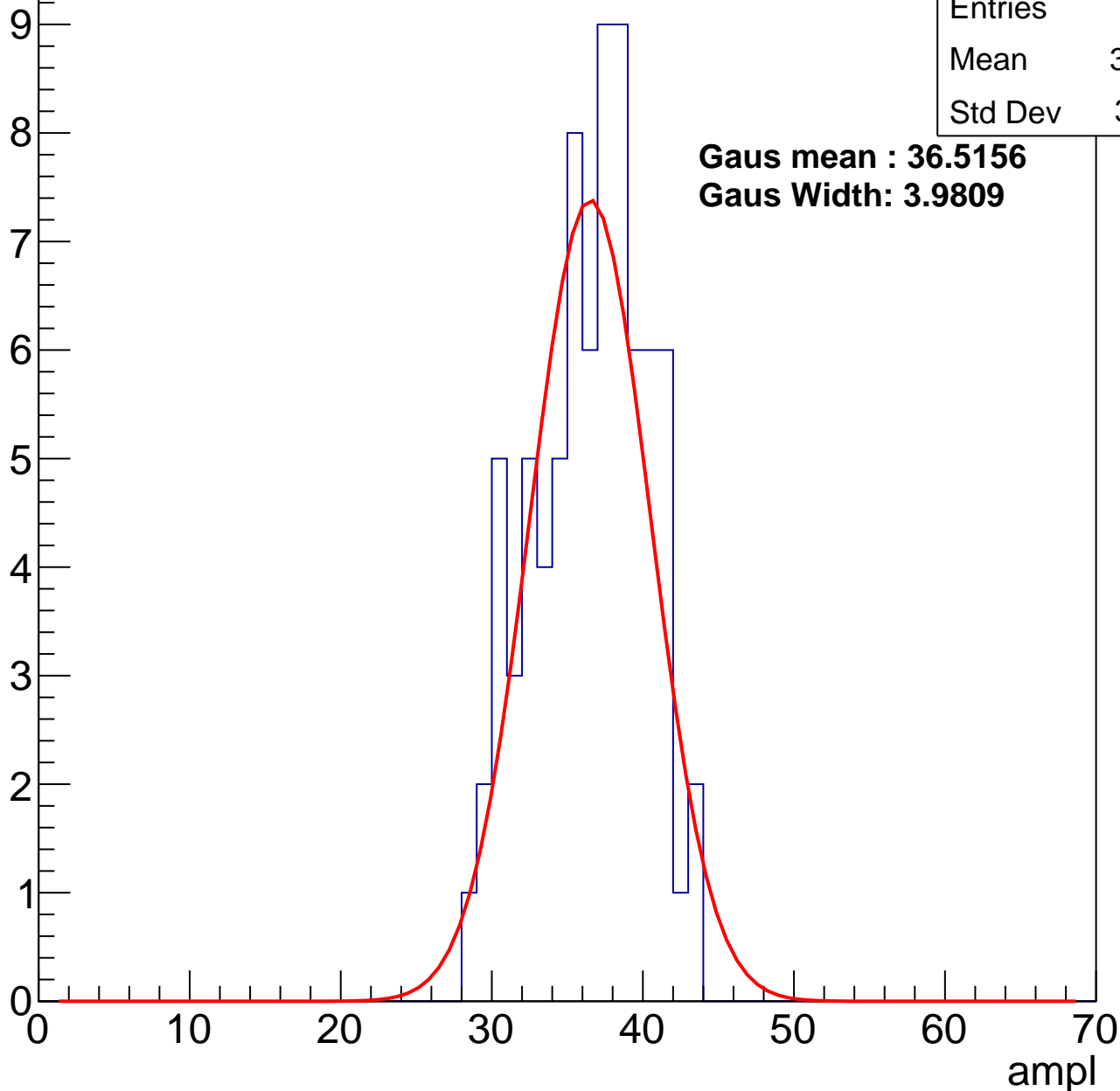
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	36.03
Std Dev	3.651

Gaus mean : 36.5156

Gaus Width: 3.9809



B1L003S, U20-ch109, adc2

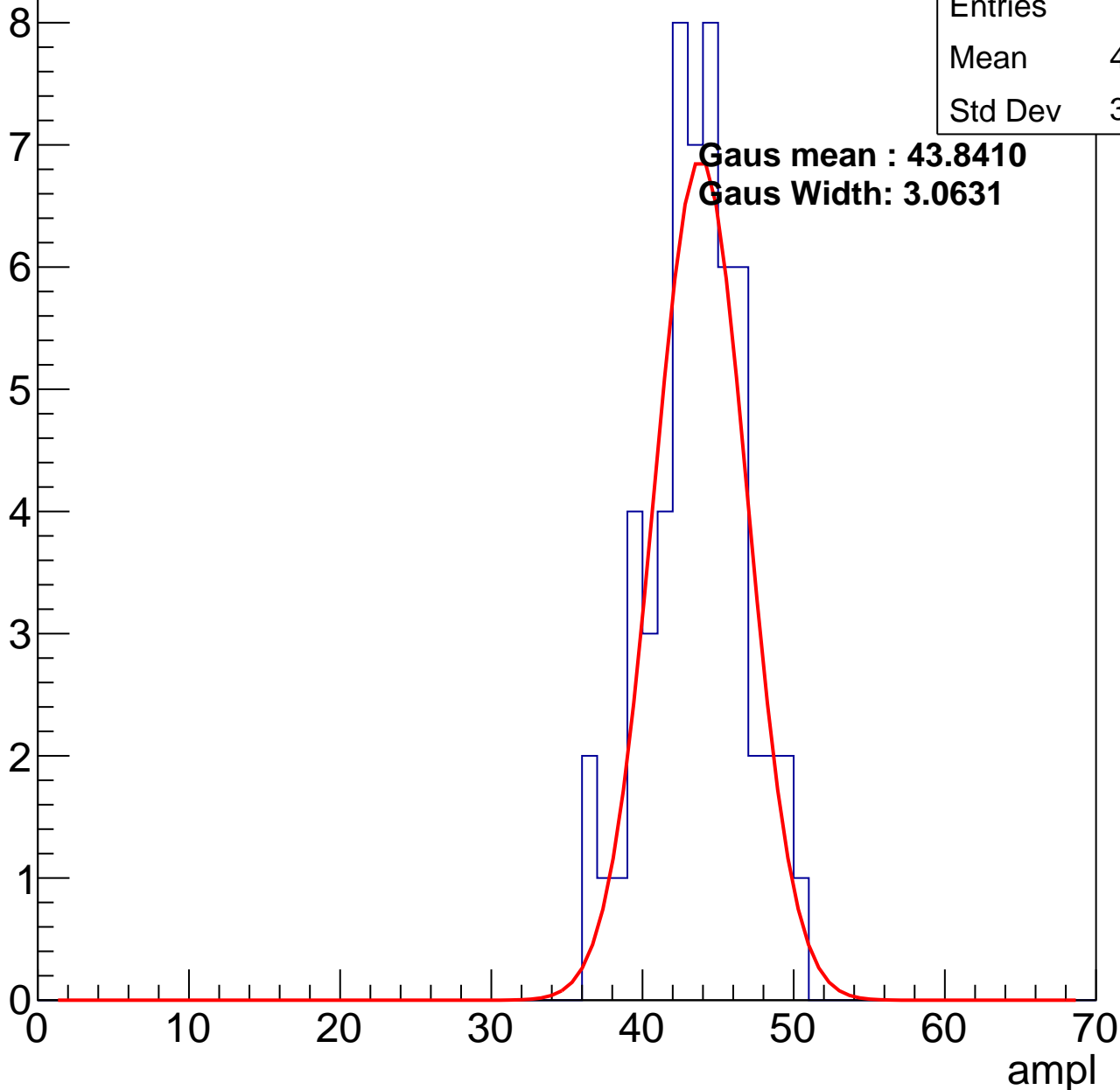
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.16
Std Dev	3.139

Gaus mean : 43.8410

Gaus Width: 3.0631

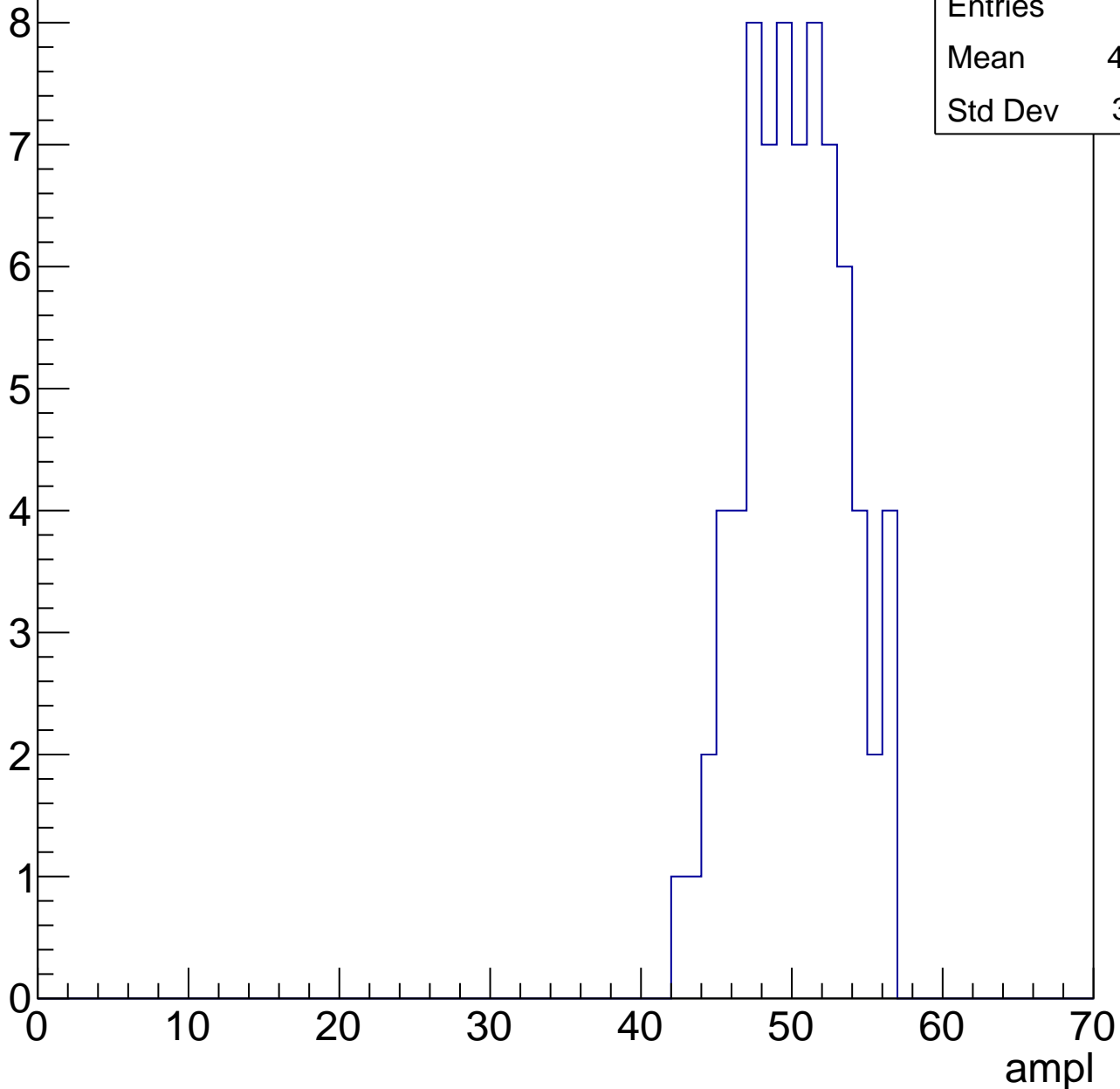


B1L003S, U20-ch109, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	49.74
Std Dev	3.311

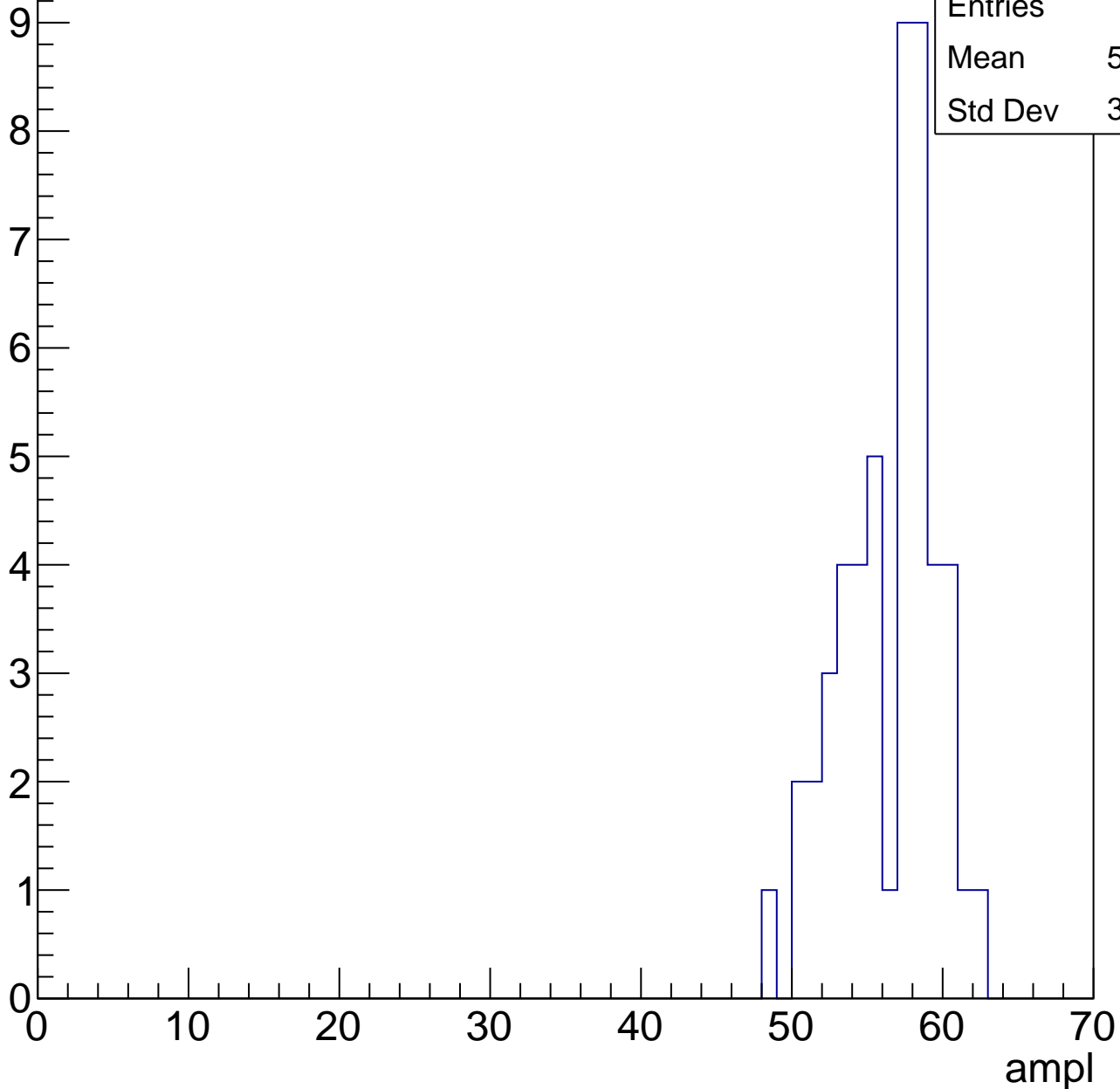


B1L003S, U20-ch109, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.98
Std Dev	3.153



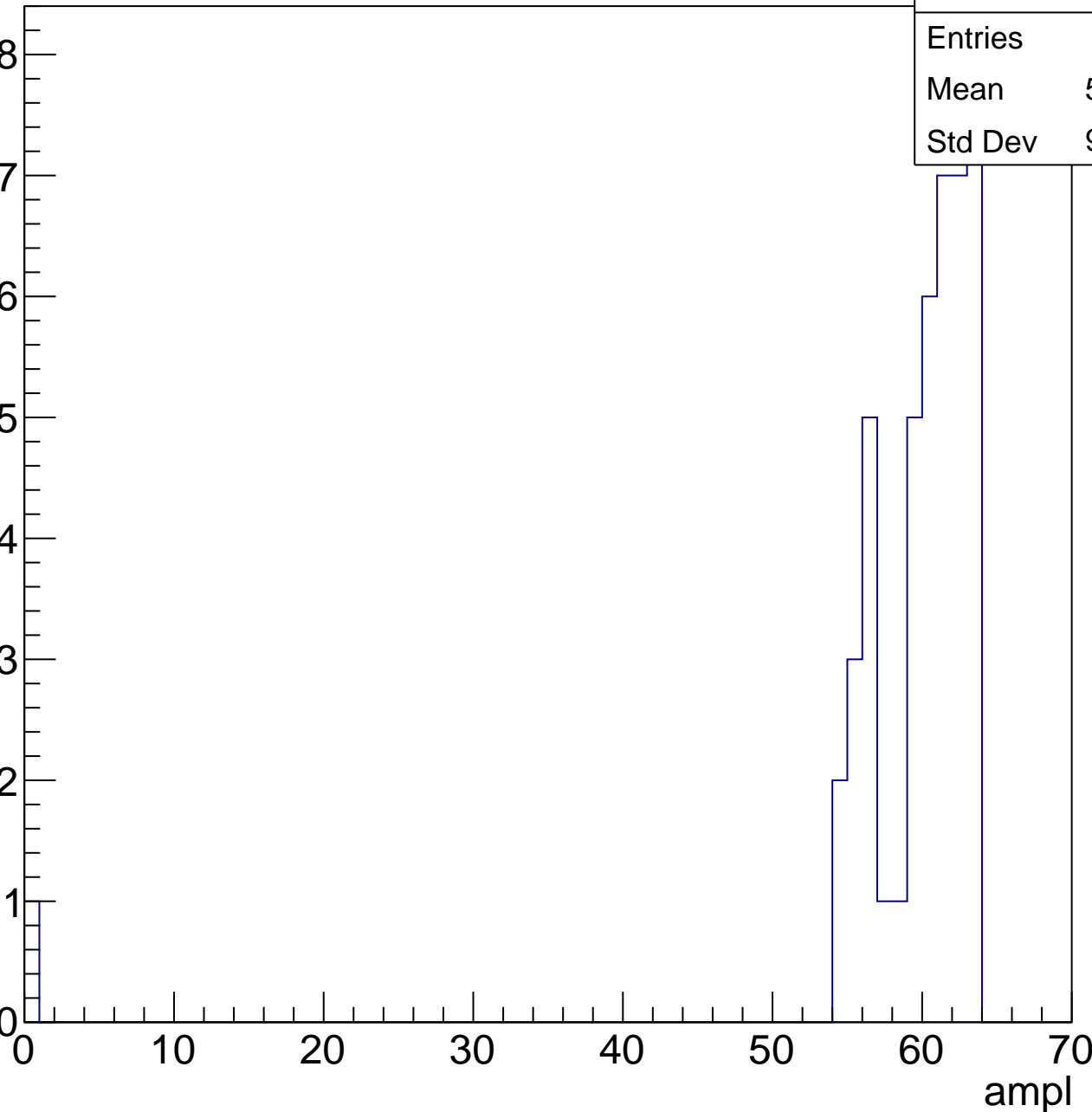
B1L003S, U20-ch109, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.43
Std Dev	9.136

8
7
6
5
4
3
2
1
0



B1L003S, U20-ch109, adc6

calib_packv5_042523_0143.root, FC#13, port D2

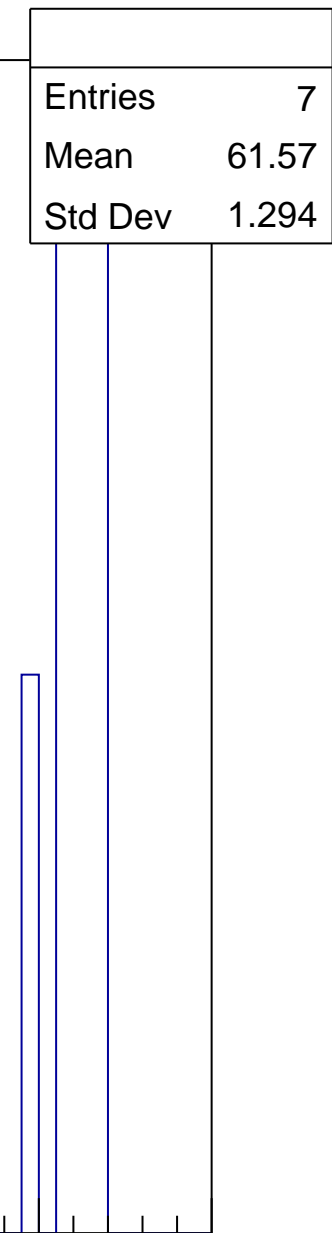
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.57
Std Dev	1.294

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch109, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch110, adc0

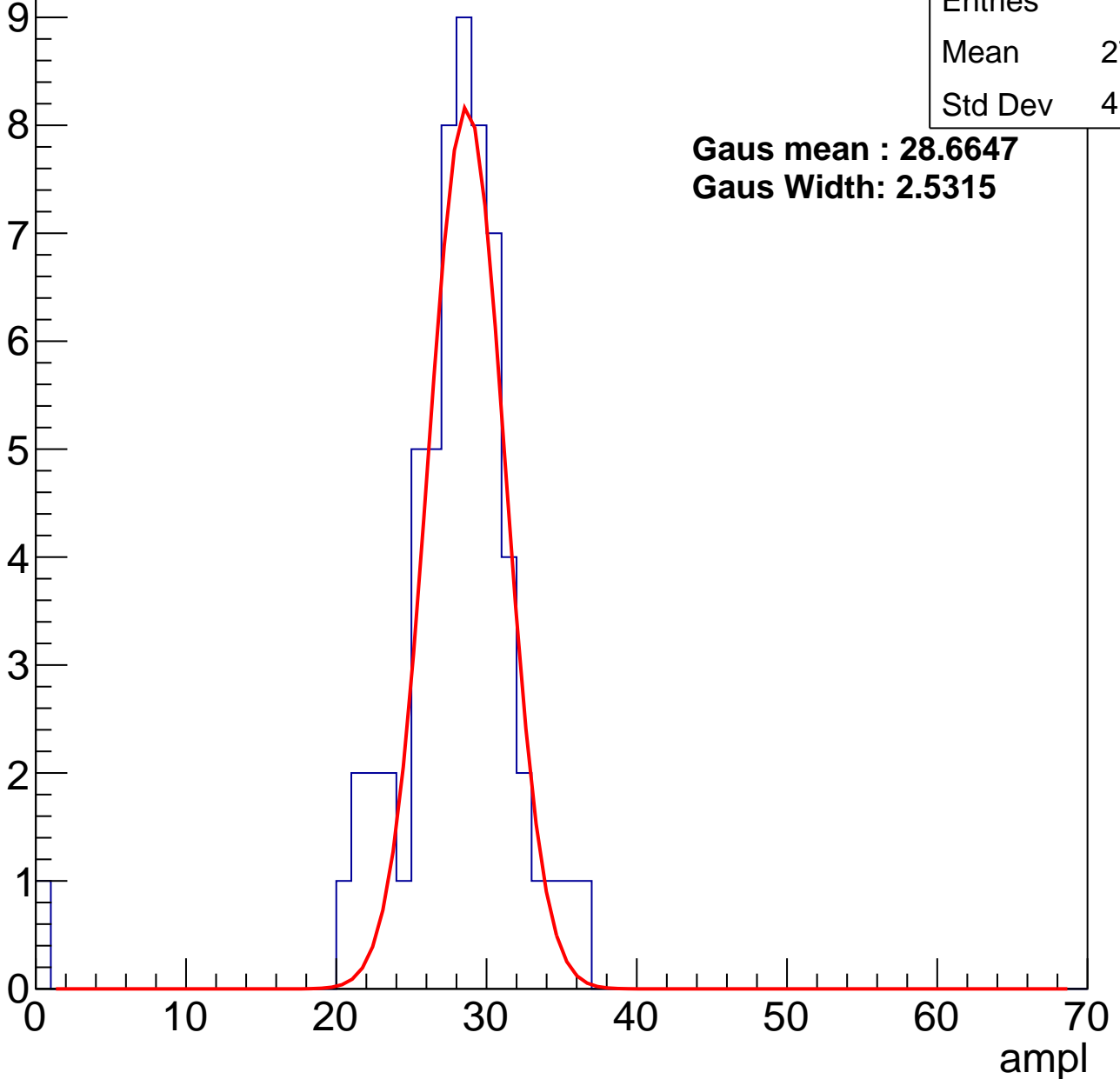
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	27.33
Std Dev	4.803

Gaus mean : 28.6647

Gaus Width: 2.5315



B1L003S, U20-ch110, adc1

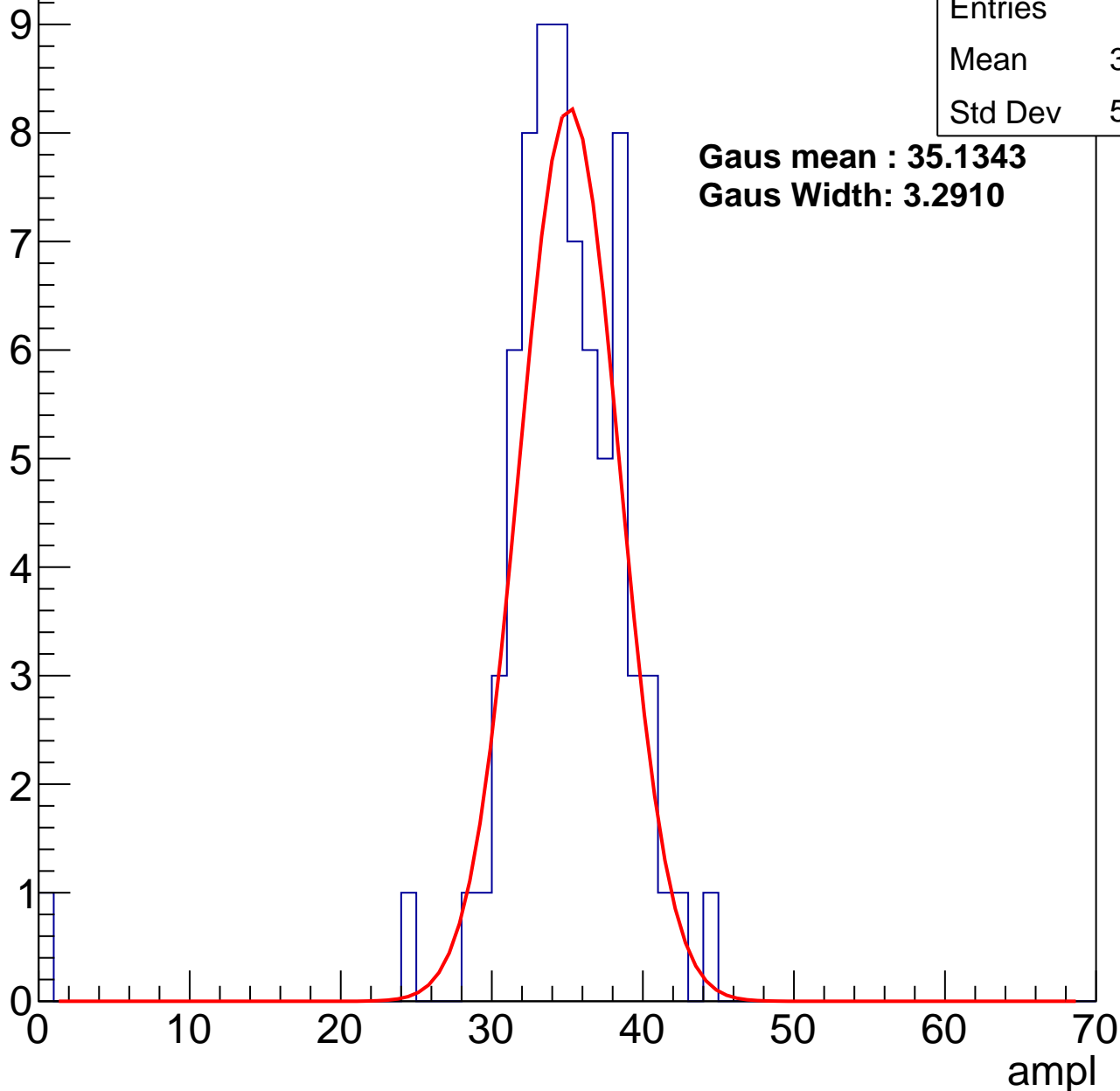
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	34.19
Std Dev	5.273

Gaus mean : 35.1343

Gaus Width: 3.2910



B1L003S, U20-ch110, adc2

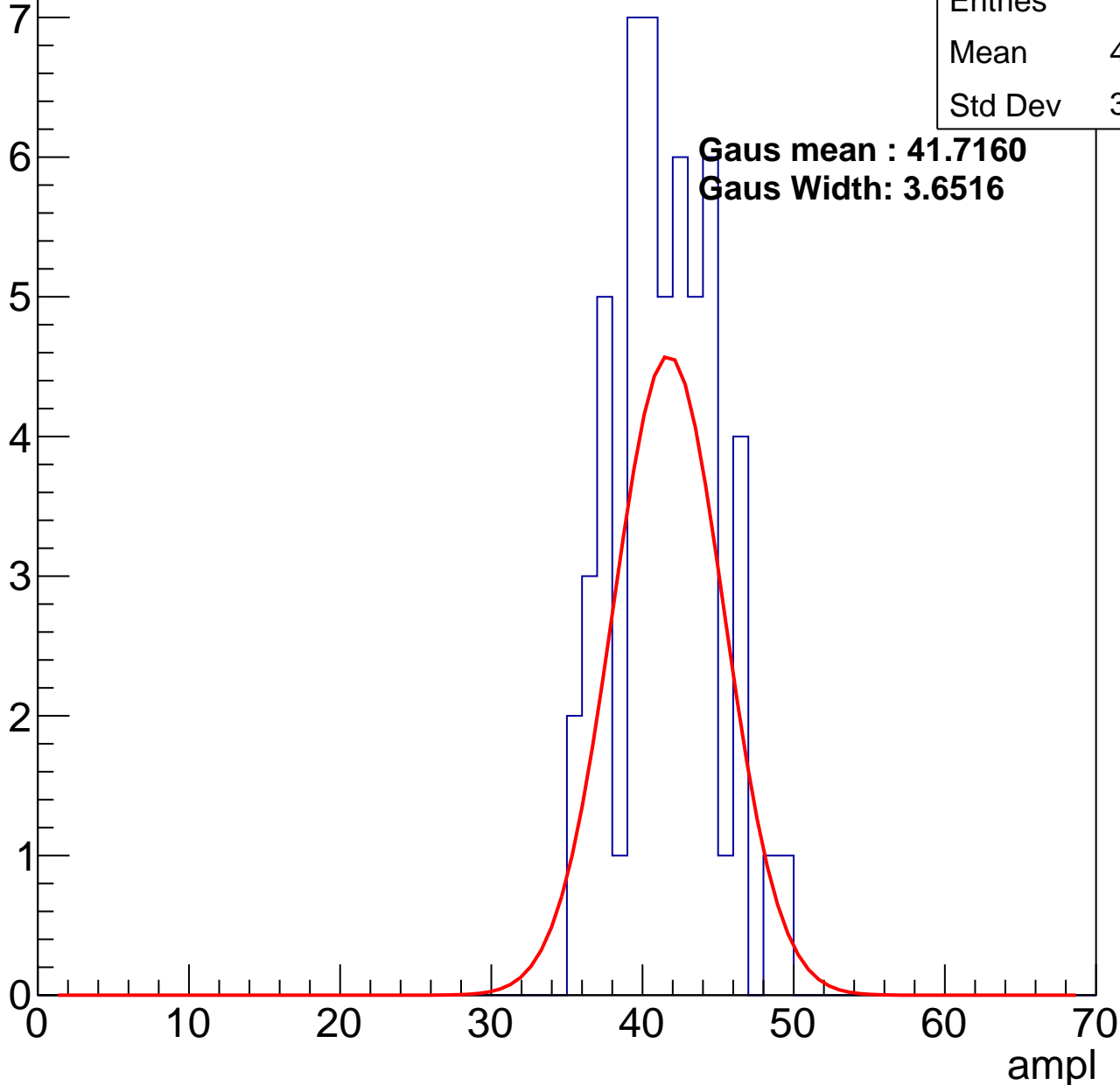
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.04
Std Dev	3.277

Gaus mean : 41.7160

Gaus Width: 3.6516



B1L003S, U20-ch110, adc3

calib_packv5_042523_0143.root, FC#13, port D2

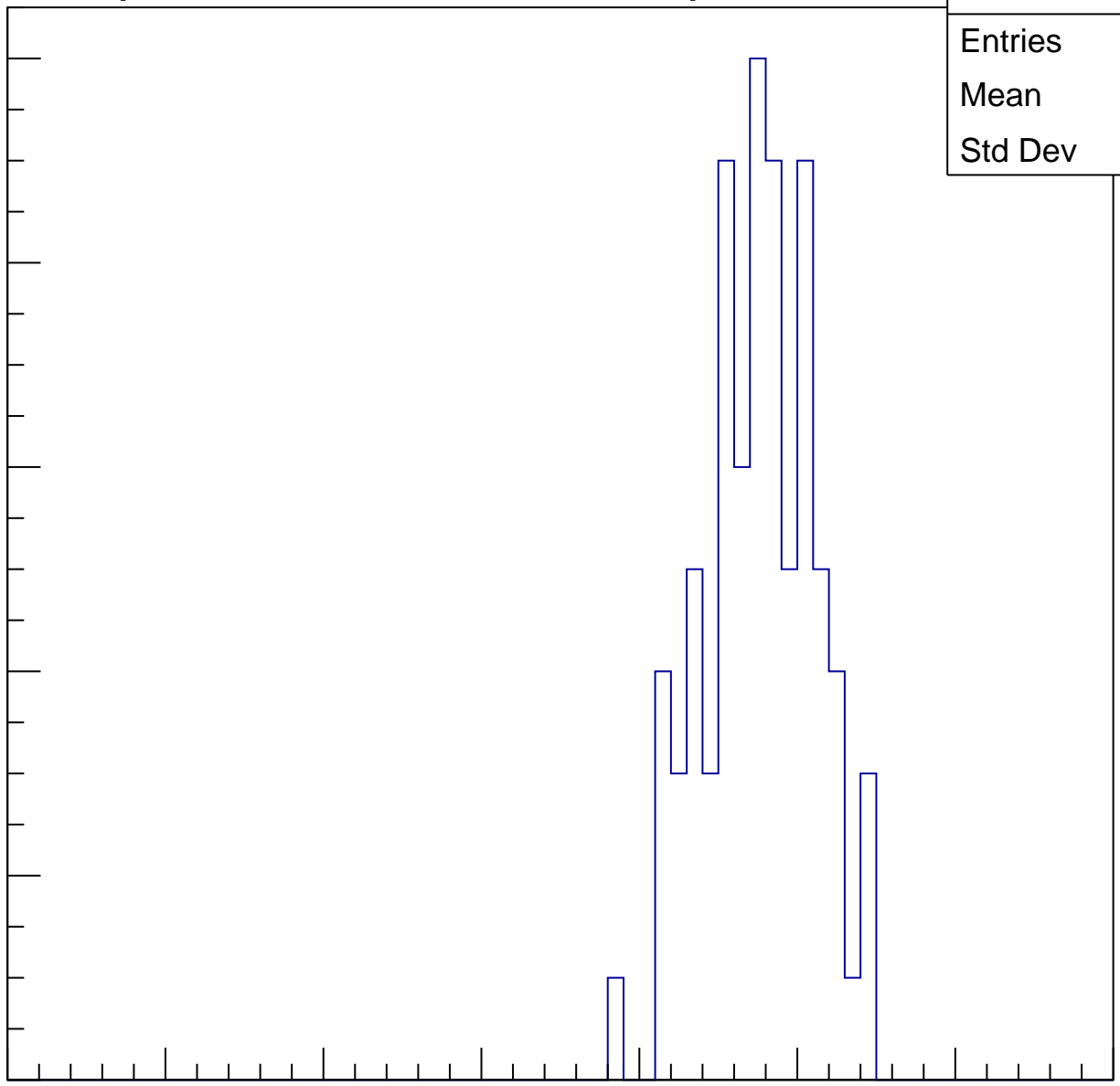
Entries	77
Mean	47.16
Std Dev	3.457

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

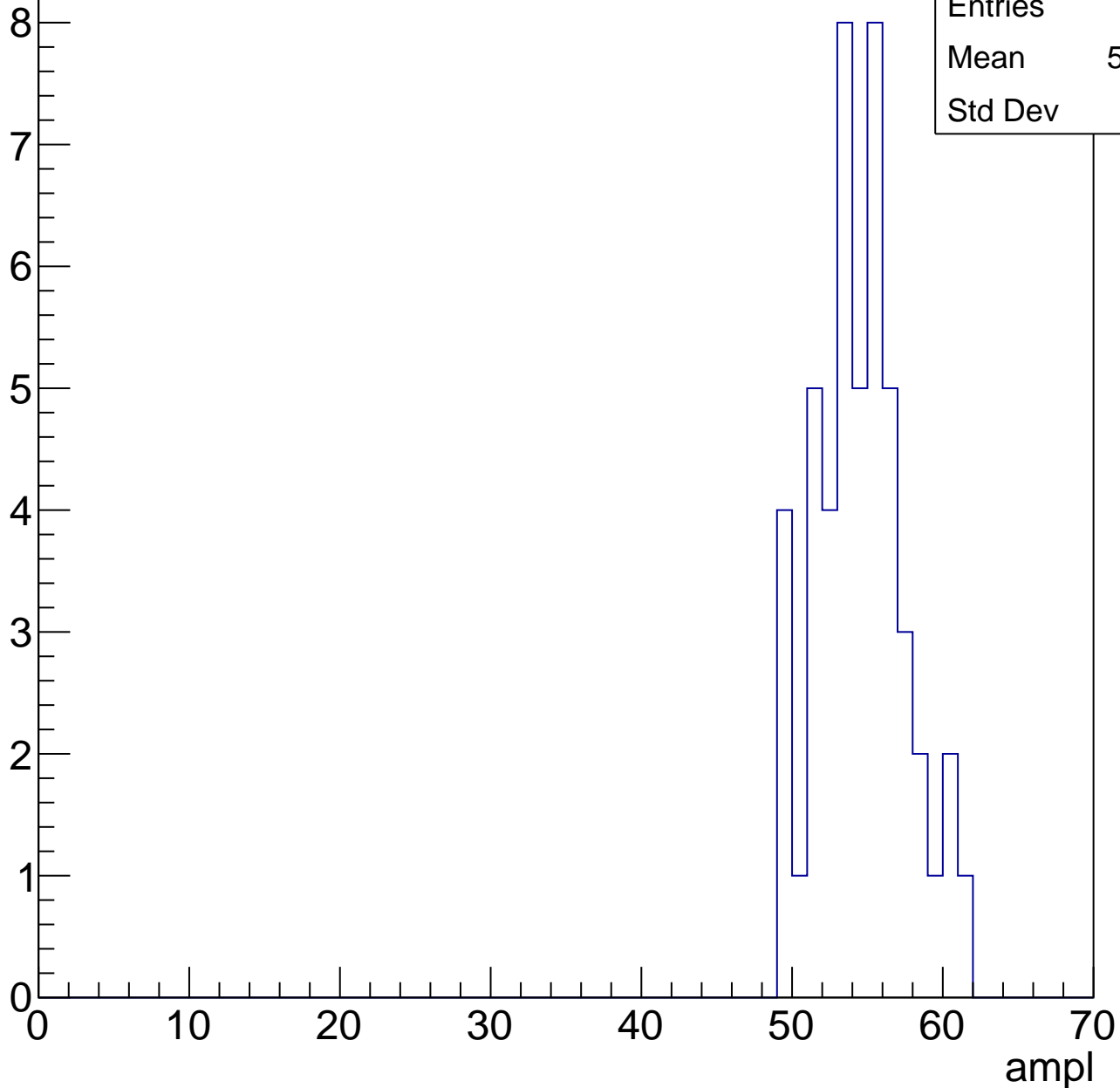


B1L003S, U20-ch110, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	54.08
Std Dev	2.92

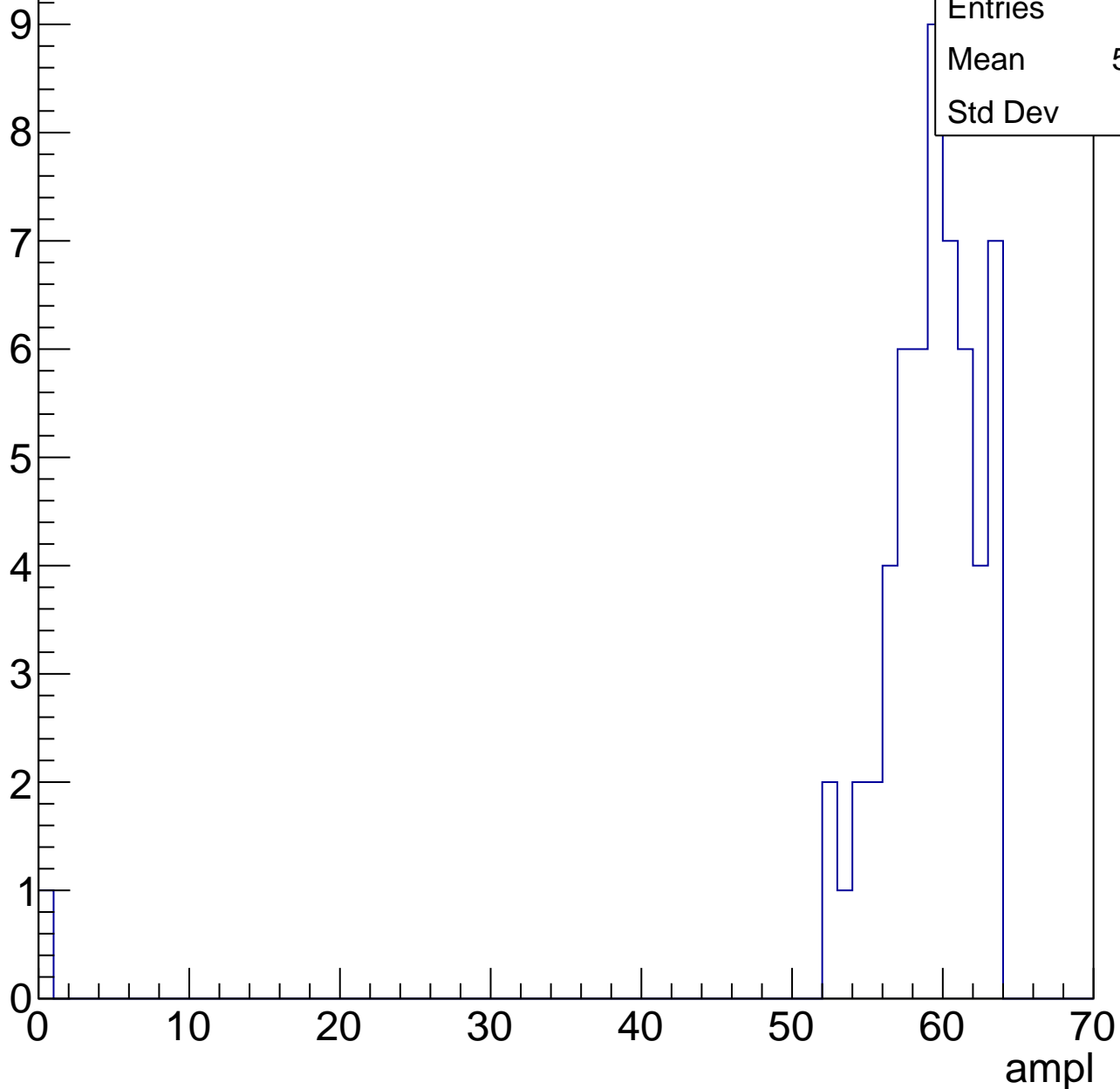


B1L003S, U20-ch110, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57.81
Std Dev	8.23

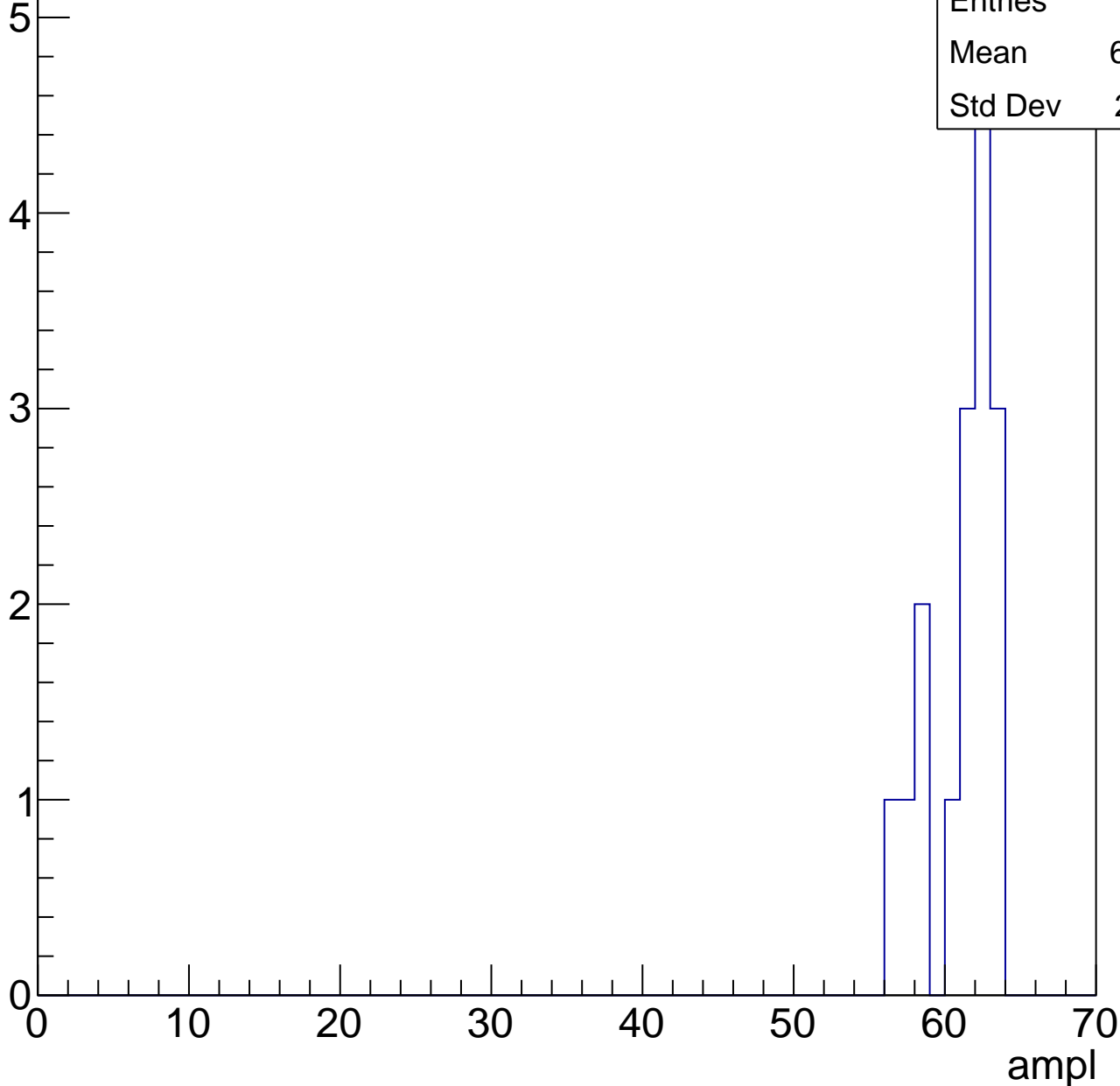


B1L003S, U20-ch110, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	60.69
Std Dev	2.171



B1L003S, U20-ch110, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch111, adc0

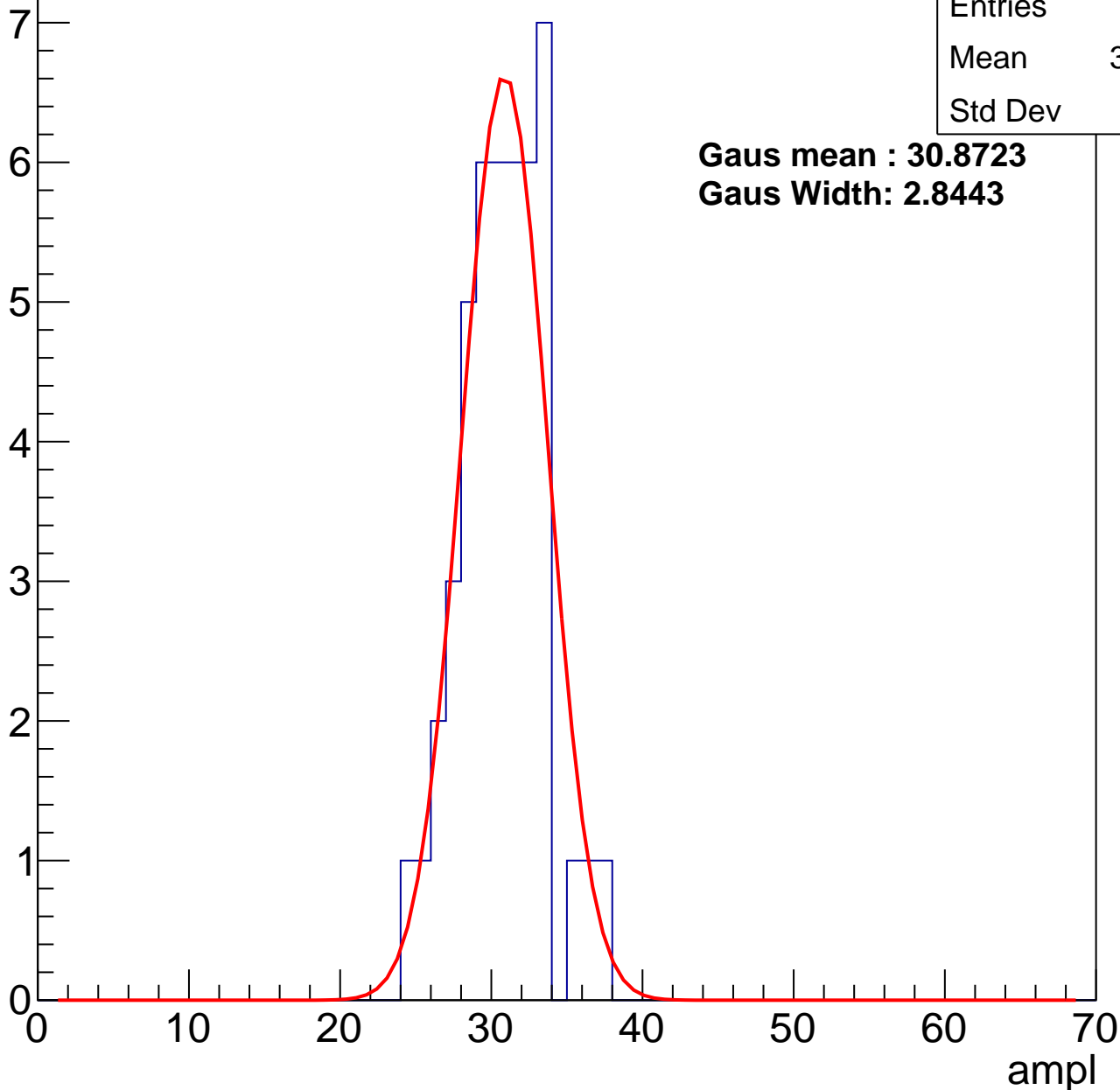
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	30.28
Std Dev	2.74

Gaus mean : 30.8723

Gaus Width: 2.8443



B1L003S, U20-ch111, adc1

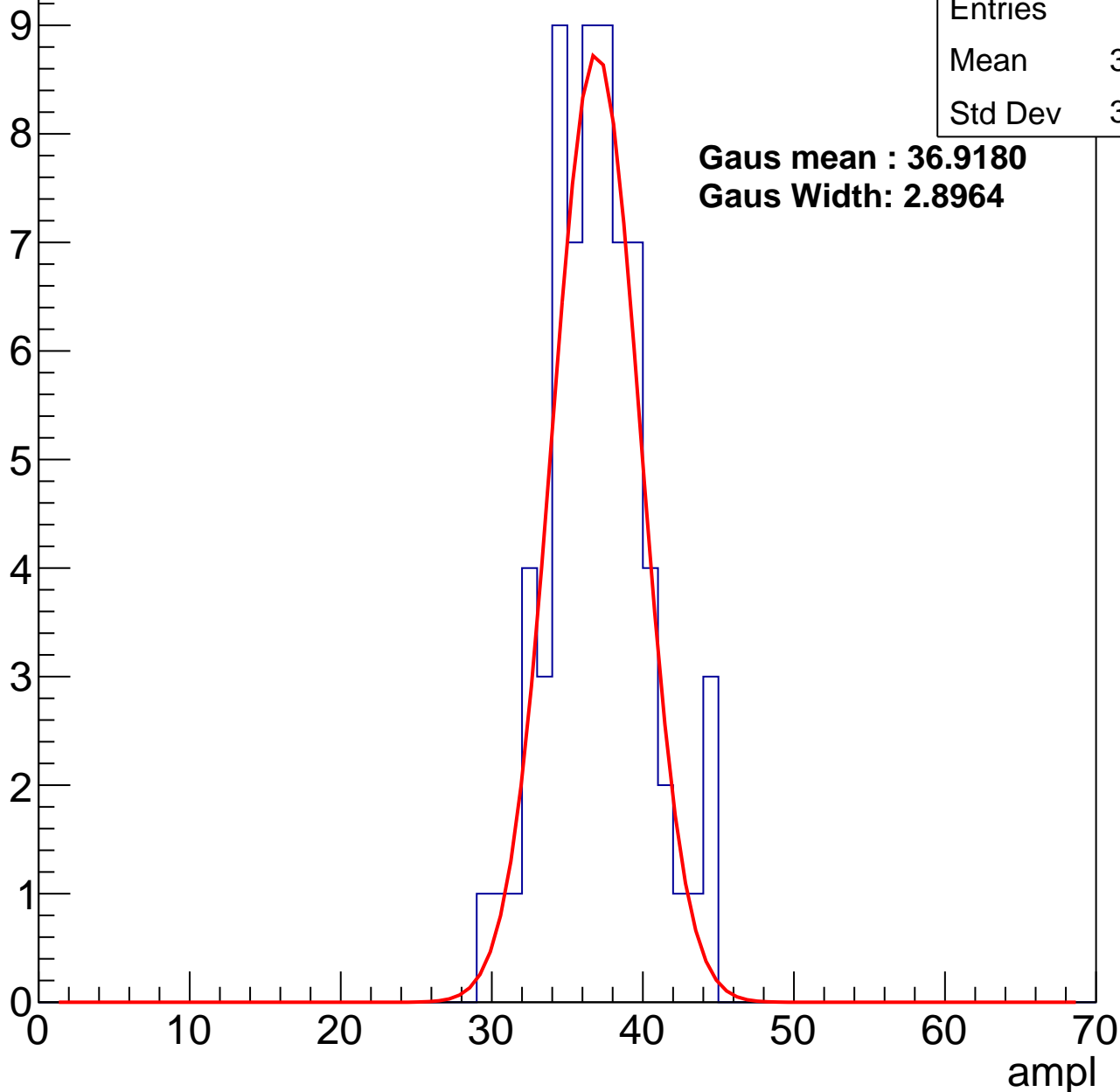
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	36.57
Std Dev	3.214

Gaus mean : 36.9180

Gaus Width: 2.8964



B1L003S, U20-ch111, adc2

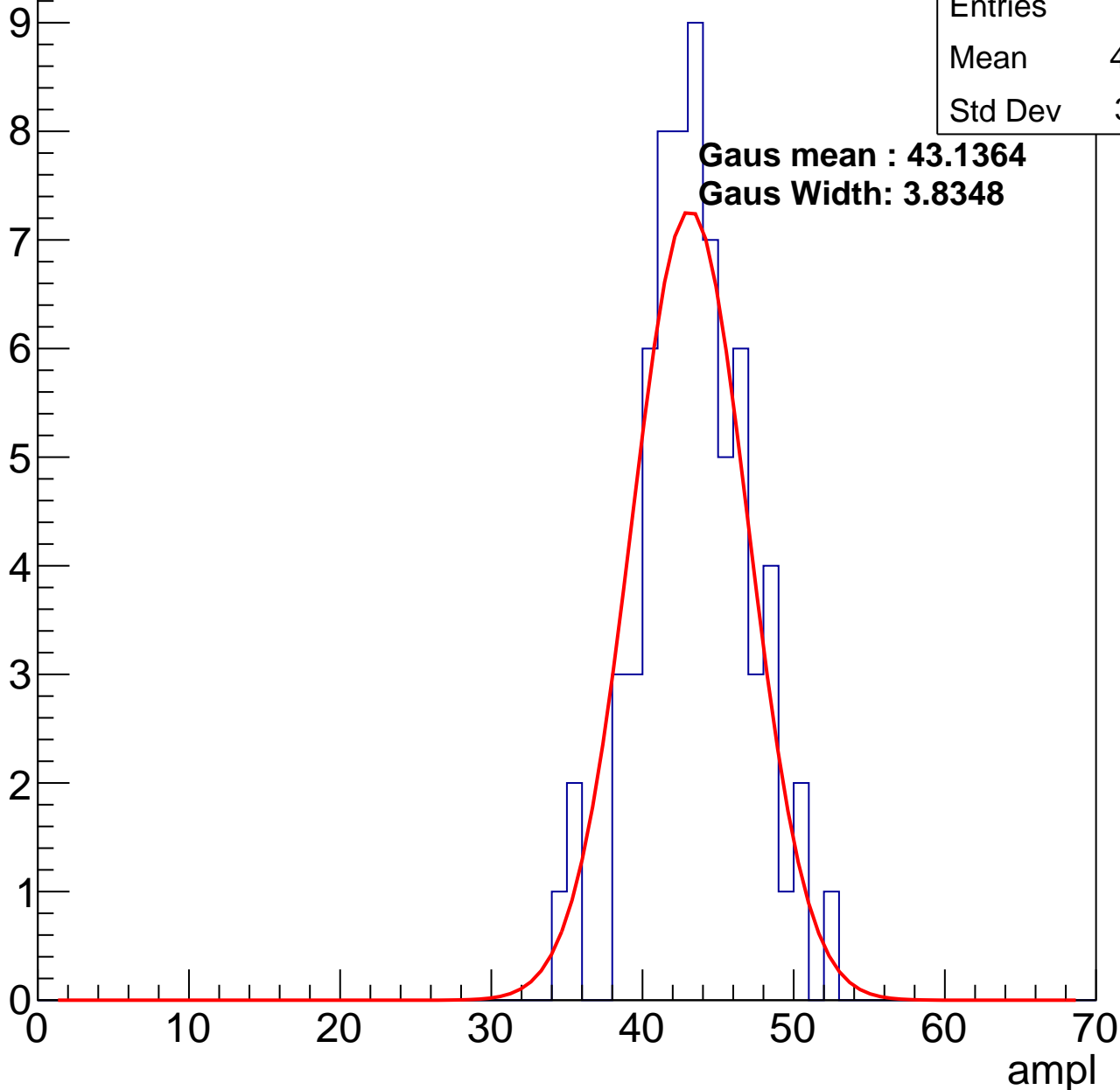
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.03
Std Dev	3.551

Gaus mean : 43.1364

Gaus Width: 3.8348

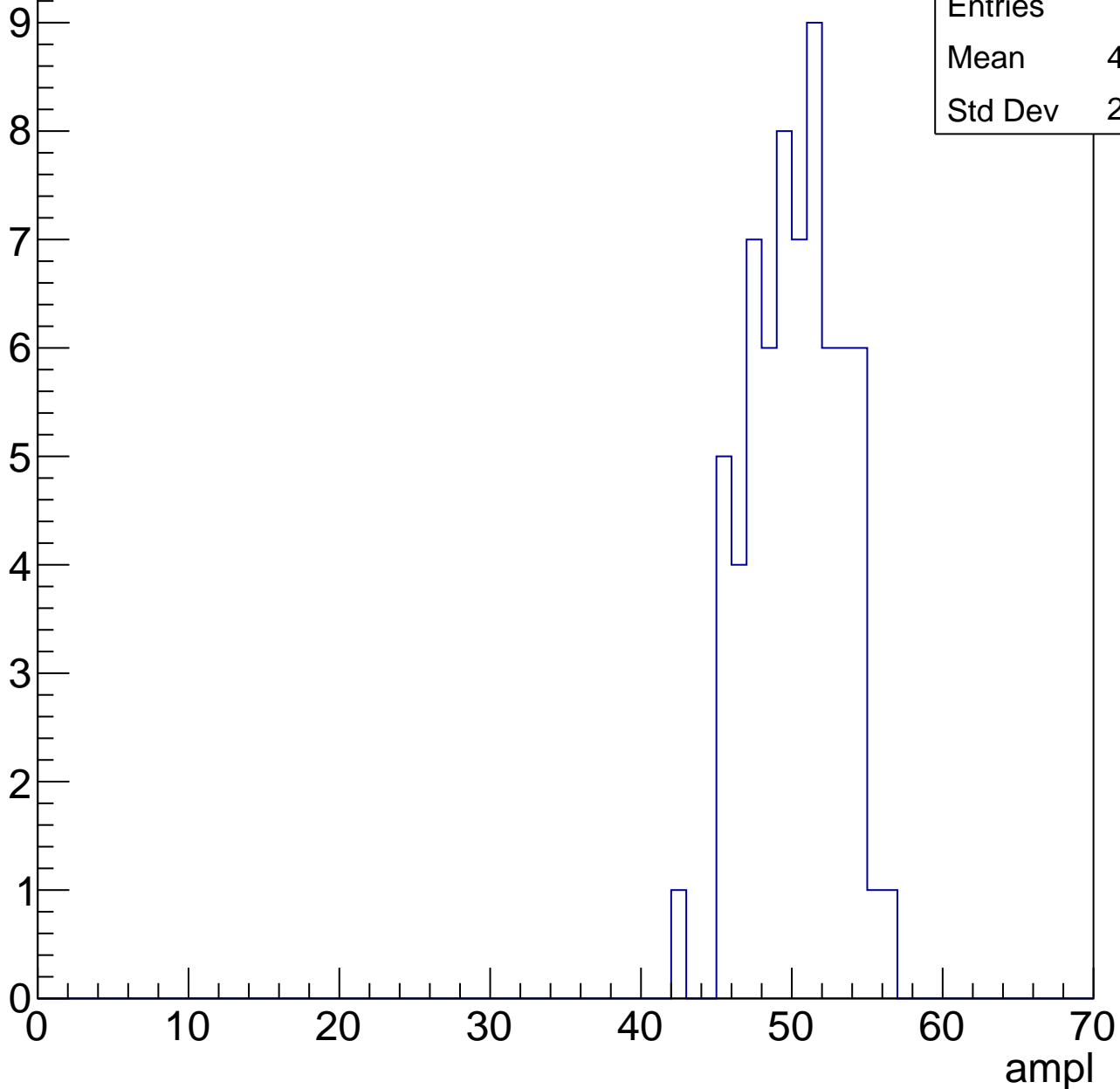


B1L003S, U20-ch111, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	49.76
Std Dev	2.963

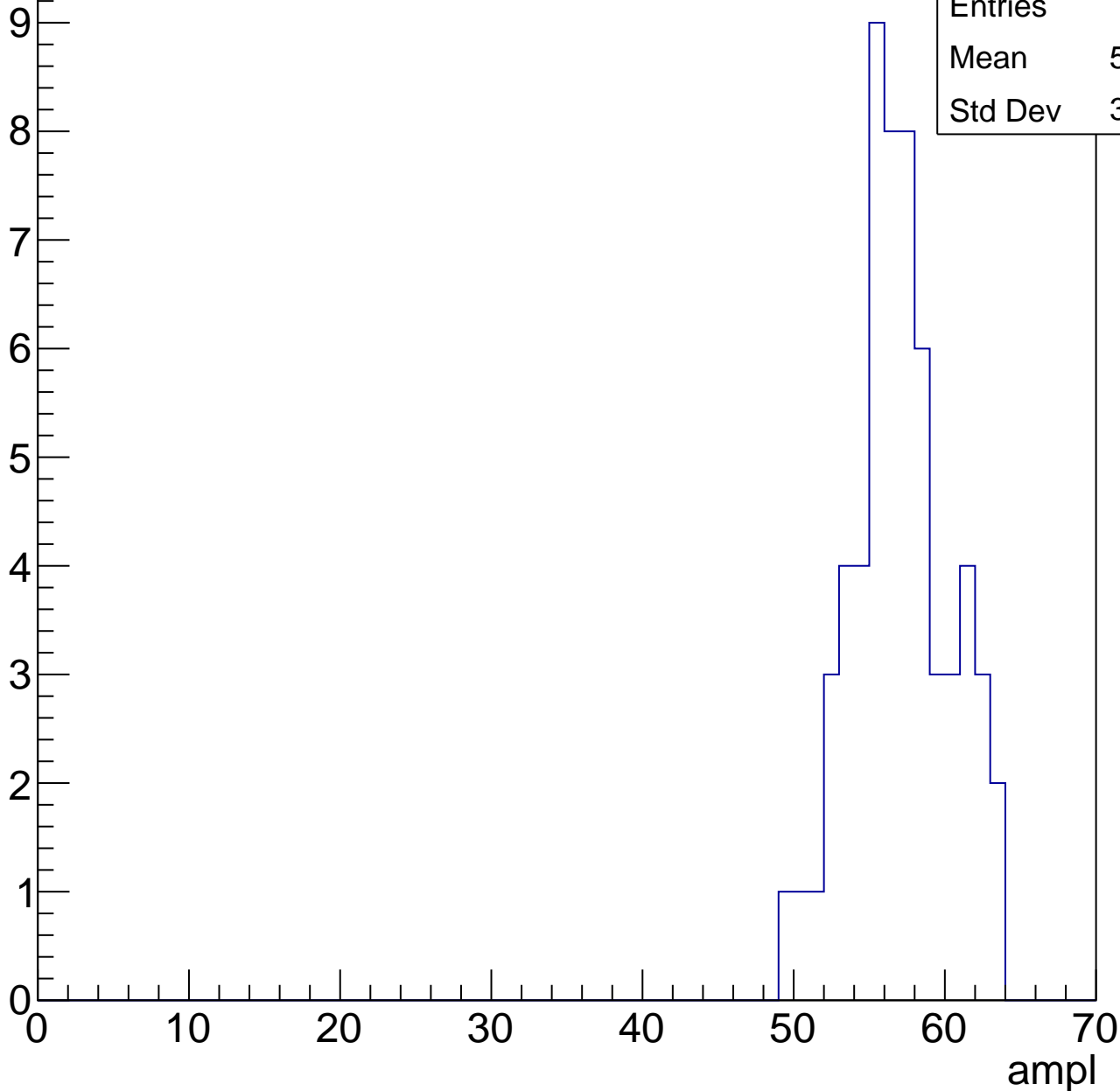


B1L003S, U20-ch111, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	56.57
Std Dev	3.196

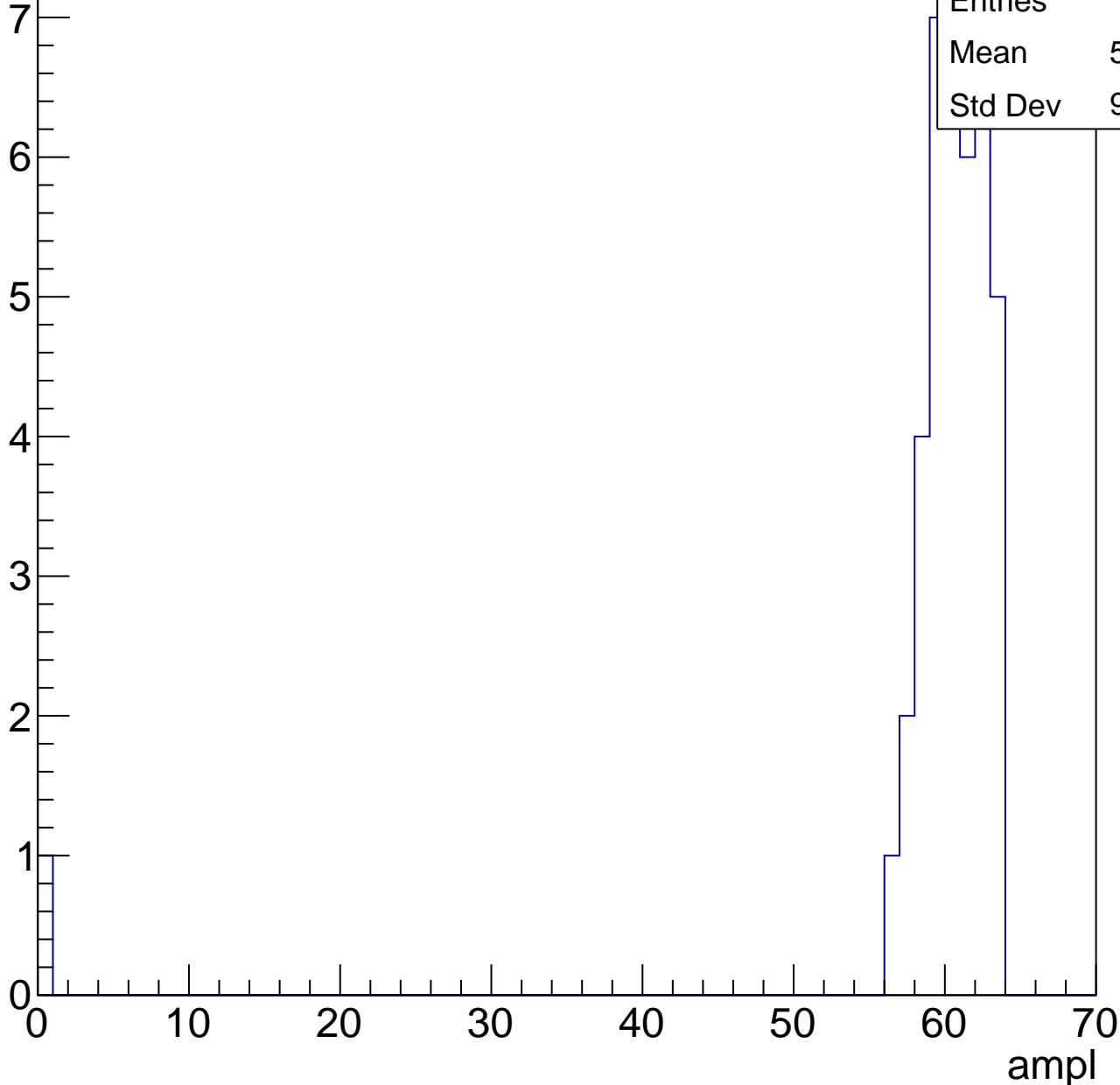


B1L003S, U20-ch111, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	58.75
Std Dev	9.583



B1L003S, U20-ch111, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U20-ch111, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch112, adc0

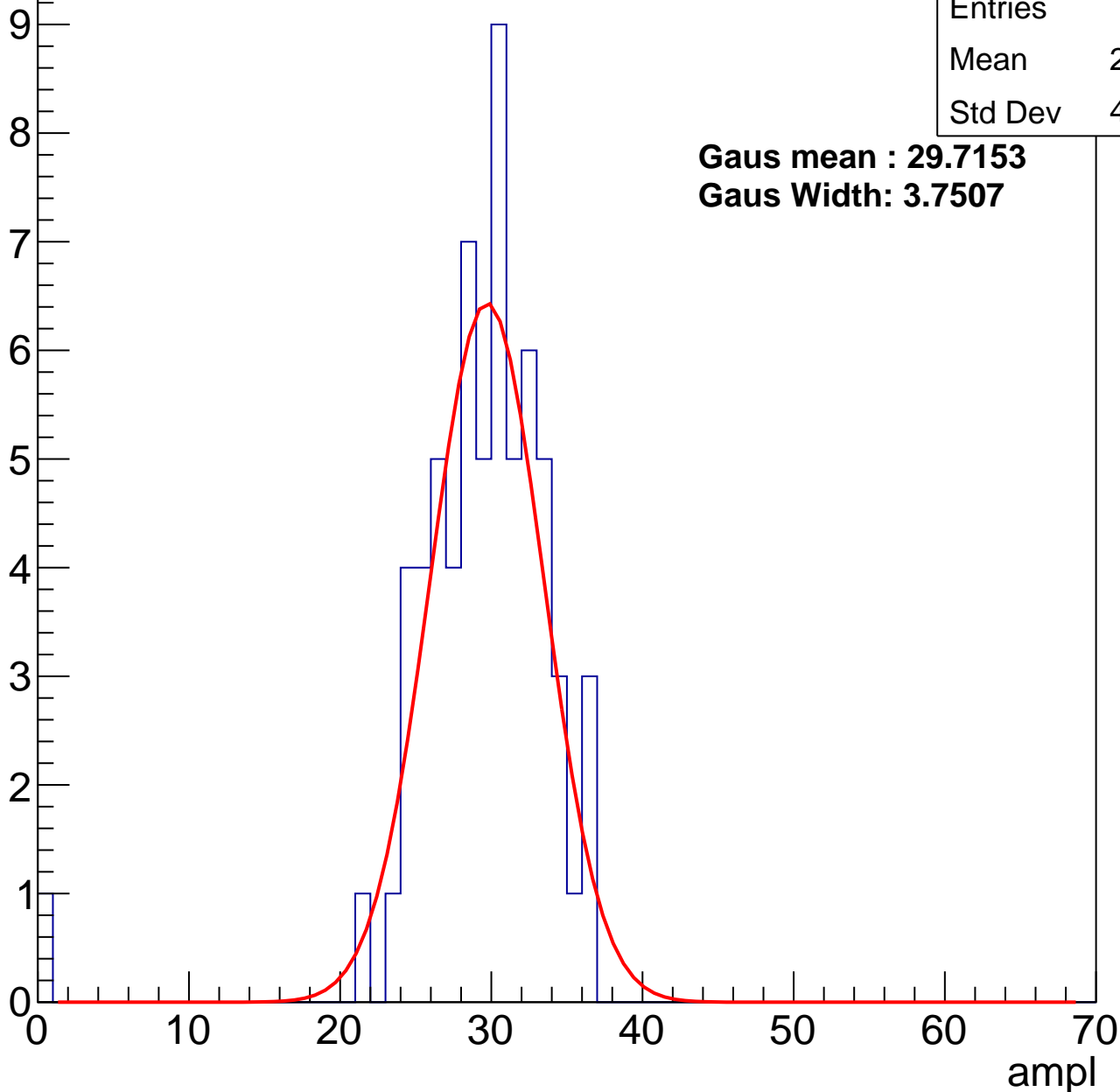
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.84
Std Dev	4.988

Gaus mean : 29.7153

Gaus Width: 3.7507



B1L003S, U20-ch112, adc1

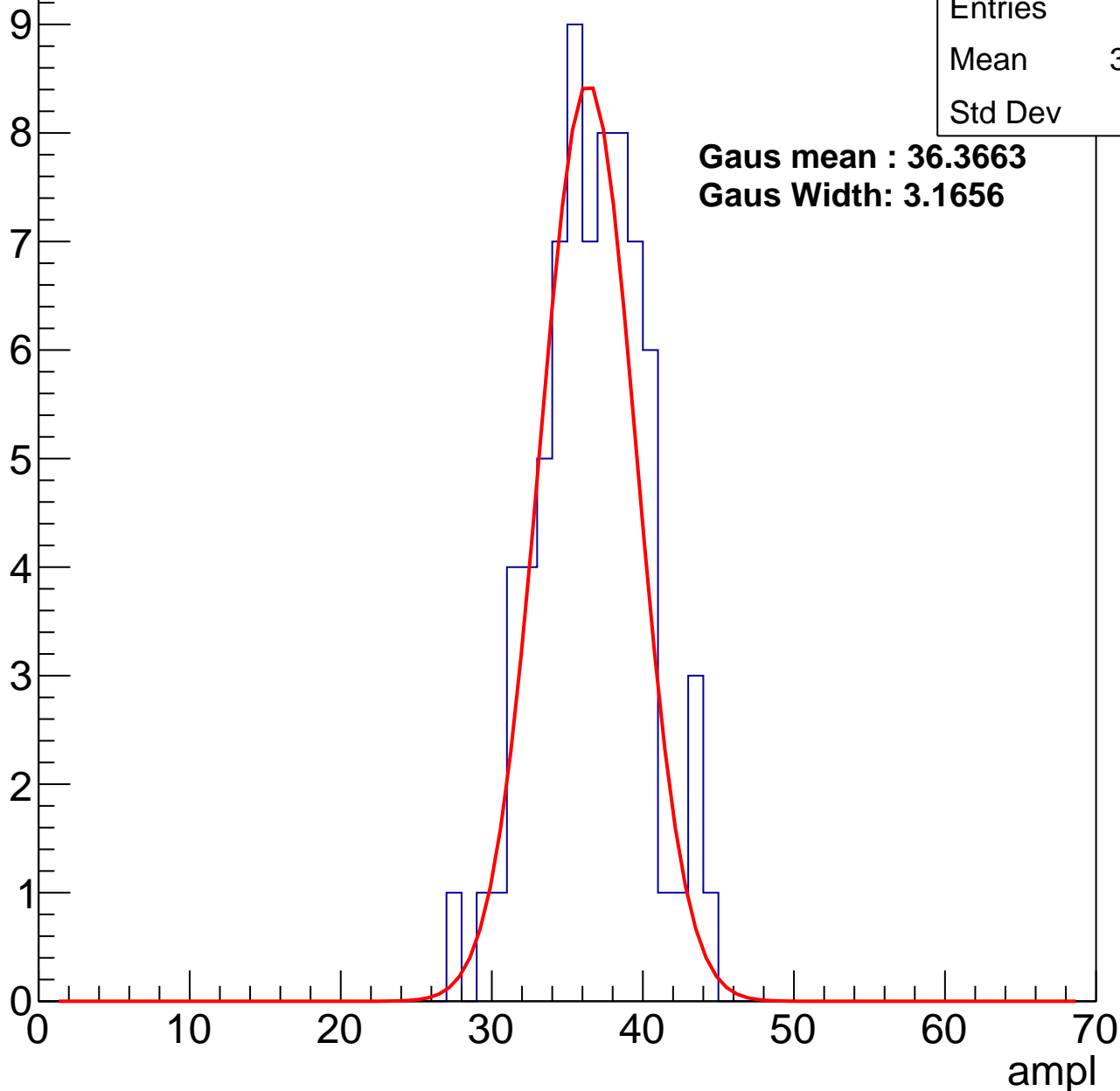
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	36.18
Std Dev	3.45

Gaus mean : 36.3663

Gaus Width: 3.1656



B1L003S, U20-ch112, adc2

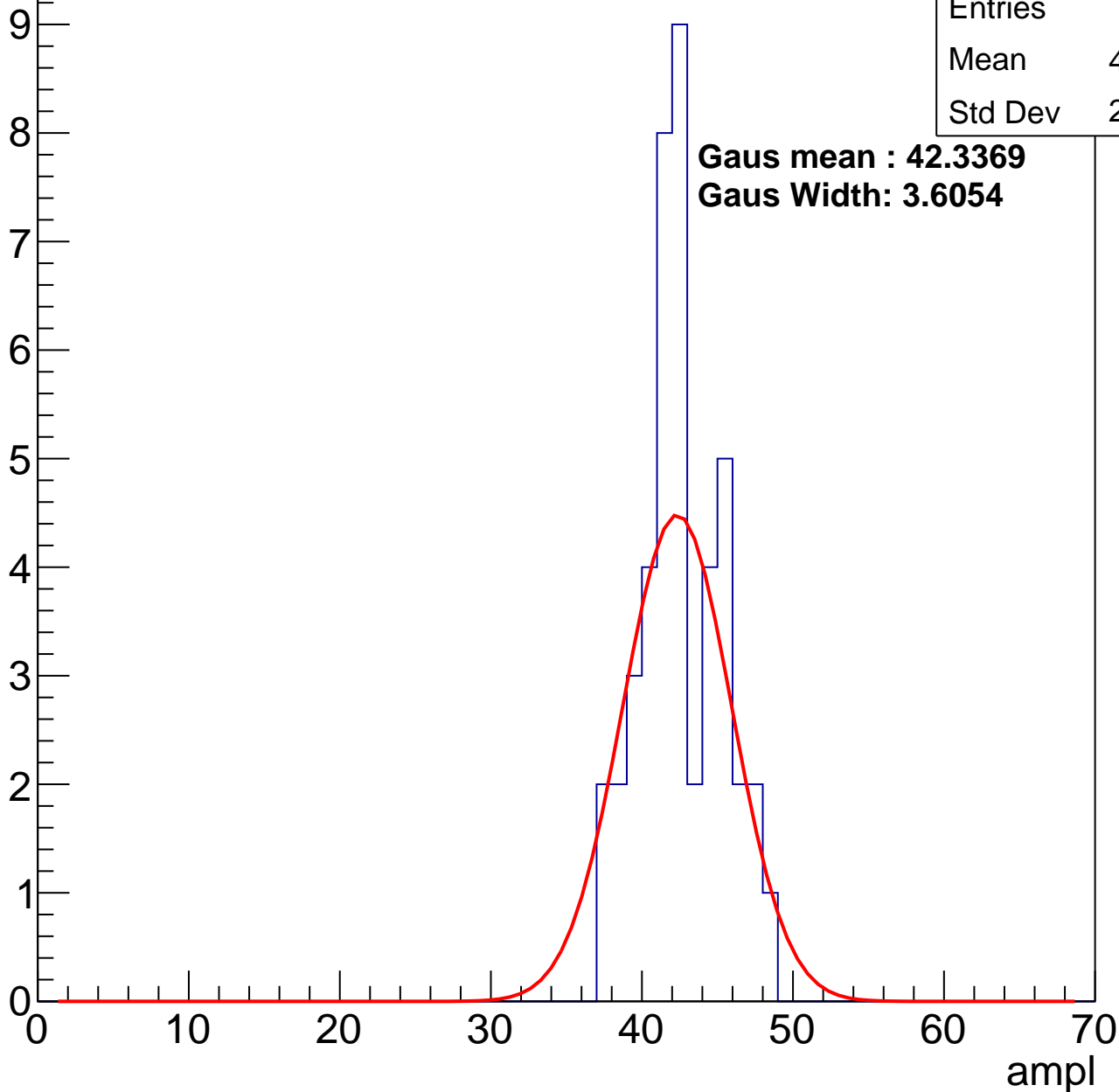
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	42.14
Std Dev	2.668

Gaus mean : 42.3369

Gaus Width: 3.6054

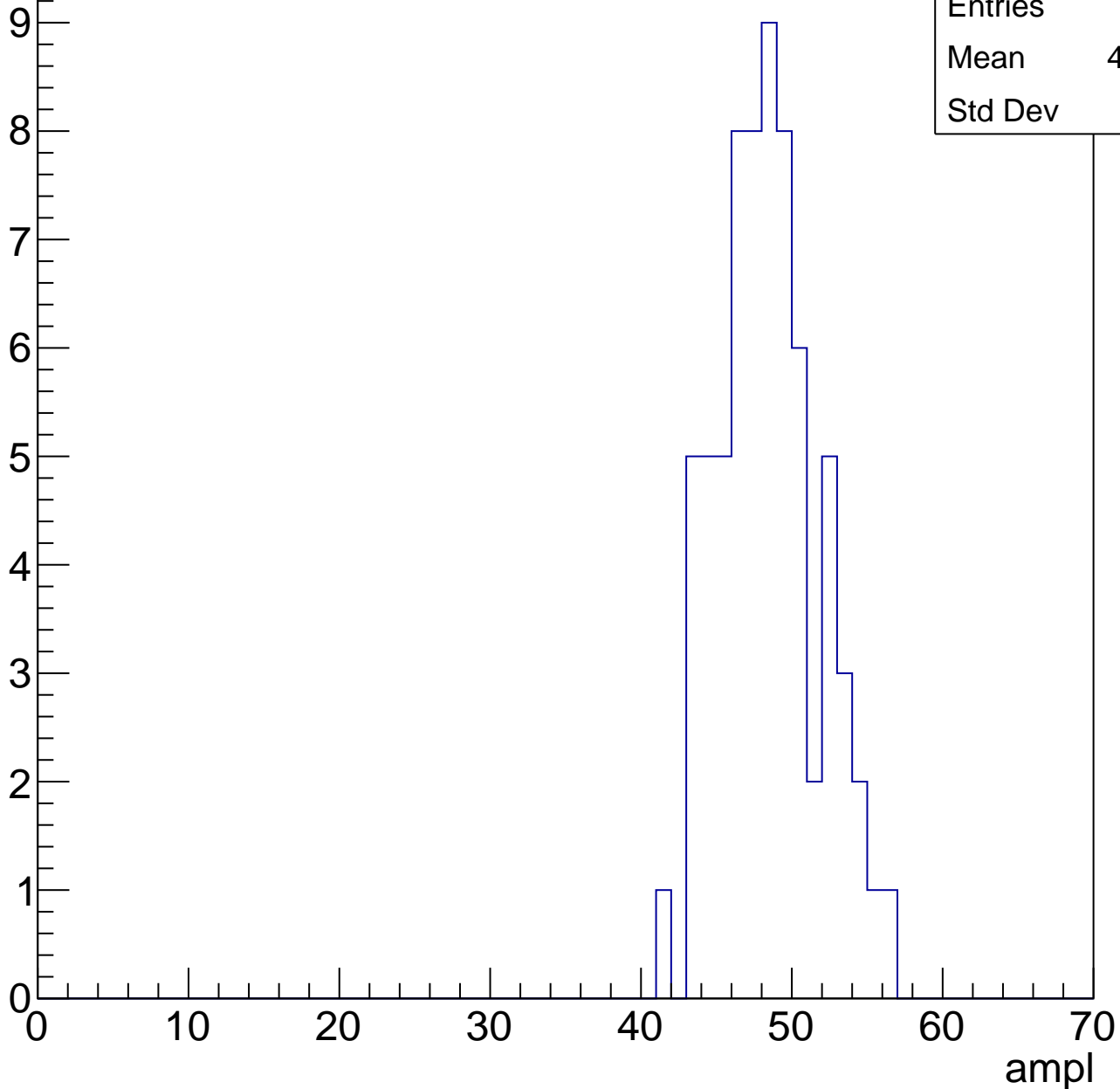


B1L003S, U20-ch112, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	47.96
Std Dev	3.25

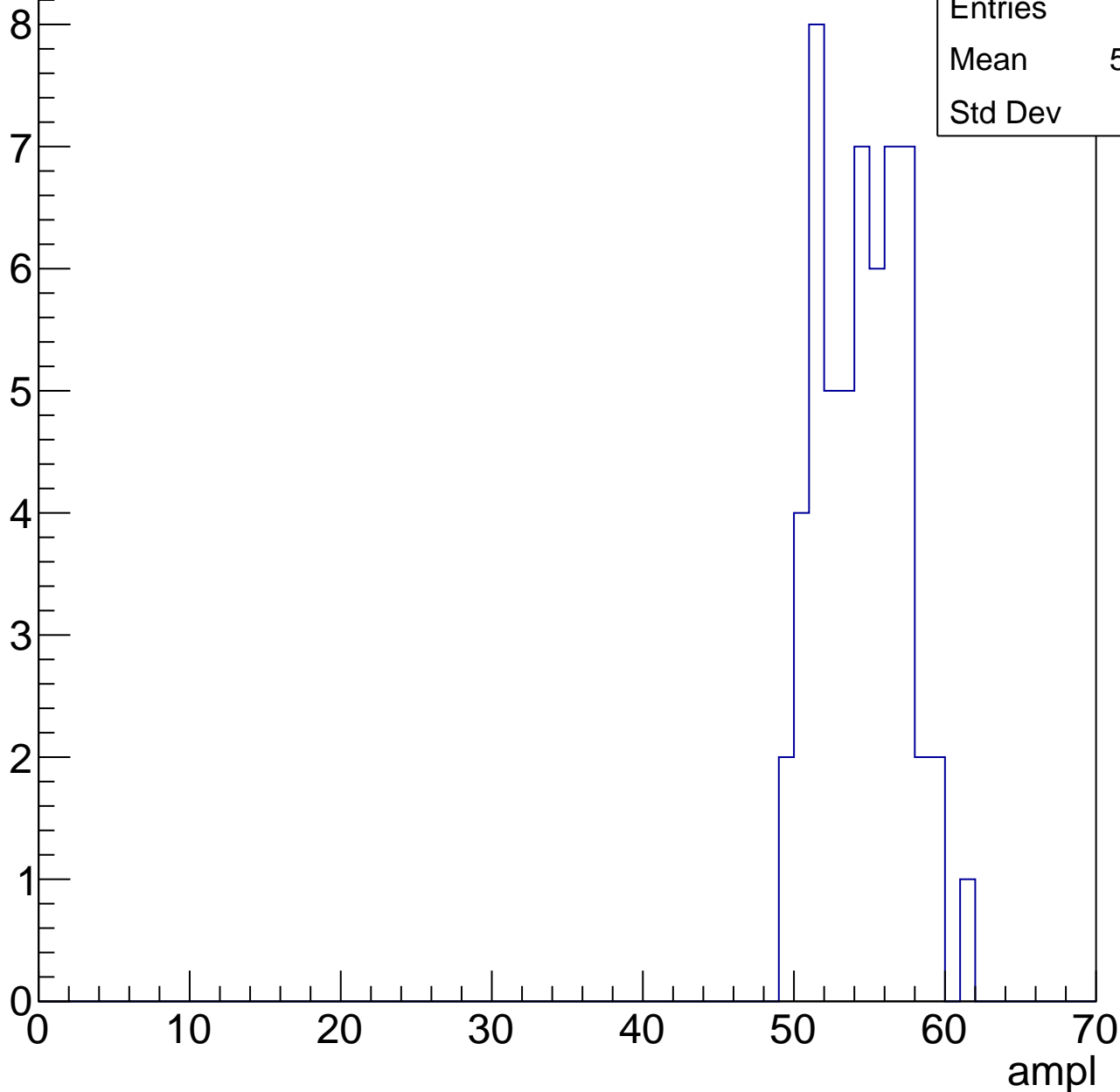


B1L003S, U20-ch112, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.02
Std Dev	2.8

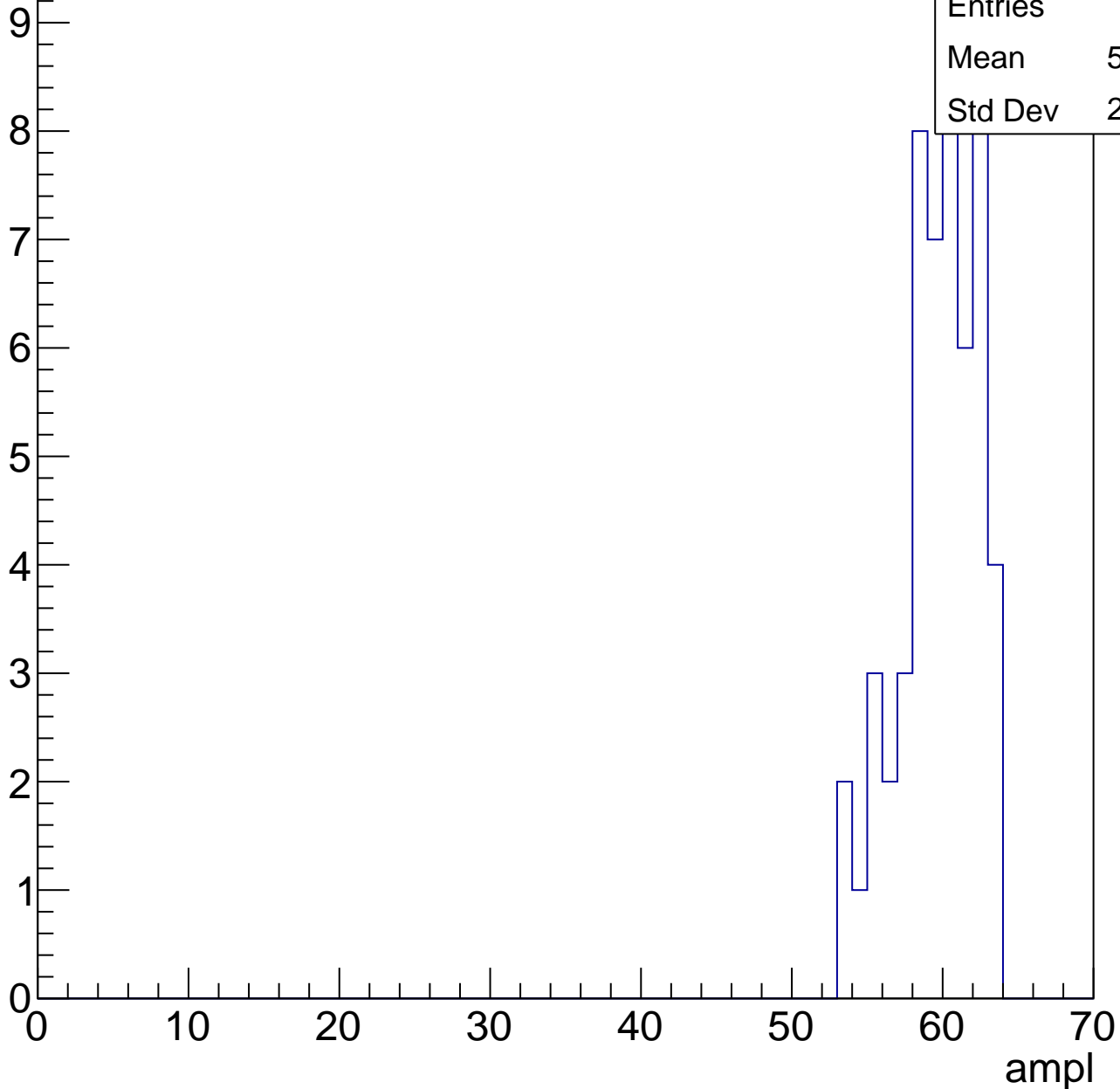


B1L003S, U20-ch112, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.23
Std Dev	2.567

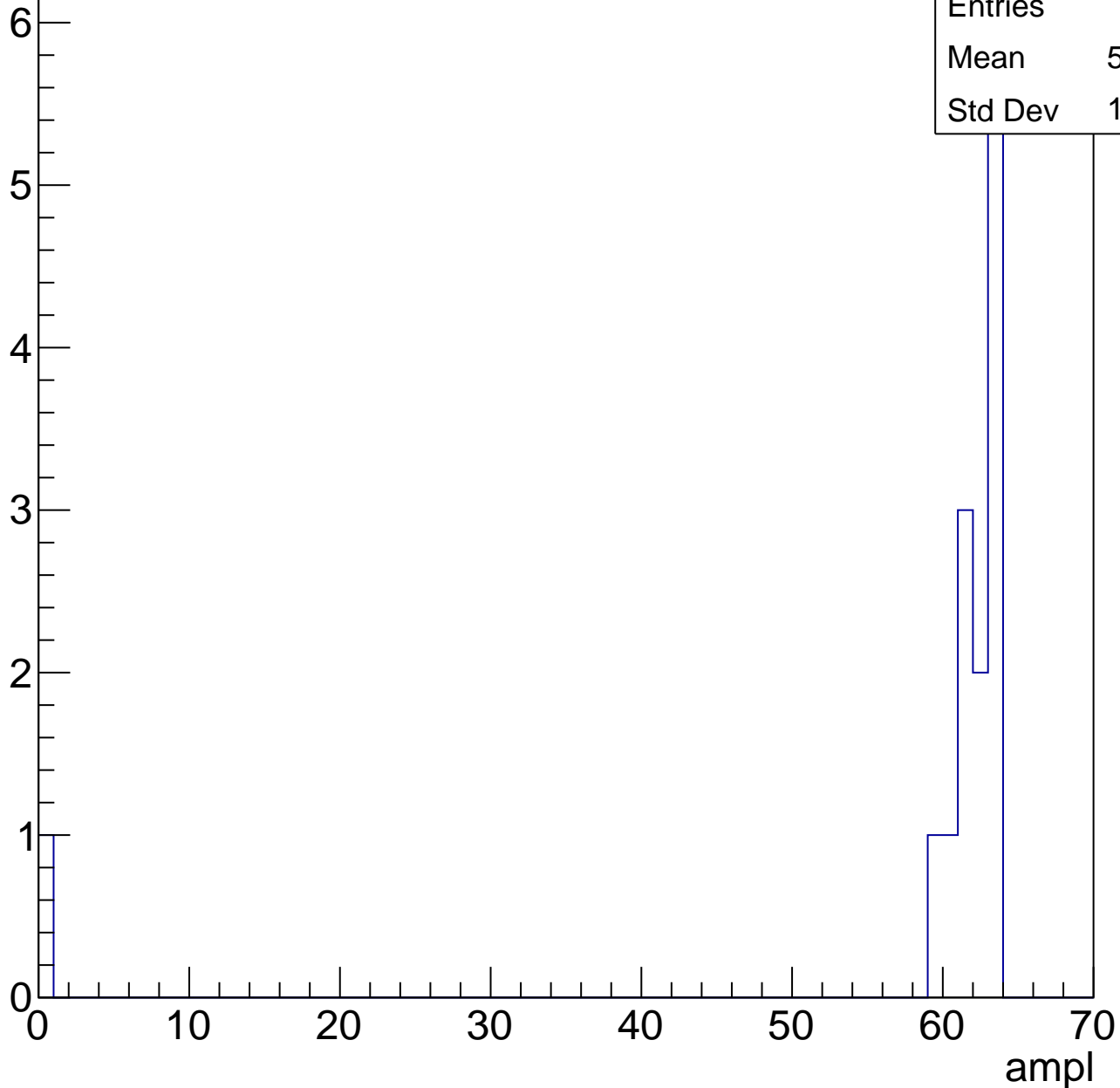


B1L003S, U20-ch112, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	57.43
Std Dev	15.98



B1L003S, U20-ch112, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U20-ch113, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	30.17
Std Dev	3.748

Gaus mean : 30.8230

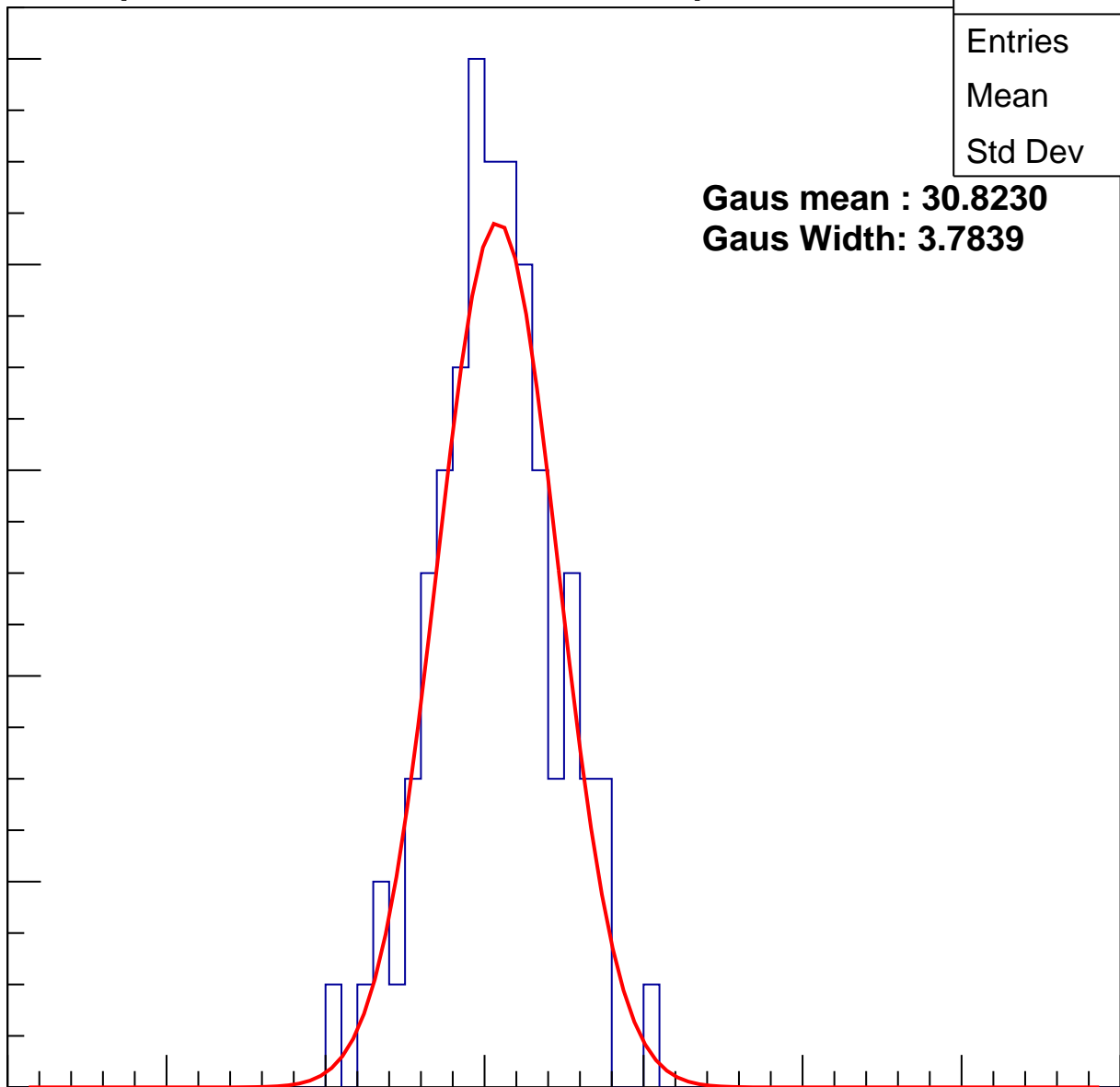
Gaus Width: 3.7839

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch113, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	37.77
Std Dev	3.348

Gaus mean : 38.1006

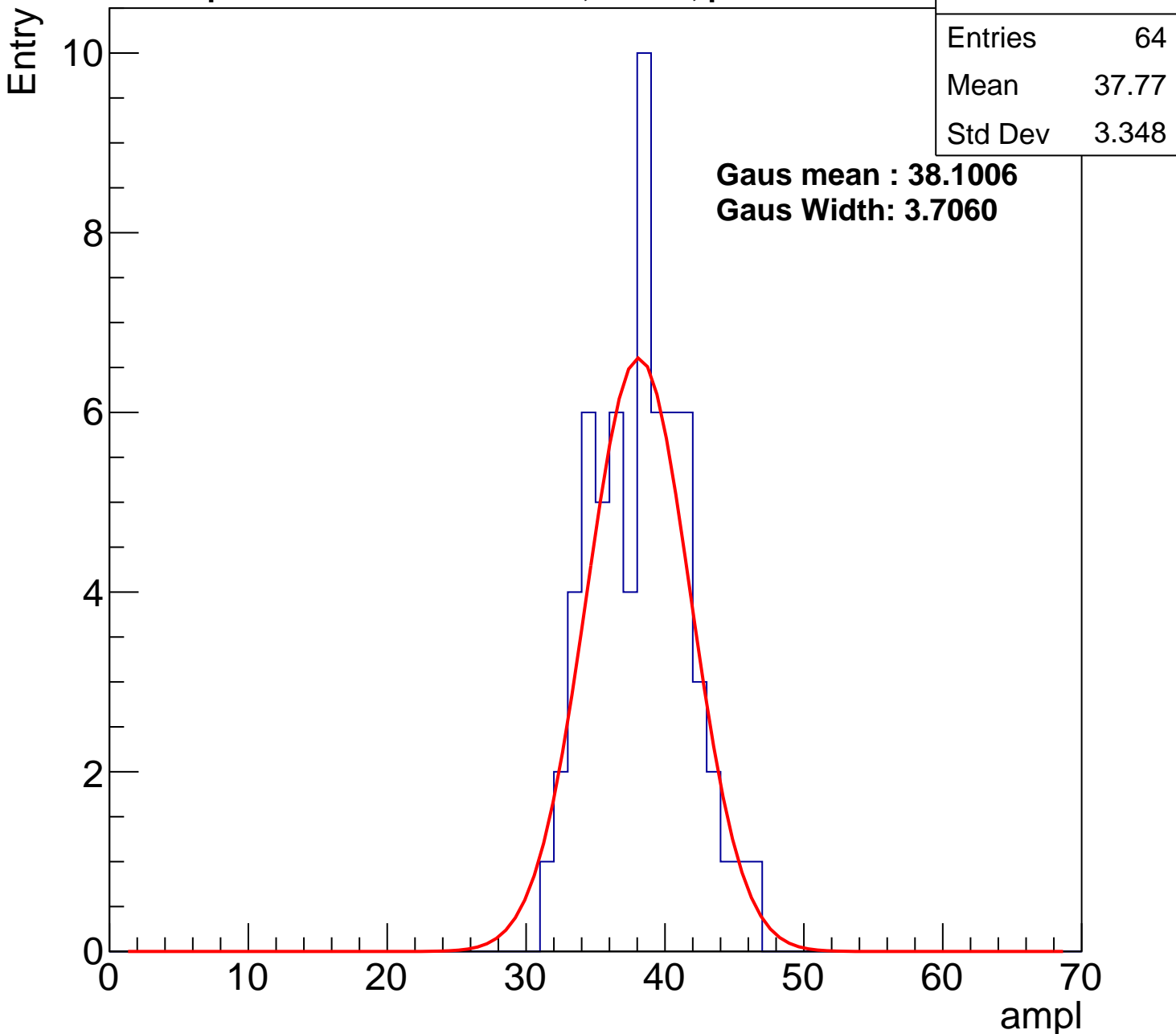
Gaus Width: 3.7060

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch113, adc2

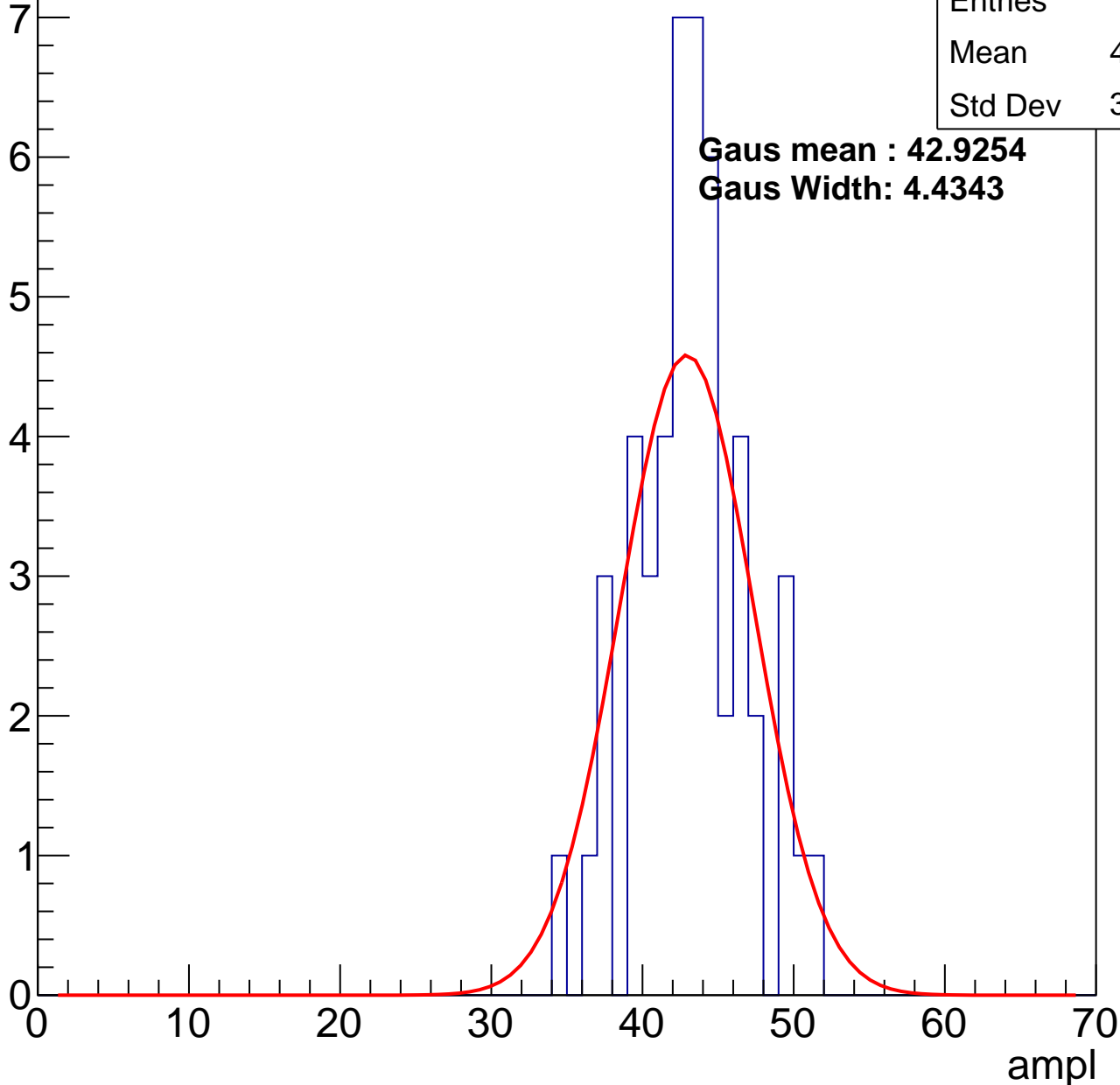
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.78
Std Dev	3.649

Gaus mean : 42.9254

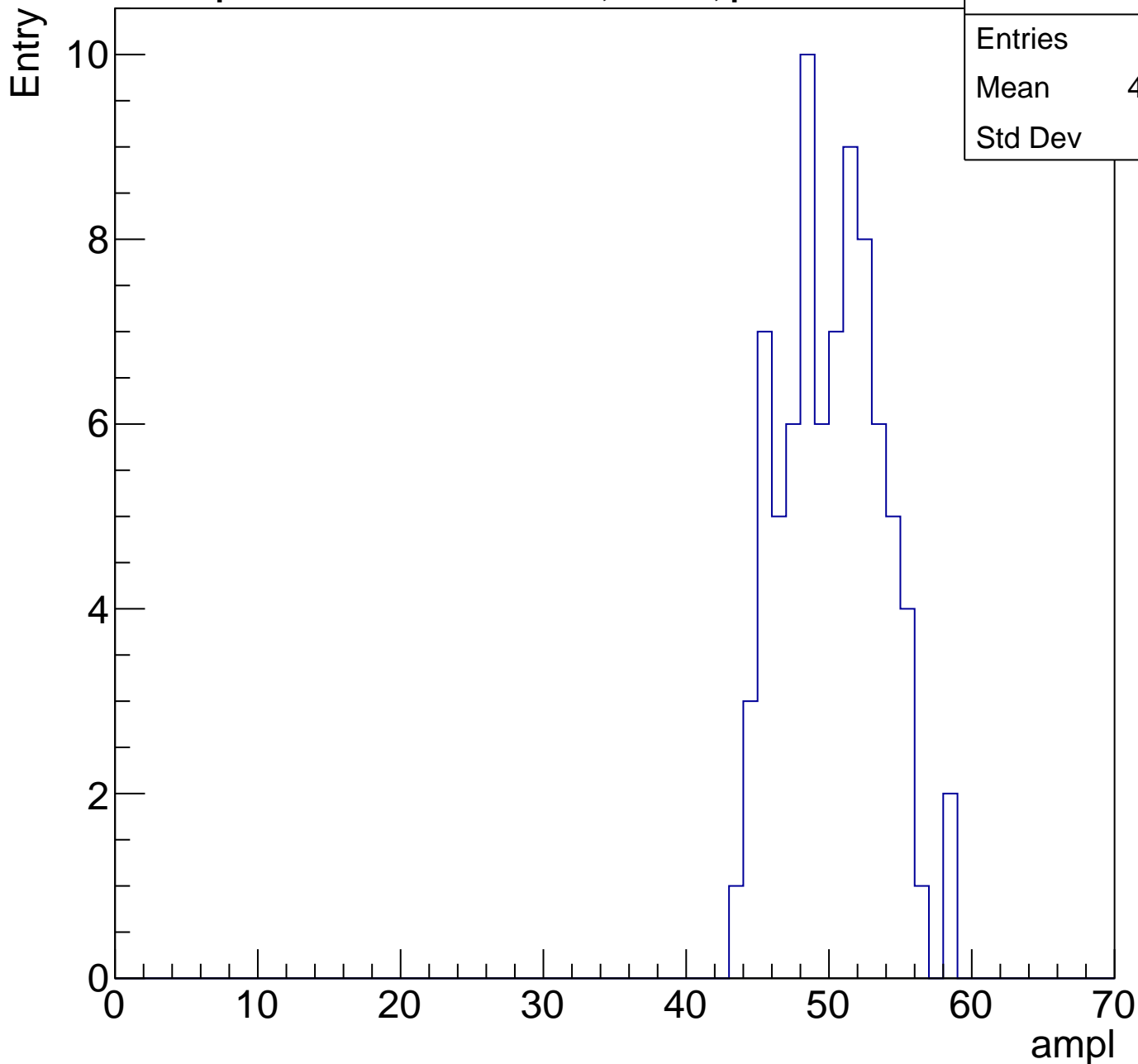
Gaus Width: 4.4343



B1L003S, U20-ch113, adc3

calib_packv5_042523_0143.root, FC#13, port D2

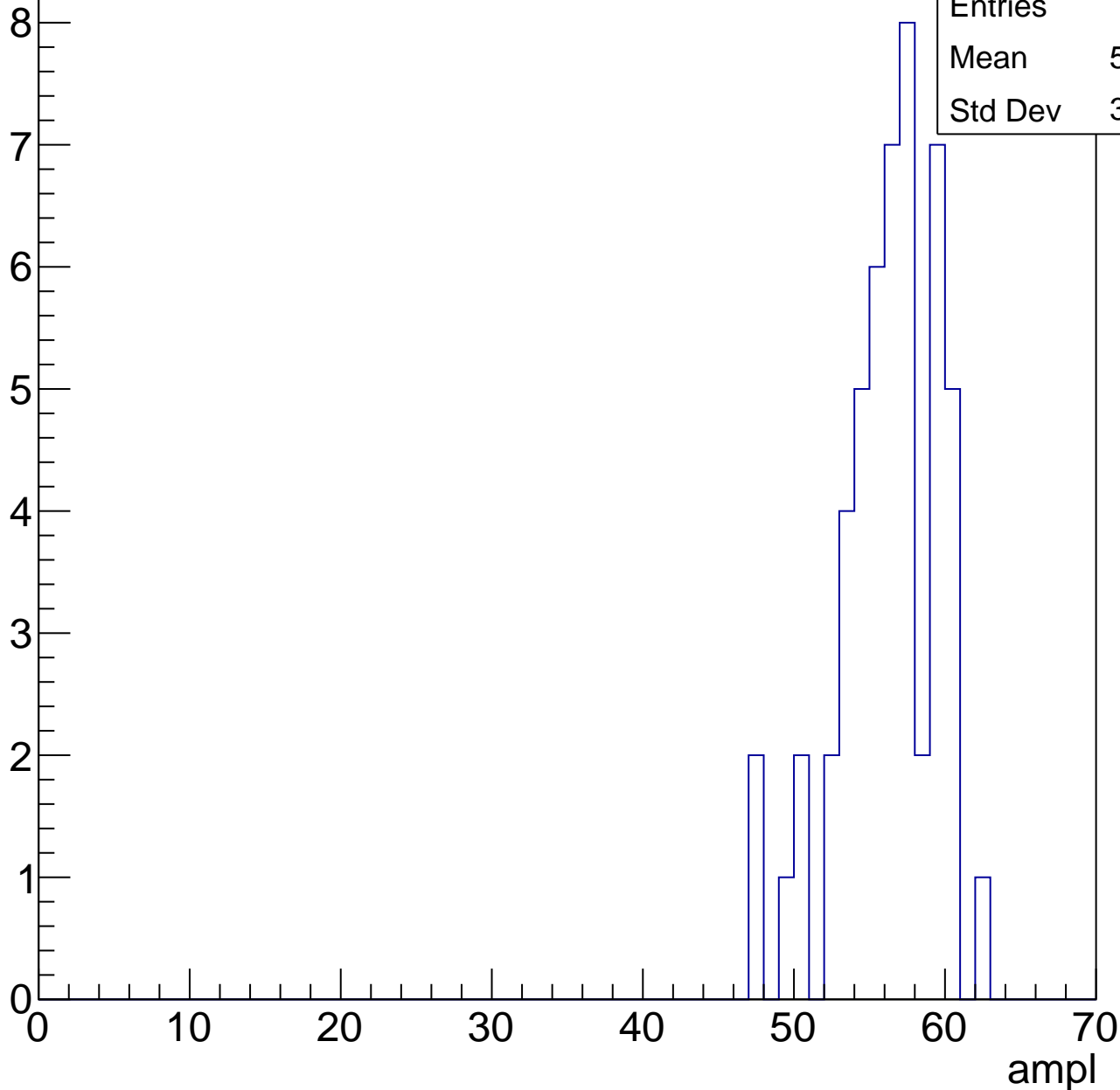
Entries	80
Mean	49.76
Std Dev	3.44



B1L003S, U20-ch113, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

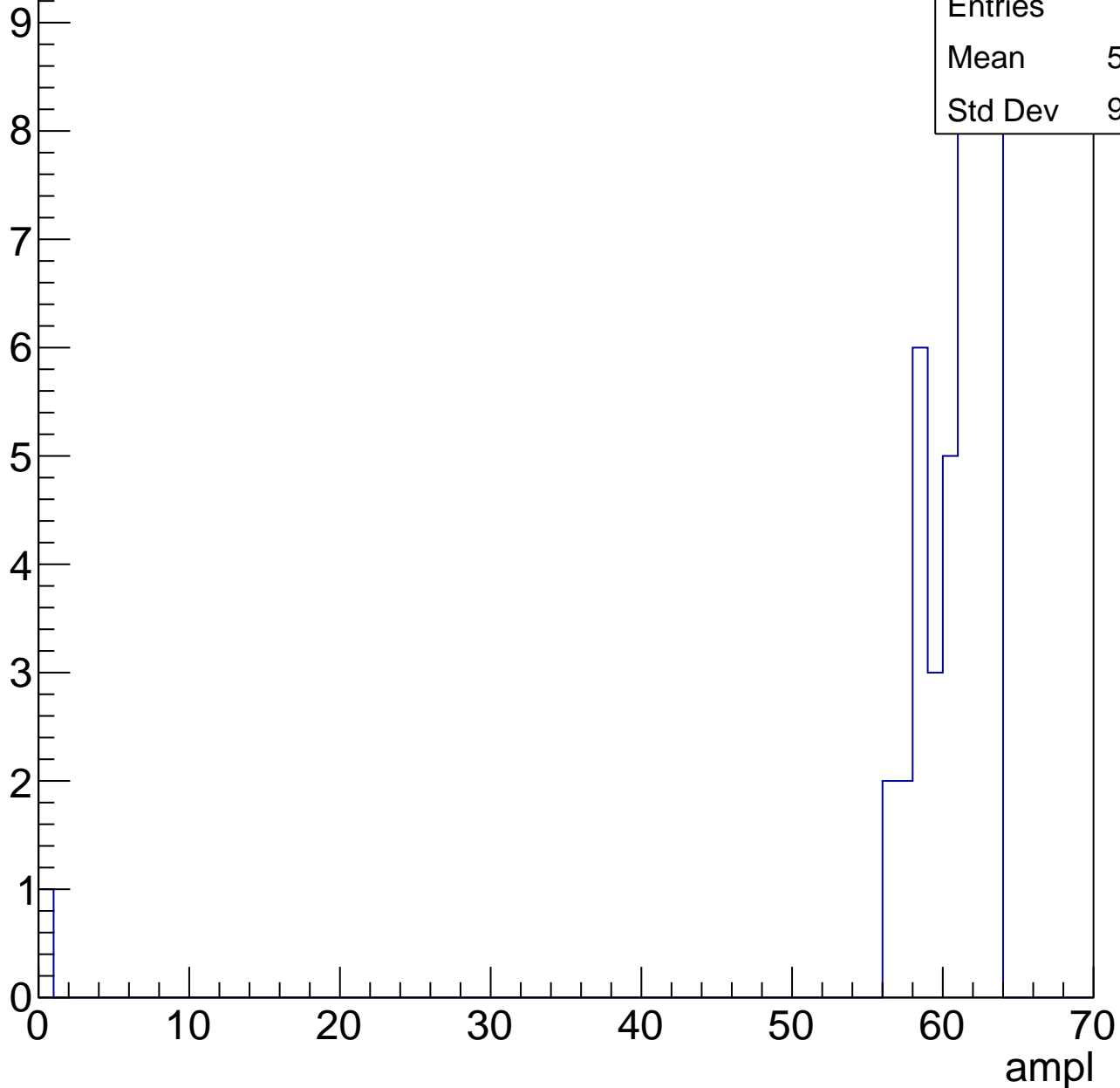


B1L003S, U20-ch113, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.16
Std Dev	9.143



B1L003S, U20-ch113, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch113, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch114, adc0

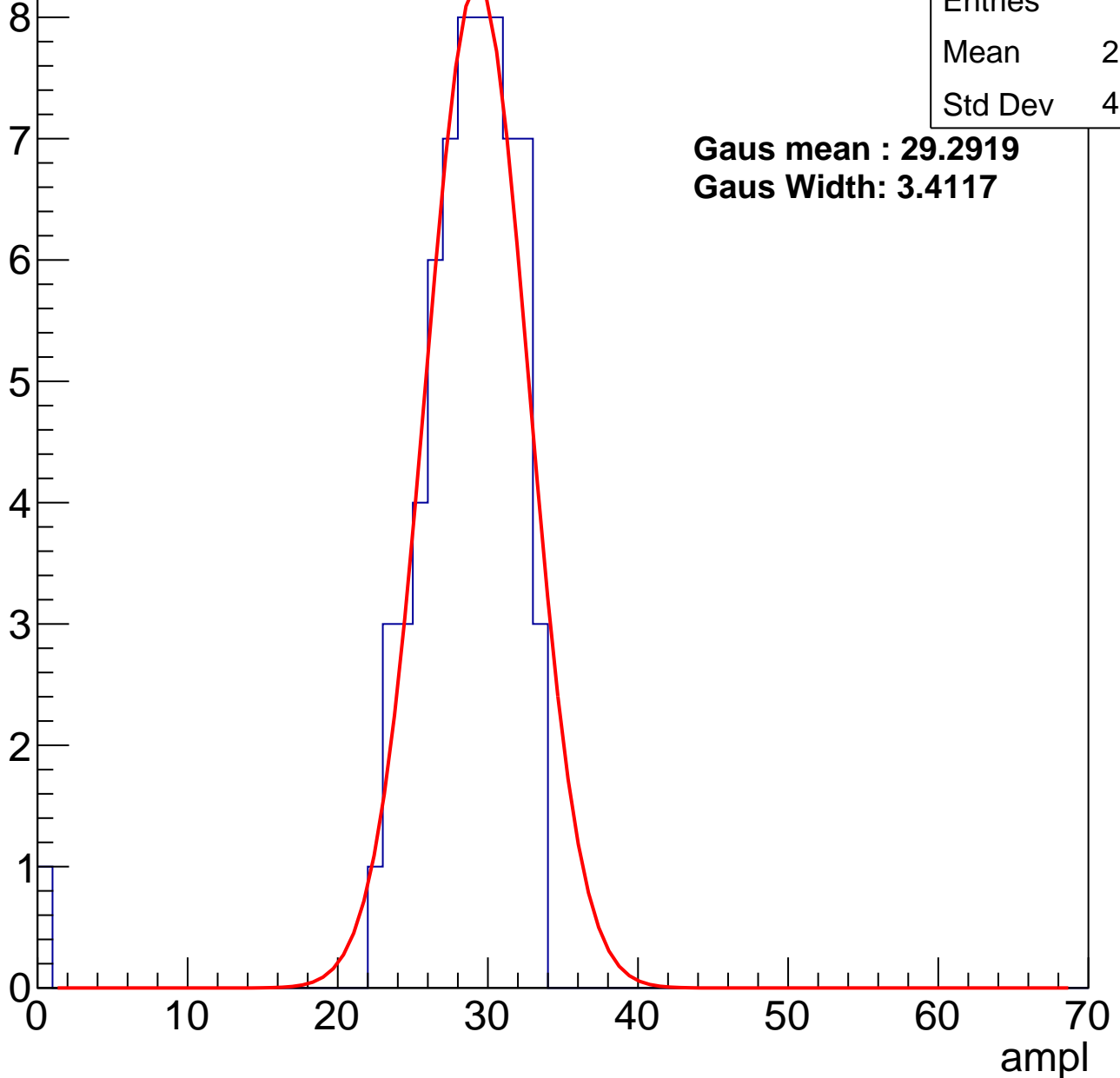
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.94
Std Dev	4.438

Gaus mean : 29.2919

Gaus Width: 3.4117



B1L003S, U20-ch114, adc1

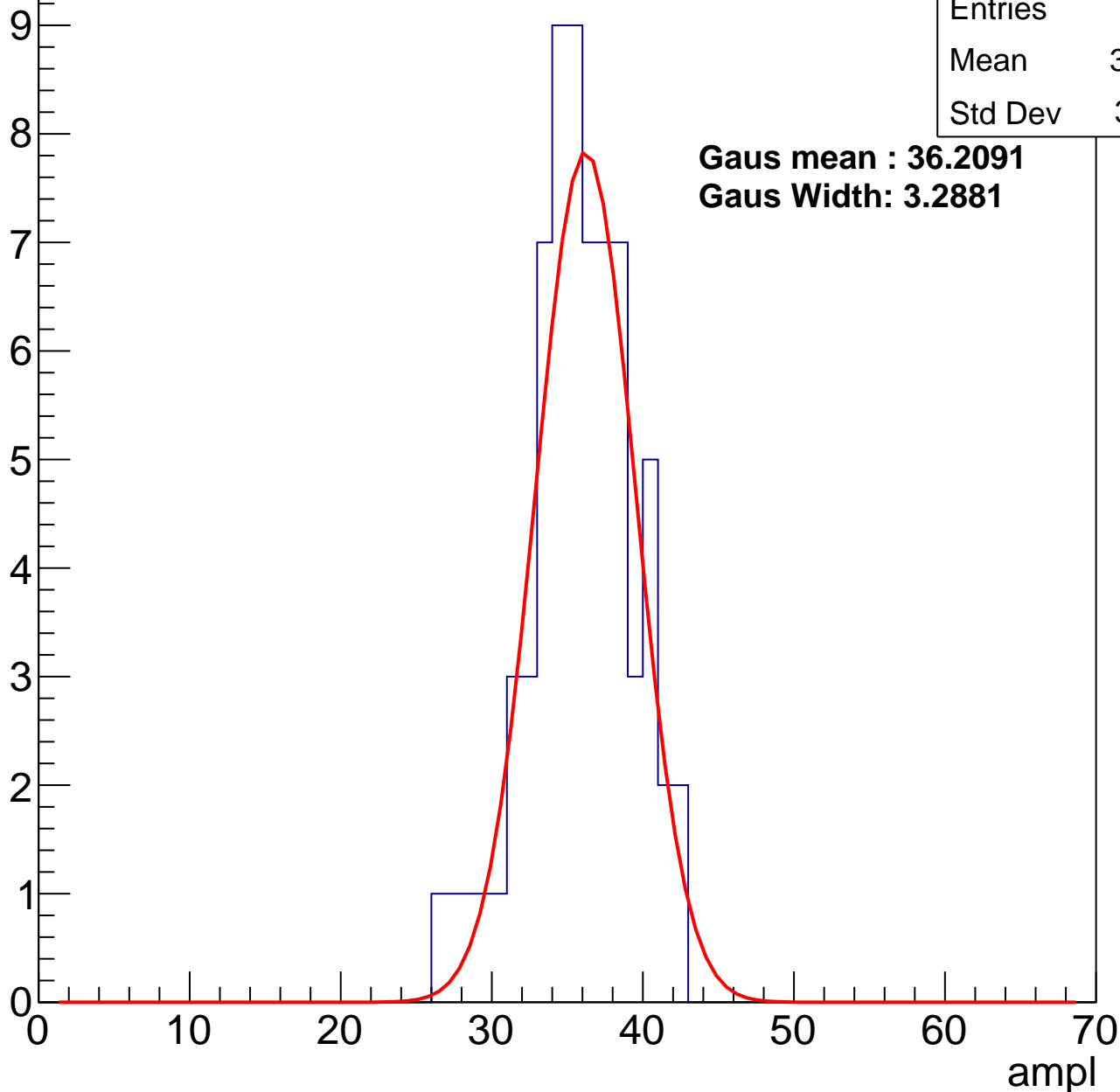
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.38
Std Dev	3.401

Gaus mean : 36.2091

Gaus Width: 3.2881



B1L003S, U20-ch114, adc2

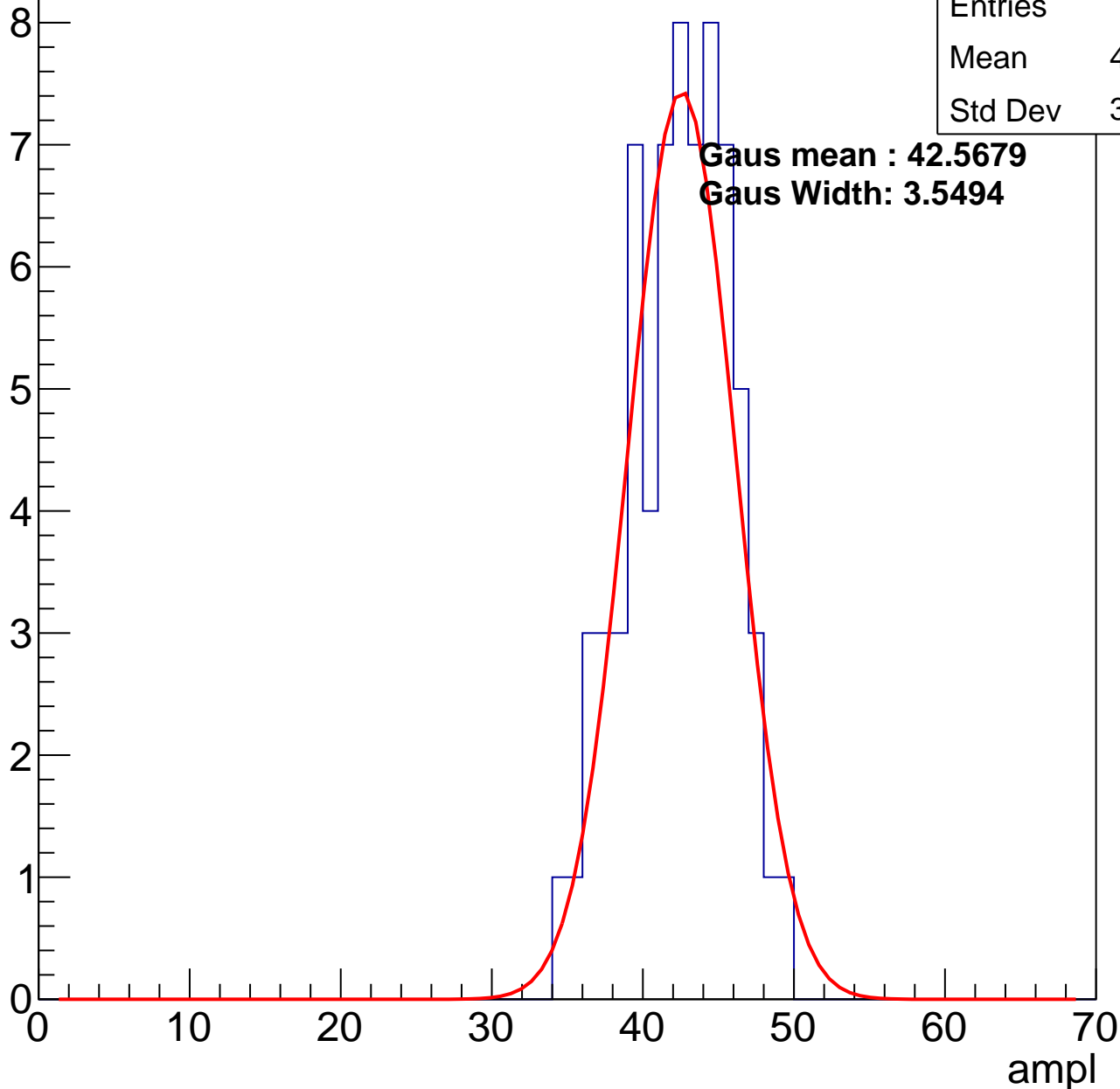
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.94
Std Dev	3.344

Gaus mean : 42.5679

Gaus Width: 3.5494

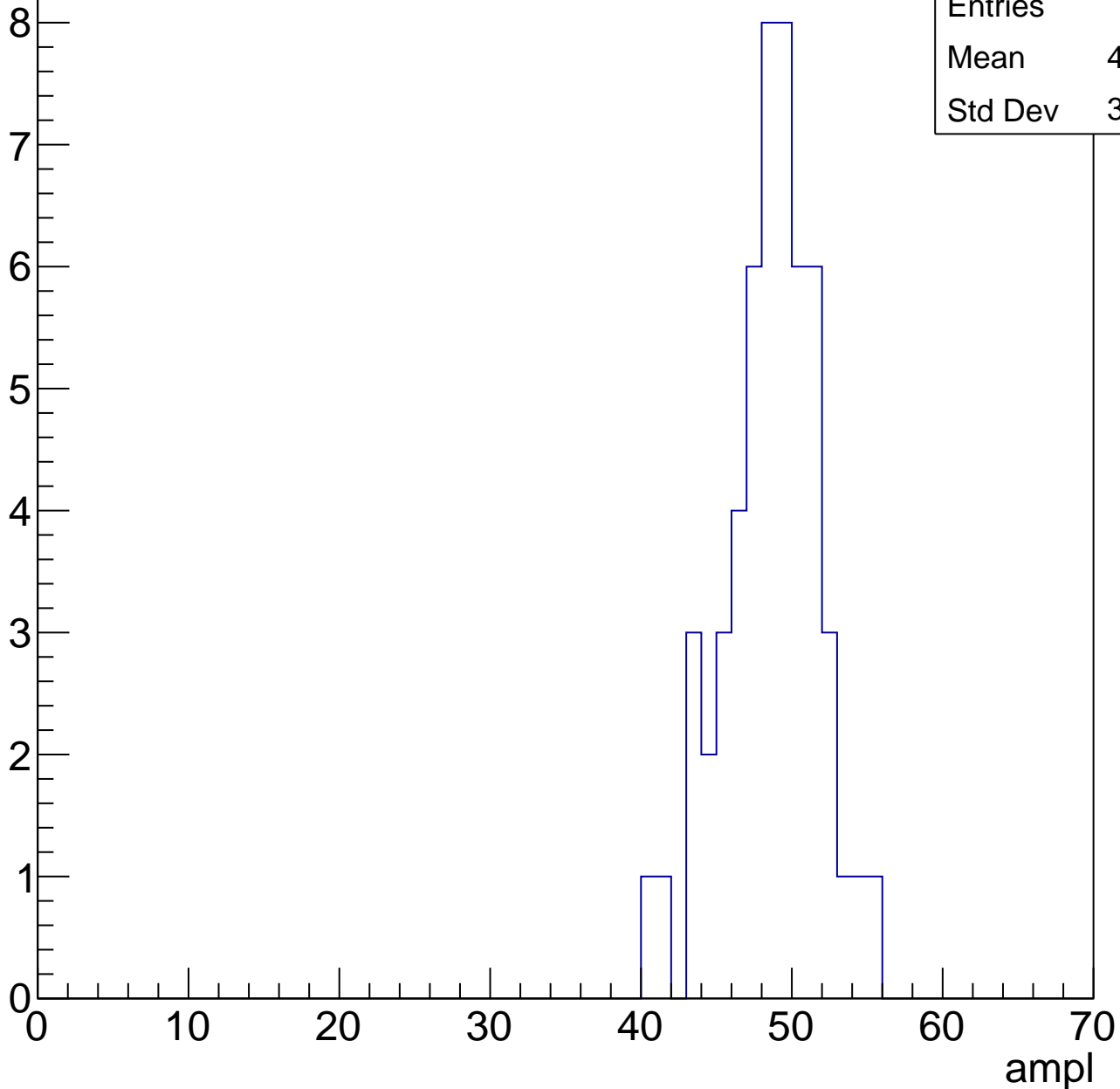


B1L003S, U20-ch114, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.13
Std Dev	3.079

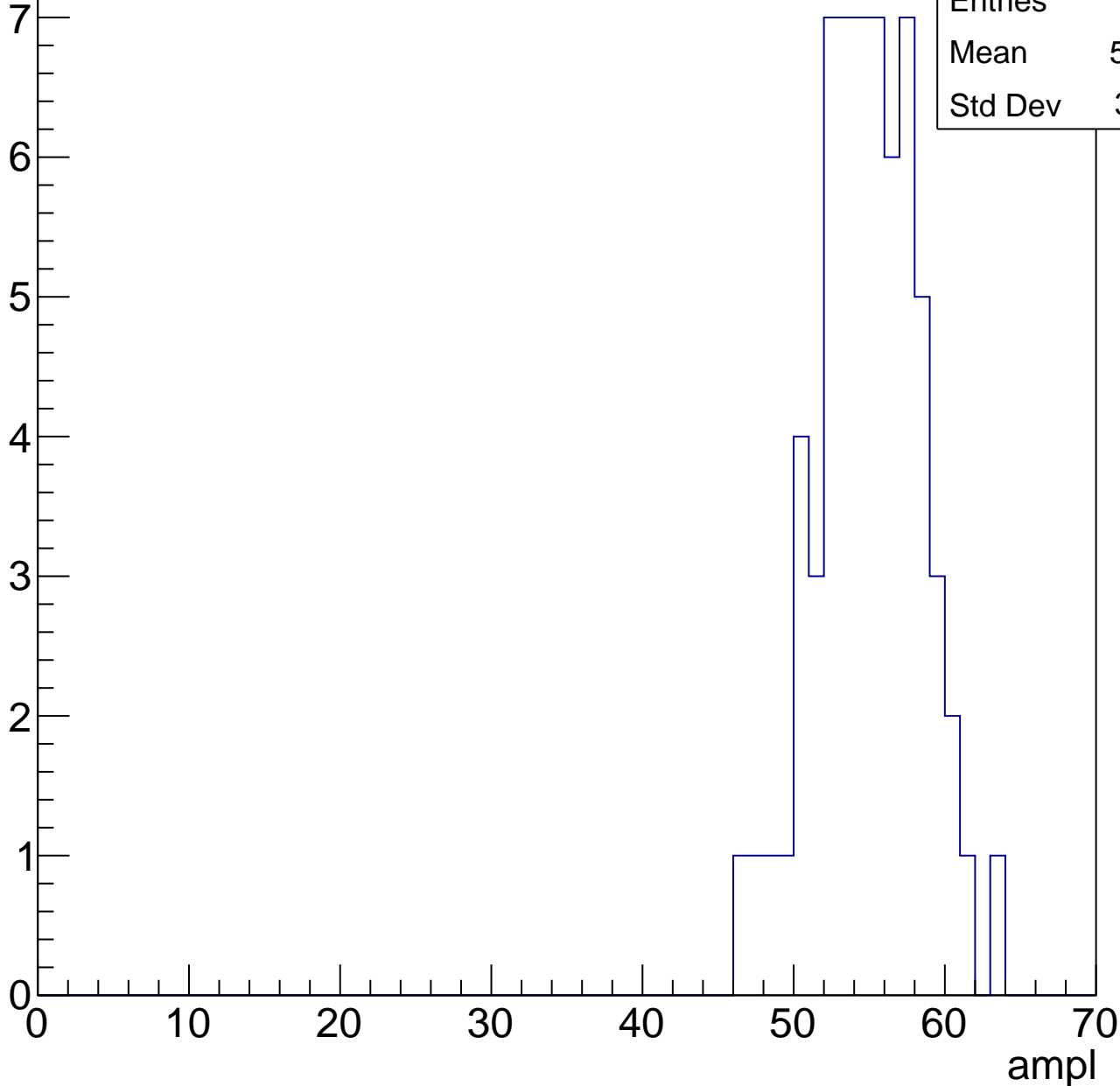


B1L003S, U20-ch114, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	54.48
Std Dev	3.391

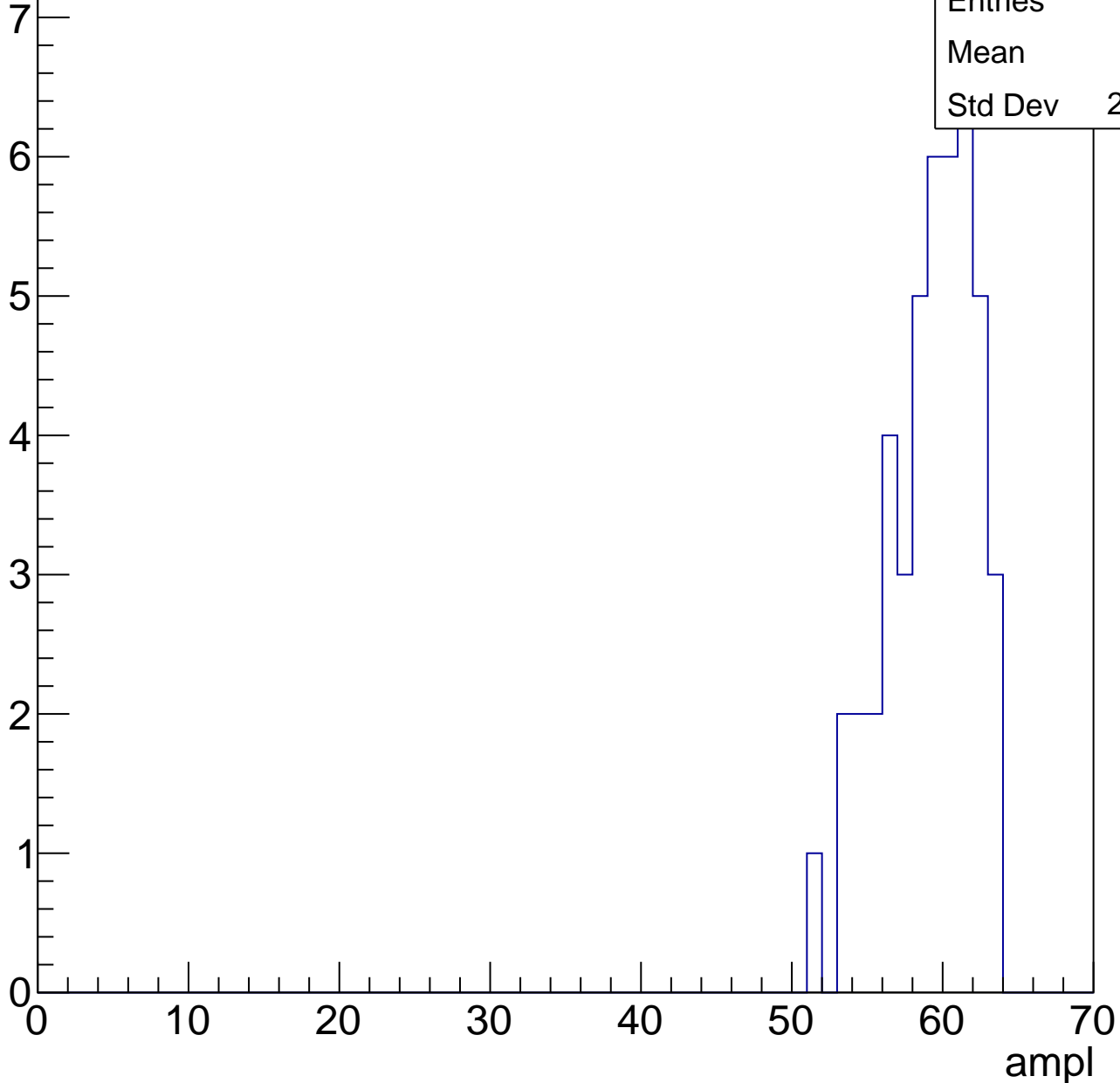


B1L003S, U20-ch114, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.7
Std Dev	2.926

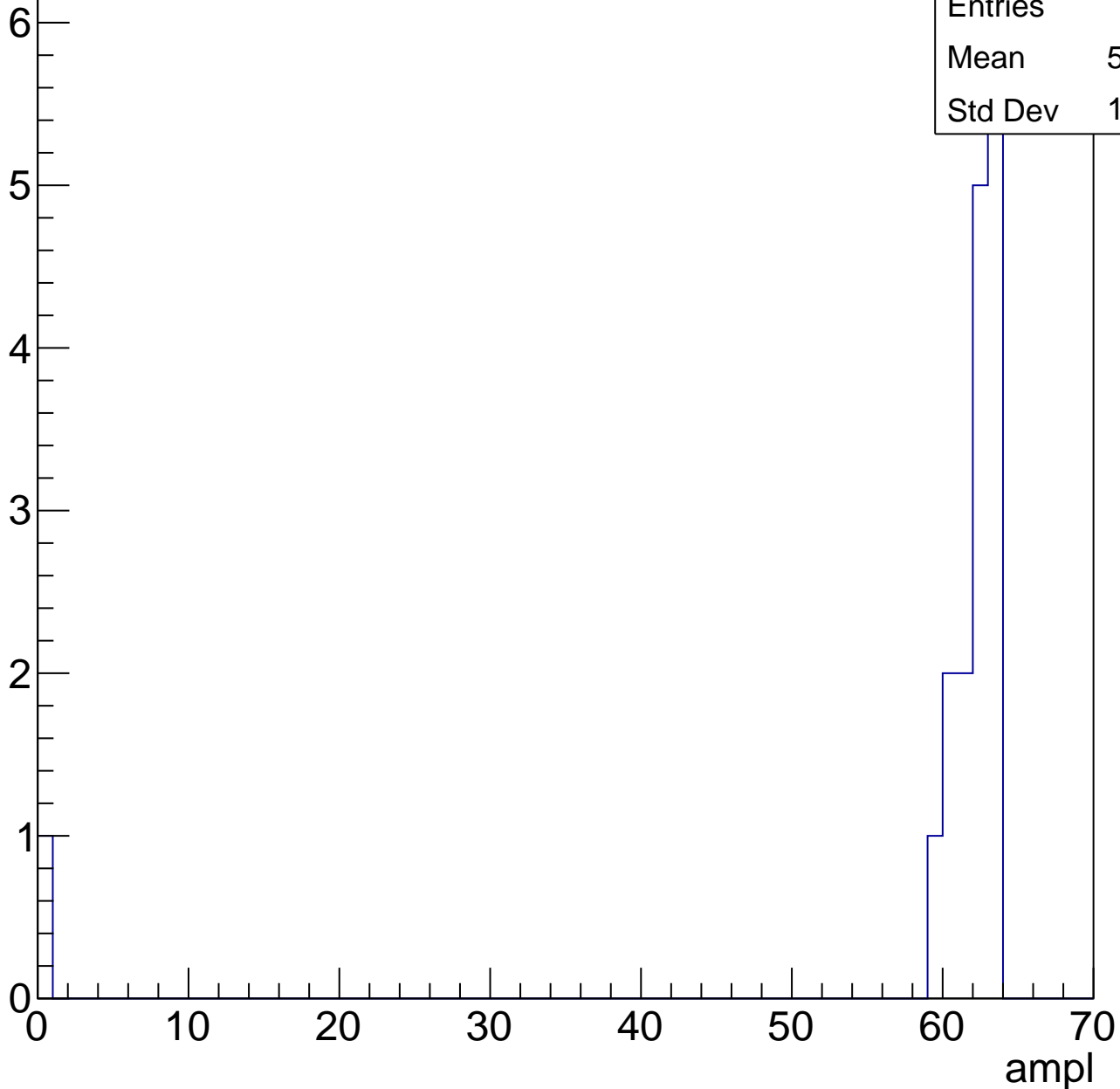


B1L003S, U20-ch114, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

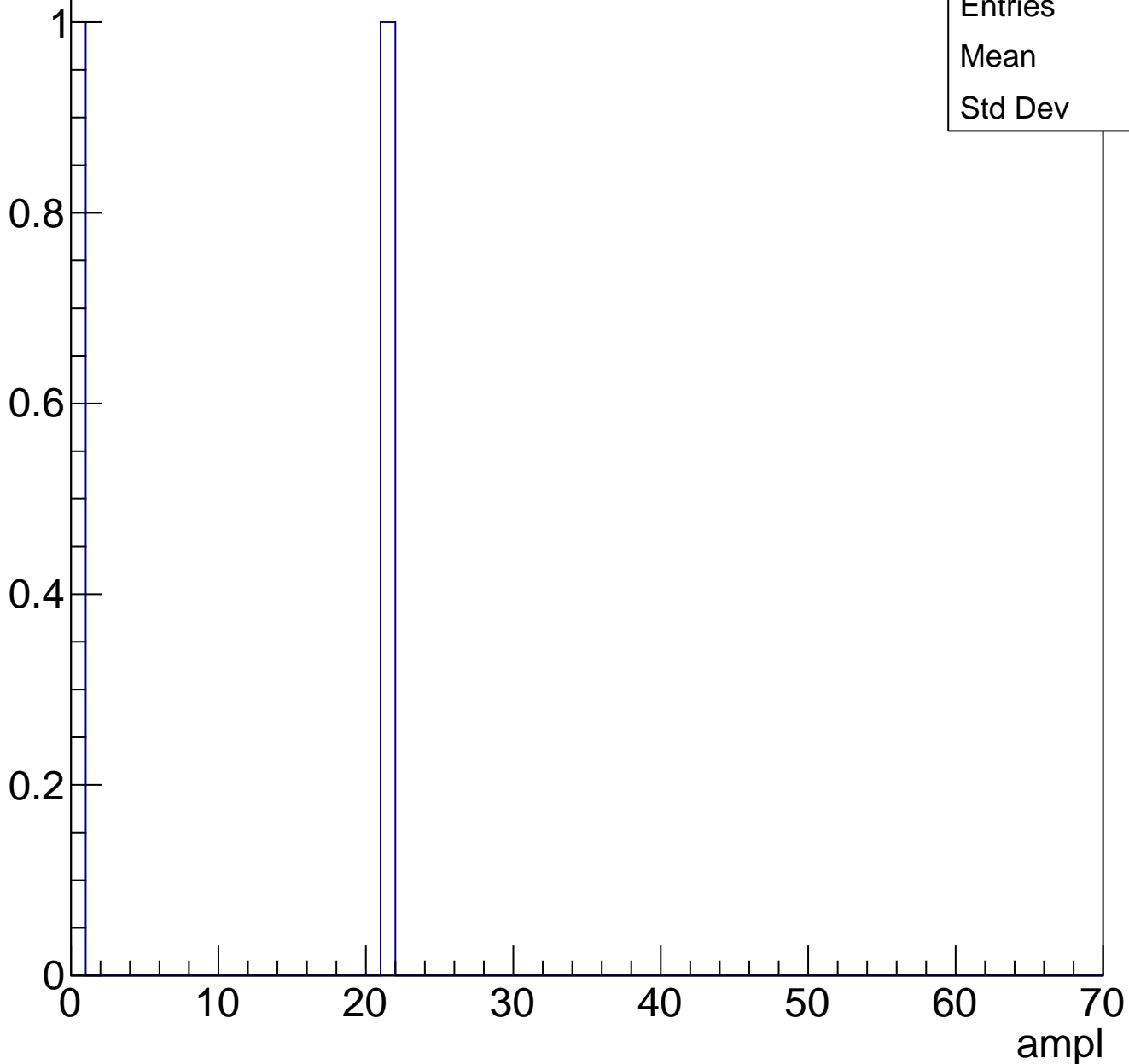
Entries	17
Mean	58.18
Std Dev	14.59



B1L003S, U20-ch114, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L003S, U20-ch115, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	28.34
Std Dev	7.008

Gaus mean : 30.3565

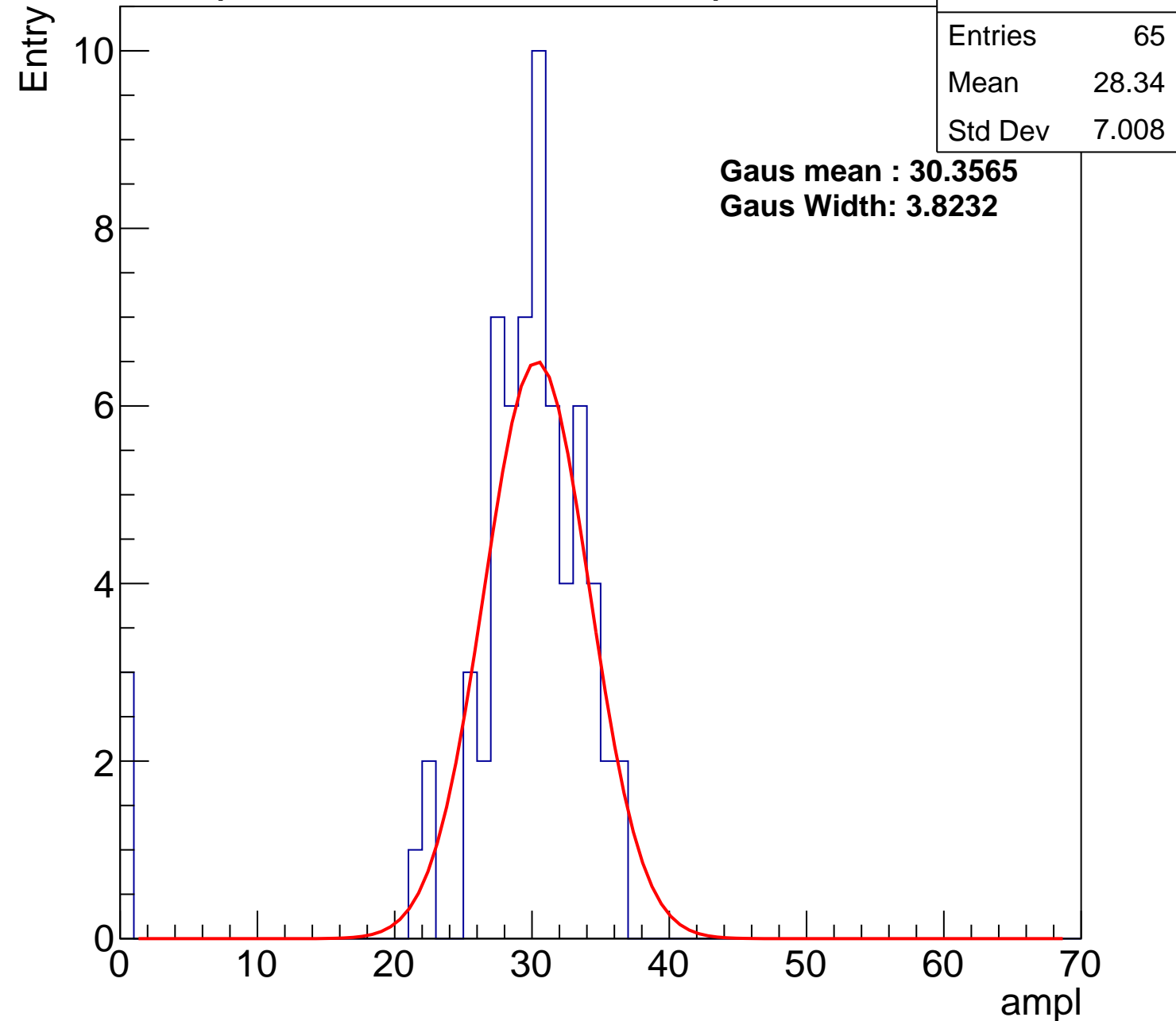
Gaus Width: 3.8232

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch115, adc1

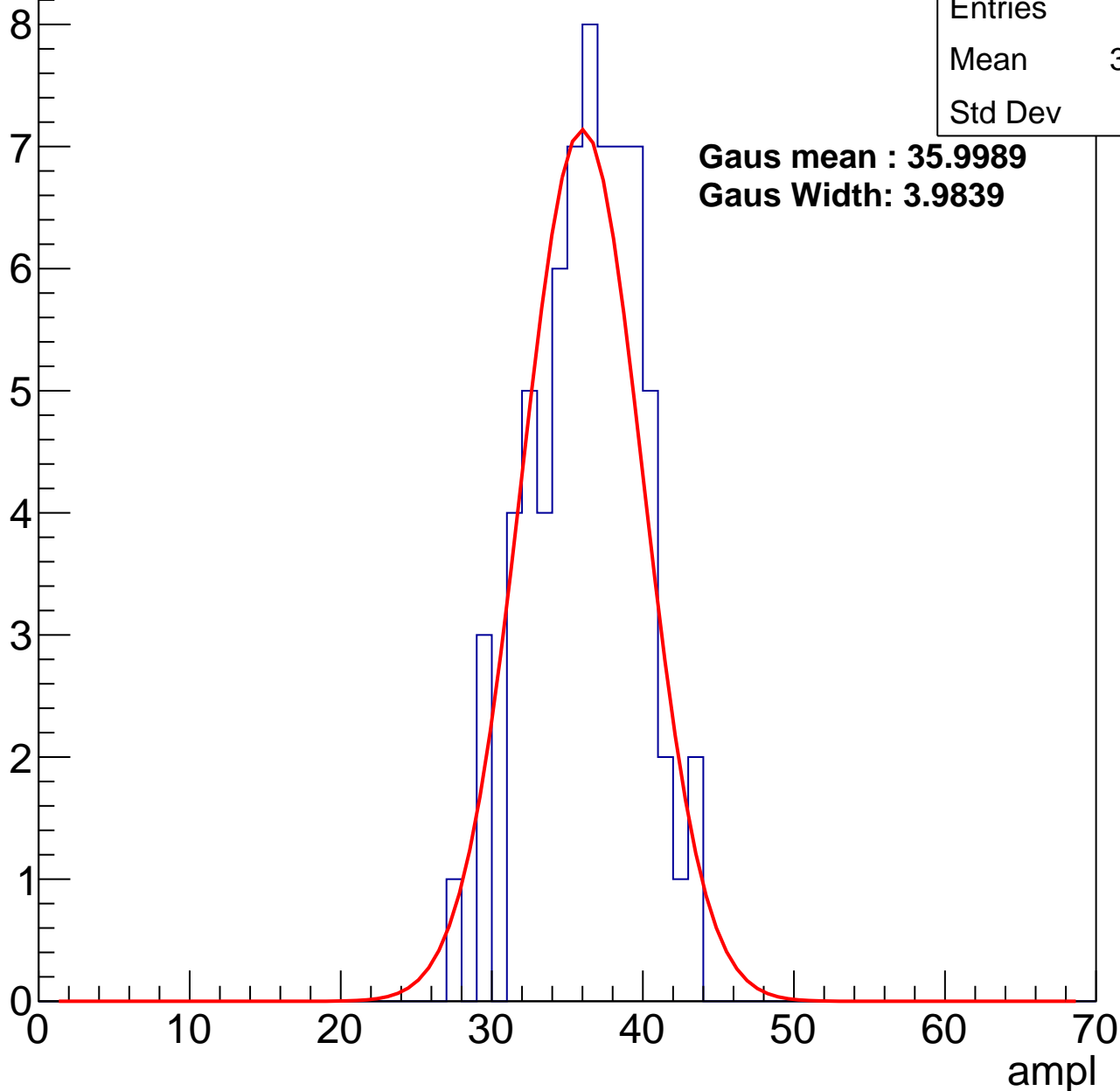
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.87
Std Dev	3.48

Gaus mean : 35.9989

Gaus Width: 3.9839



B1L003S, U20-ch115, adc2

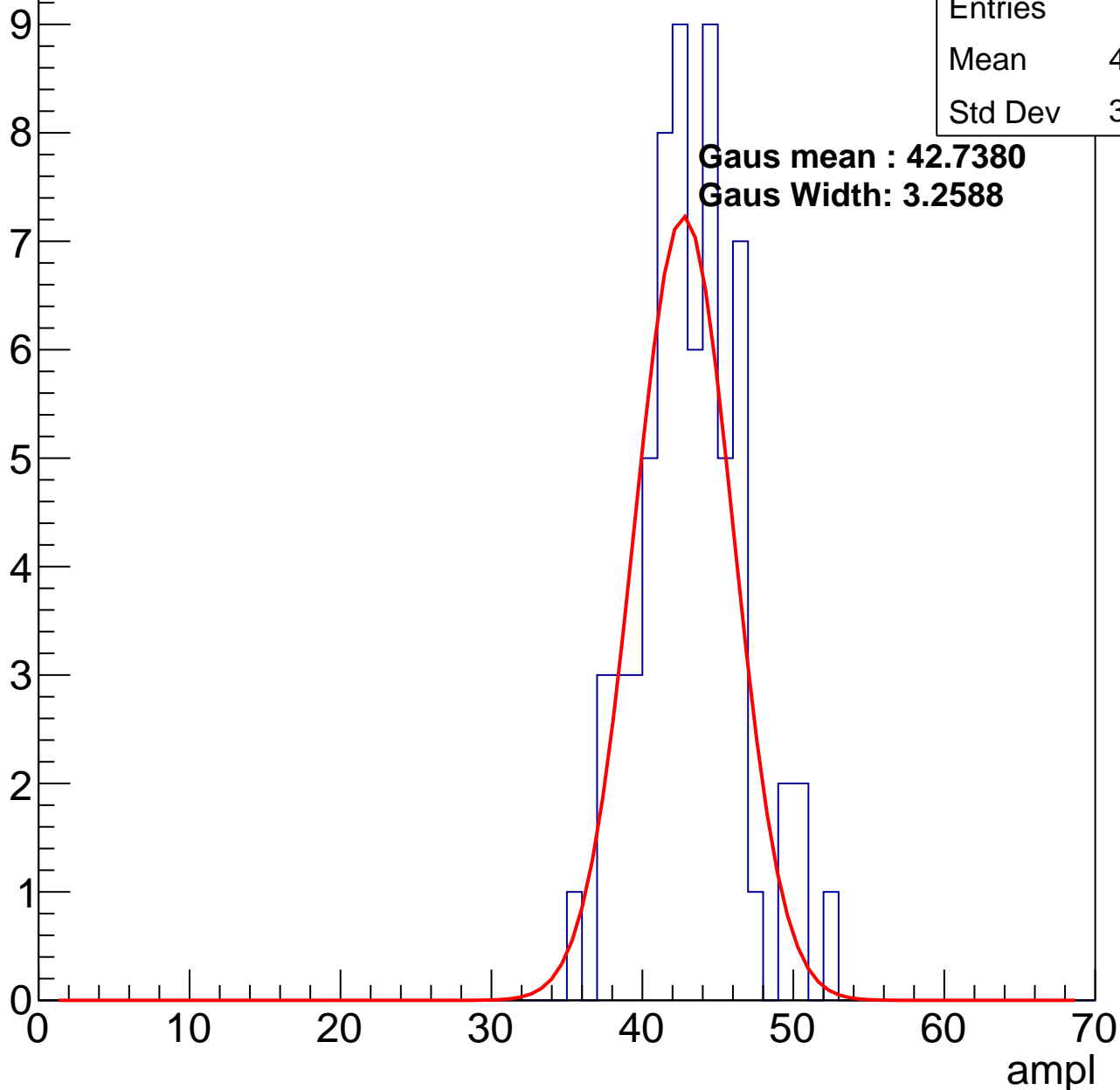
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.78
Std Dev	3.367

Gaus mean : 42.7380

Gaus Width: 3.2588

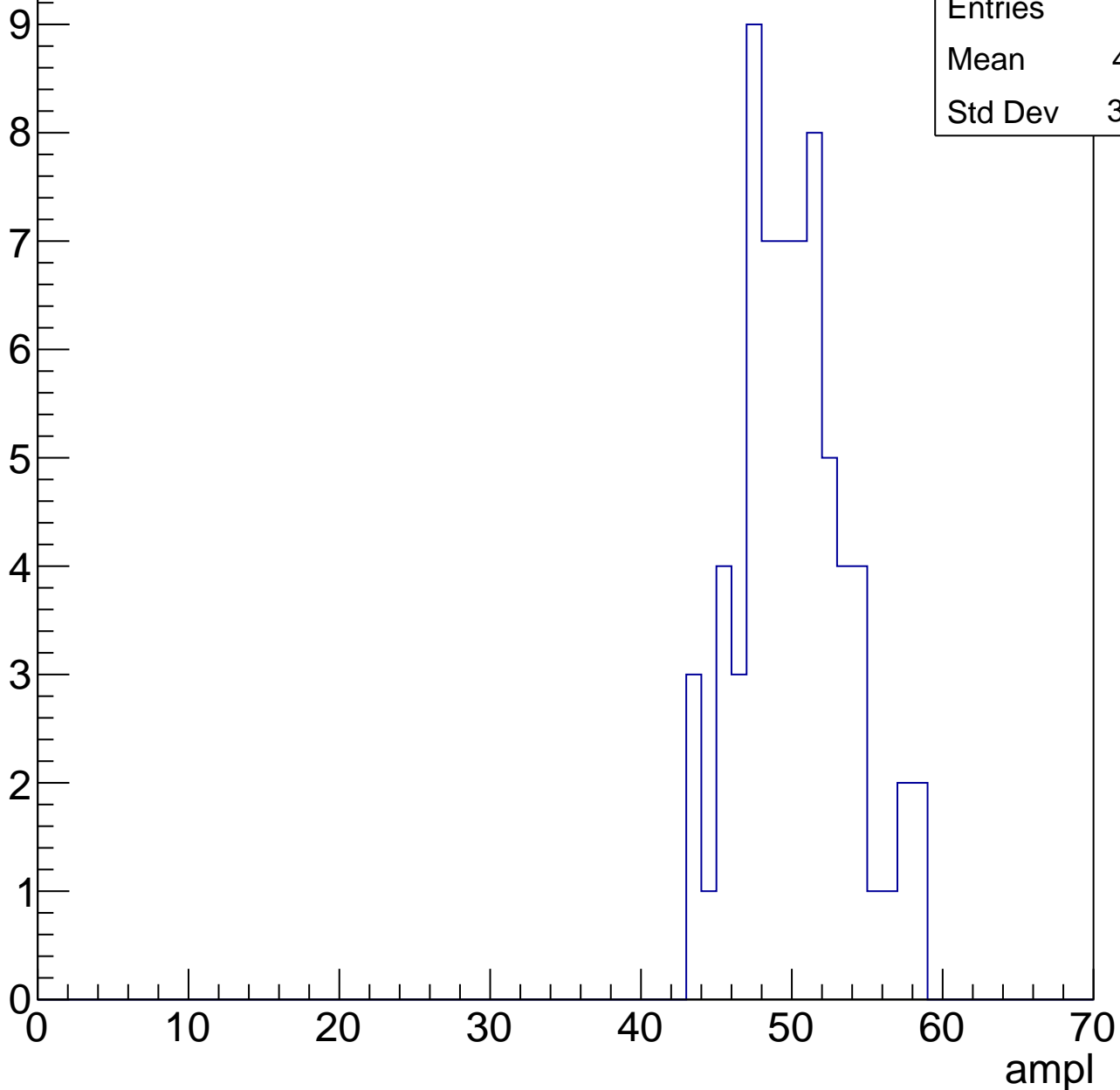


B1L003S, U20-ch115, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.71
Std Dev	3.548

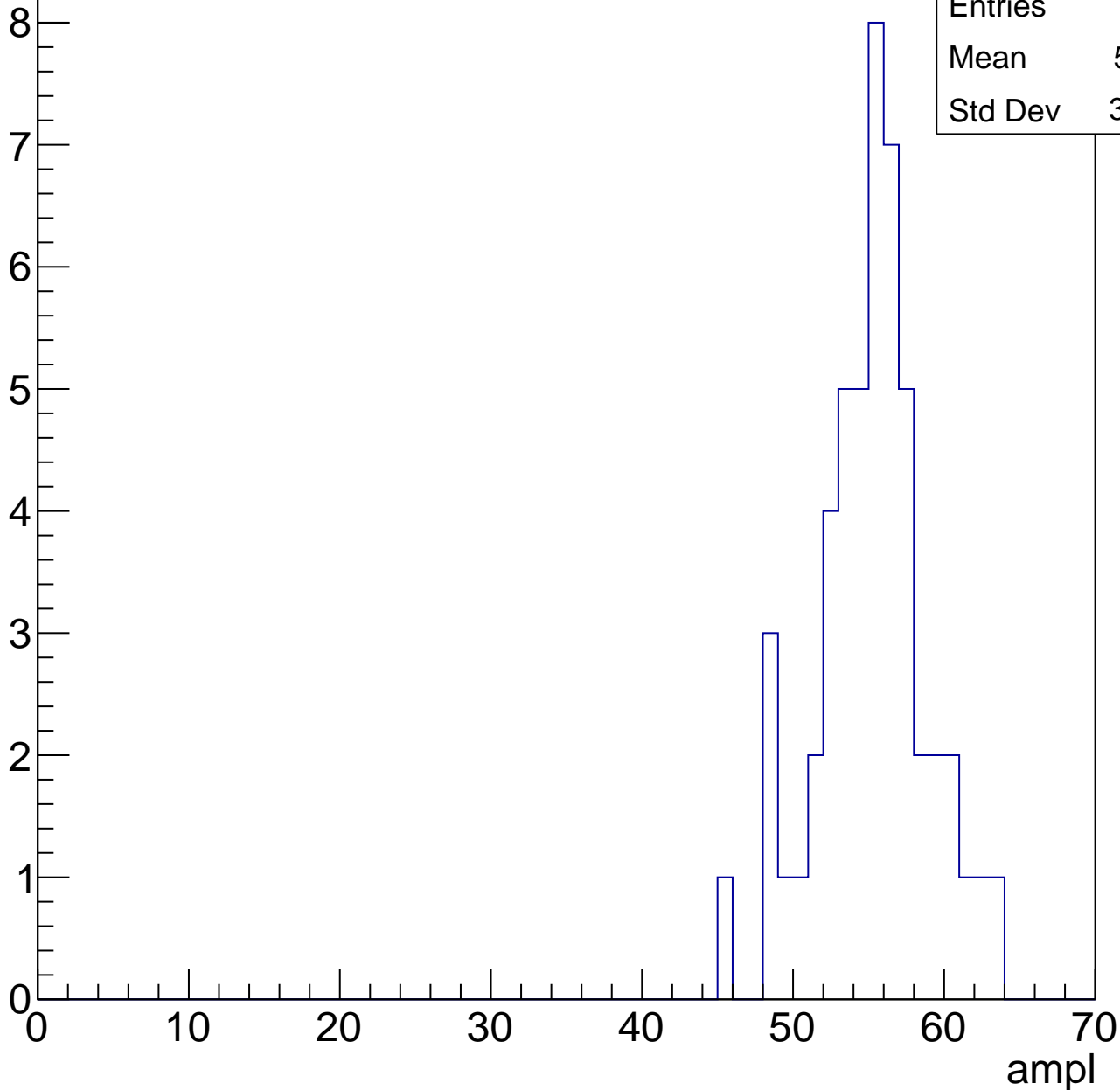


B1L003S, U20-ch115, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.71
Std Dev	3.632

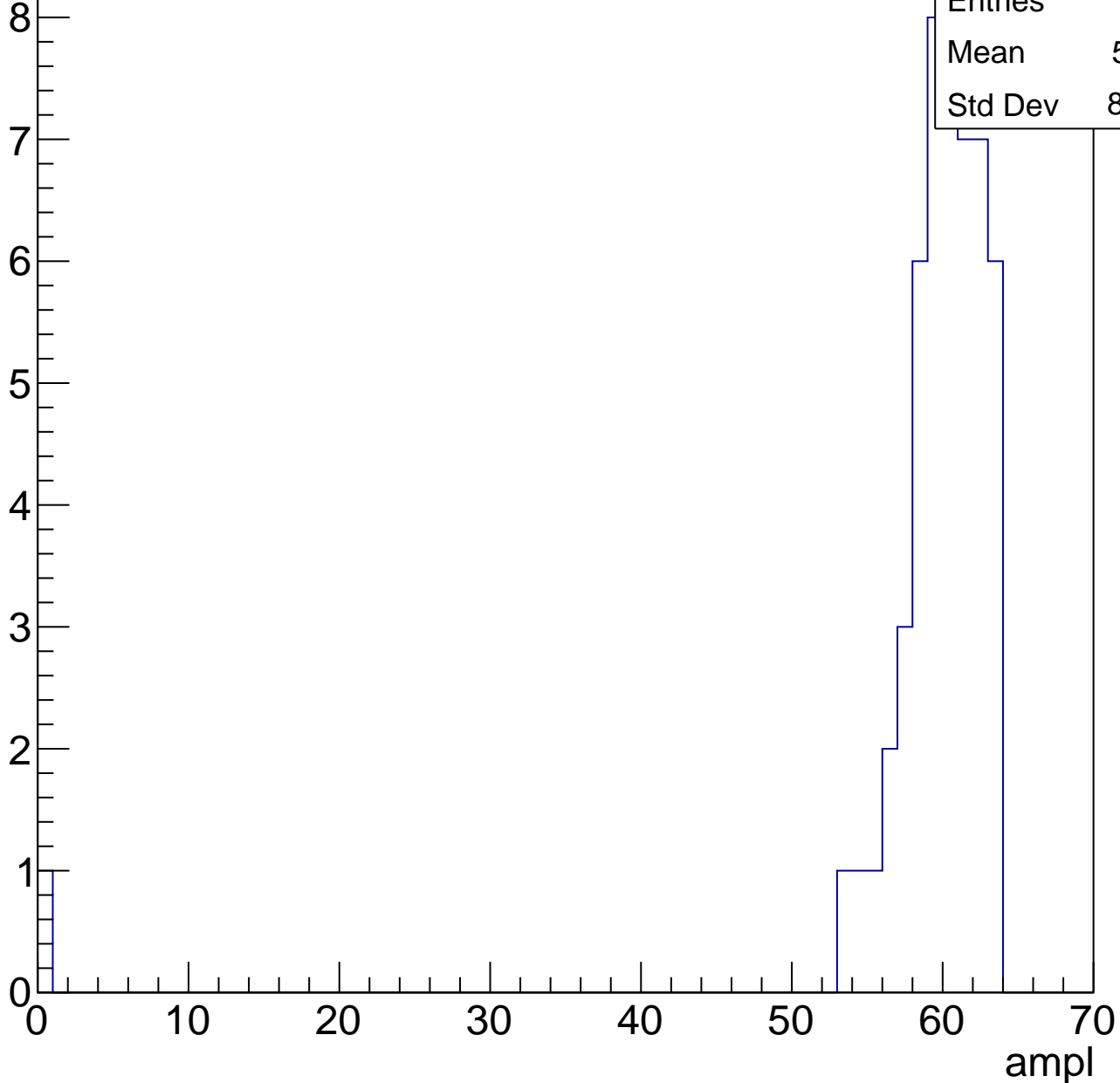


B1L003S, U20-ch115, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.51
Std Dev	8.605



B1L003S, U20-ch115, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch115, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U20-ch116, adc0

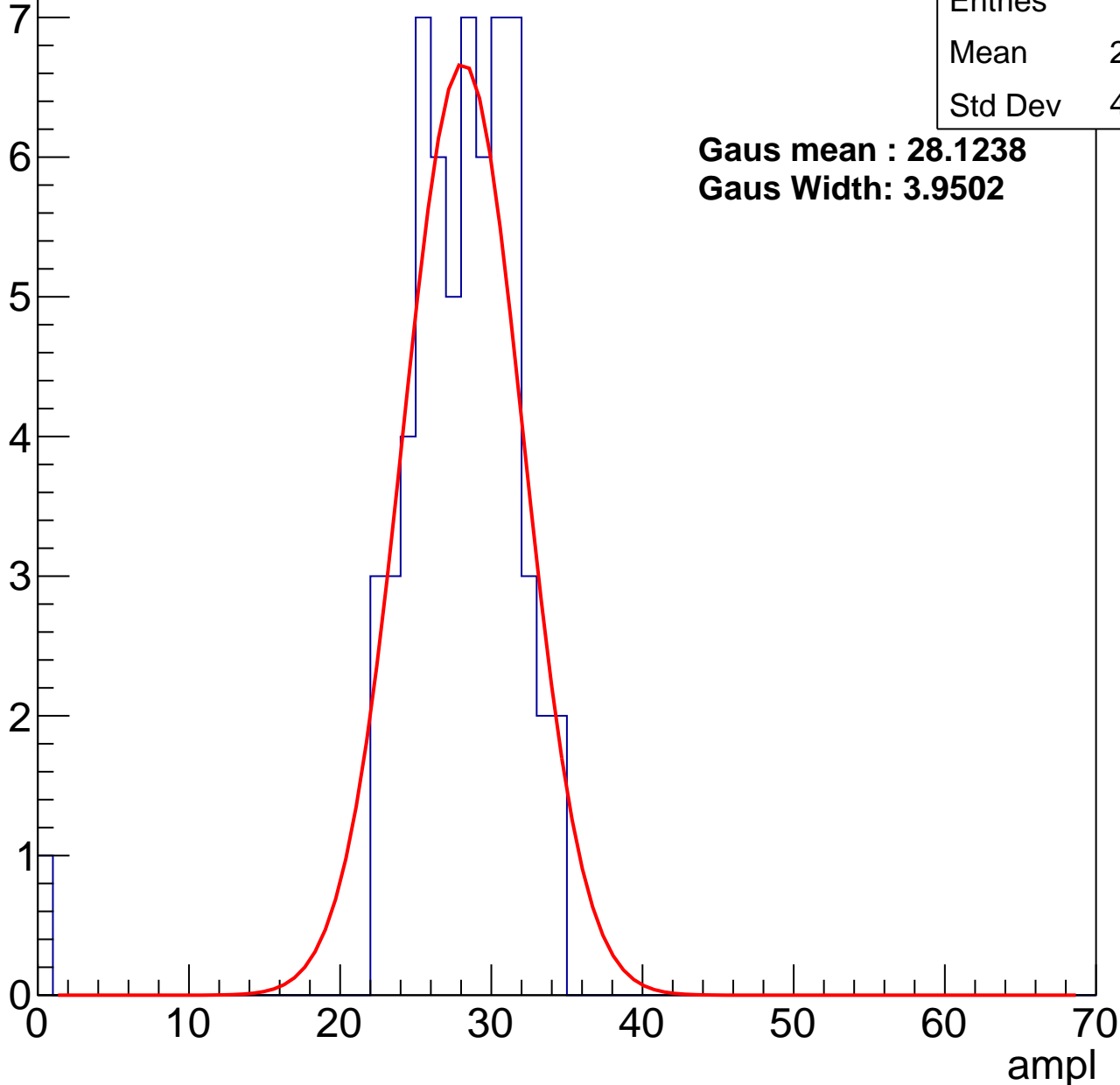
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	27.37
Std Dev	4.654

Gaus mean : 28.1238

Gaus Width: 3.9502



B1L003S, U20-ch116, adc1

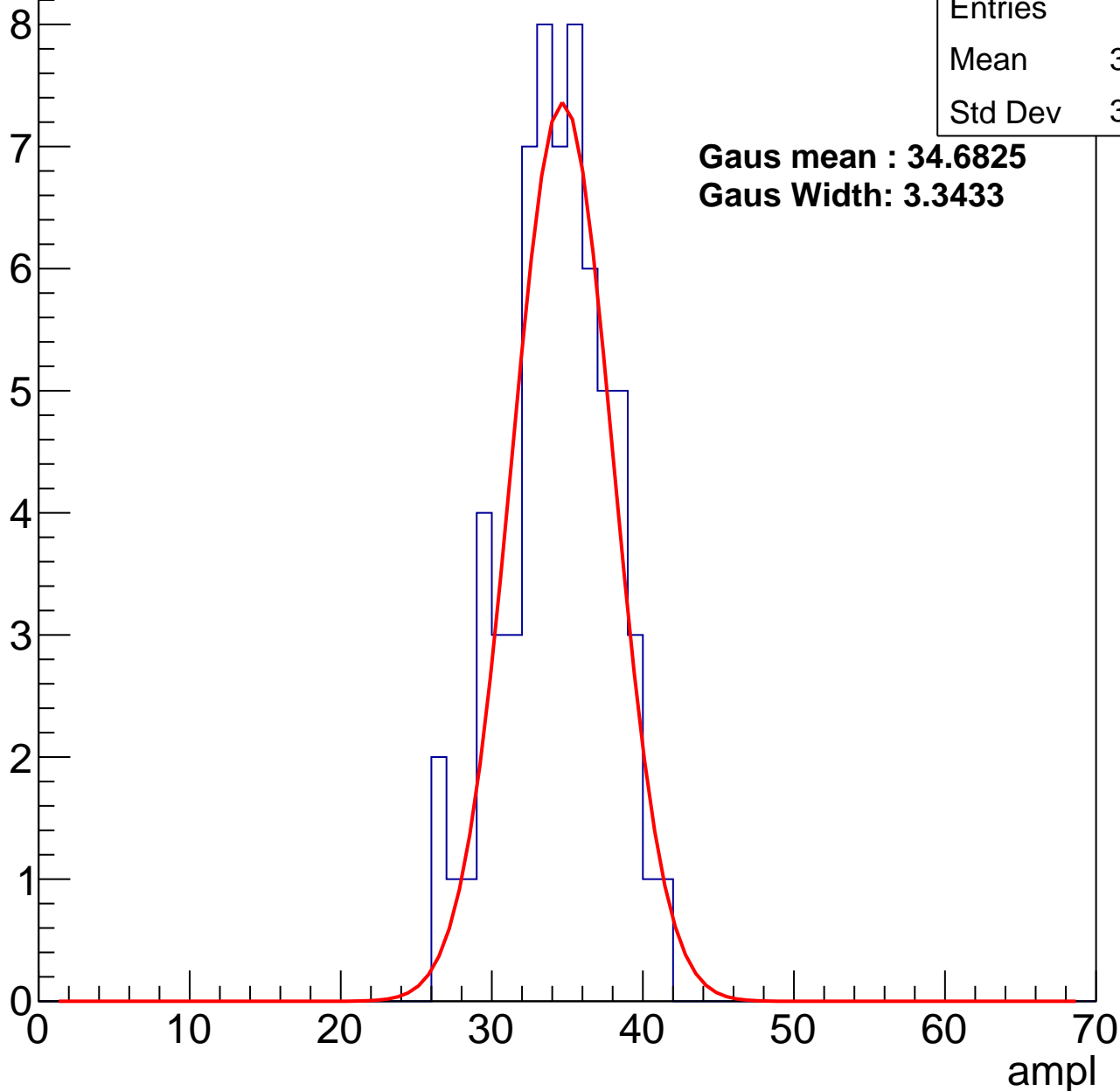
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	33.86
Std Dev	3.374

Gaus mean : 34.6825

Gaus Width: 3.3433



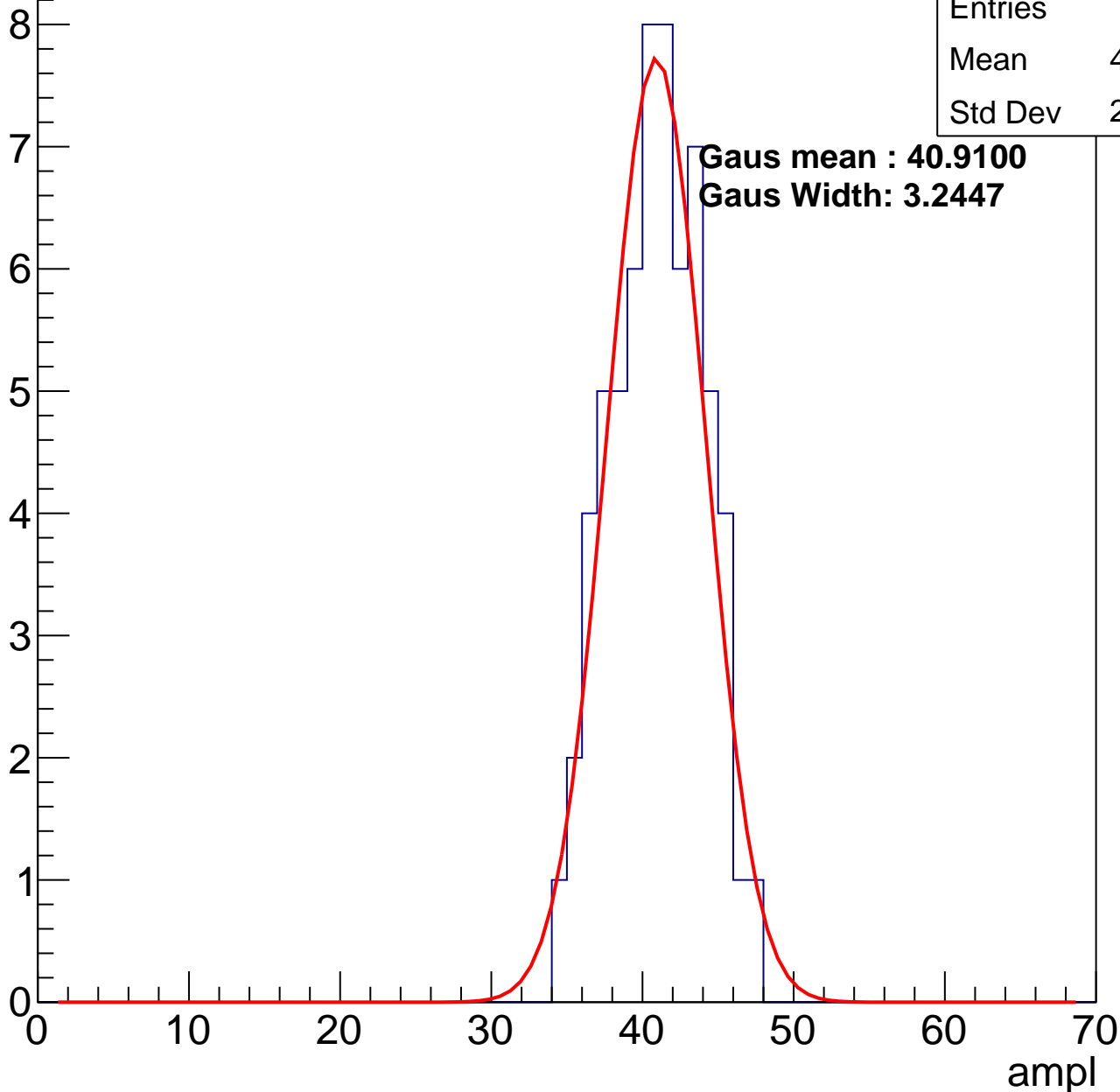
B1L003S, U20-ch116, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	40.49
Std Dev	2.997

Gaus mean : 40.9100
Gaus Width: 3.2447

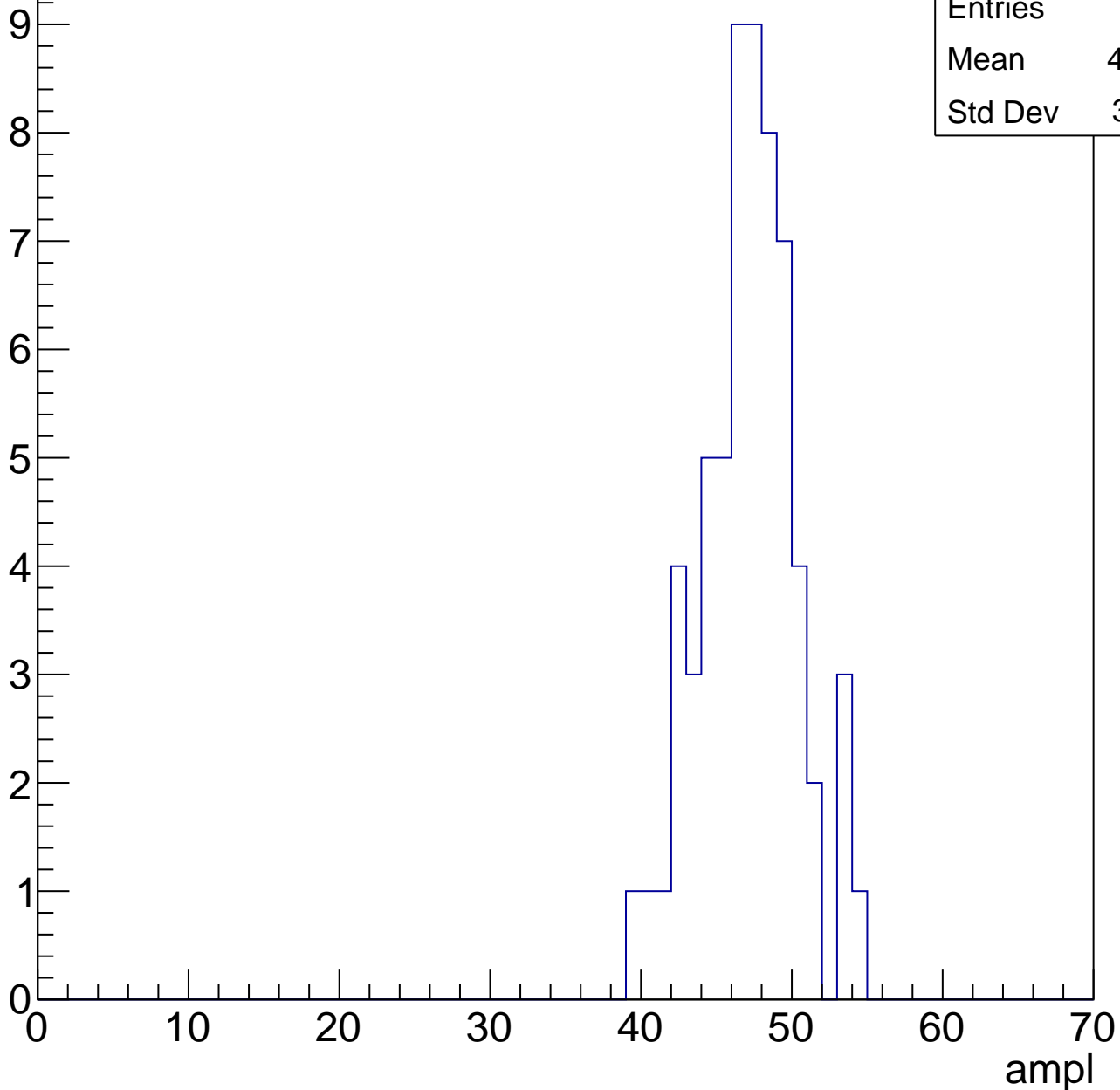


B1L003S, U20-ch116, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	46.68
Std Dev	3.151

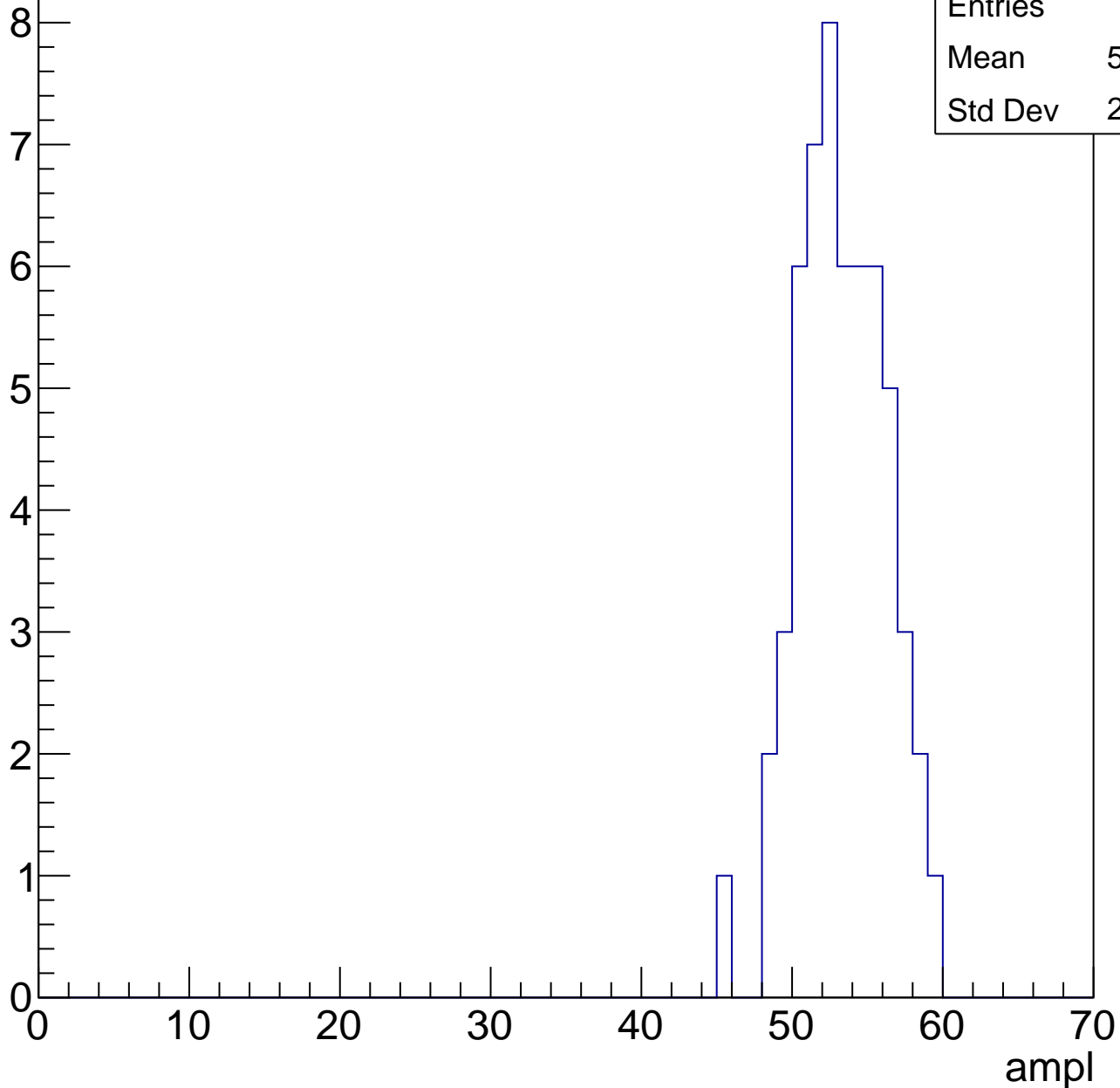


B1L003S, U20-ch116, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	52.84
Std Dev	2.865

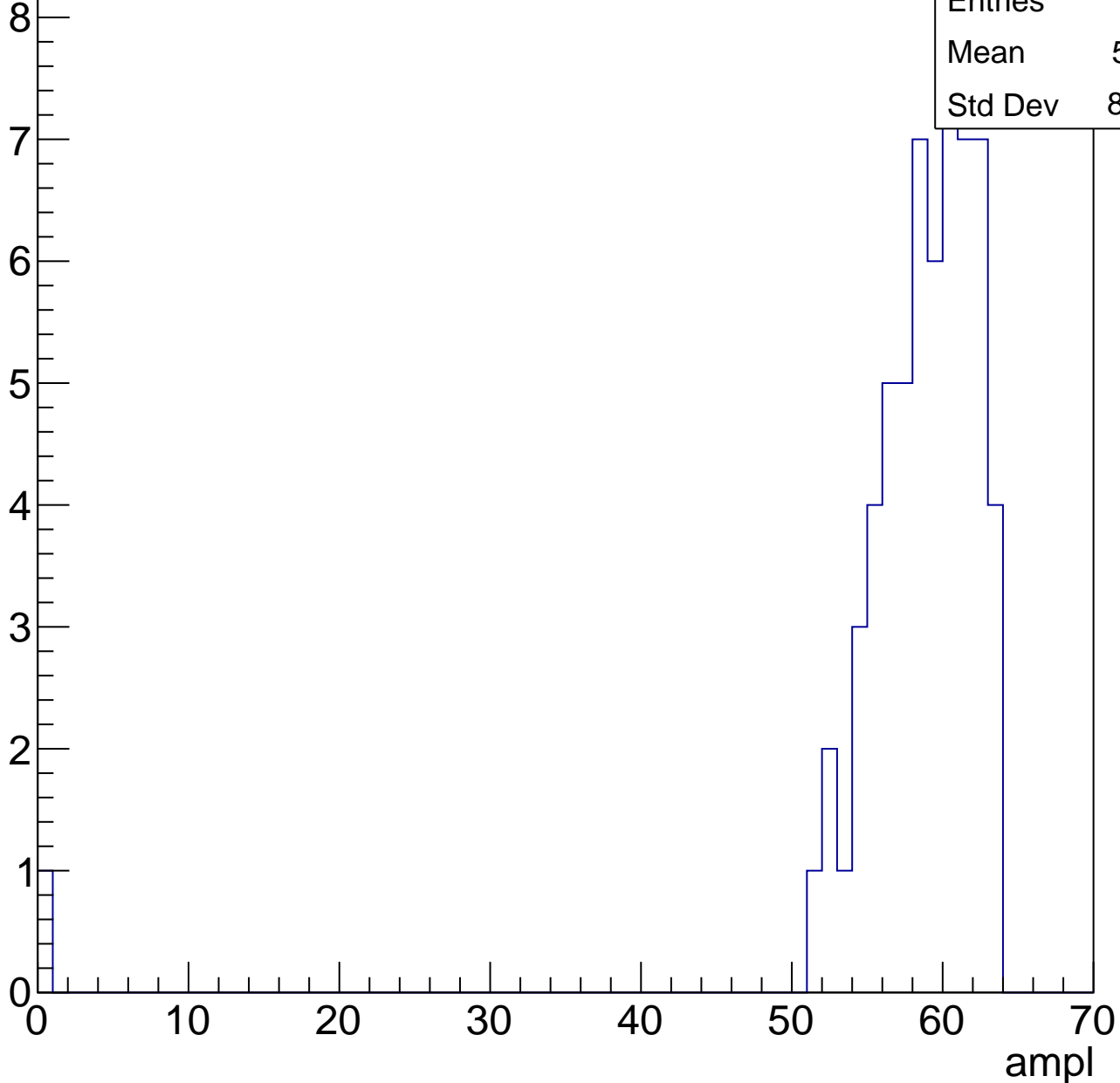


B1L003S, U20-ch116, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	57.51
Std Dev	8.014

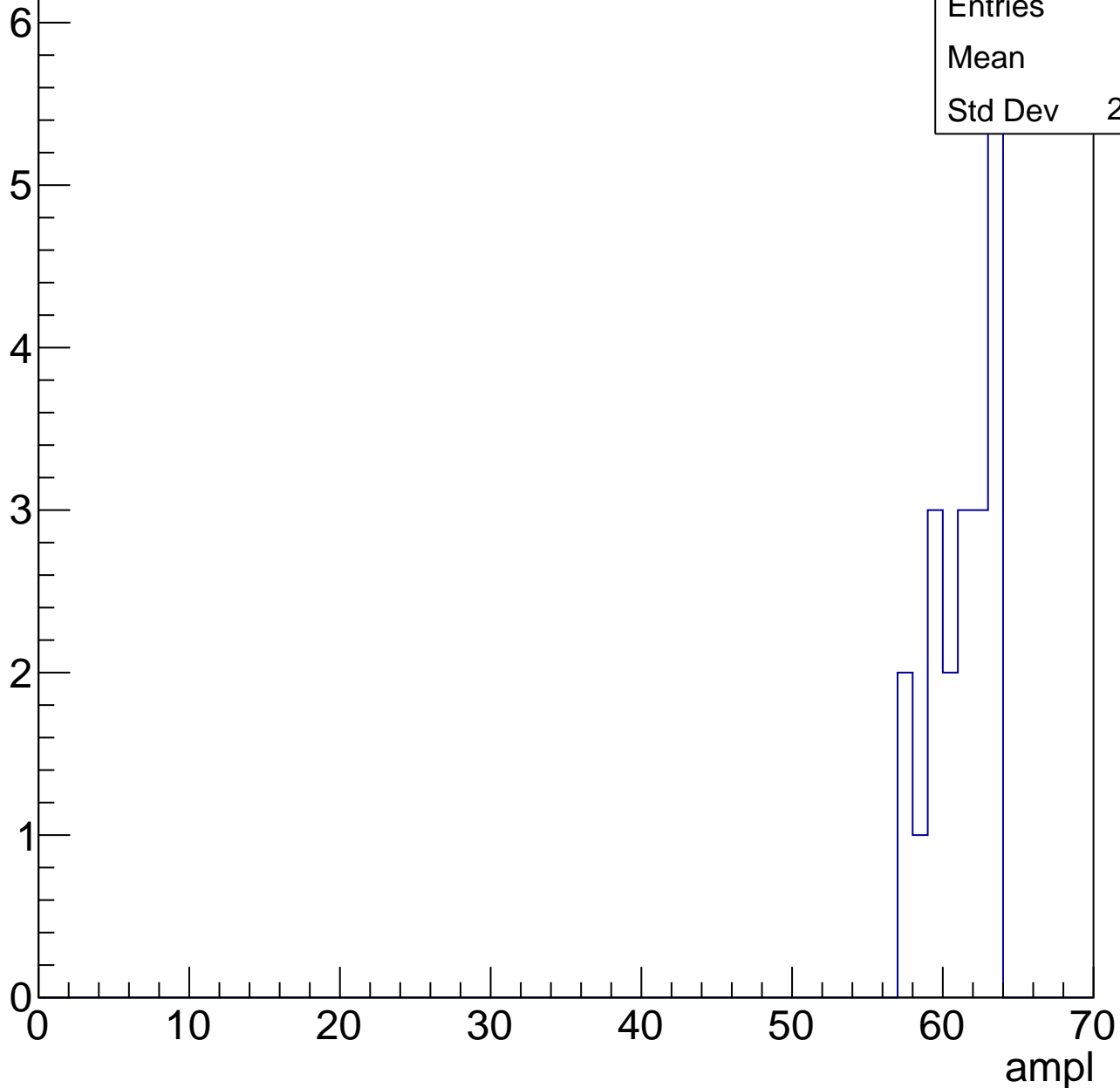


B1L003S, U20-ch116, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	60.8
Std Dev	2.015



B1L003S, U20-ch116, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L003S, U20-ch117, adc0

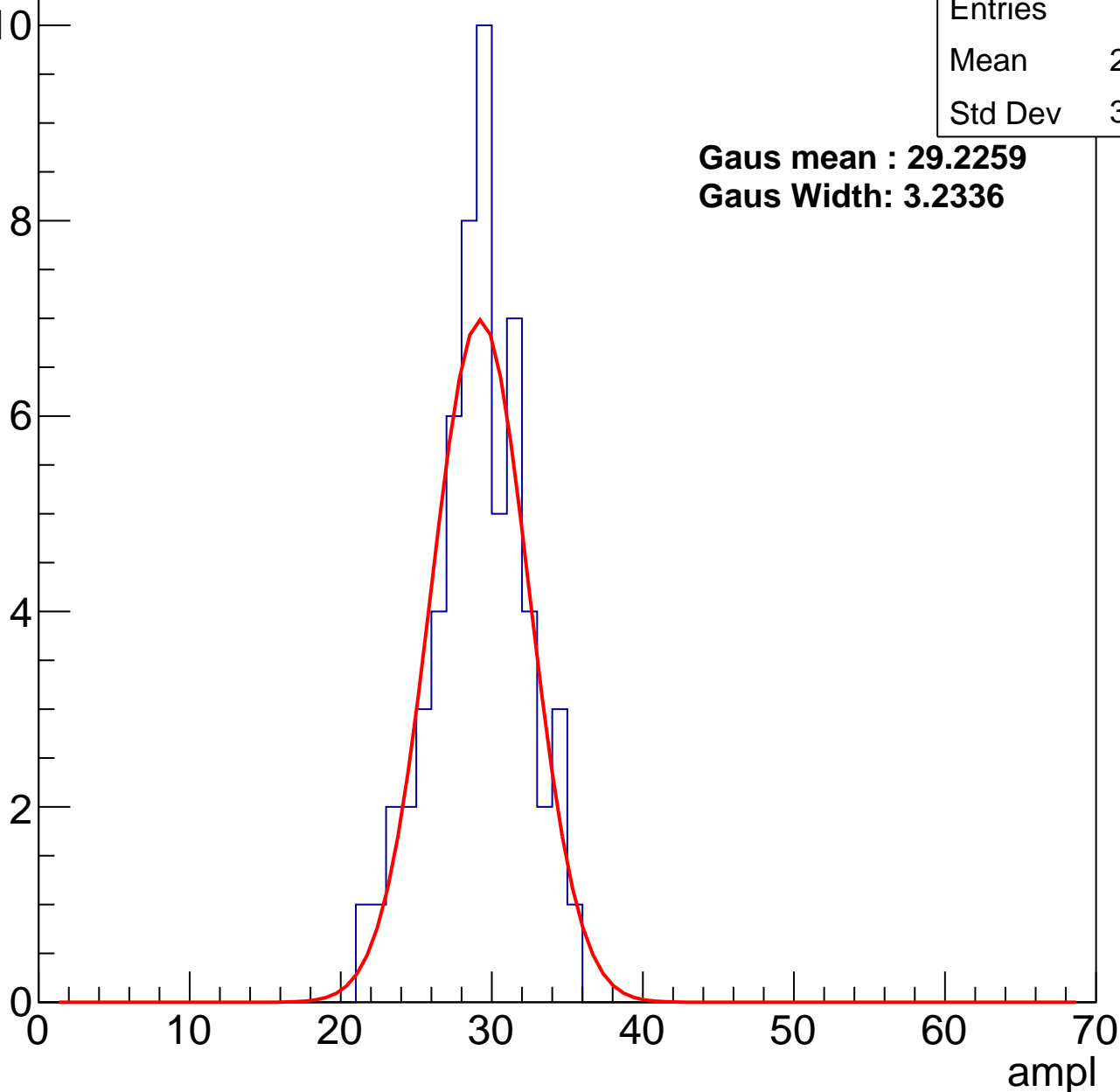
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28.64
Std Dev	3.068

Gaus mean : 29.2259

Gaus Width: 3.2336



B1L003S, U20-ch117, adc1

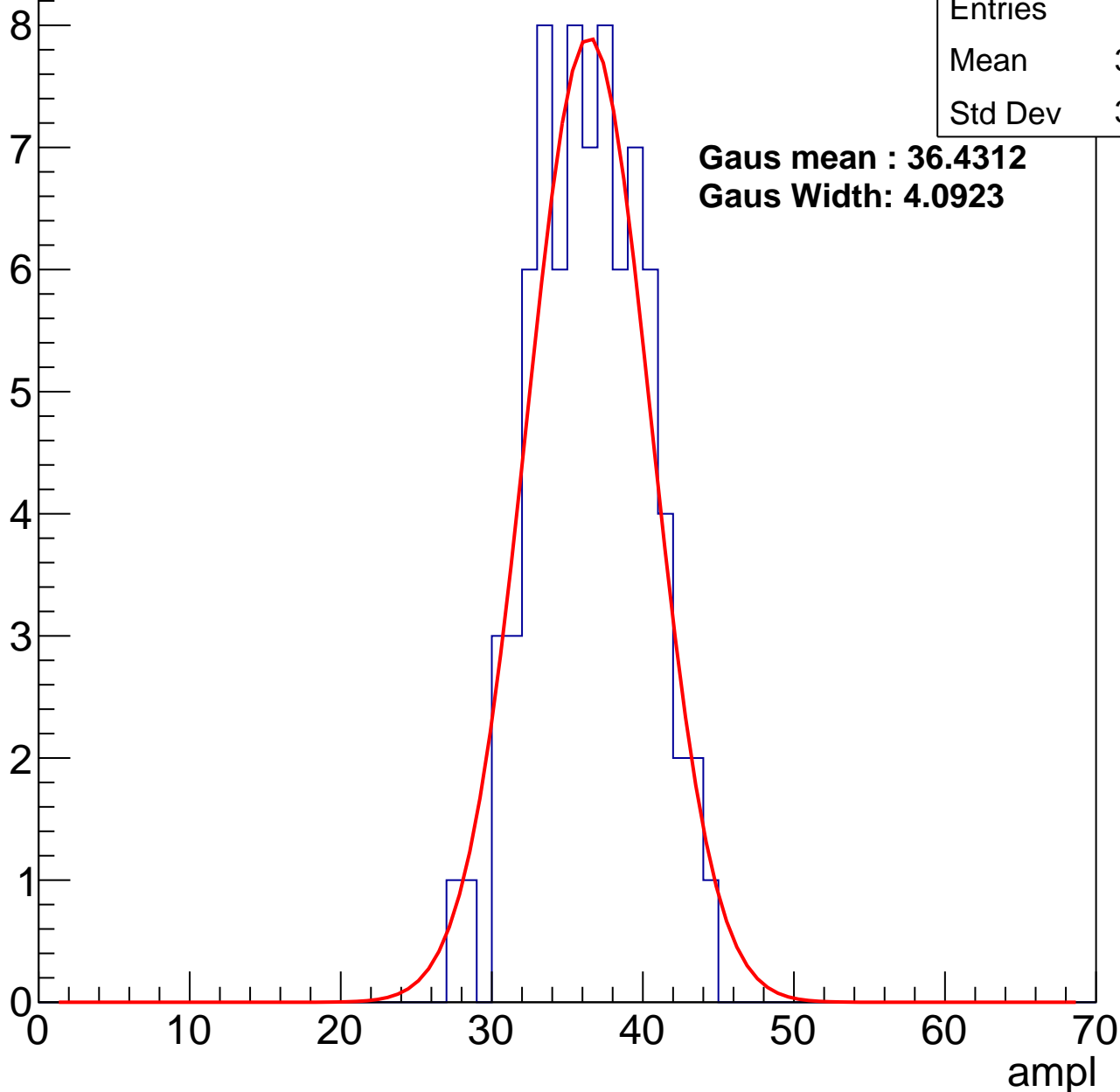
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	36.01
Std Dev	3.651

Gaus mean : 36.4312

Gaus Width: 4.0923



B1L003S, U20-ch117, adc2

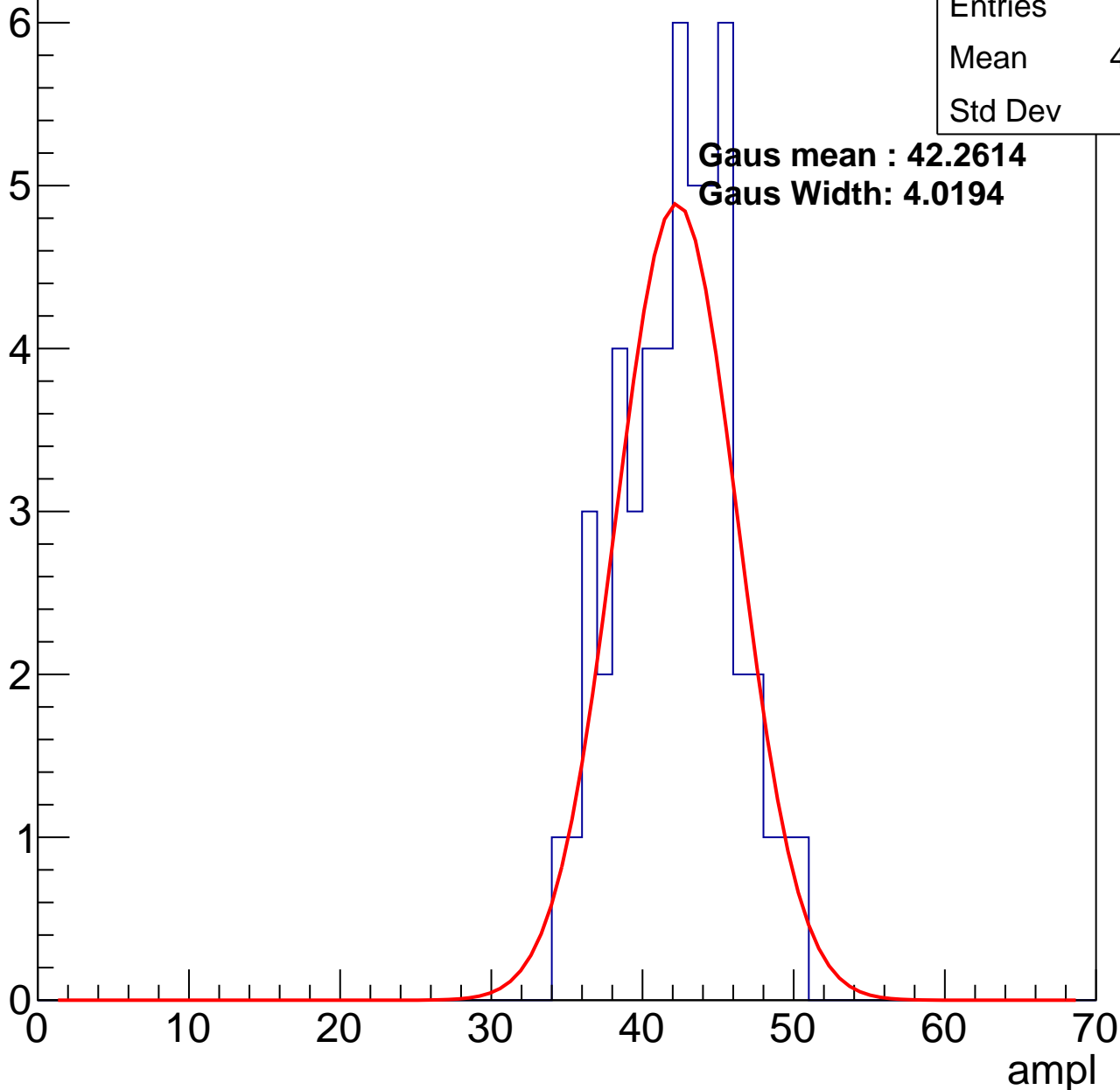
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	41.84
Std Dev	3.68

Gaus mean : 42.2614

Gaus Width: 4.0194

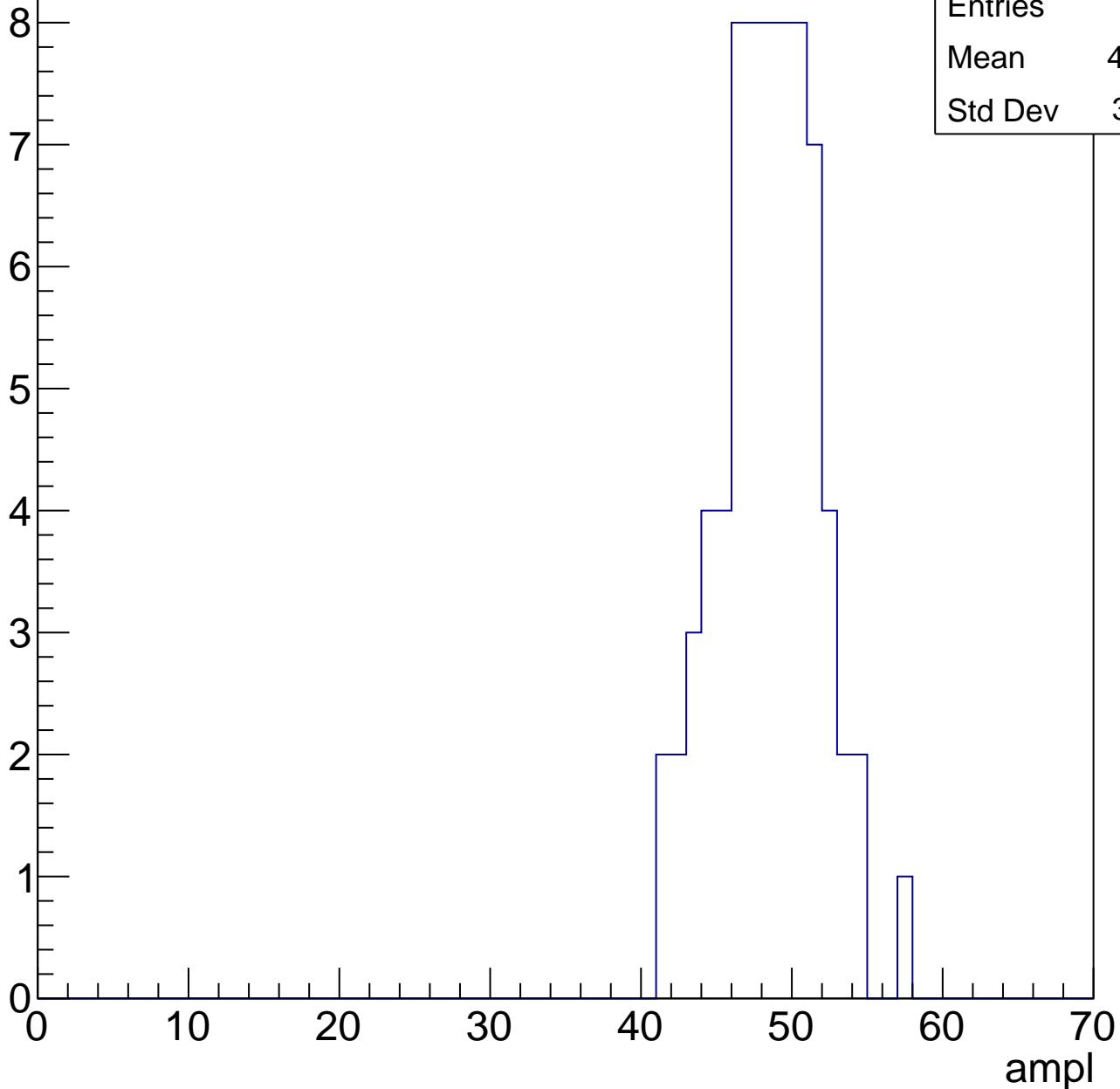


B1L003S, U20-ch117, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	47.99
Std Dev	3.261

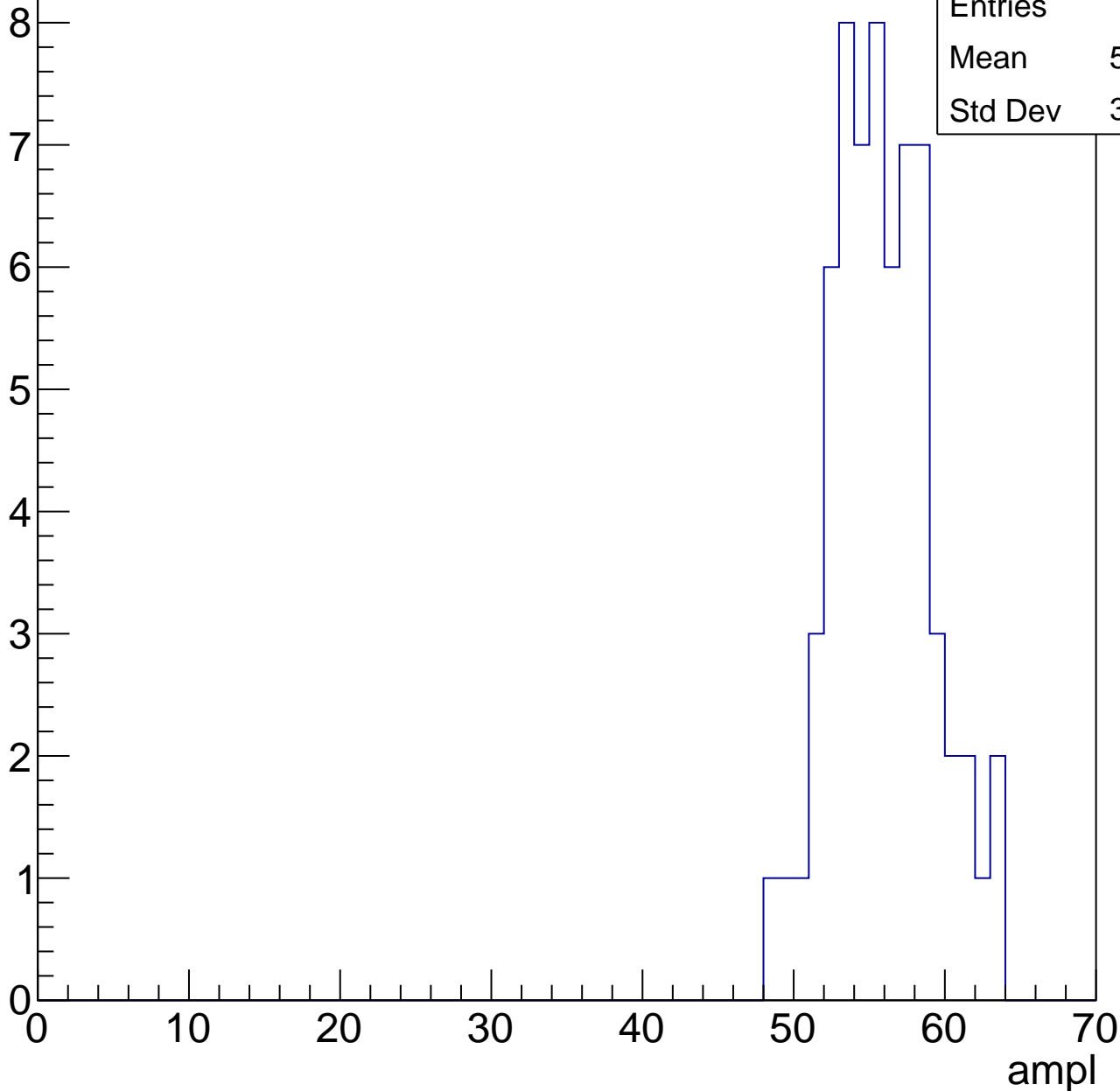


B1L003S, U20-ch117, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.42
Std Dev	3.243

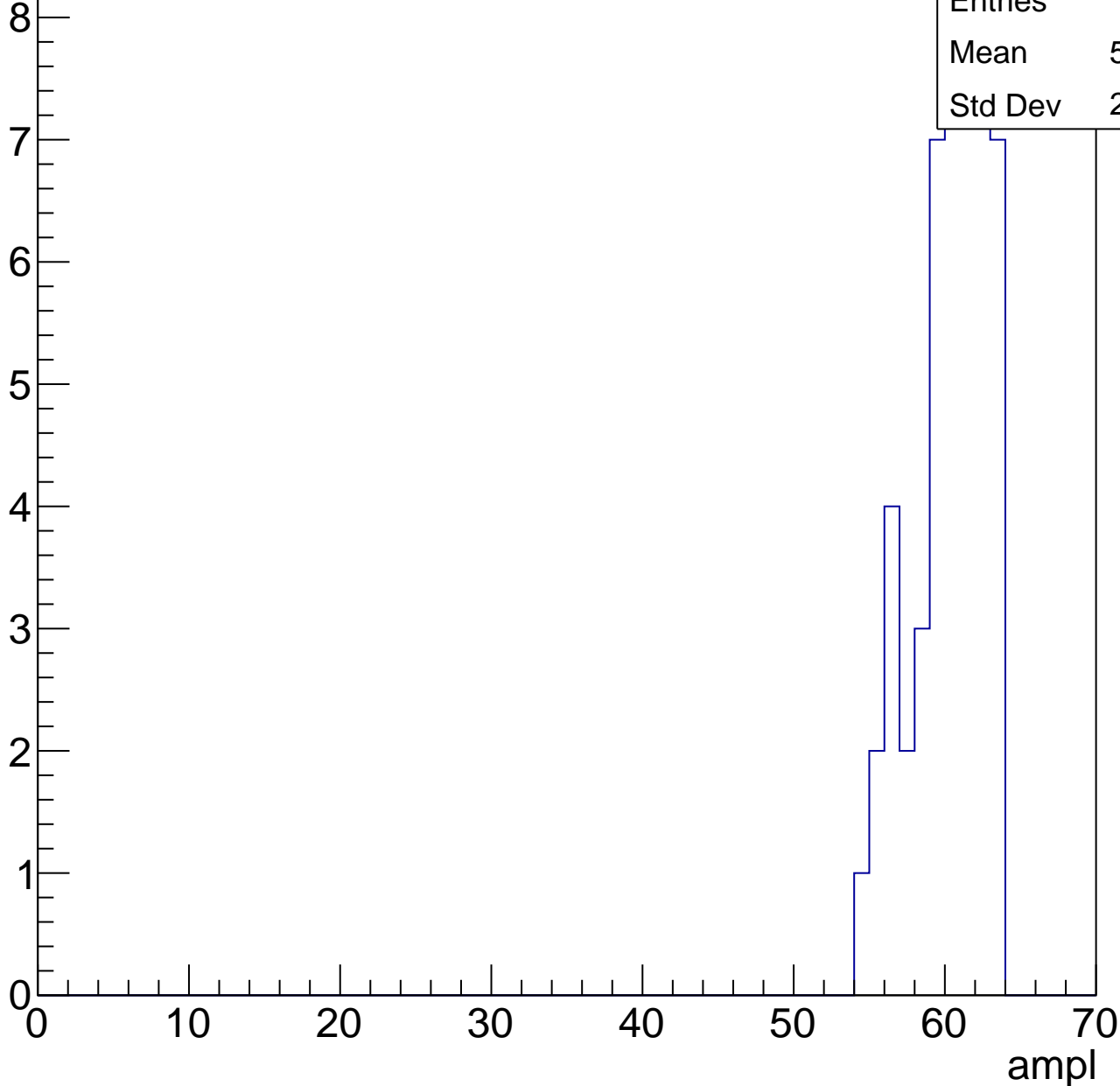


B1L003S, U20-ch117, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

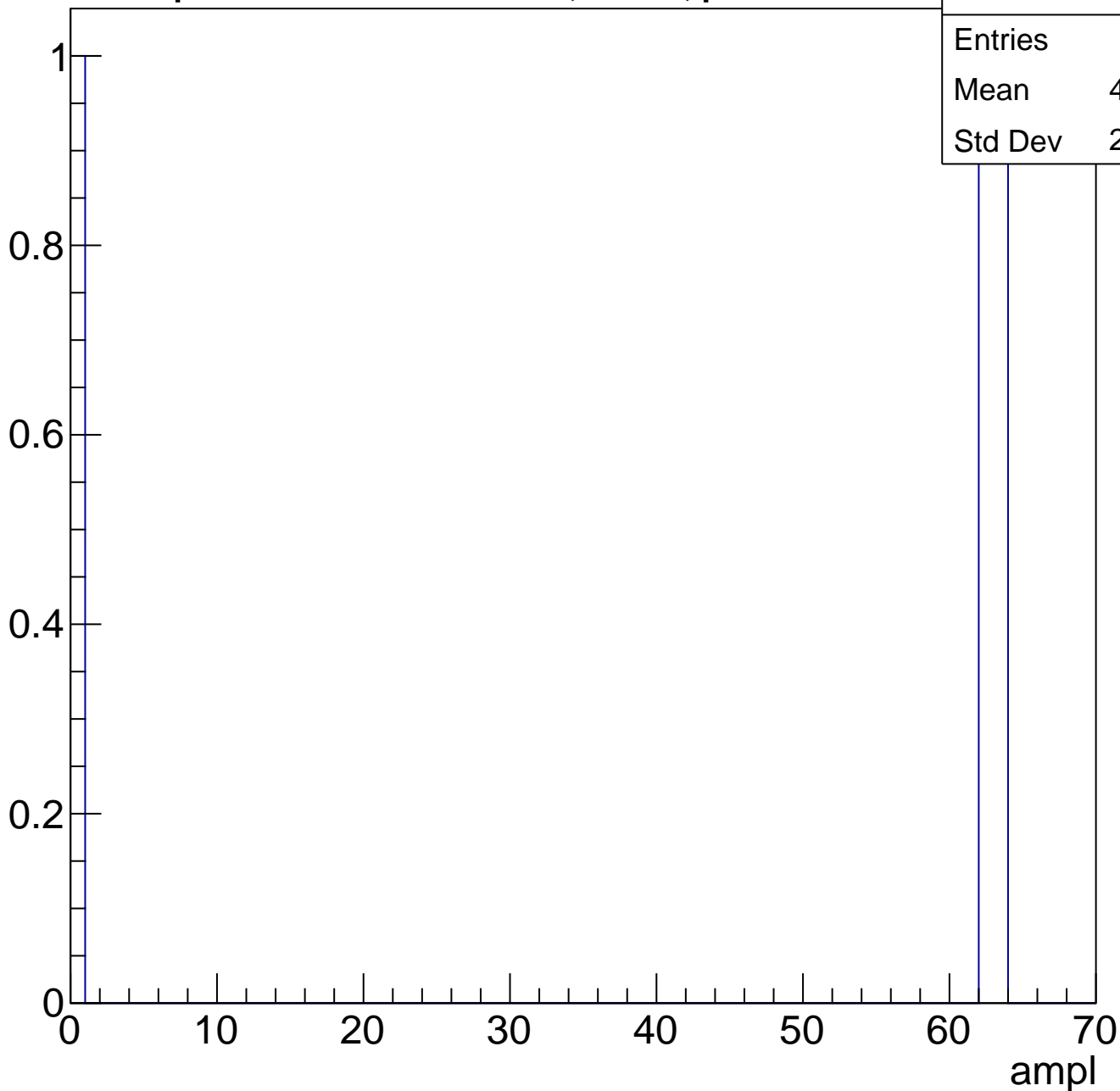
Entries	50
Mean	59.88
Std Dev	2.405



B1L003S, U20-ch117, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	41.67
Std Dev	29.47

B1L003S, U20-ch117, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch118, adc0

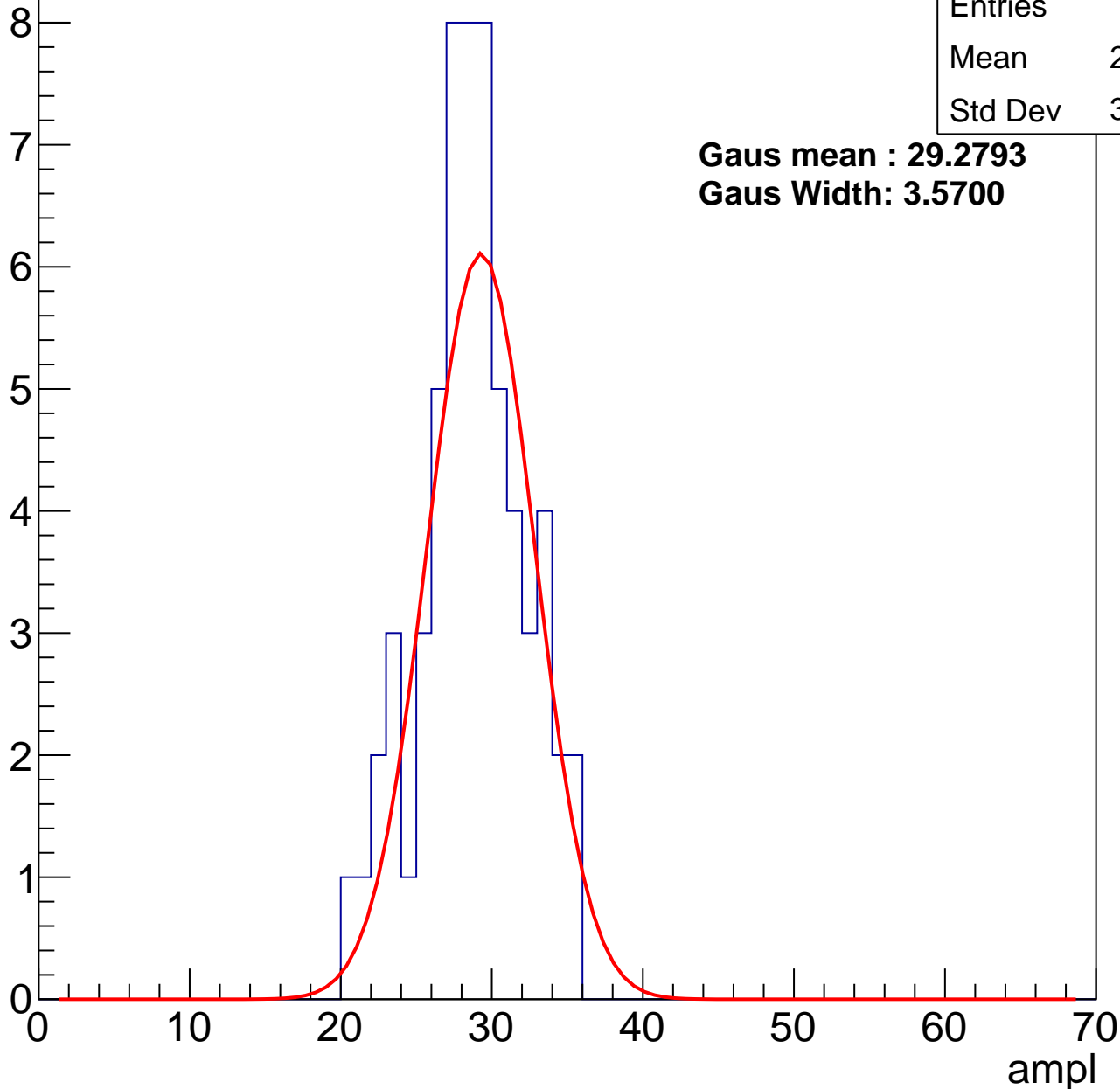
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.25
Std Dev	3.438

Gaus mean : 29.2793

Gaus Width: 3.5700



B1L003S, U20-ch118, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	34.41
Std Dev	3.216

Gaus mean : 34.1198

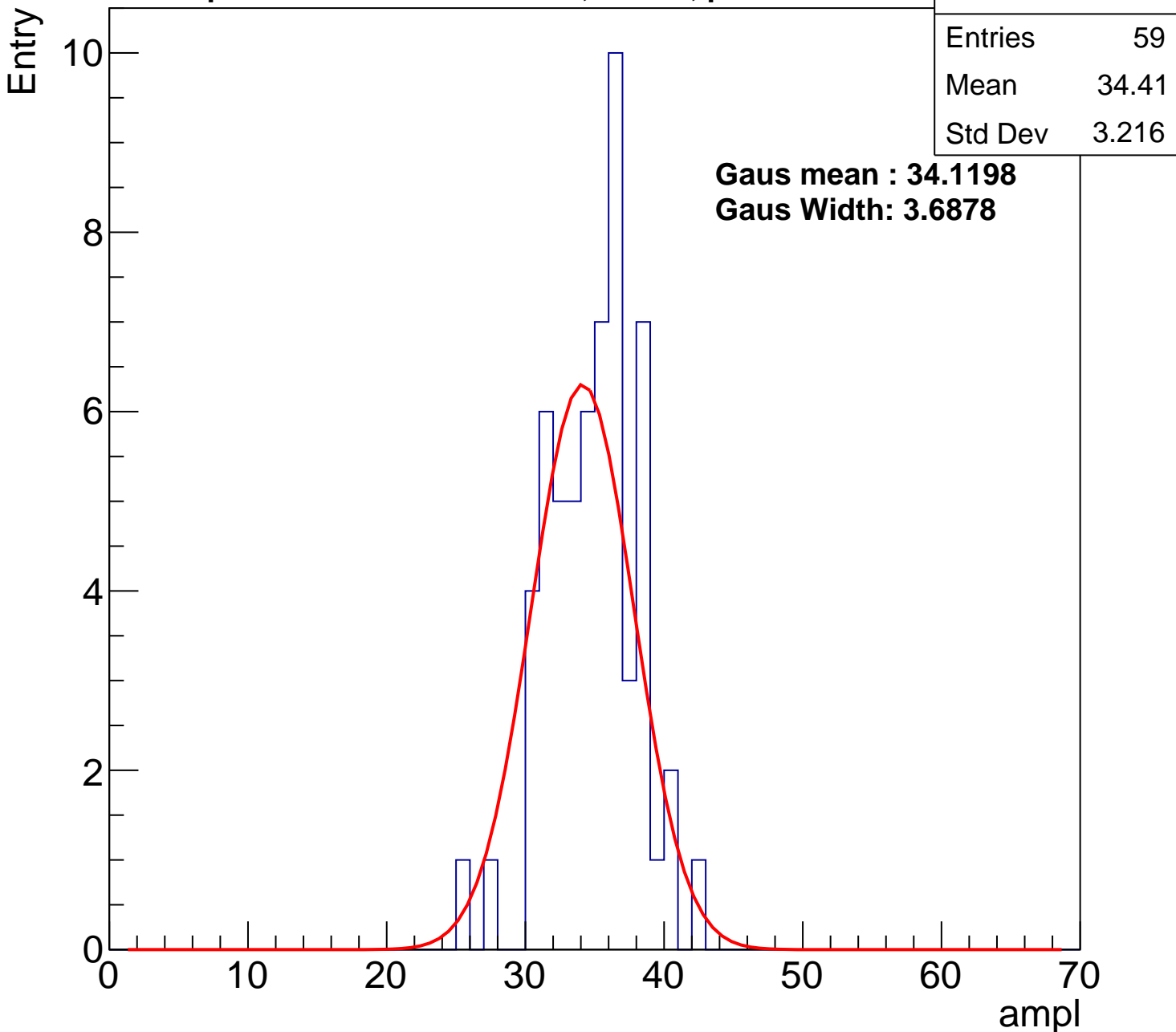
Gaus Width: 3.6878

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch118, adc2

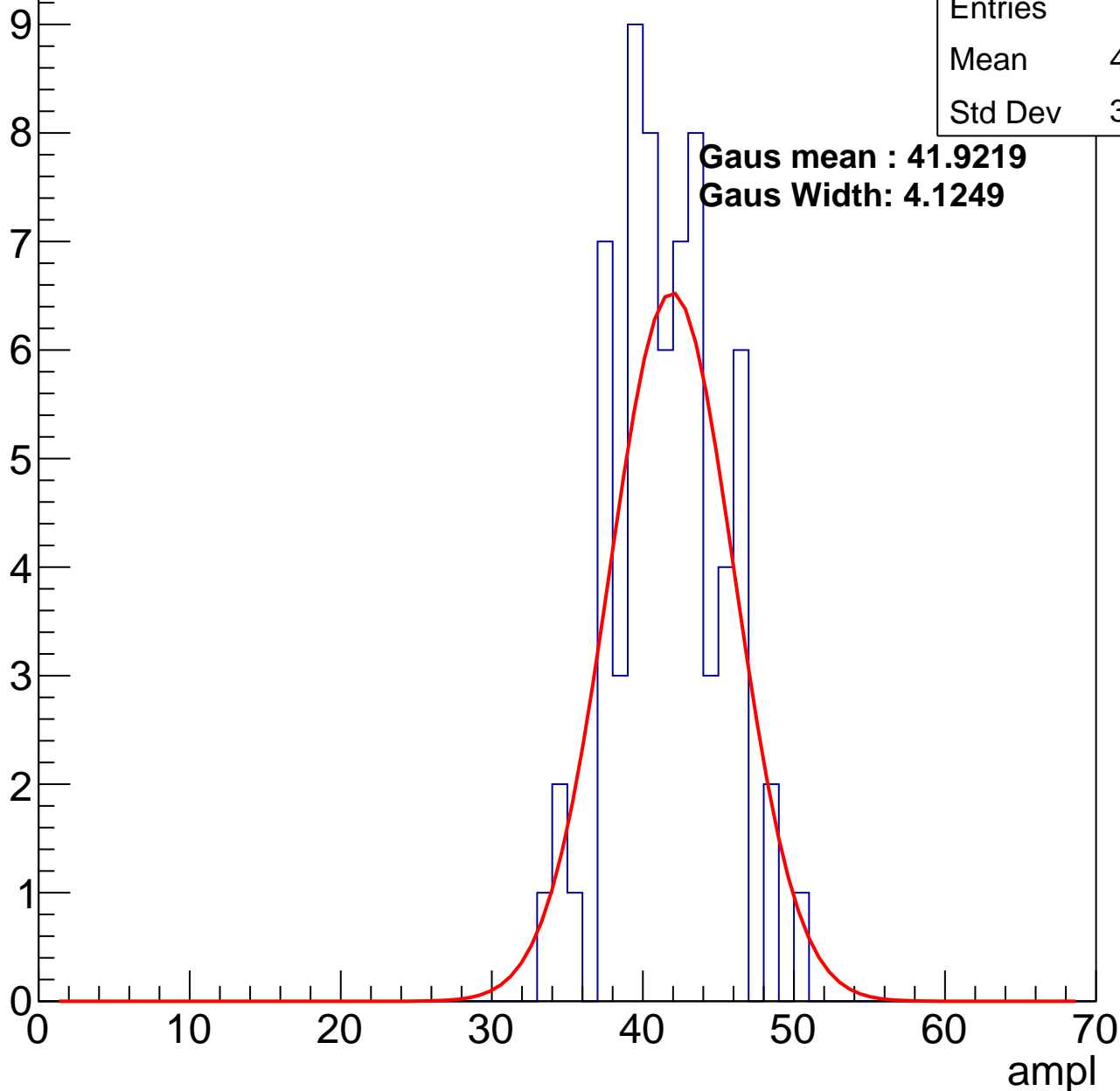
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.15
Std Dev	3.512

Gaus mean : 41.9219

Gaus Width: 4.1249

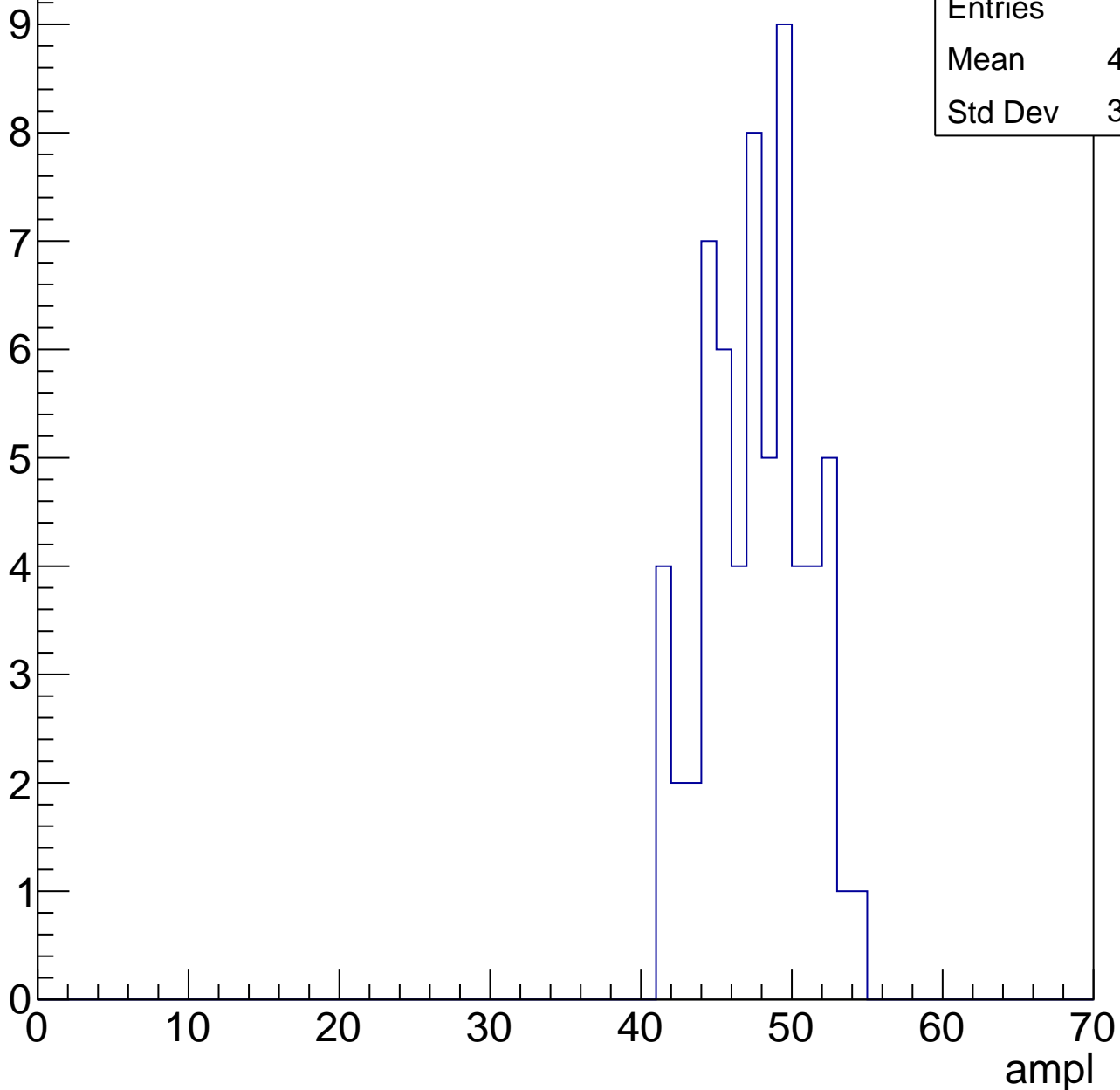


B1L003S, U20-ch118, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	47.16
Std Dev	3.278

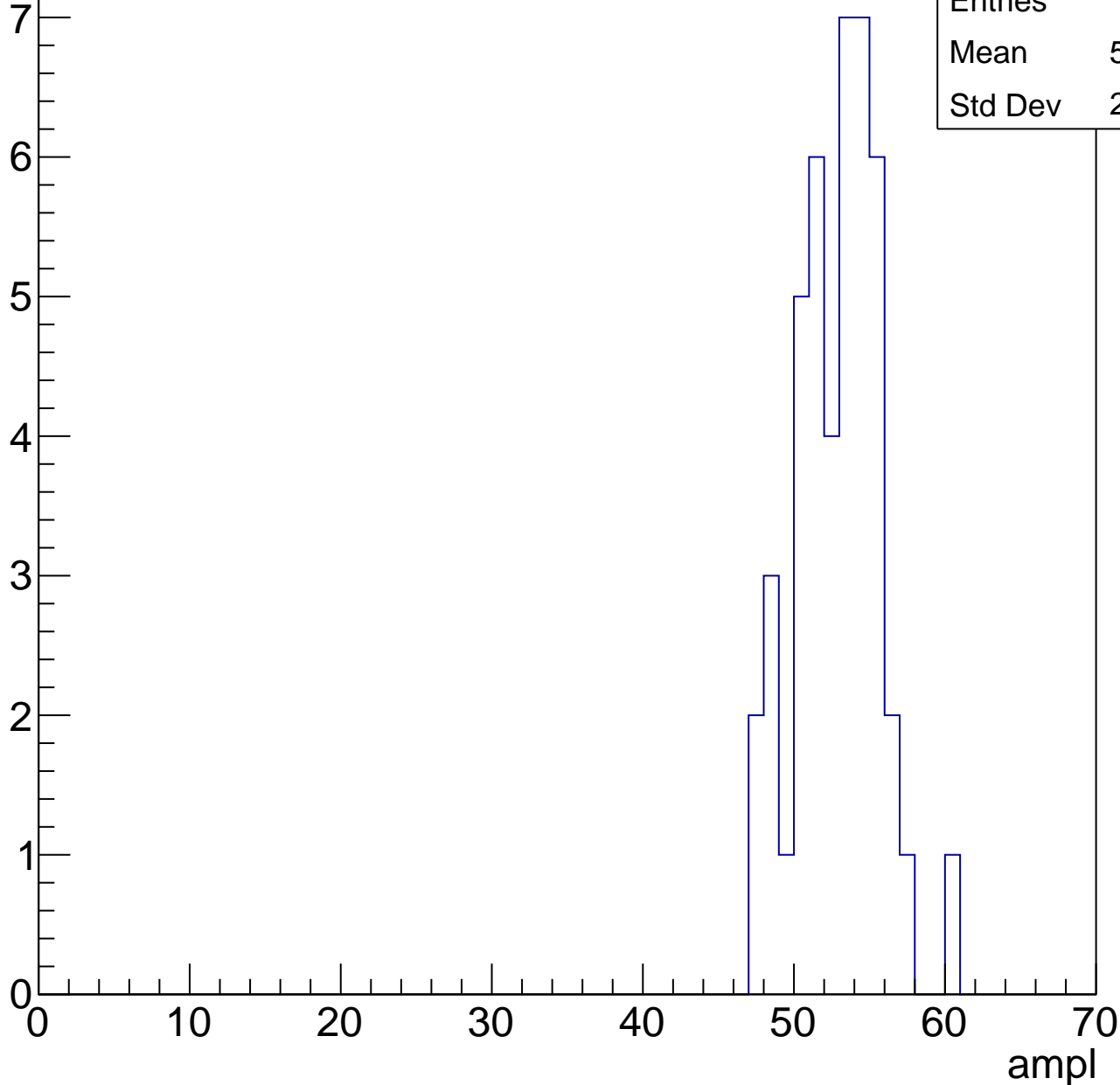


B1L003S, U20-ch118, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	52.42
Std Dev	2.728

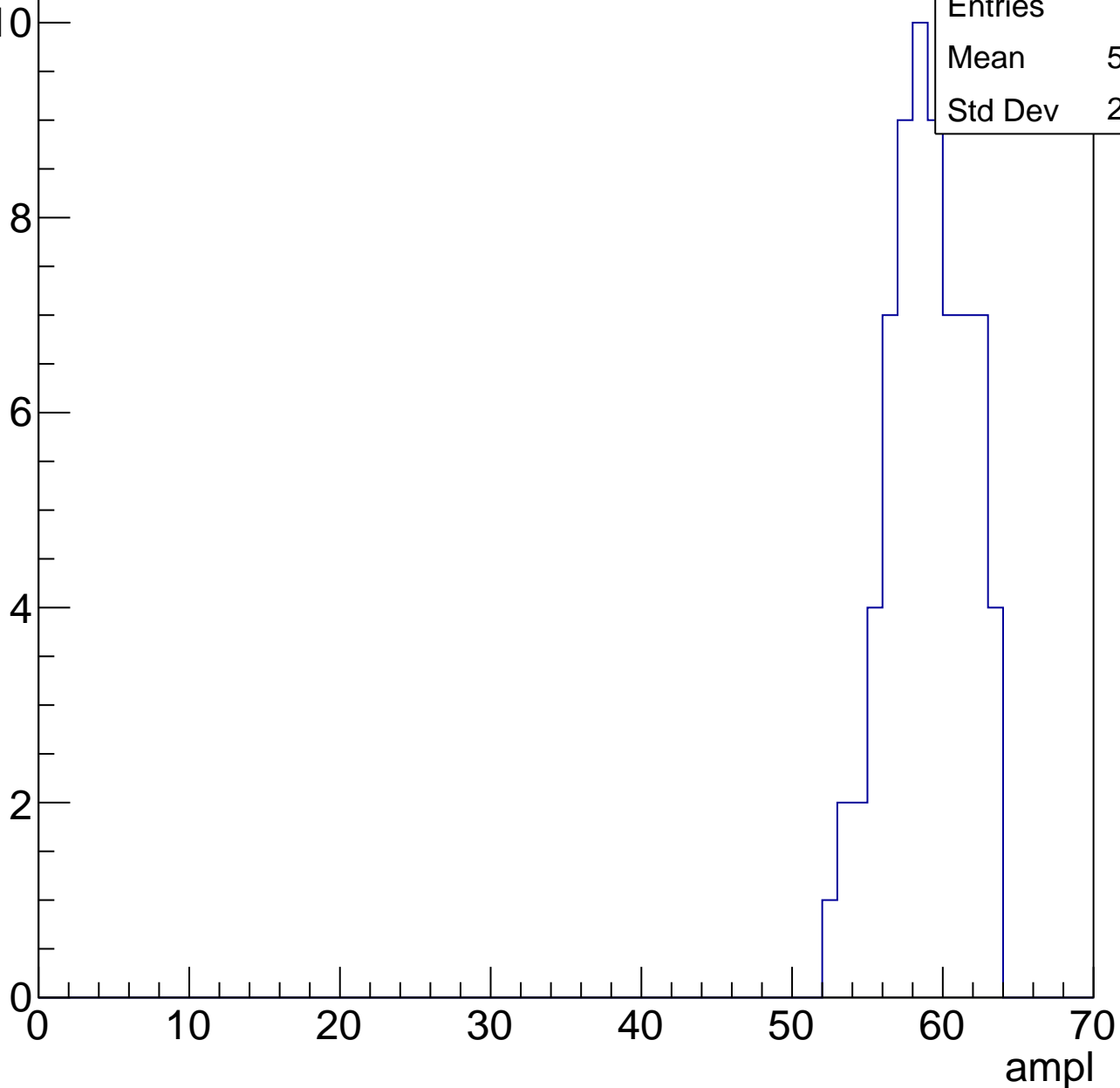


B1L003S, U20-ch118, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	58.48
Std Dev	2.657



B1L003S, U20-ch118, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

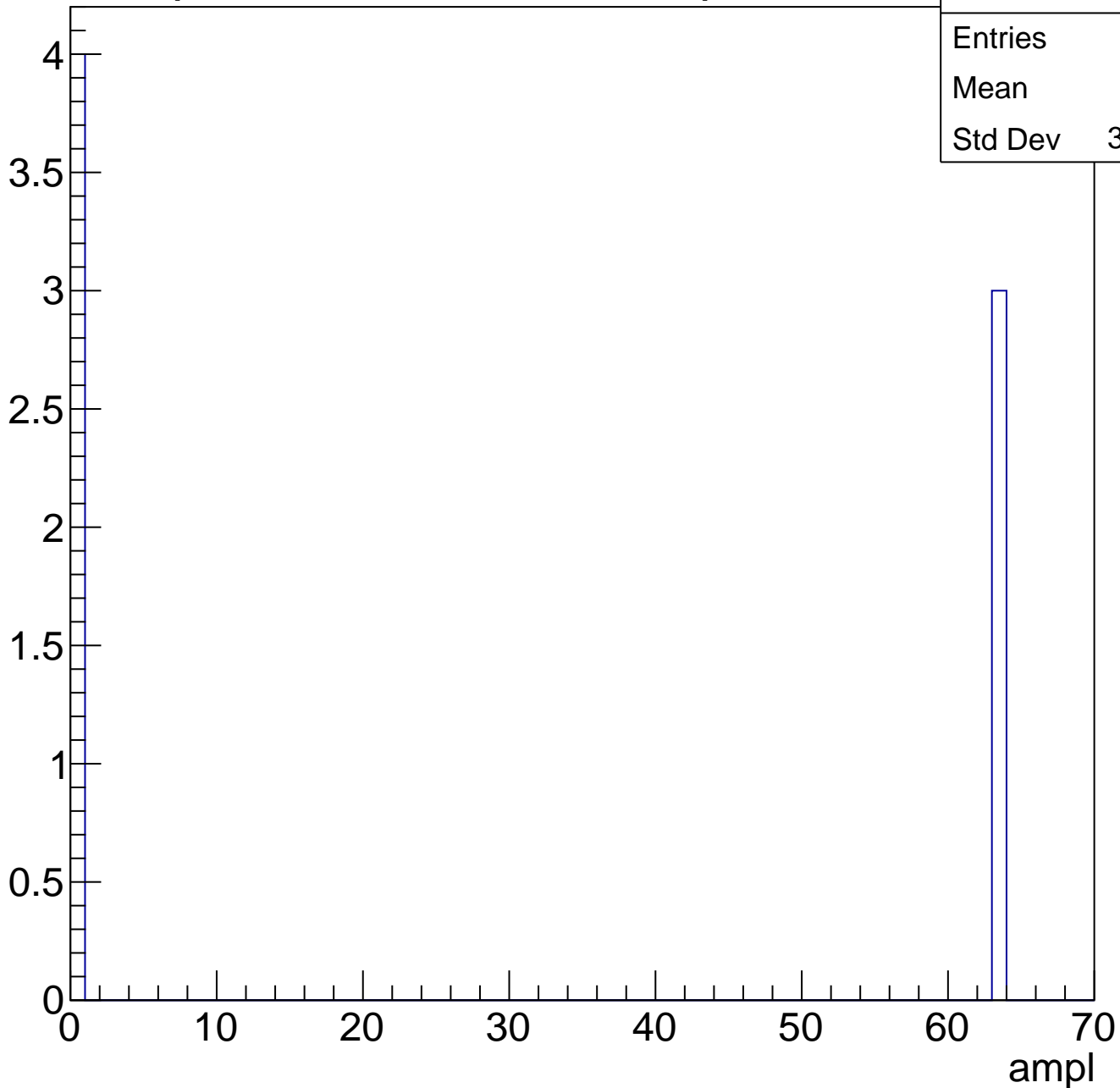
Entries	13
Mean	61
Std Dev	1.881

0

B1L003S, U20-ch118, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	7
Mean	27
Std Dev	31.18

B1L003S, U20-ch119, adc0

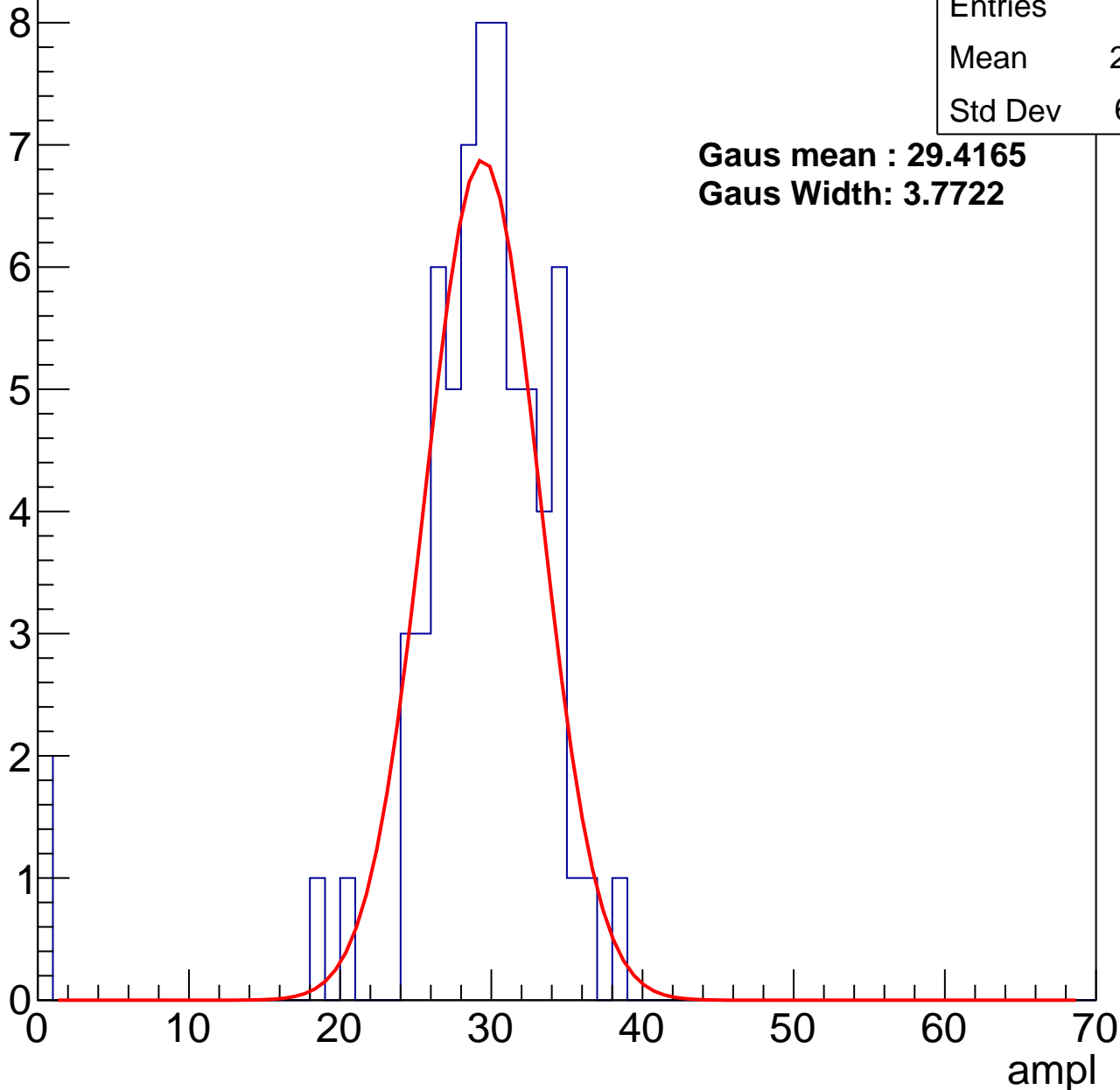
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.42
Std Dev	6.131

Gaus mean : 29.4165

Gaus Width: 3.7722



B1L003S, U20-ch119, adc1

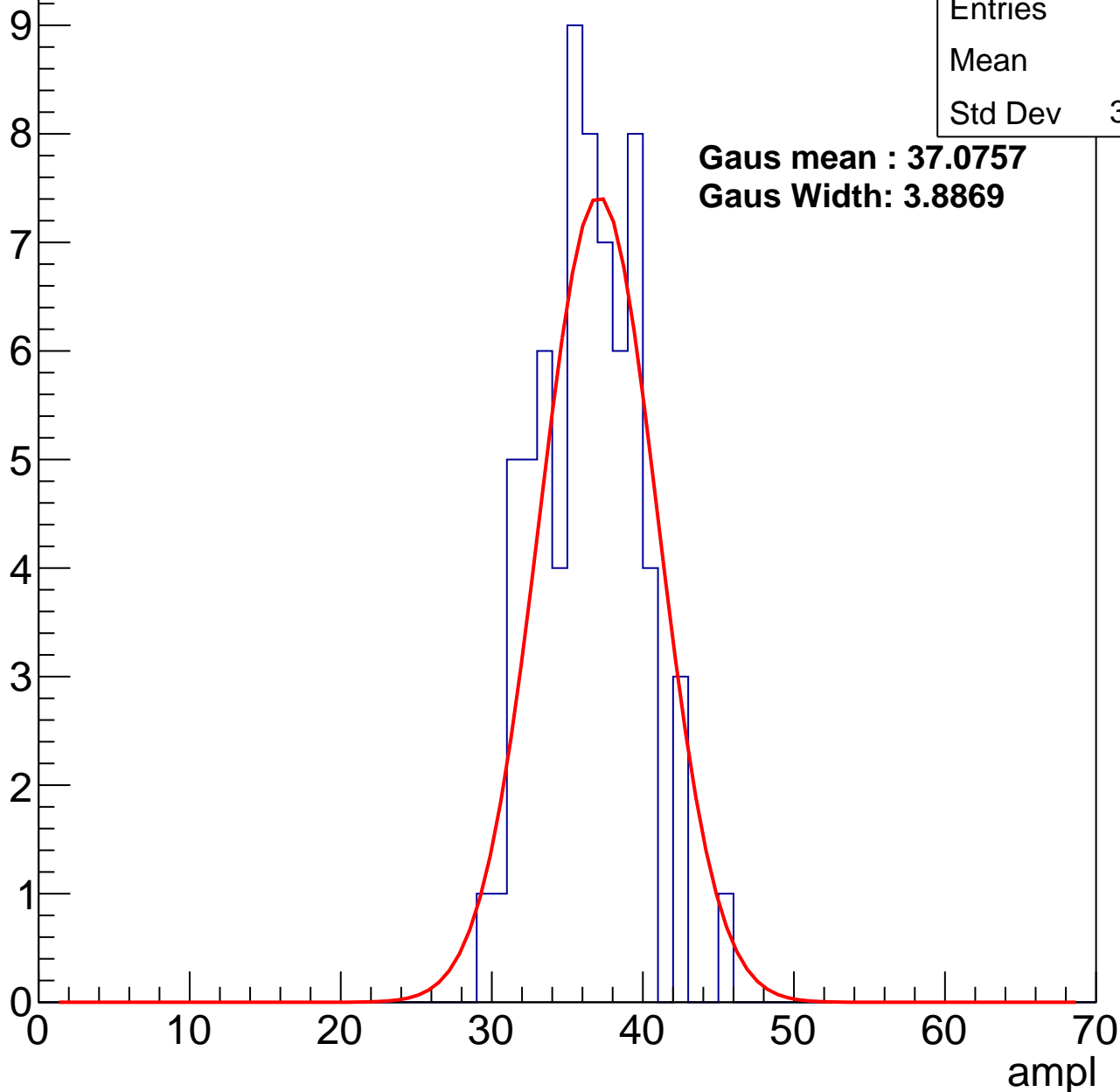
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.9
Std Dev	3.264

Gaus mean : 37.0757

Gaus Width: 3.8869



B1L003S, U20-ch119, adc2

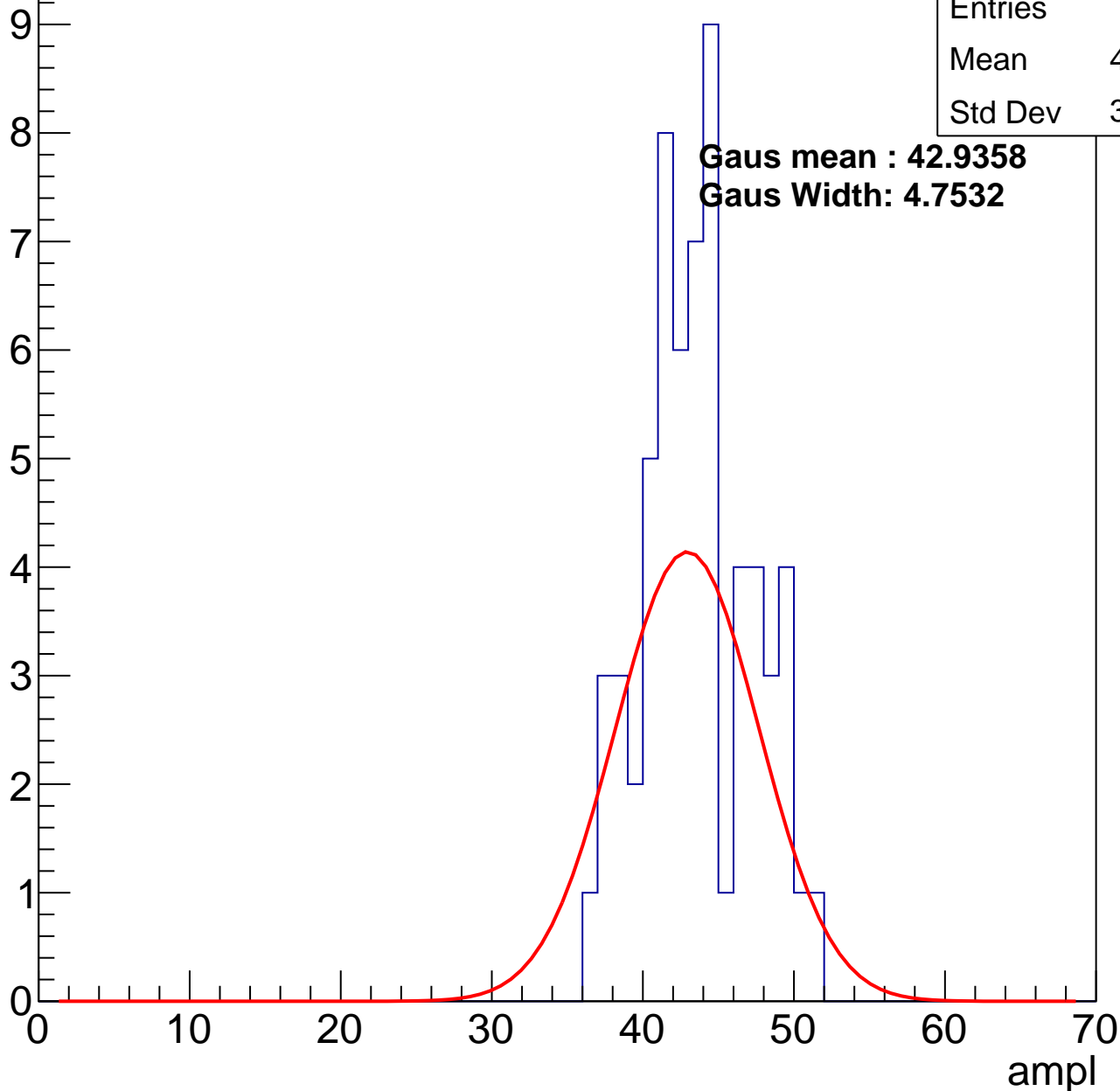
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.13
Std Dev	3.572

Gaus mean : 42.9358

Gaus Width: 4.7532

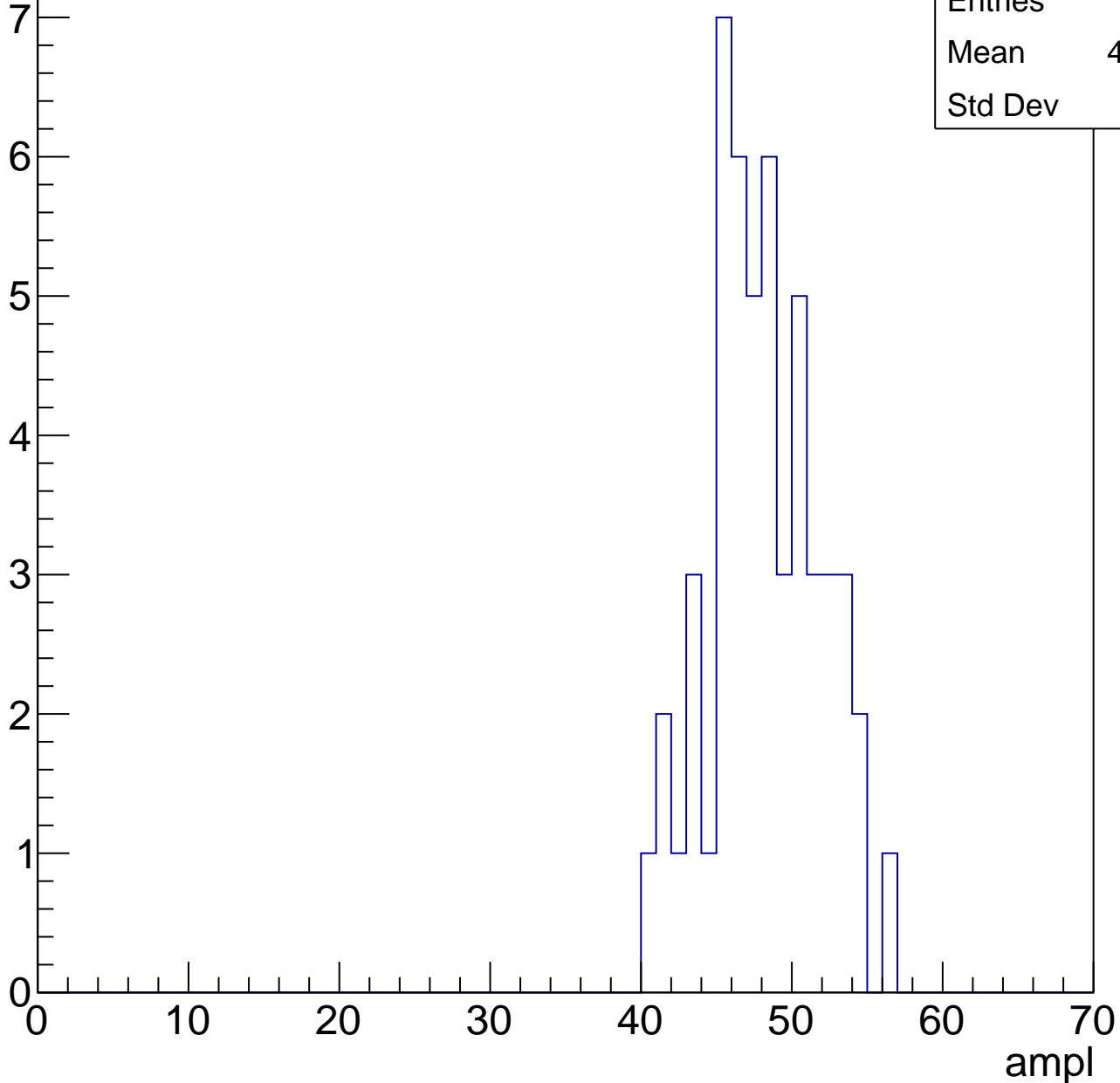


B1L003S, U20-ch119, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	47.69
Std Dev	3.63

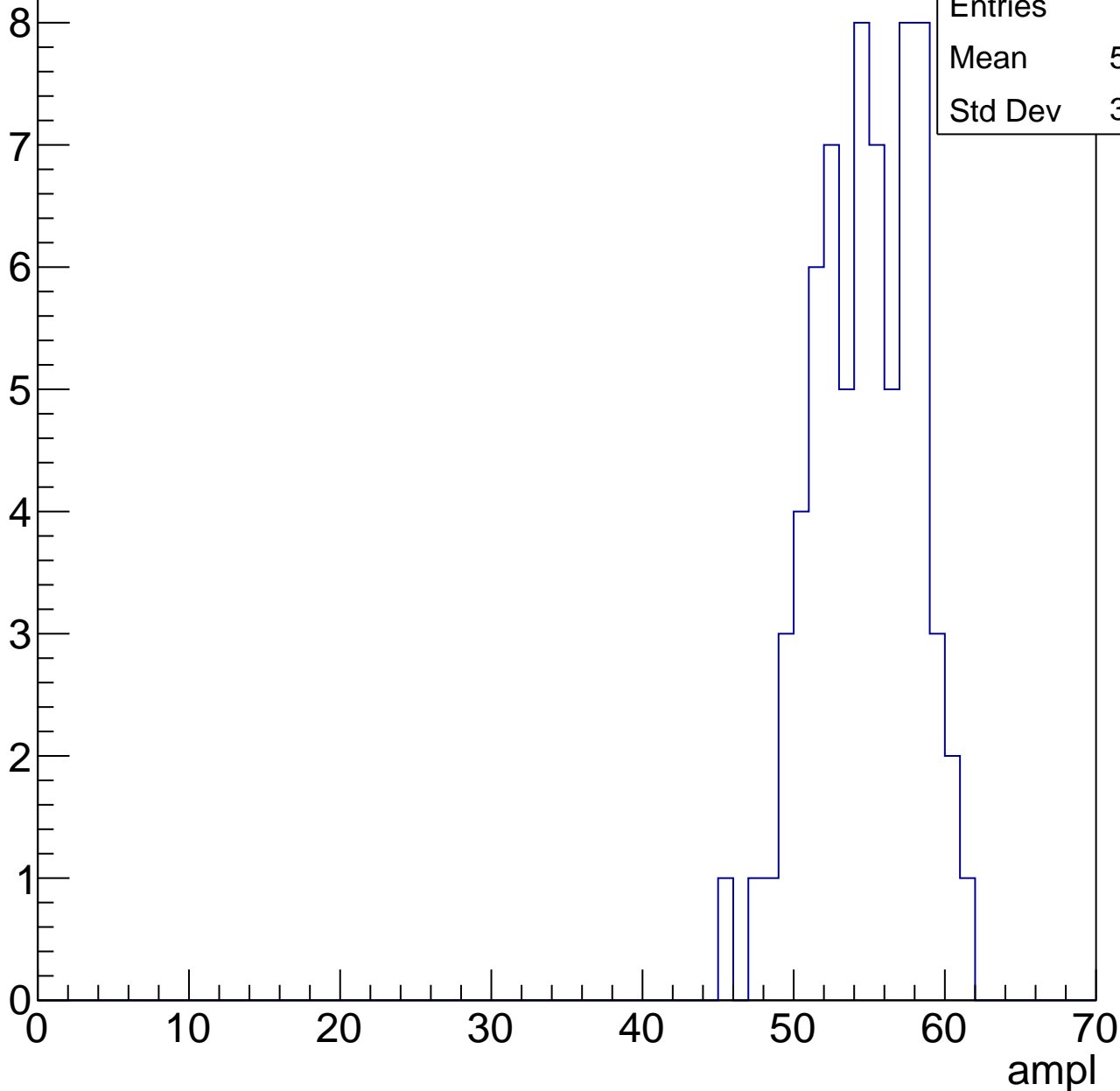


B1L003S, U20-ch119, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	54.24
Std Dev	3.404

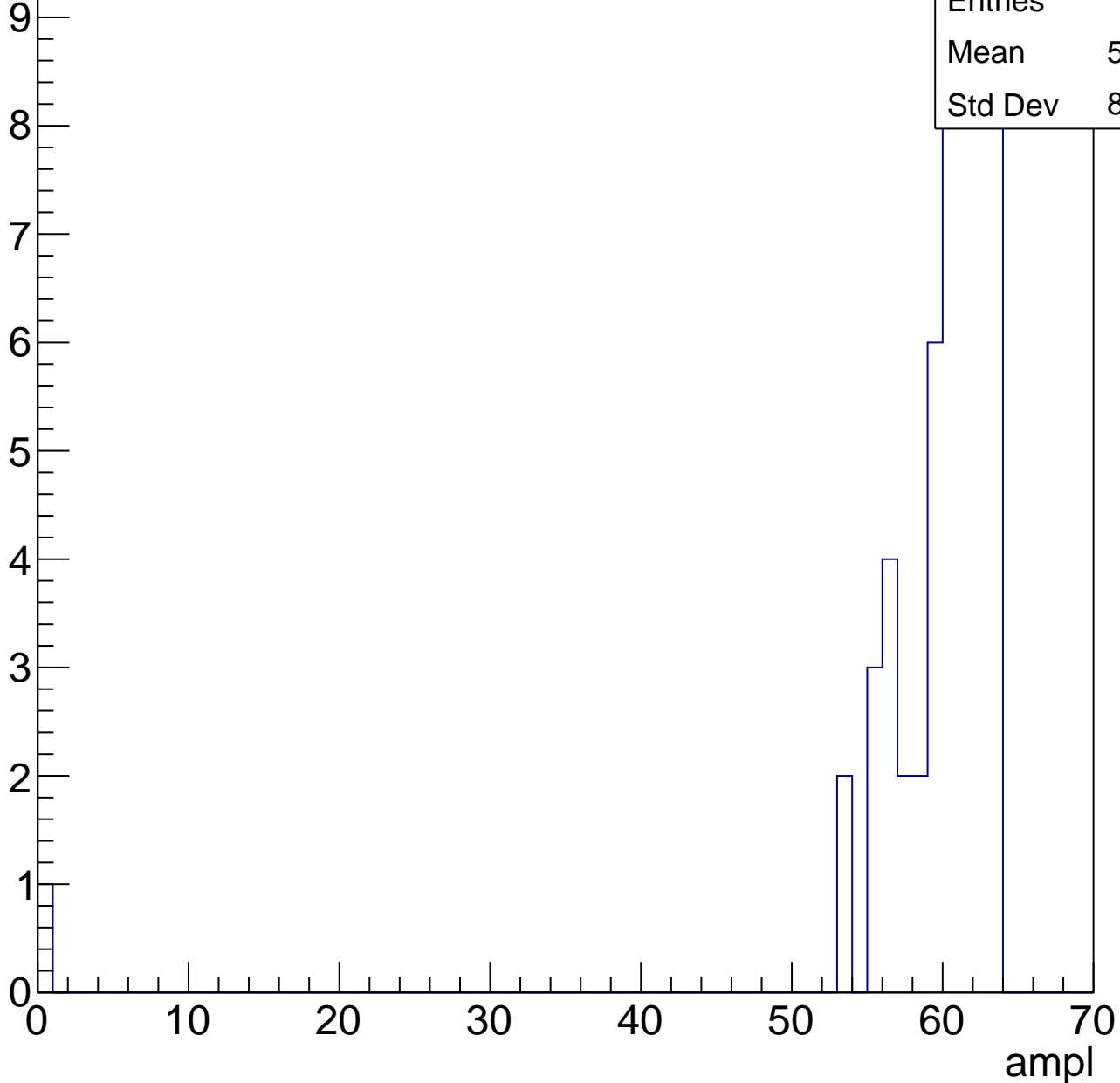


B1L003S, U20-ch119, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

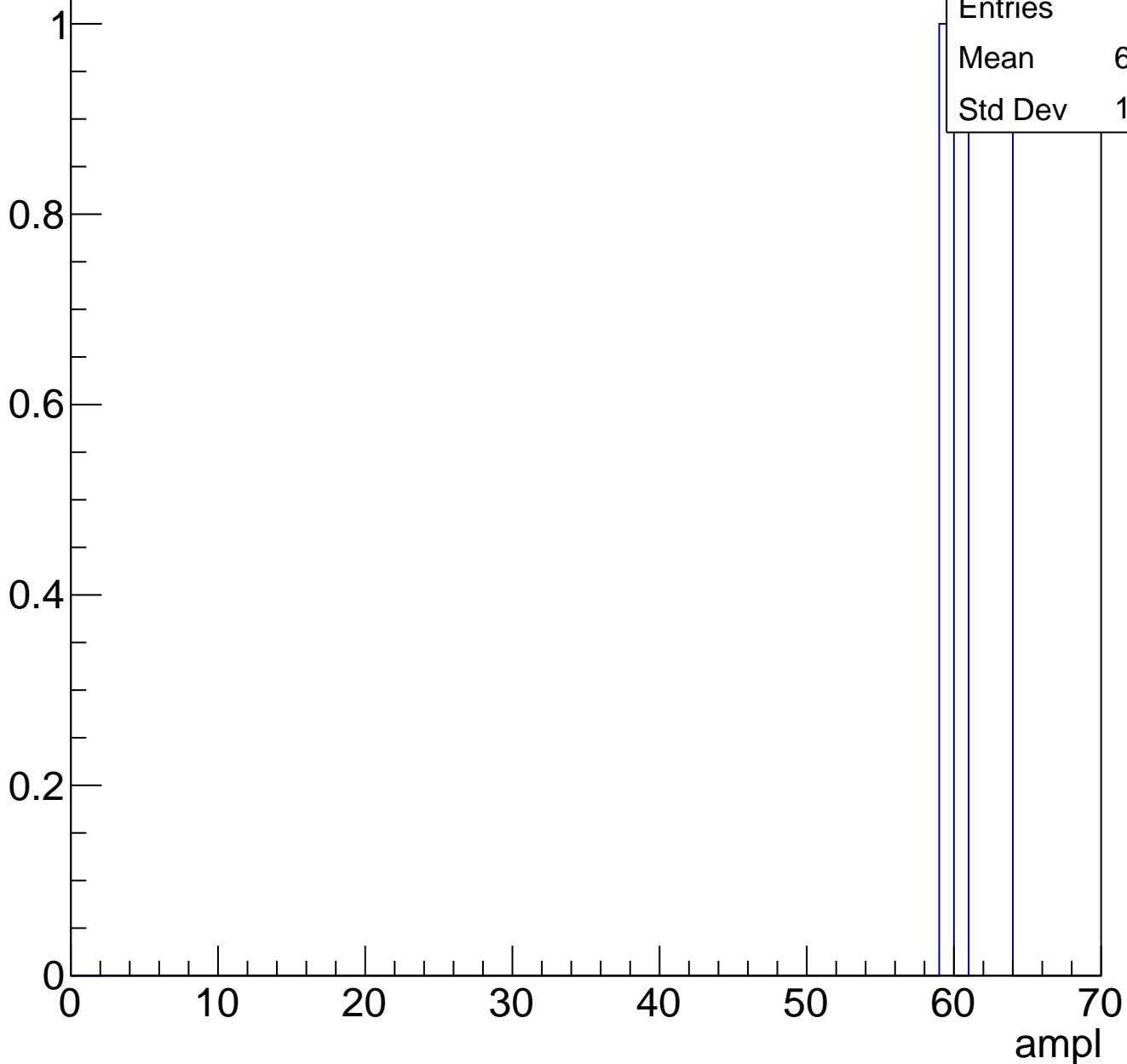
Entries	54
Mean	58.74
Std Dev	8.507



B1L003S, U20-ch119, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch119, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch120, adc0

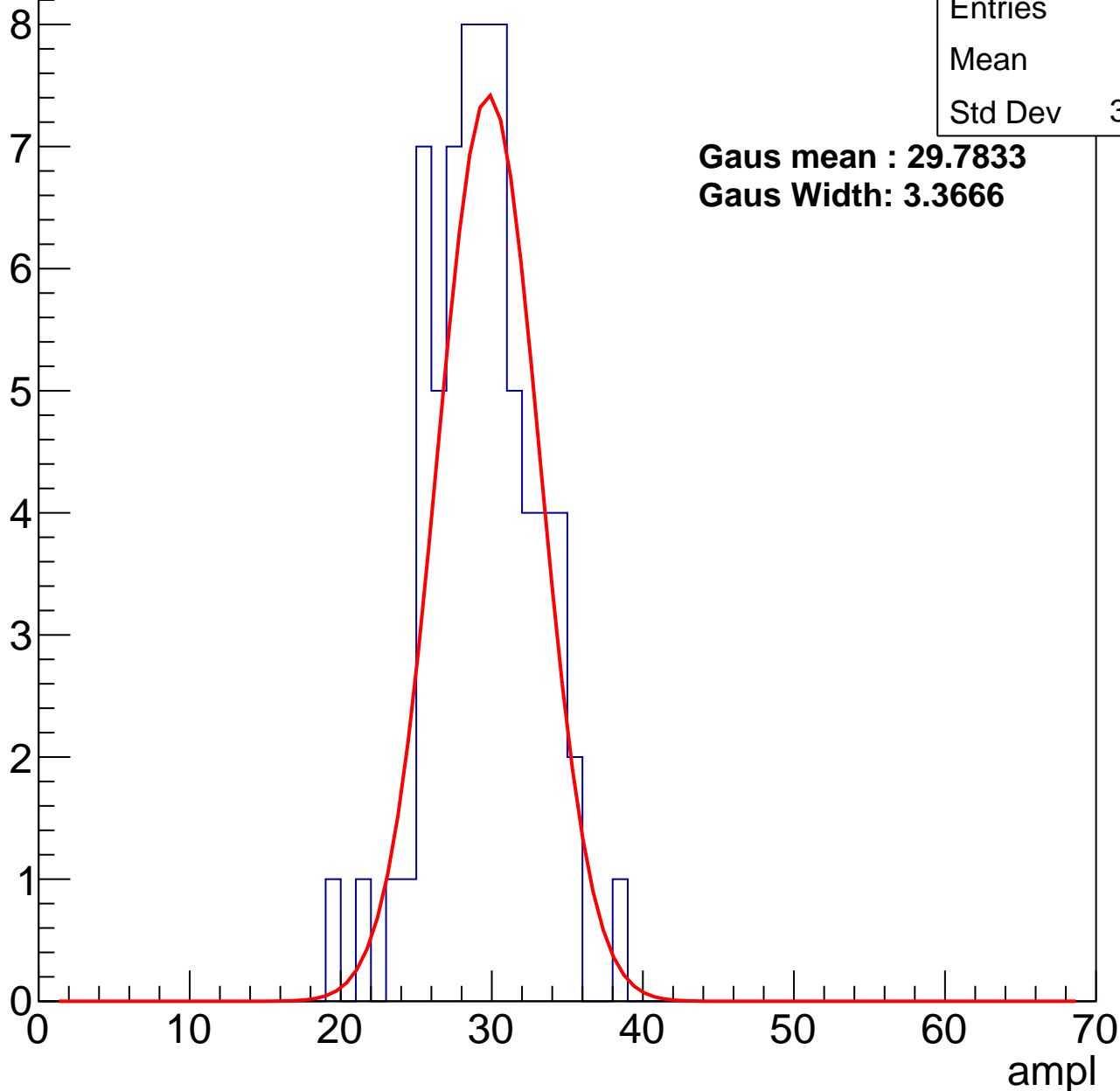
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.9
Std Dev	3.452

Gaus mean : 29.7833

Gaus Width: 3.3666



B1L003S, U20-ch120, adc1

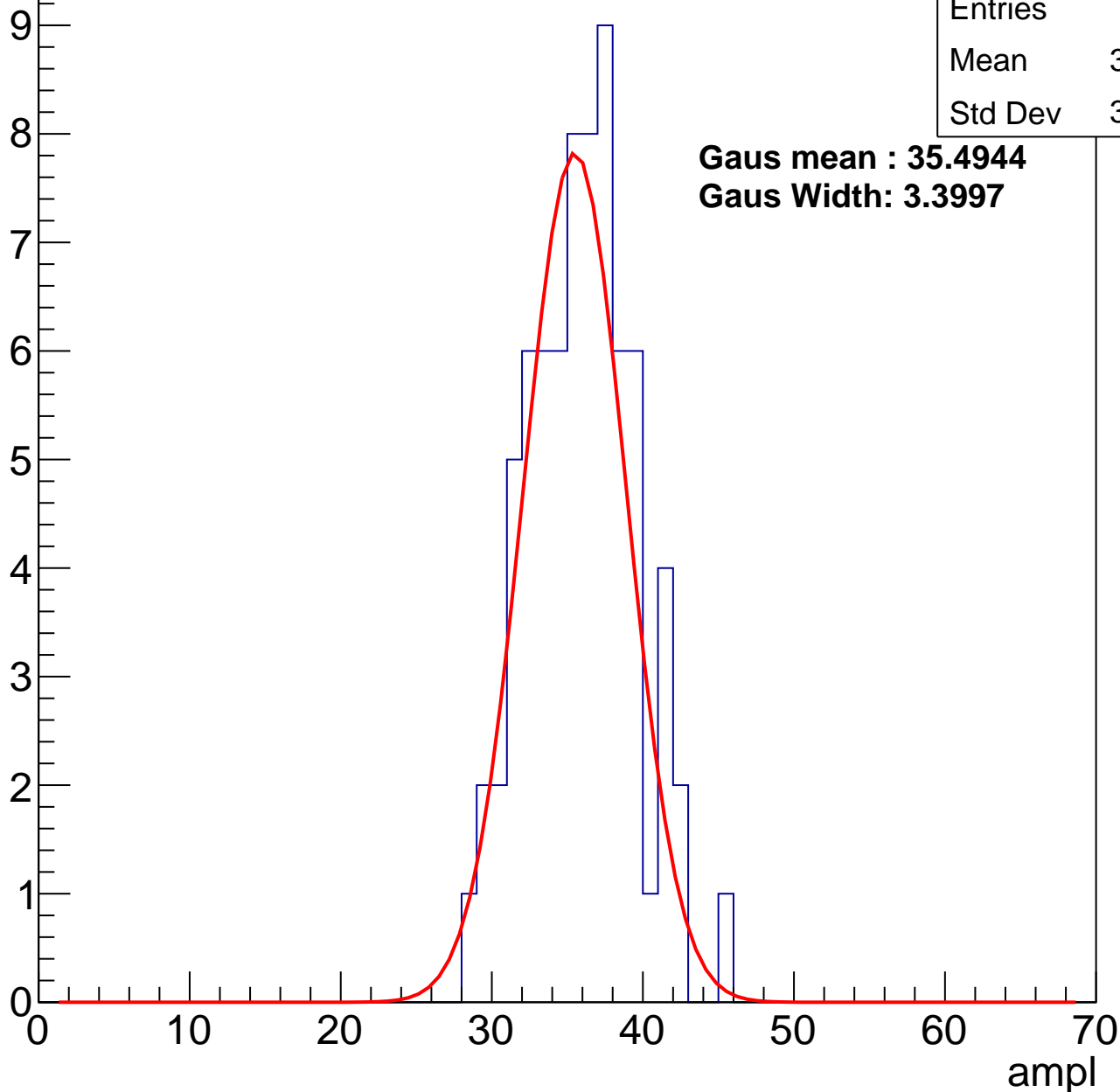
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.49
Std Dev	3.469

Gaus mean : 35.4944

Gaus Width: 3.3997



B1L003S, U20-ch120, adc2

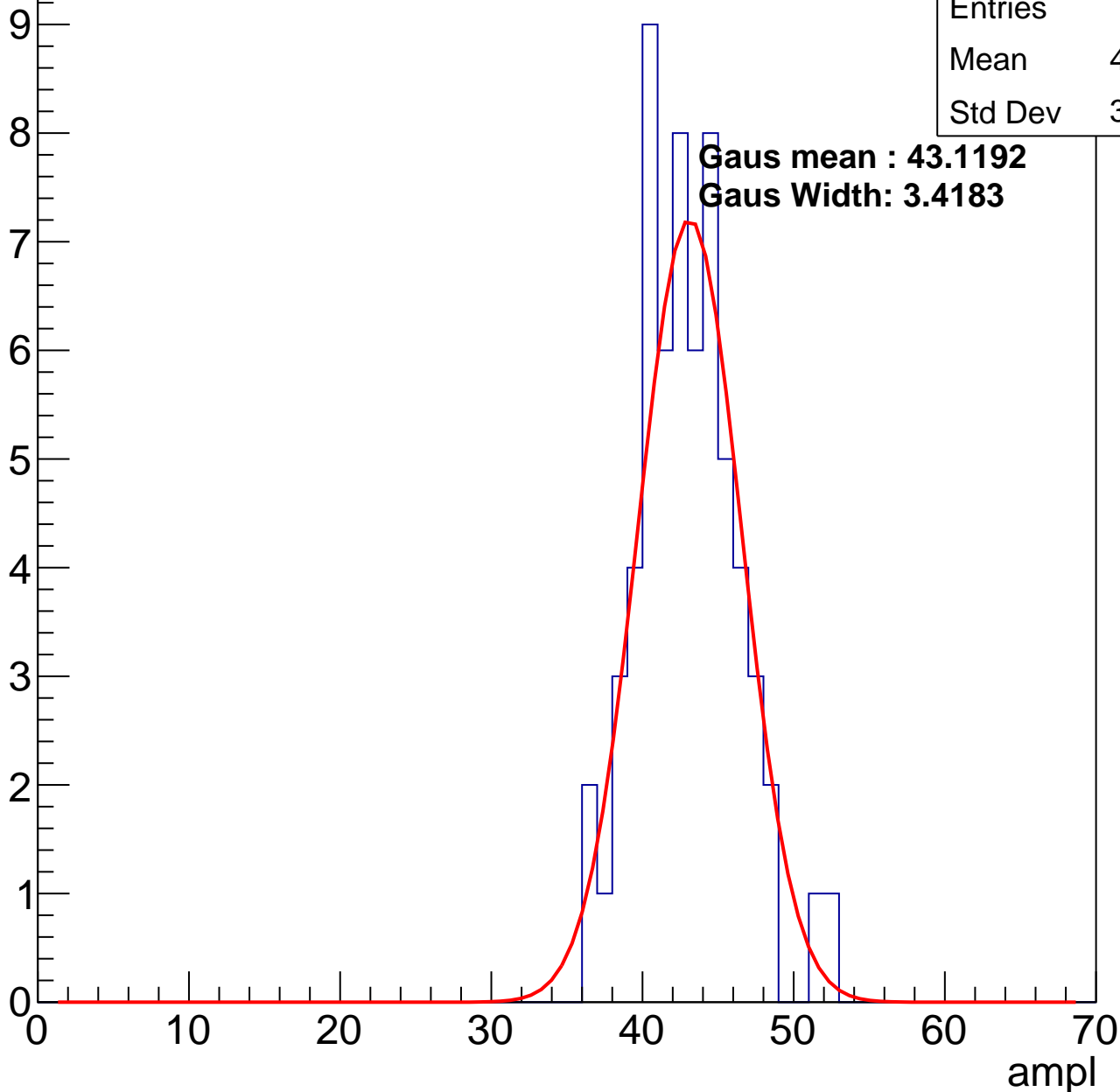
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.54
Std Dev	3.285

Gaus mean : 43.1192

Gaus Width: 3.4183

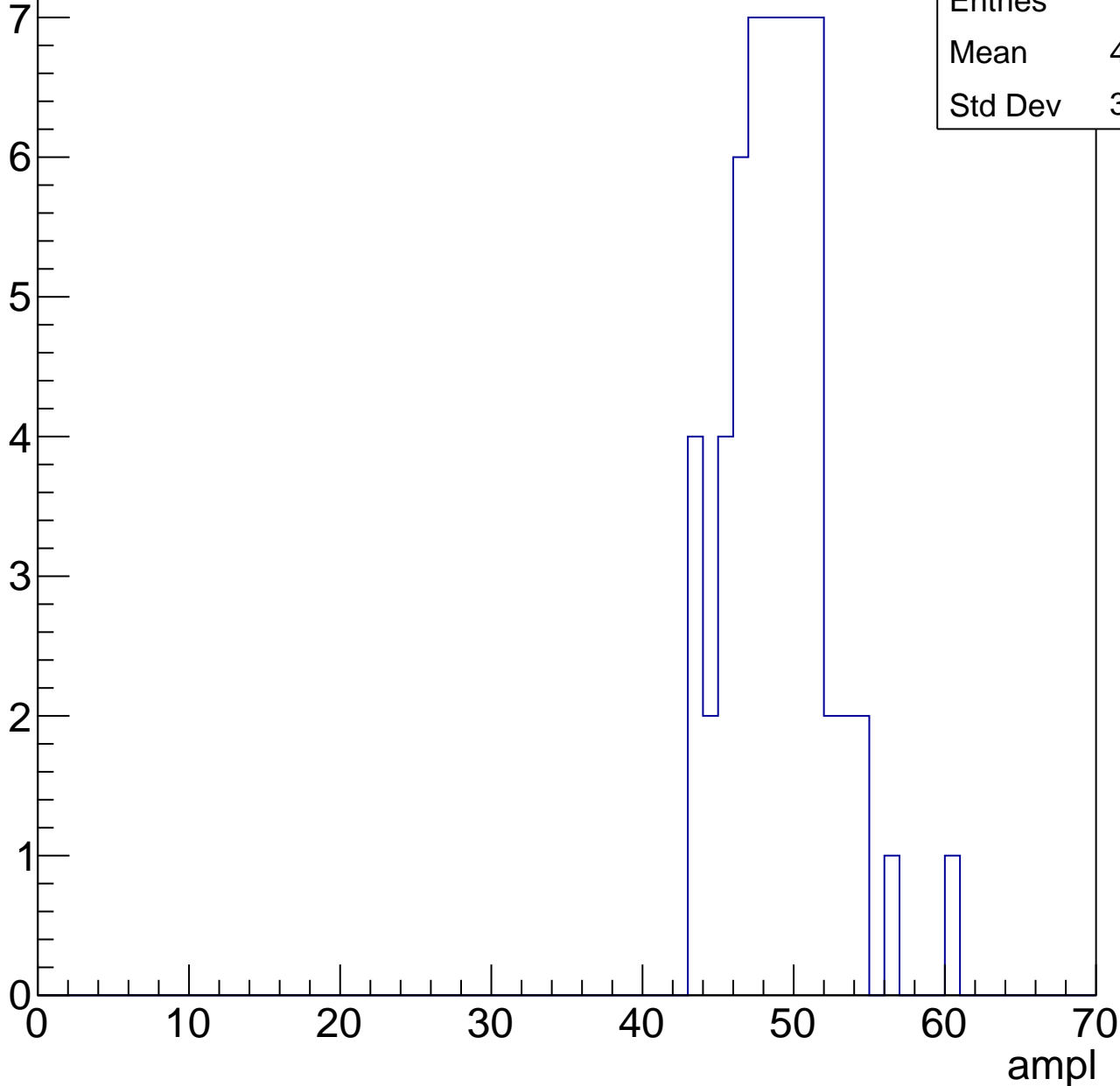


B1L003S, U20-ch120, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

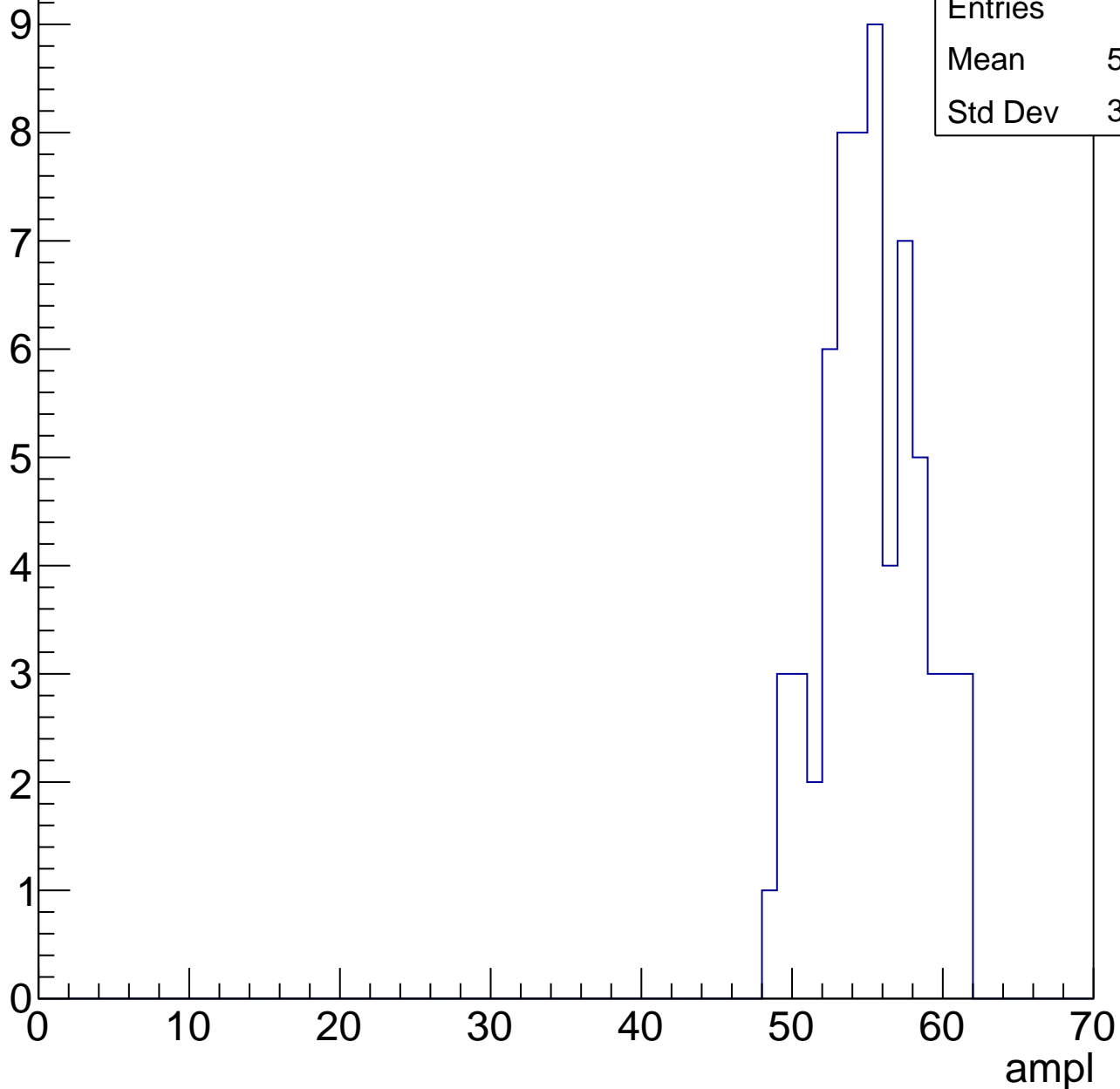
Entries	59
Mean	48.56
Std Dev	3.295



B1L003S, U20-ch120, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

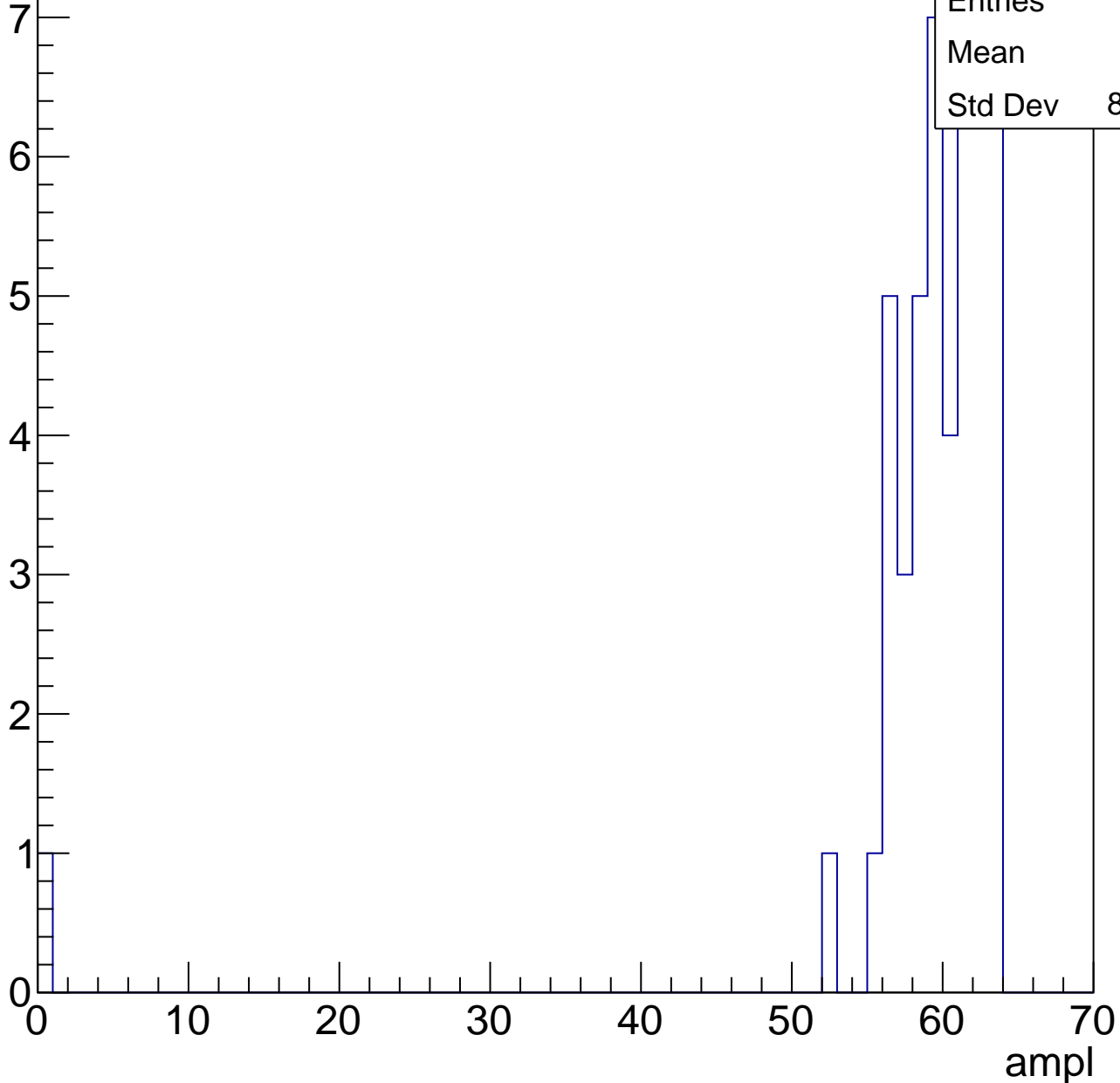


B1L003S, U20-ch120, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

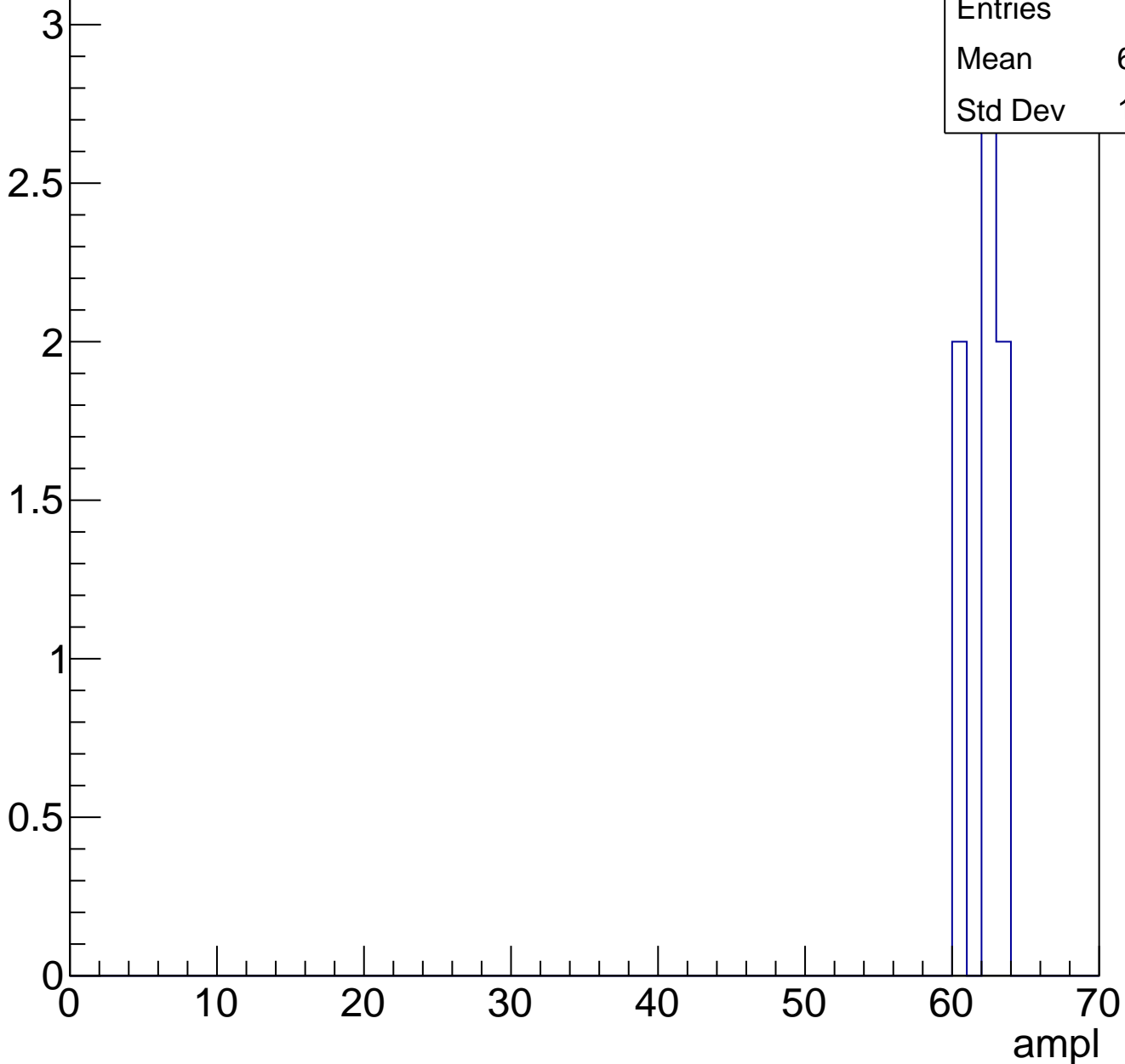
Entries	48
Mean	58.4
Std Dev	8.895



B1L003S, U20-ch120, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch120, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U20-ch121, adc0

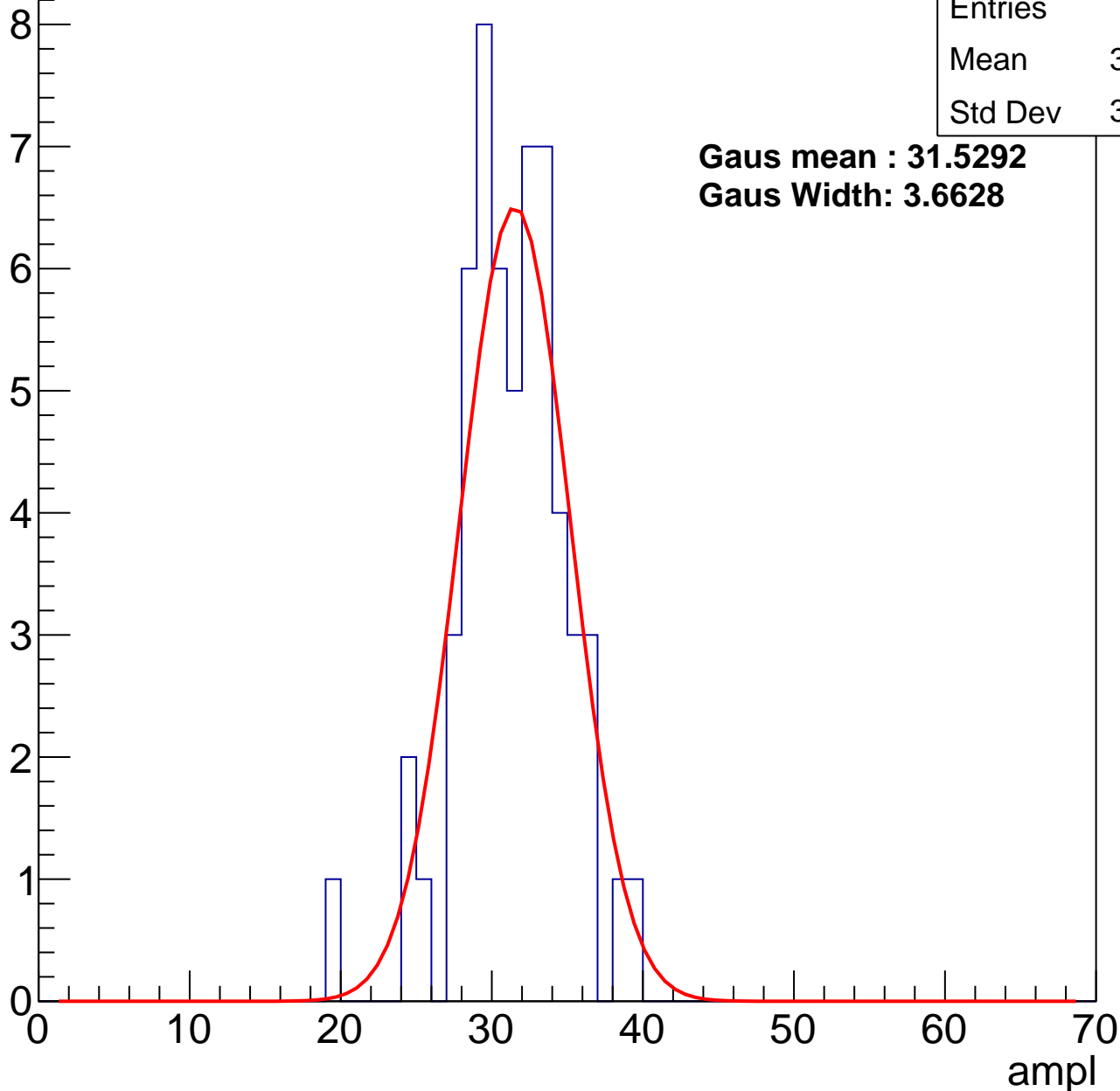
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.84
Std Dev	3.532

Gaus mean : 31.5292

Gaus Width: 3.6628



B1L003S, U20-ch121, adc1

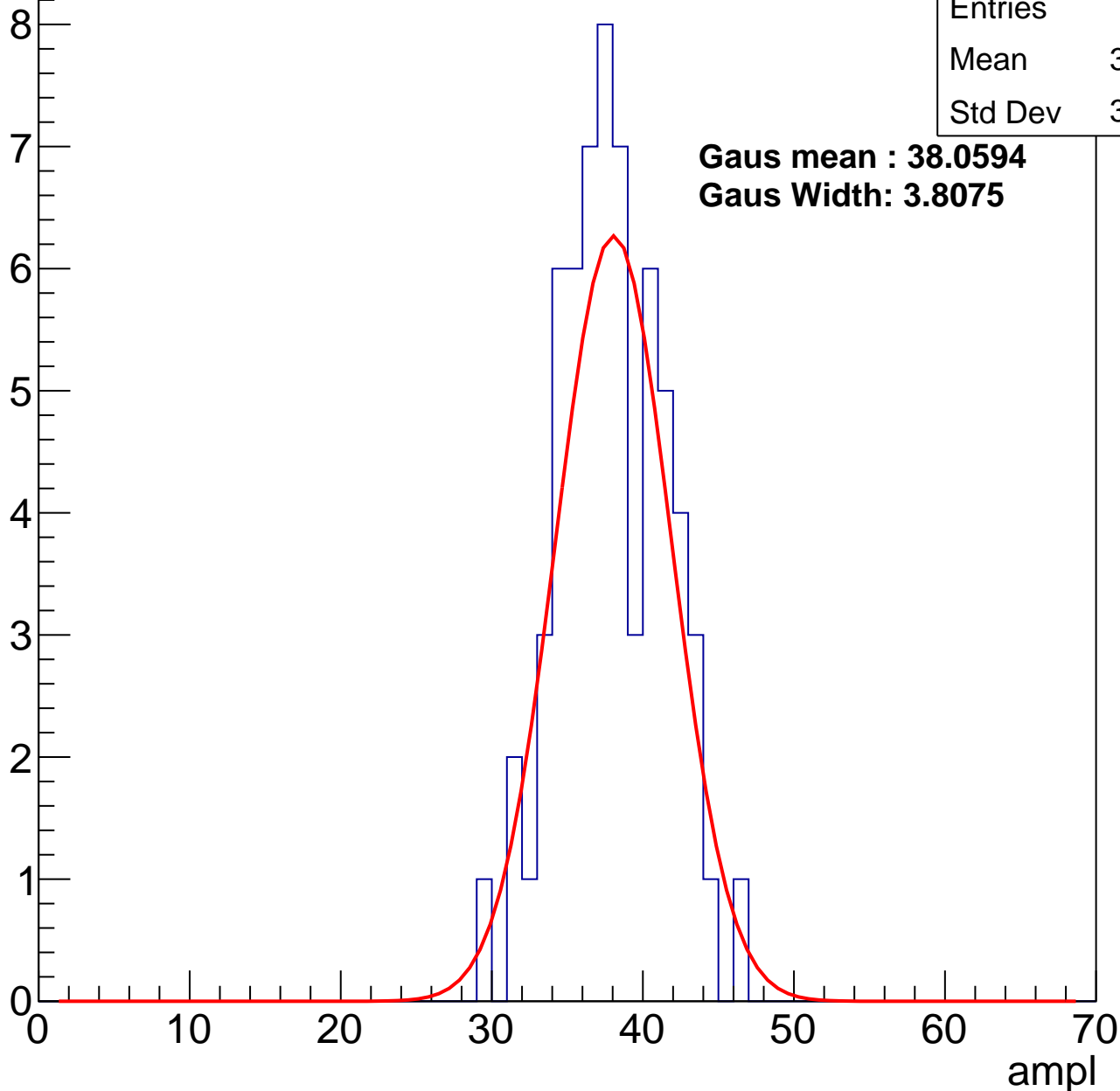
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.48
Std Dev	3.469

Gaus mean : 38.0594

Gaus Width: 3.8075



B1L003S, U20-ch121, adc2

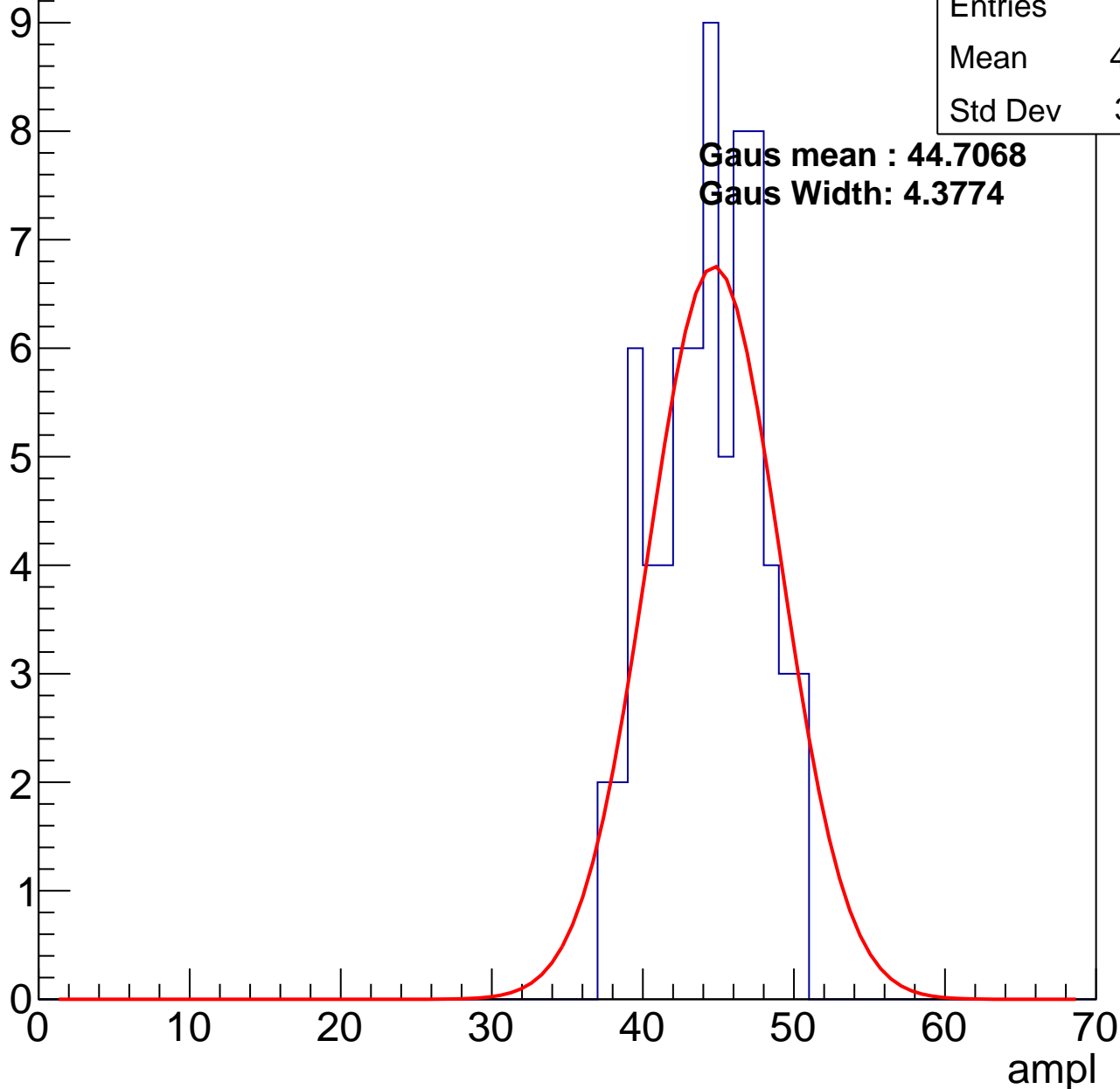
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	43.89
Std Dev	3.391

Gaus mean : 44.7068

Gaus Width: 4.3774

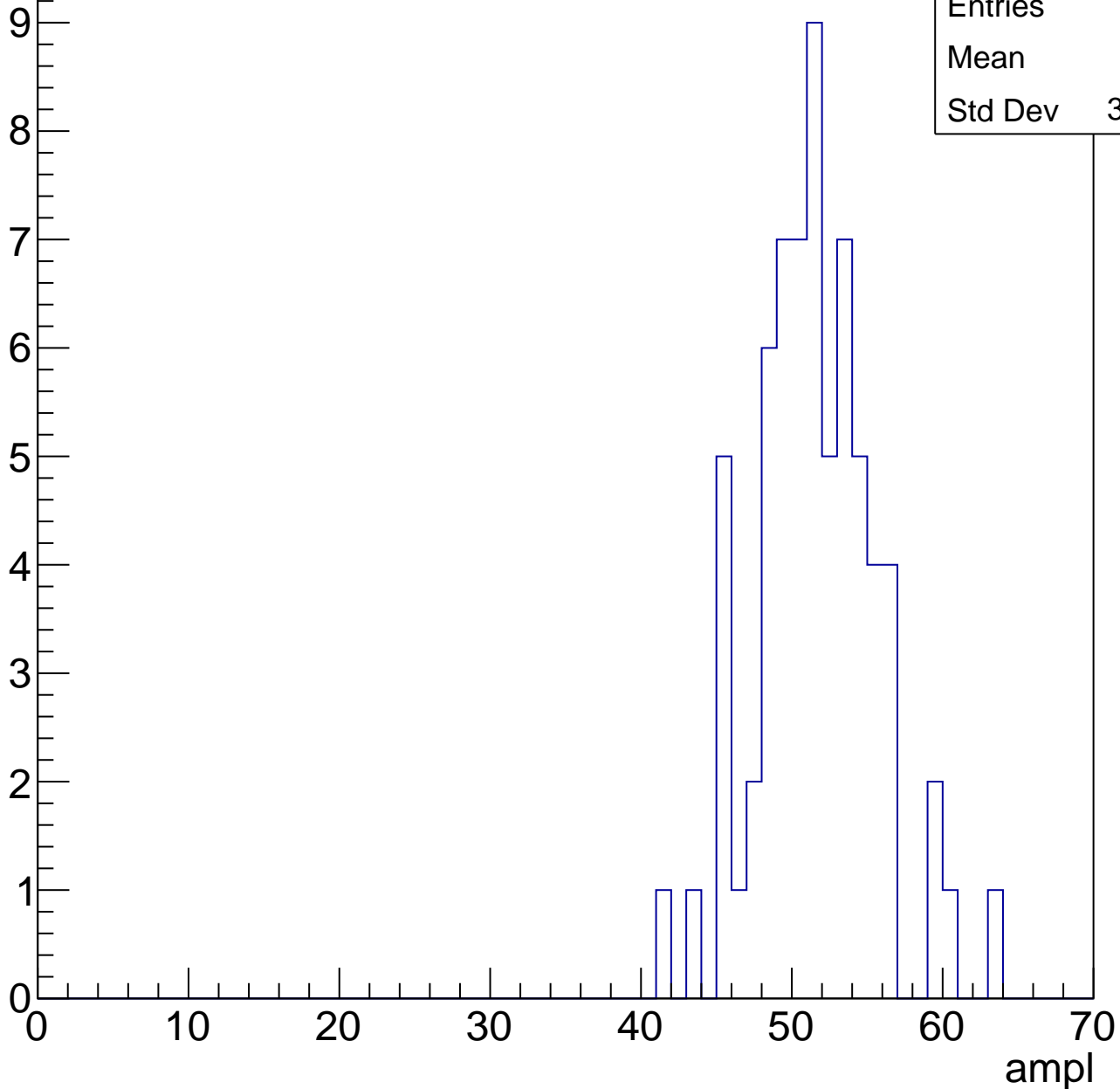


B1L003S, U20-ch121, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	51.1
Std Dev	3.993

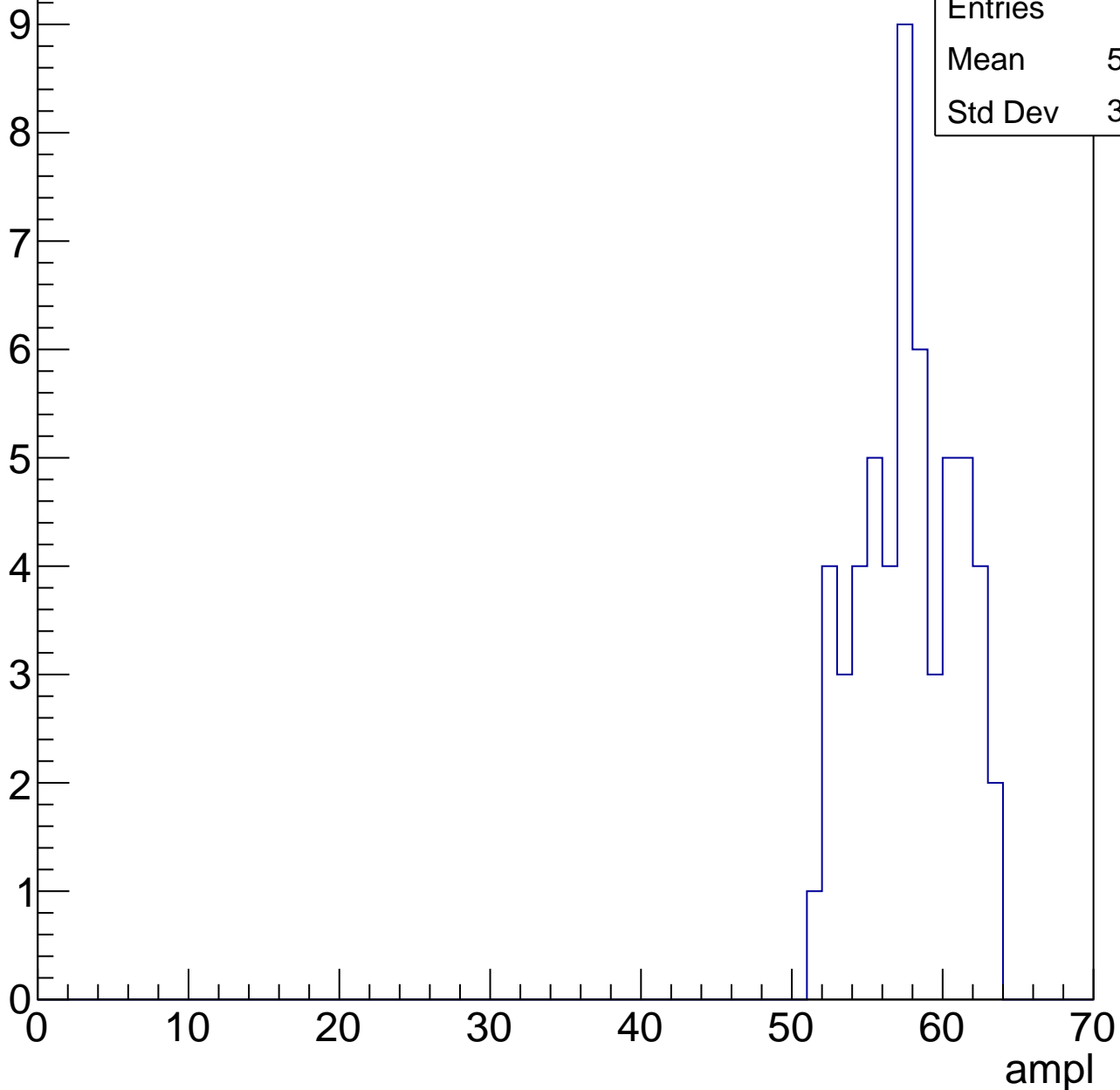


B1L003S, U20-ch121, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	57.27
Std Dev	3.176

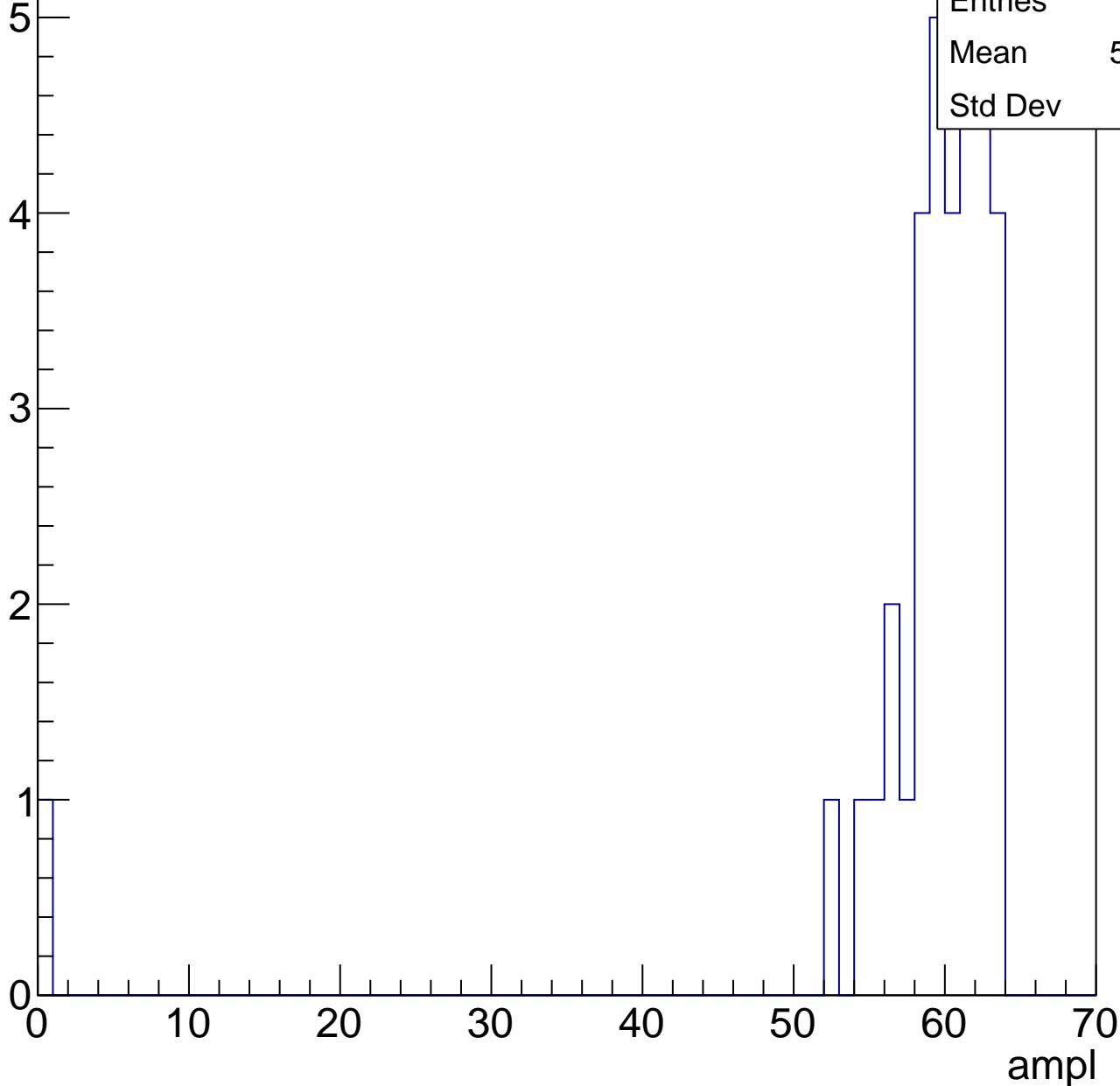


B1L003S, U20-ch121, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	57.76
Std Dev	10.4



B1L003S, U20-ch121, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch121, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch122, adc0

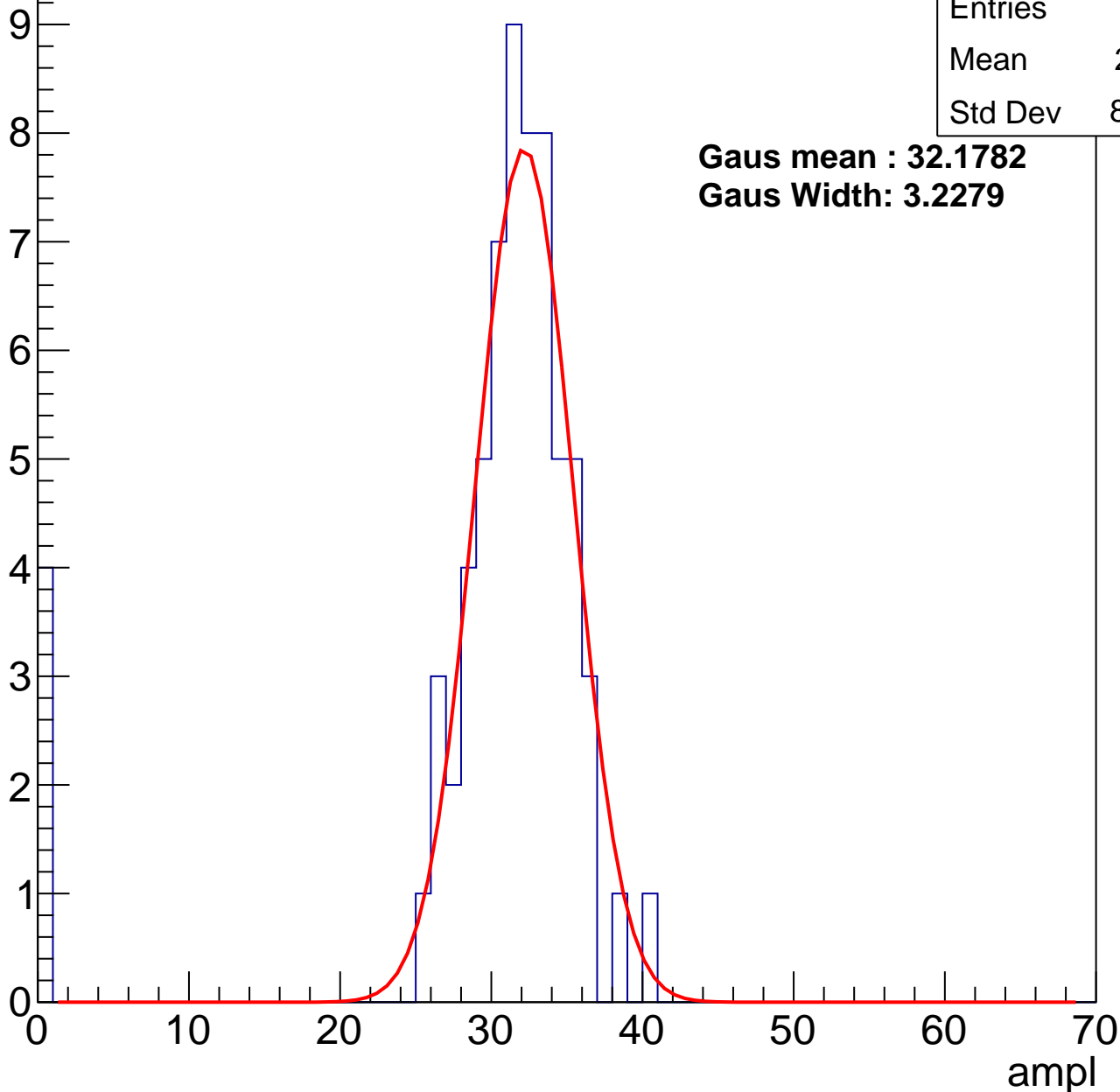
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.61
Std Dev	8.064

Gaus mean : 32.1782

Gaus Width: 3.2279



B1L003S, U20-ch122, adc1

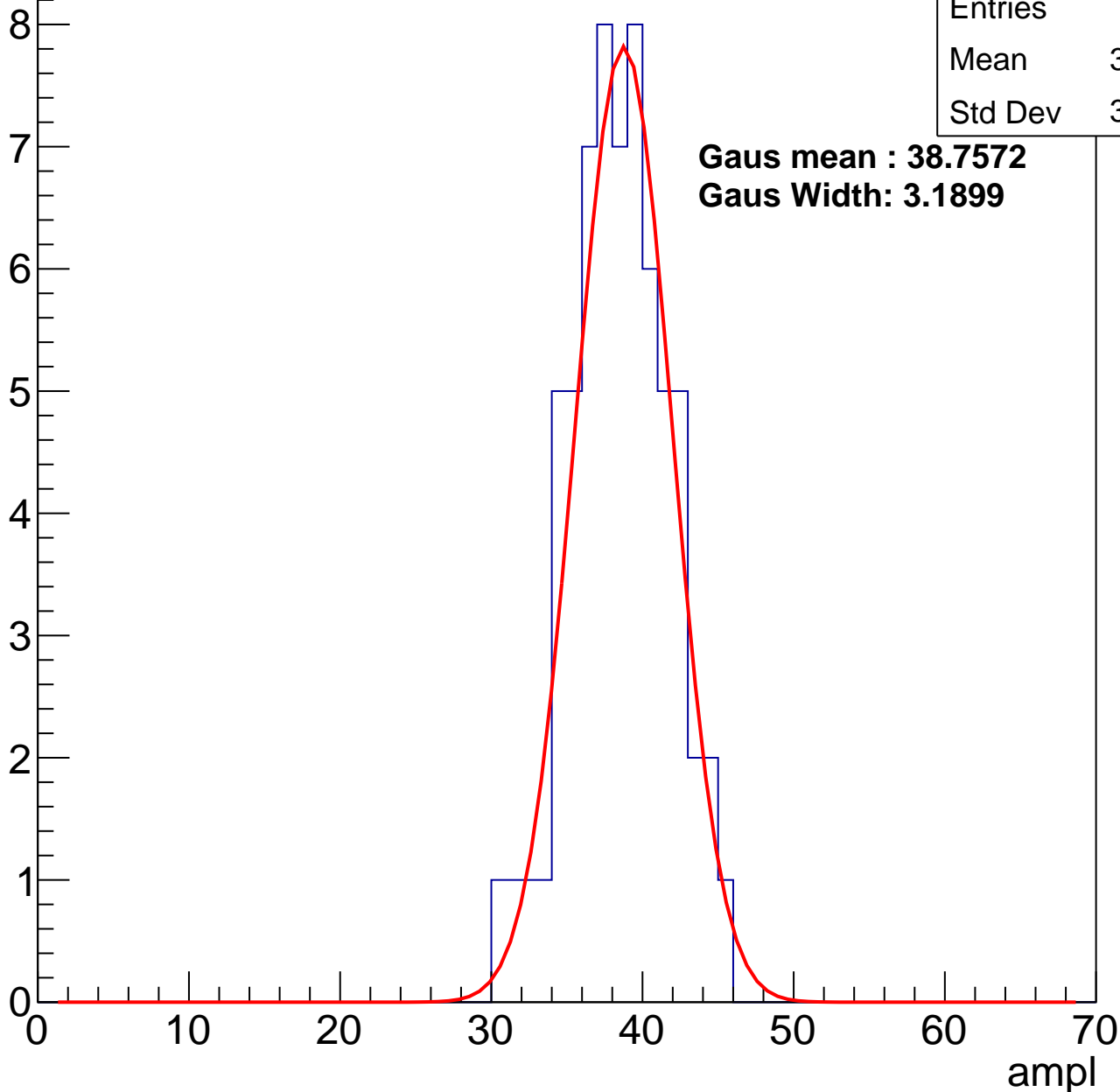
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	38.02
Std Dev	3.194

Gaus mean : 38.7572

Gaus Width: 3.1899



B1L003S, U20-ch122, adc2

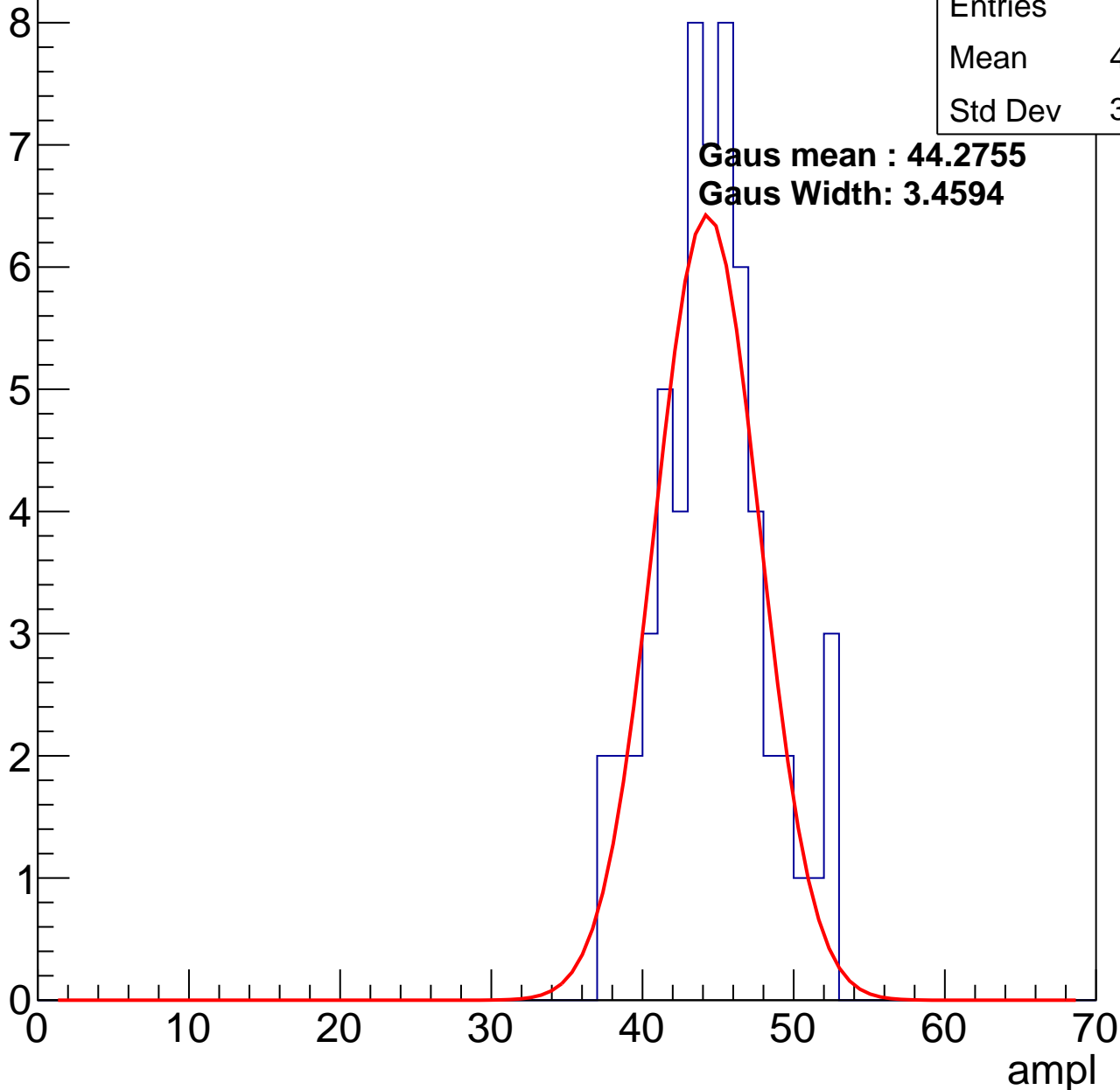
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	44.13
Std Dev	3.566

Gaus mean : 44.2755

Gaus Width: 3.4594

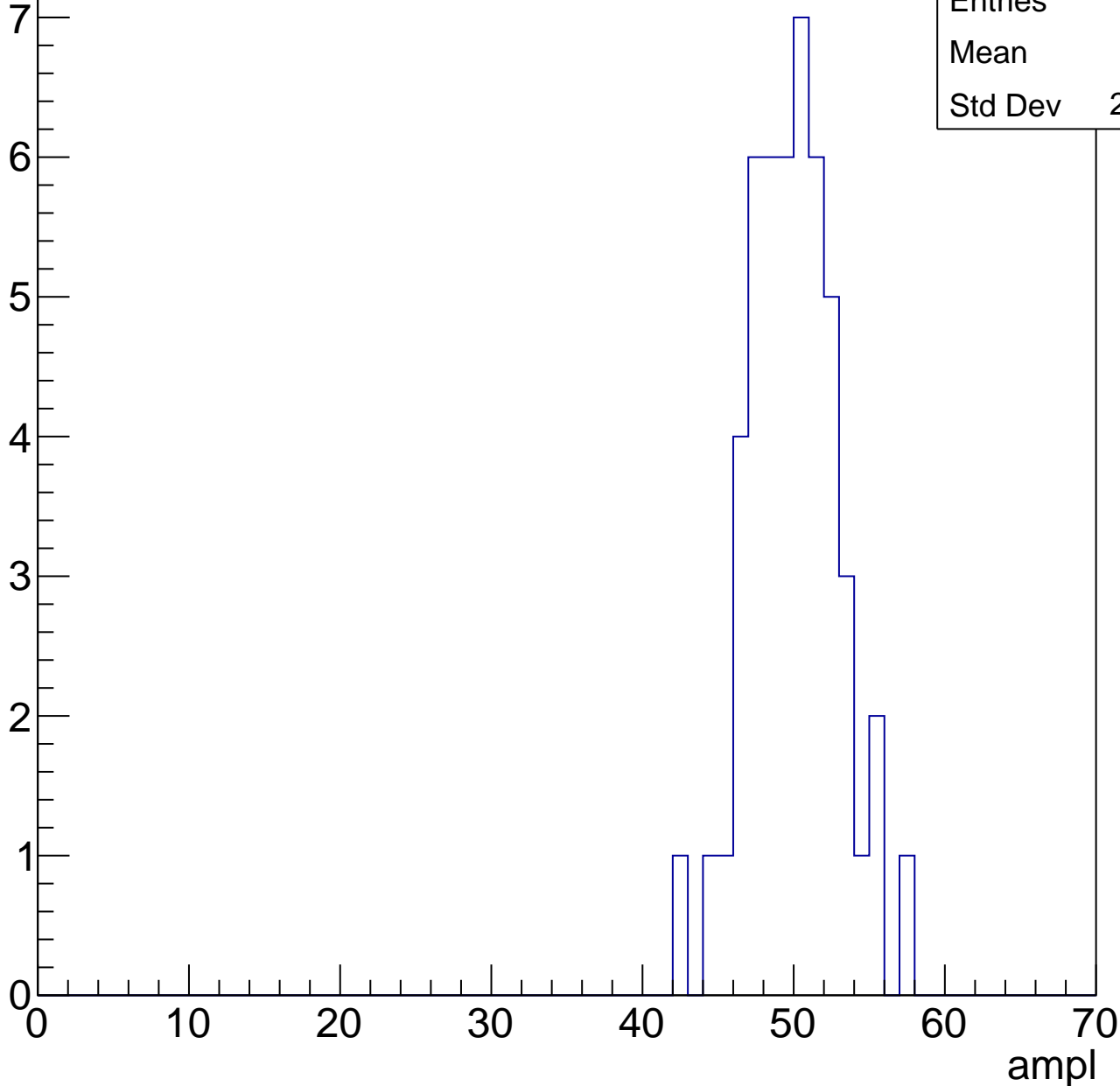


B1L003S, U20-ch122, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	49.5
Std Dev	2.927

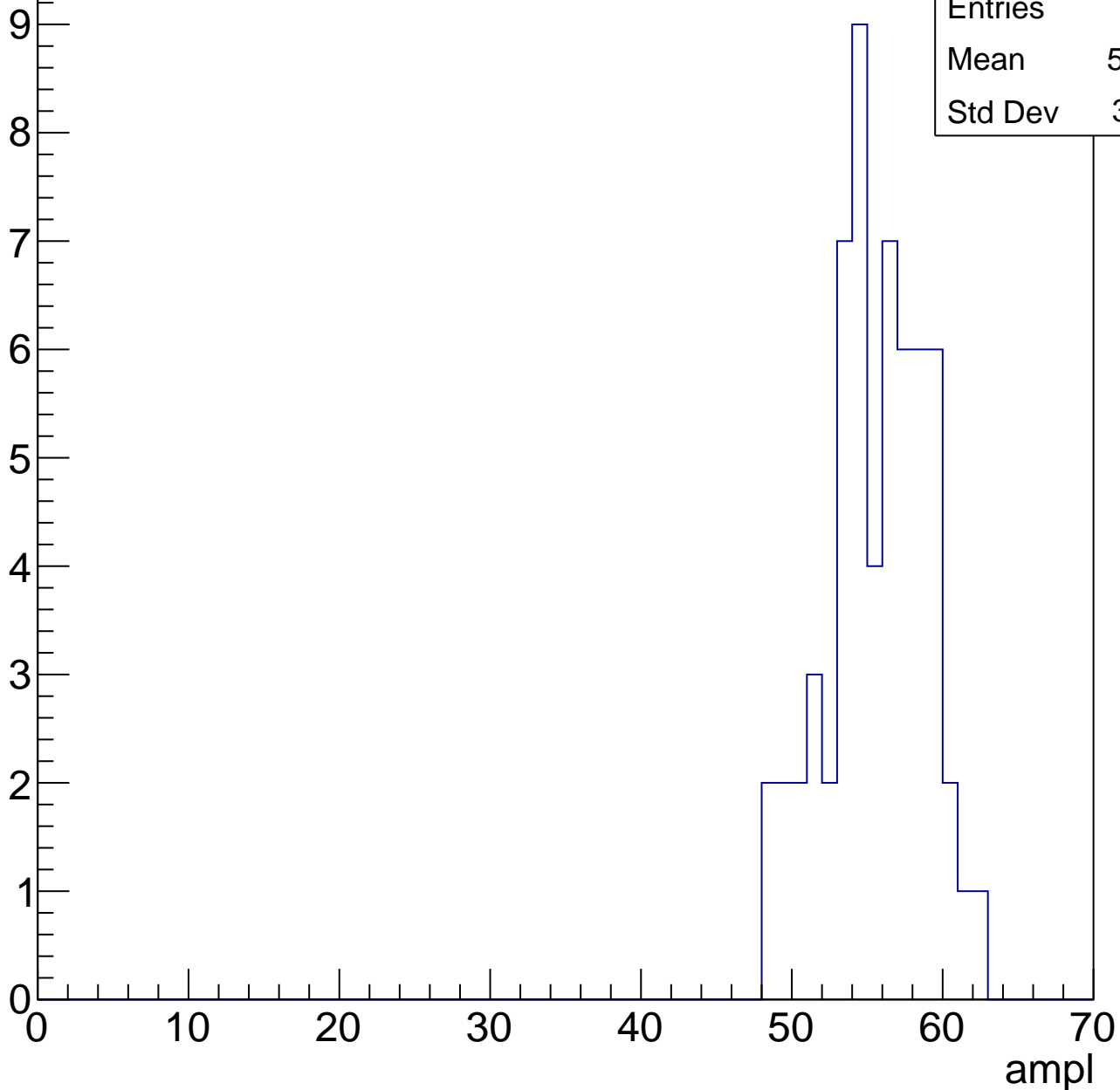


B1L003S, U20-ch122, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.12
Std Dev	3.261

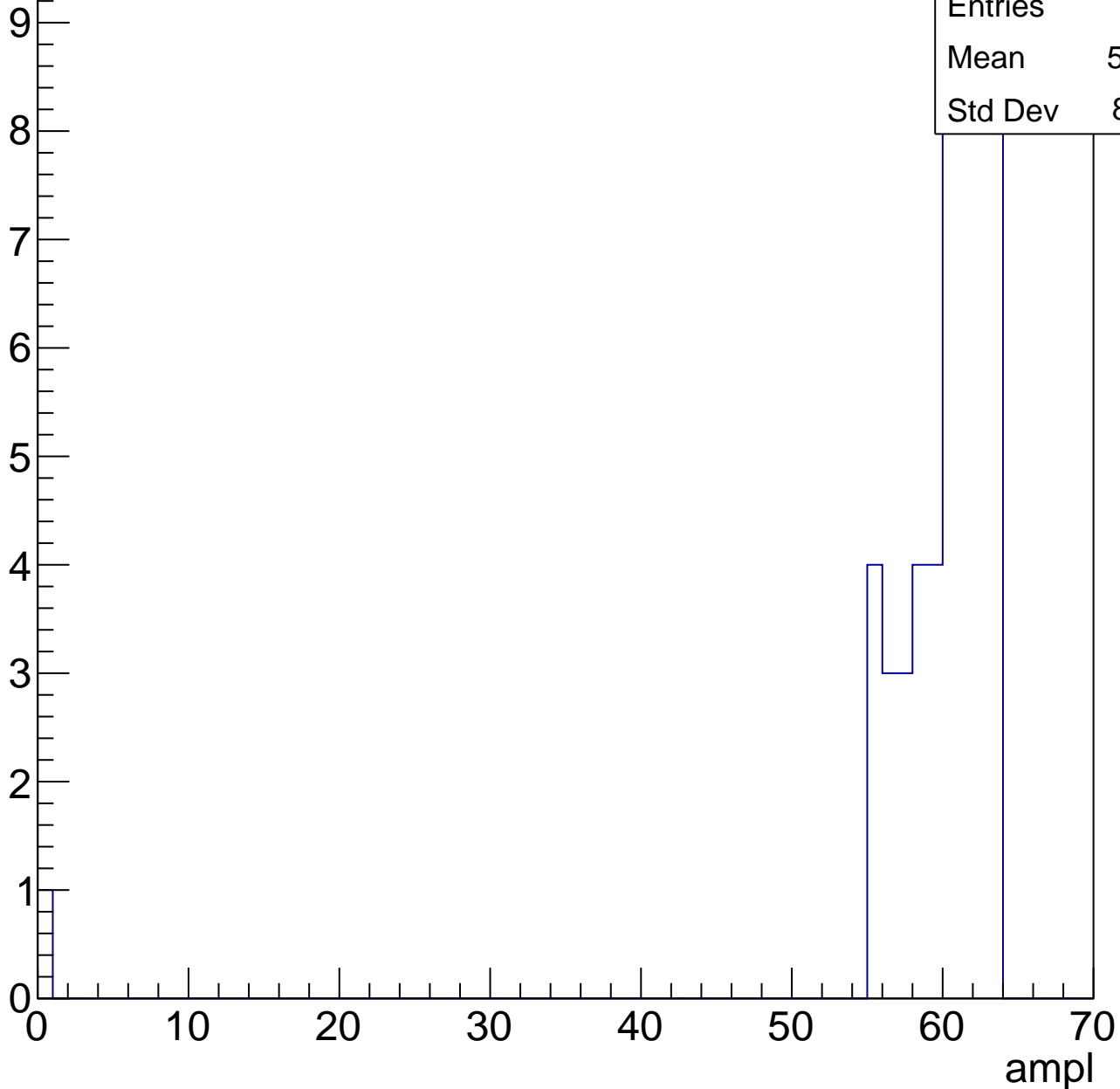


B1L003S, U20-ch122, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.83
Std Dev	8.511



B1L003S, U20-ch122, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U20-ch122, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch123, adc0

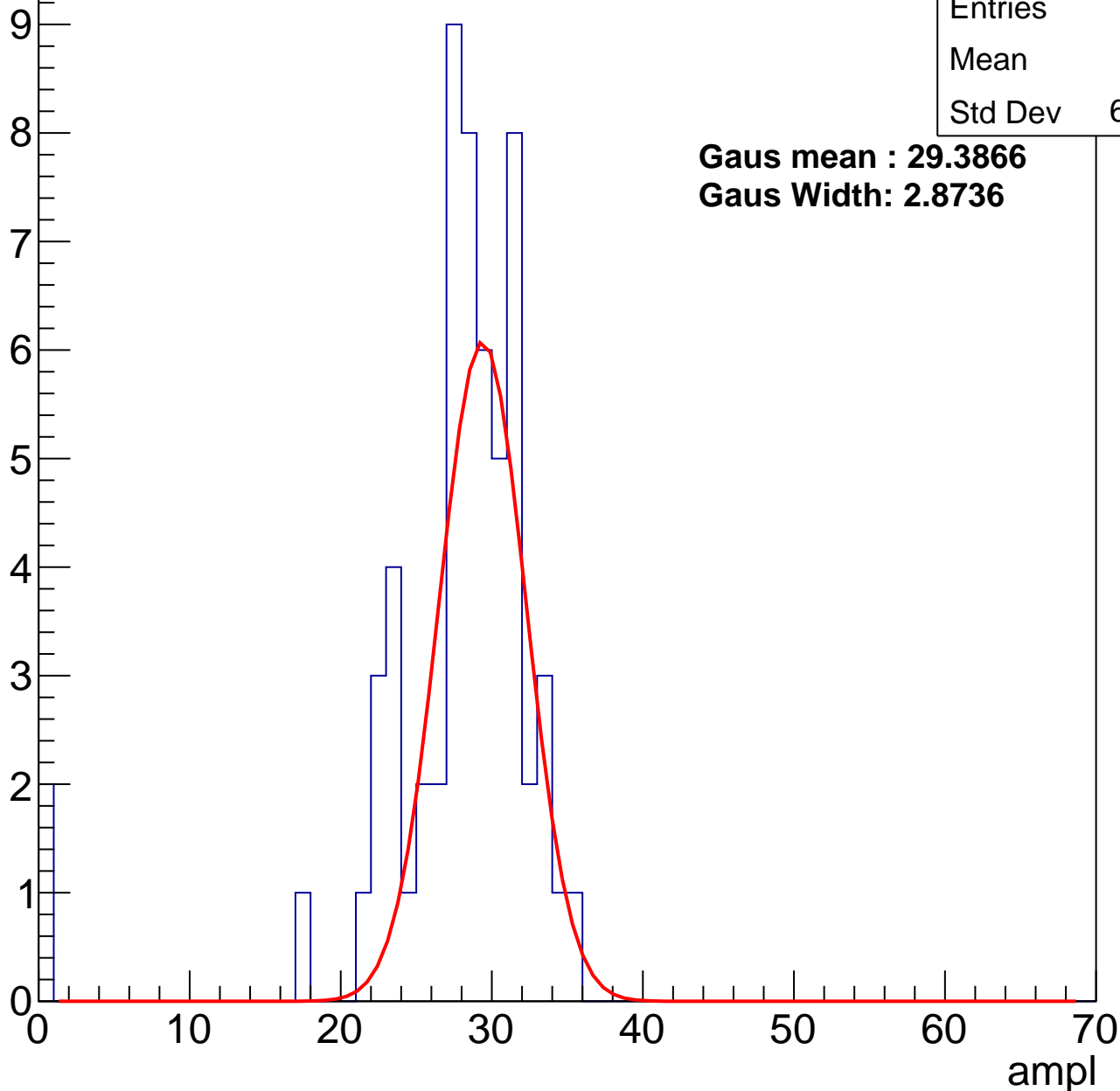
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	27
Std Dev	6.145

Gaus mean : 29.3866

Gaus Width: 2.8736



B1L003S, U20-ch123, adc1

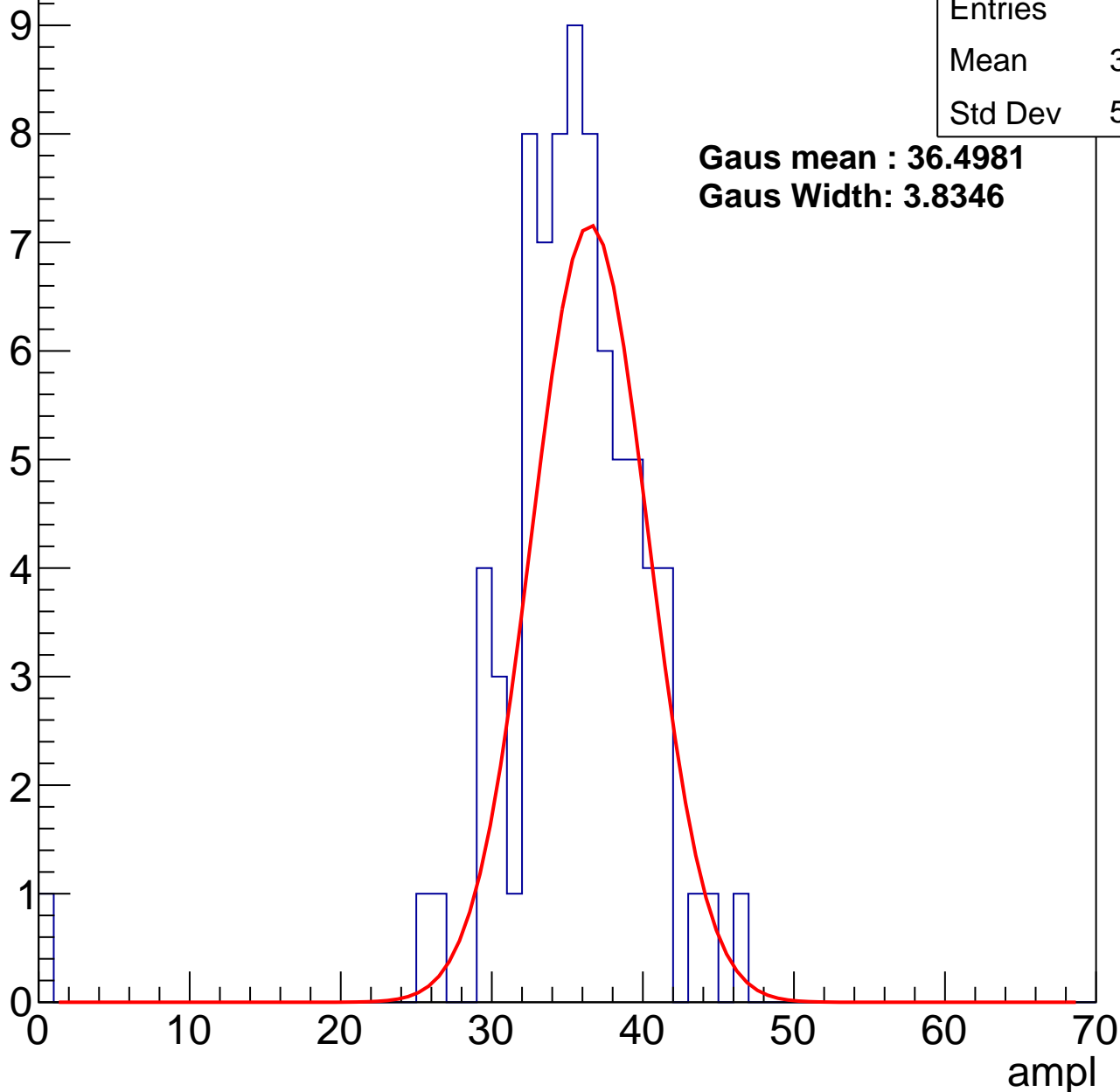
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	34.79
Std Dev	5.562

Gaus mean : 36.4981

Gaus Width: 3.8346



B1L003S, U20-ch123, adc2

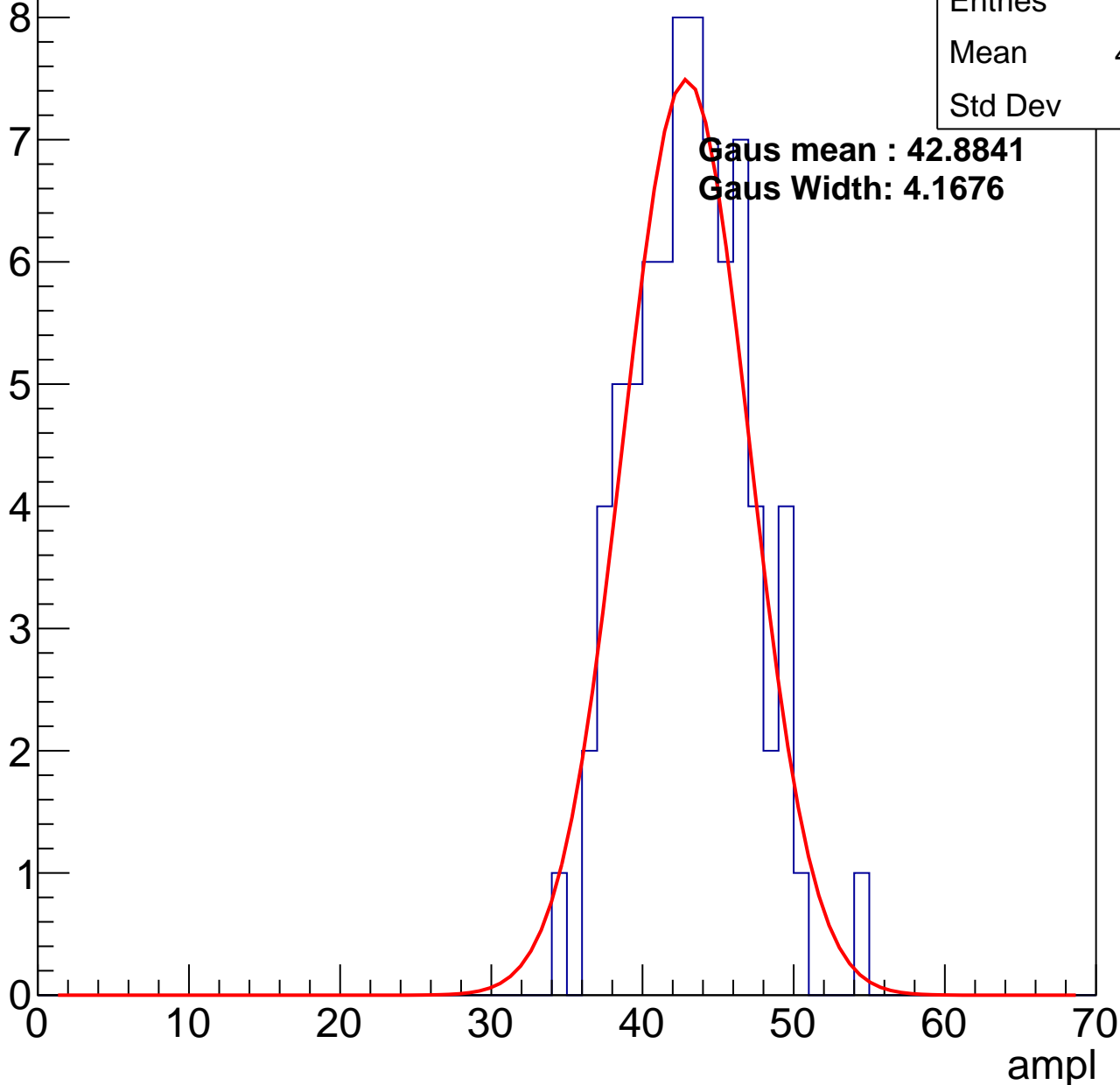
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	42.71
Std Dev	3.83

Gaus mean : 42.8841

Gaus Width: 4.1676

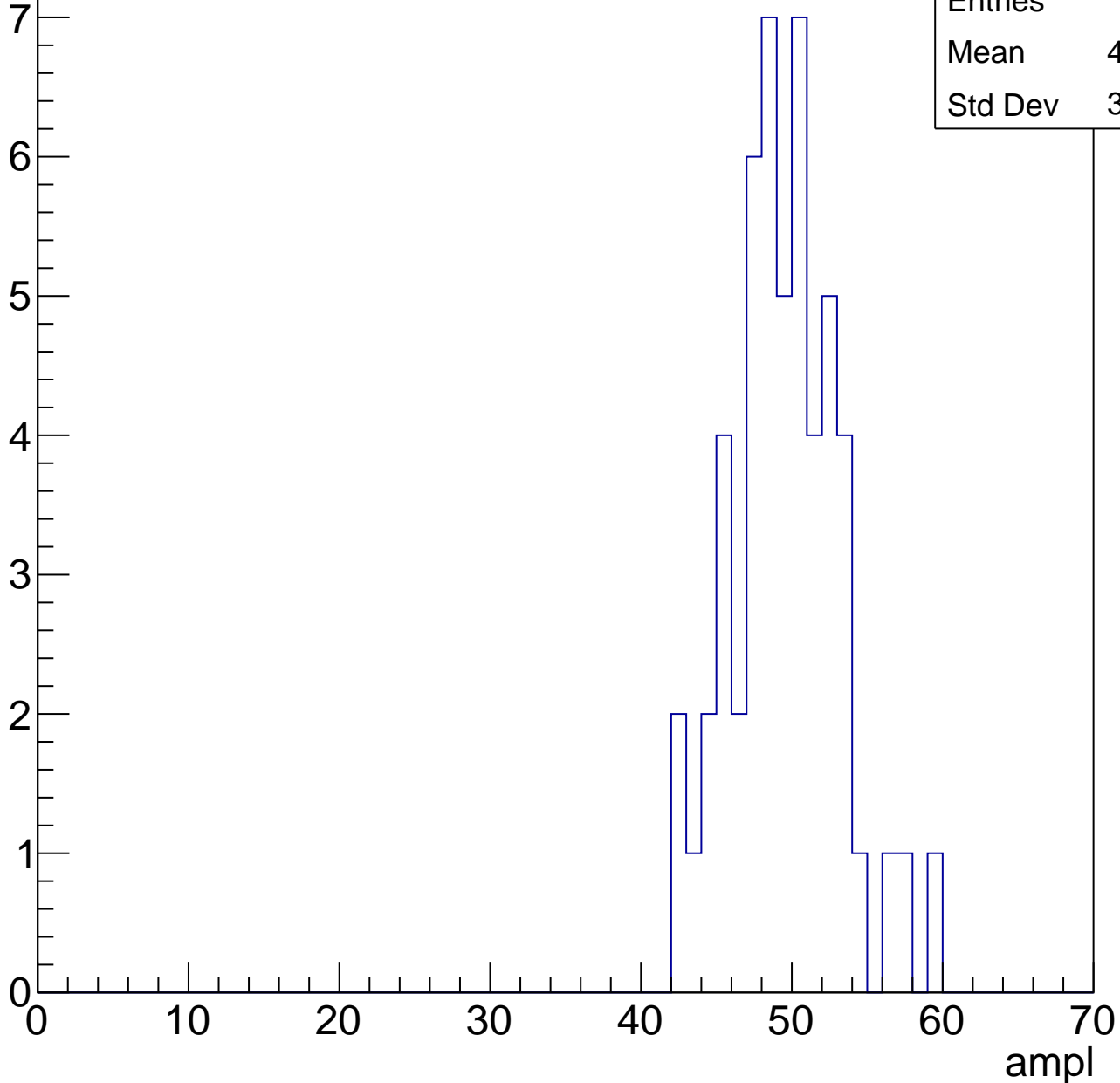


B1L003S, U20-ch123, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	49.09
Std Dev	3.546

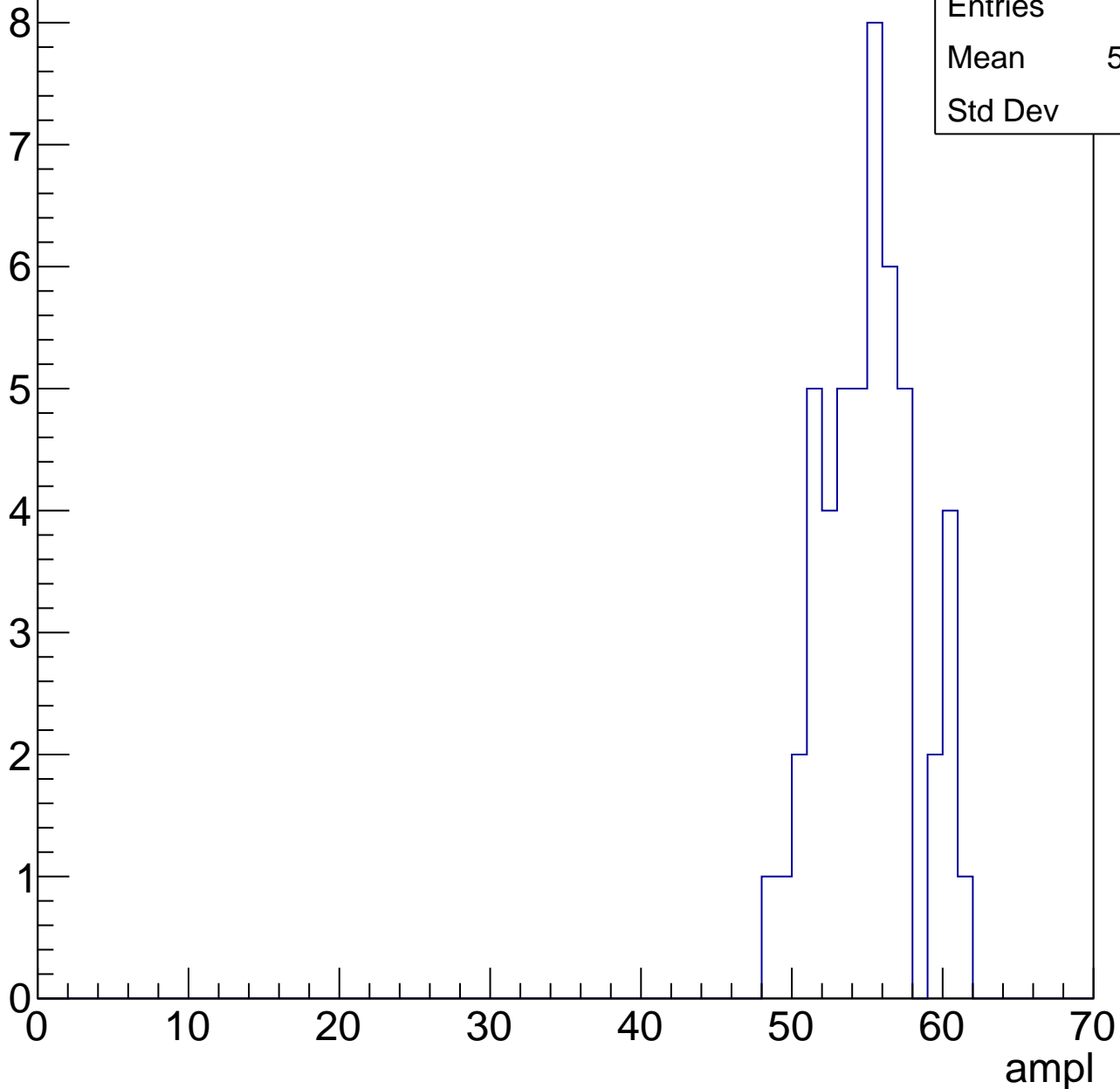


B1L003S, U20-ch123, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	54.59
Std Dev	3.07



B1L003S, U20-ch123, adc5

calib_packv5_042523_0143.root, FC#13, port D2

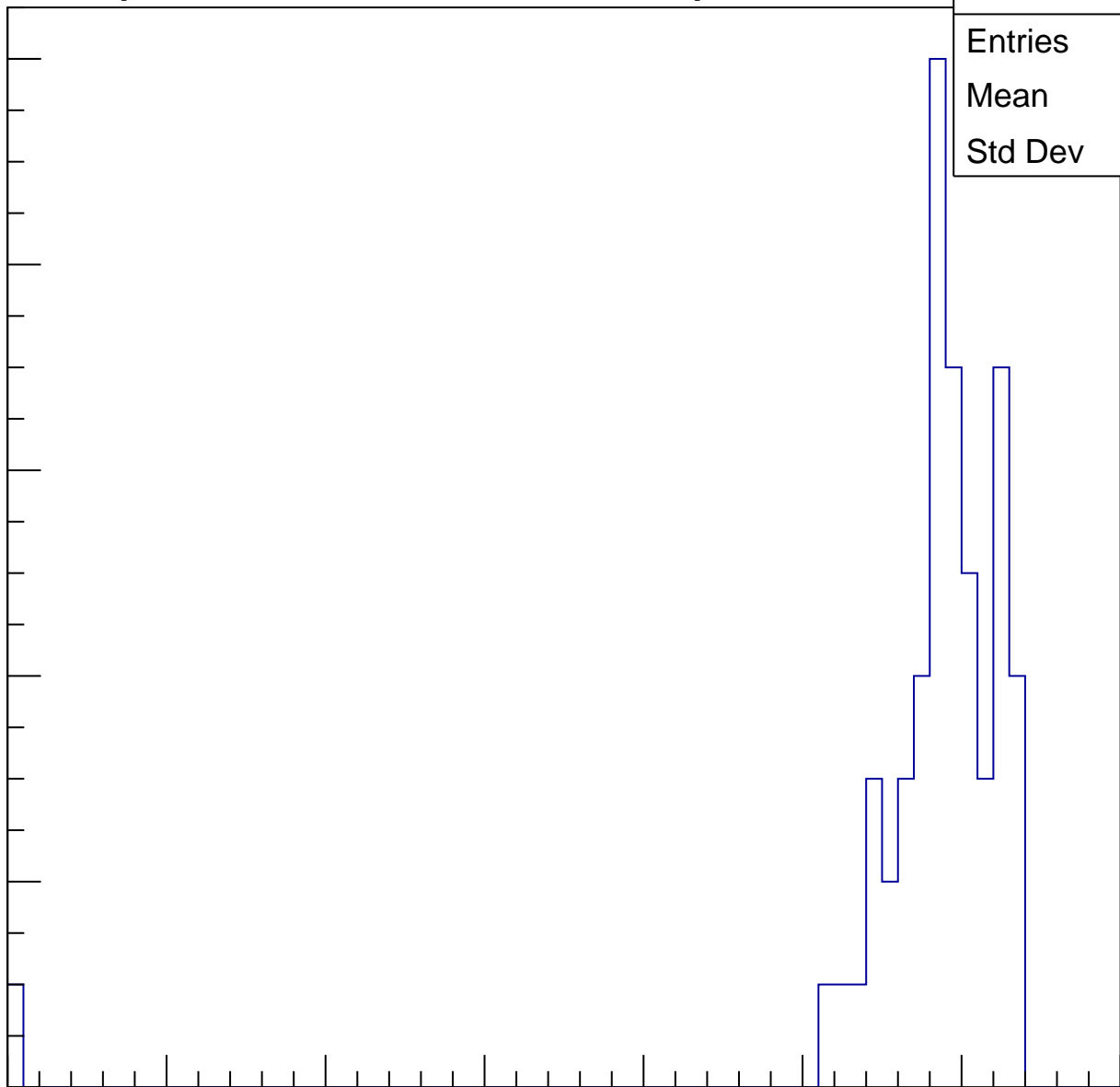
Entries	52
Mean	57.42
Std Dev	8.554

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

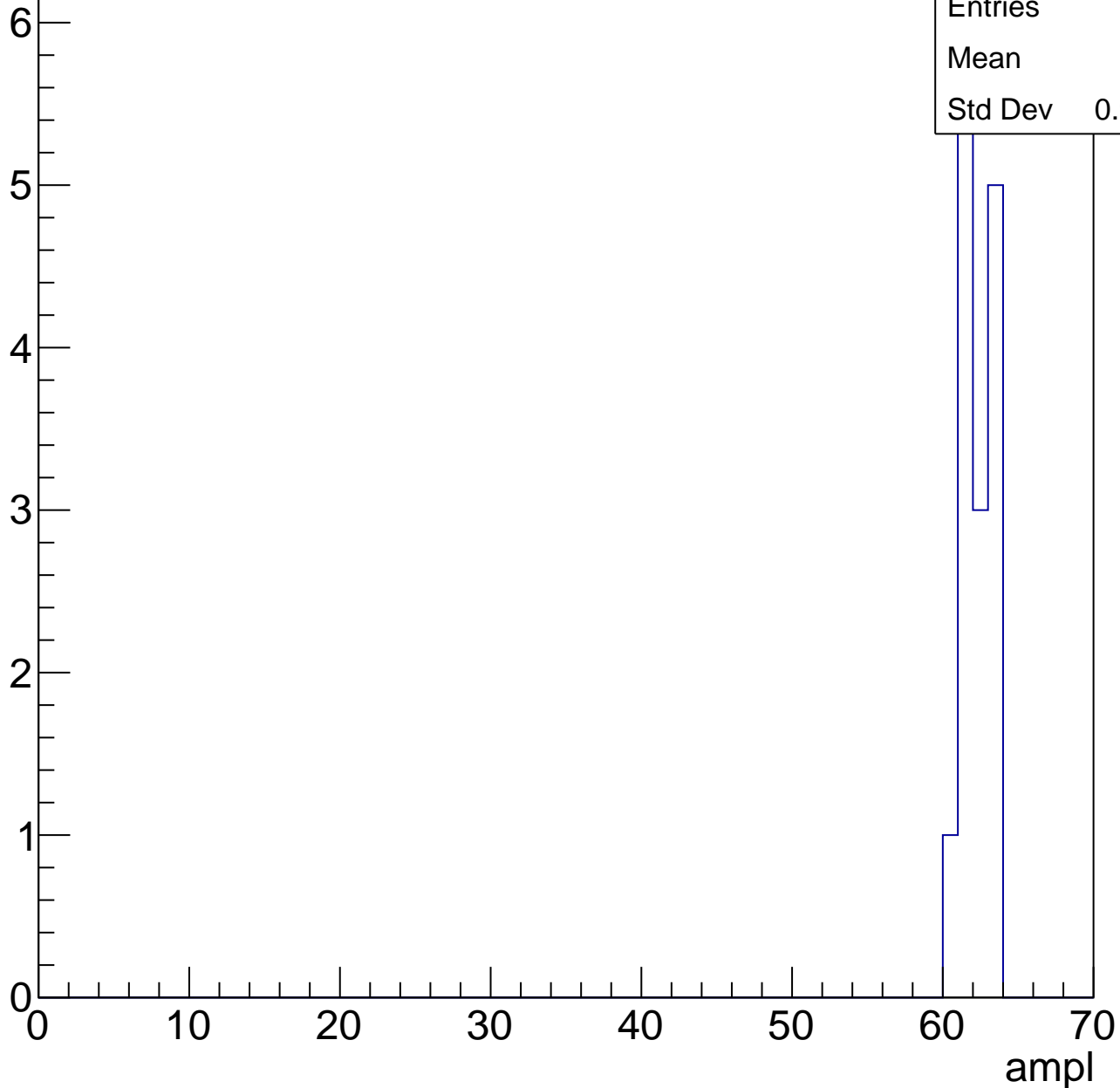


B1L003S, U20-ch123, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.8
Std Dev	0.9798



B1L003S, U20-ch123, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch124, adc0

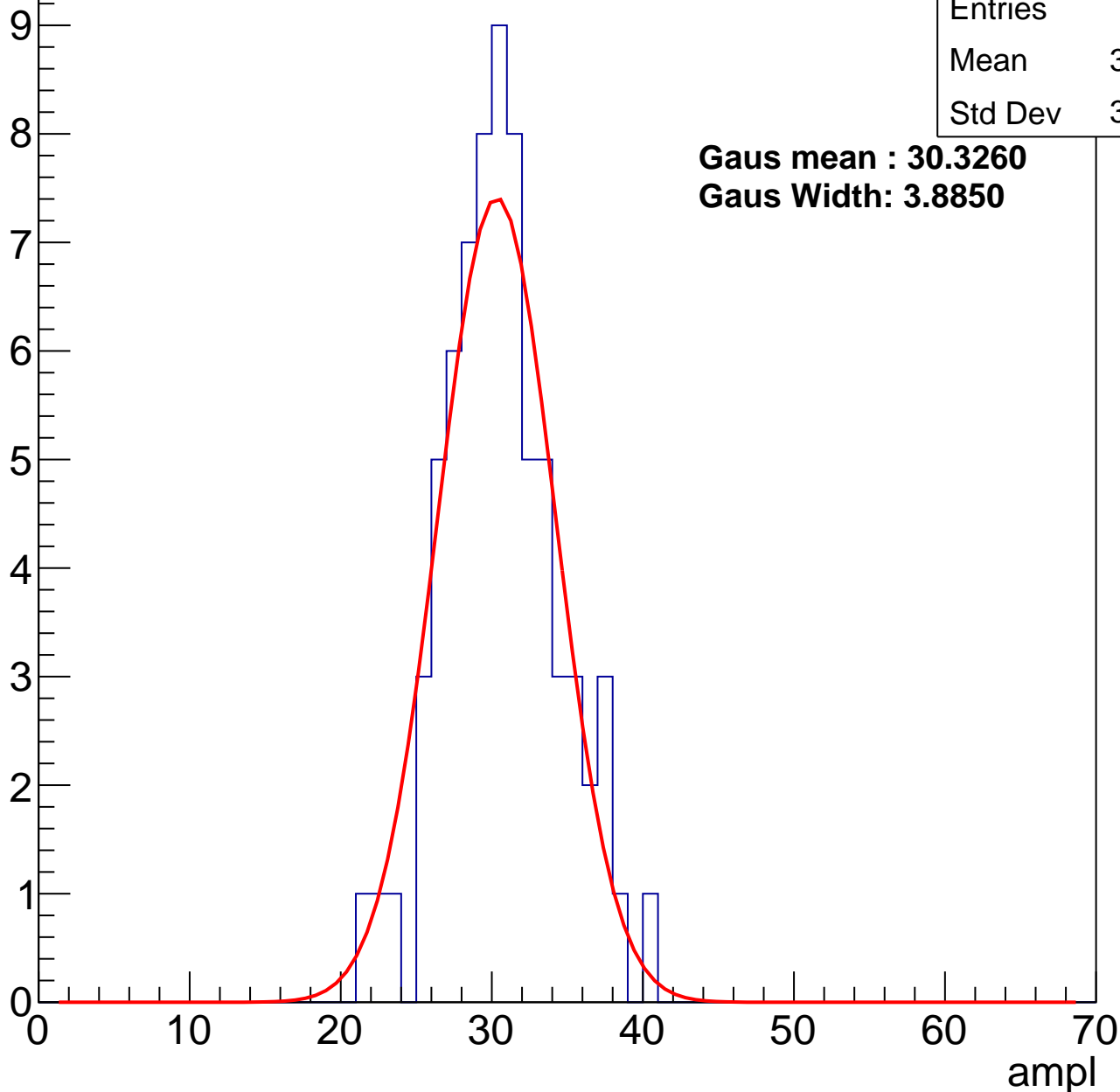
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.17
Std Dev	3.756

Gaus mean : 30.3260

Gaus Width: 3.8850



B1L003S, U20-ch124, adc1

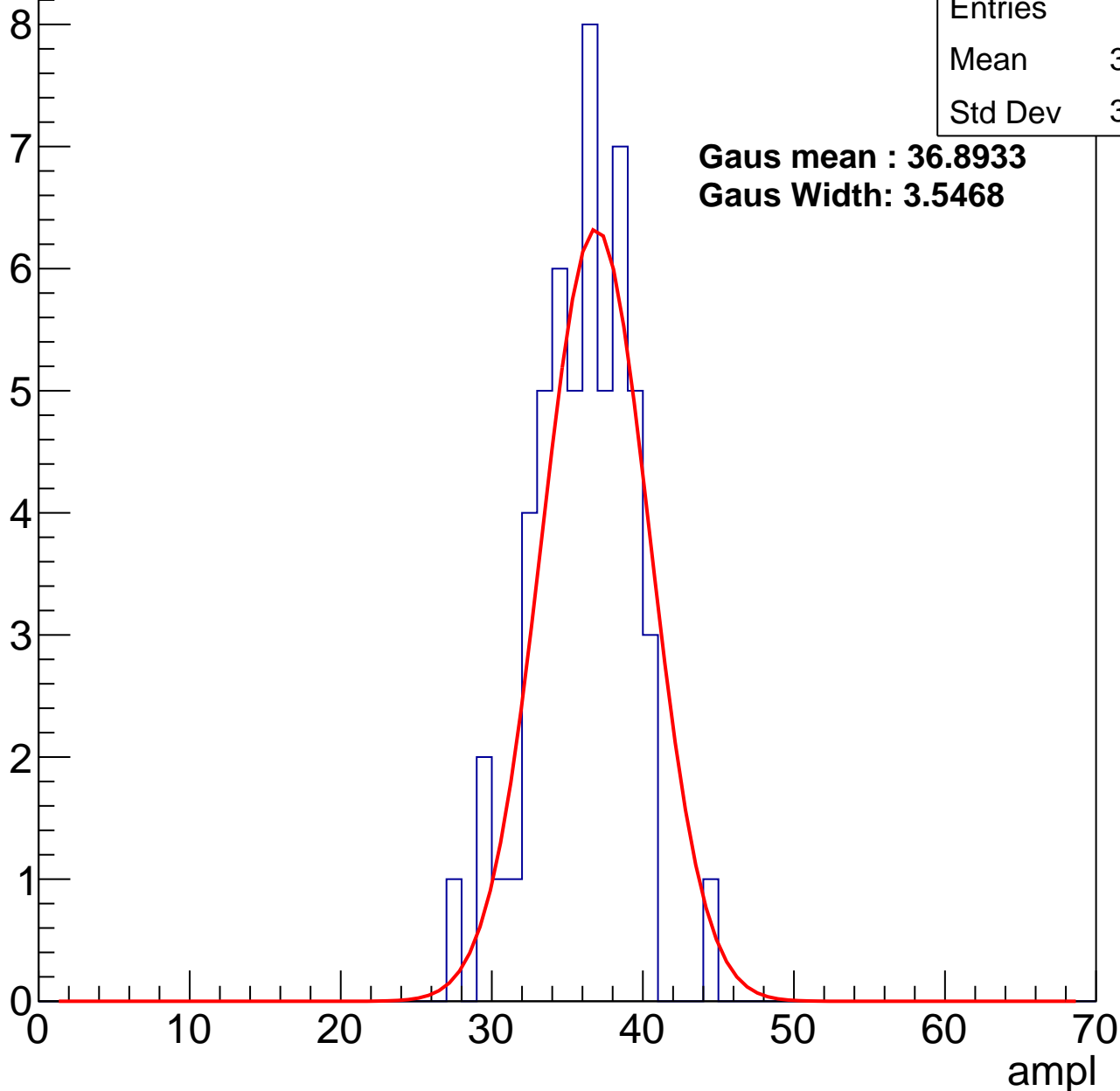
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	35.48
Std Dev	3.202

Gaus mean : 36.8933

Gaus Width: 3.5468



B1L003S, U20-ch124, adc2

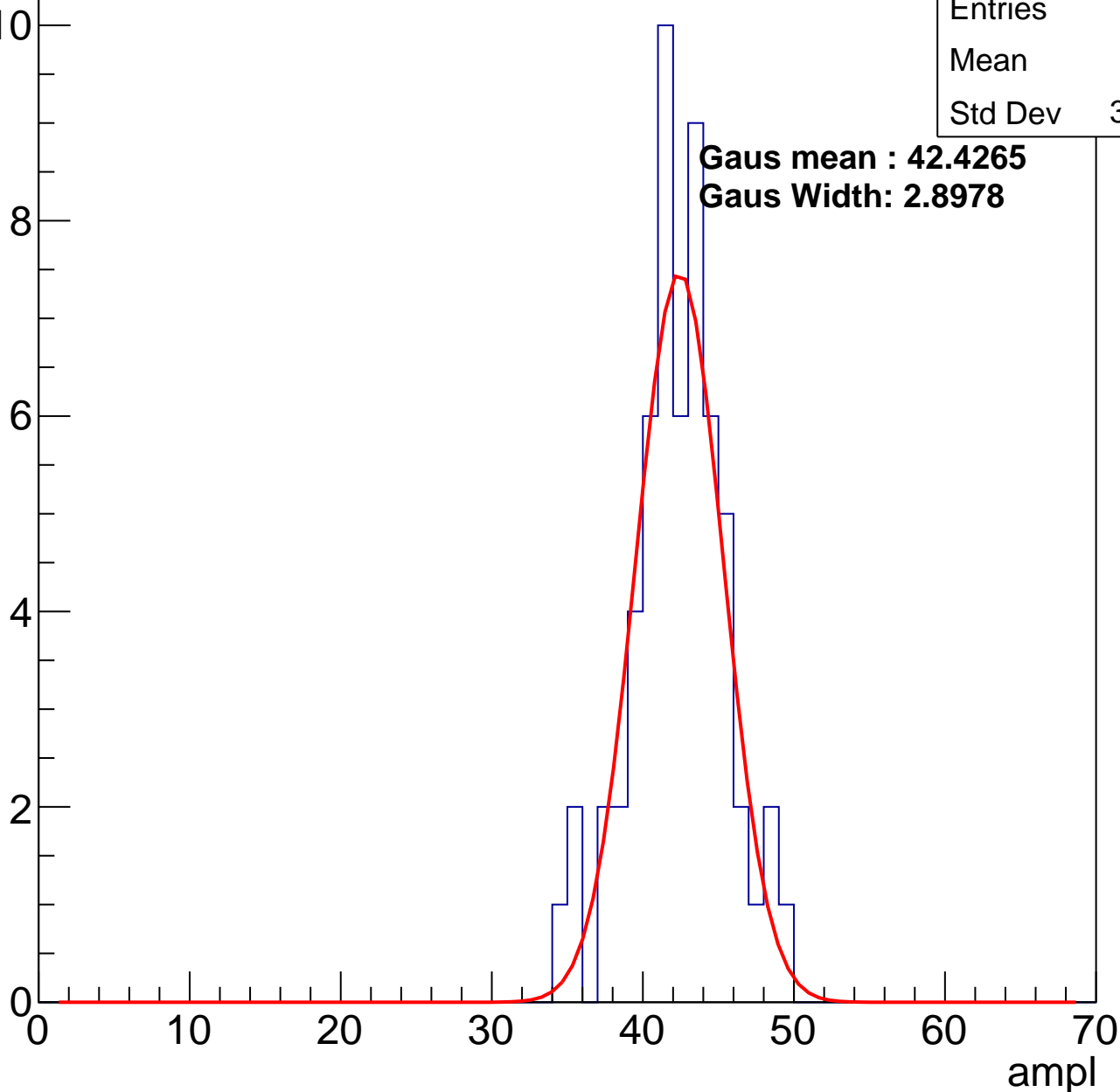
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.9
Std Dev	3.107

Gaus mean : 42.4265

Gaus Width: 2.8978

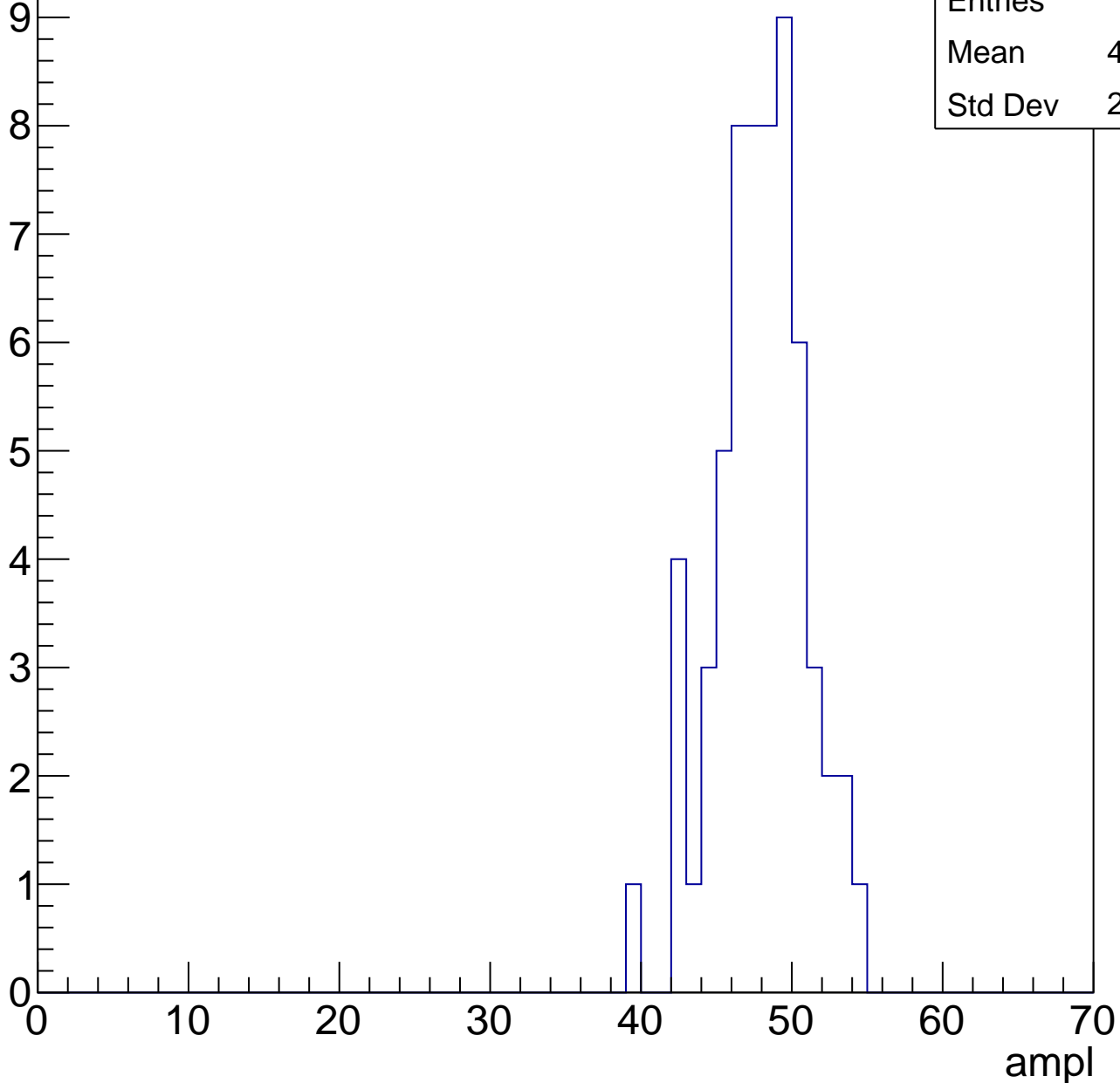


B1L003S, U20-ch124, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	47.43
Std Dev	2.978

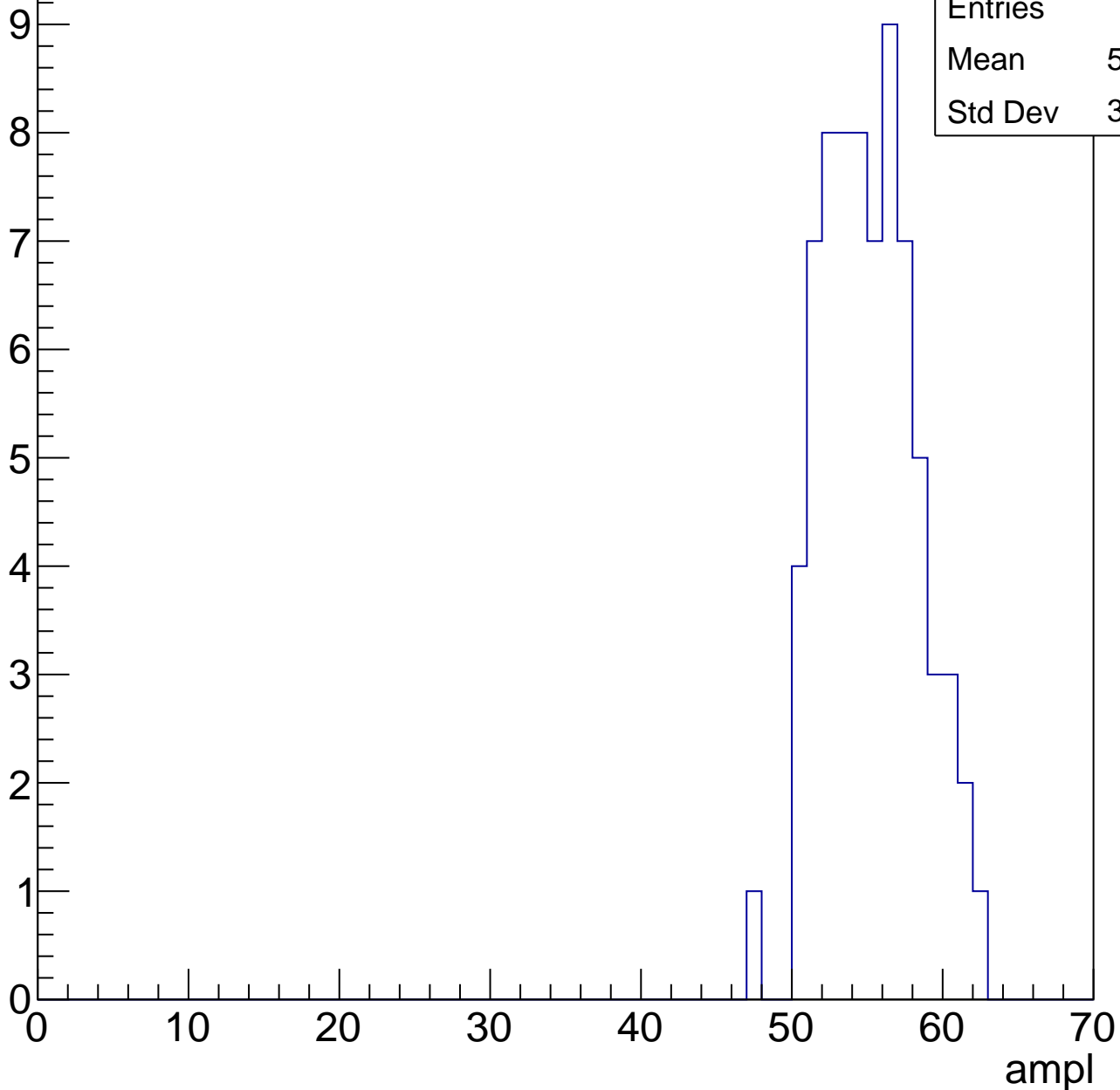


B1L003S, U20-ch124, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

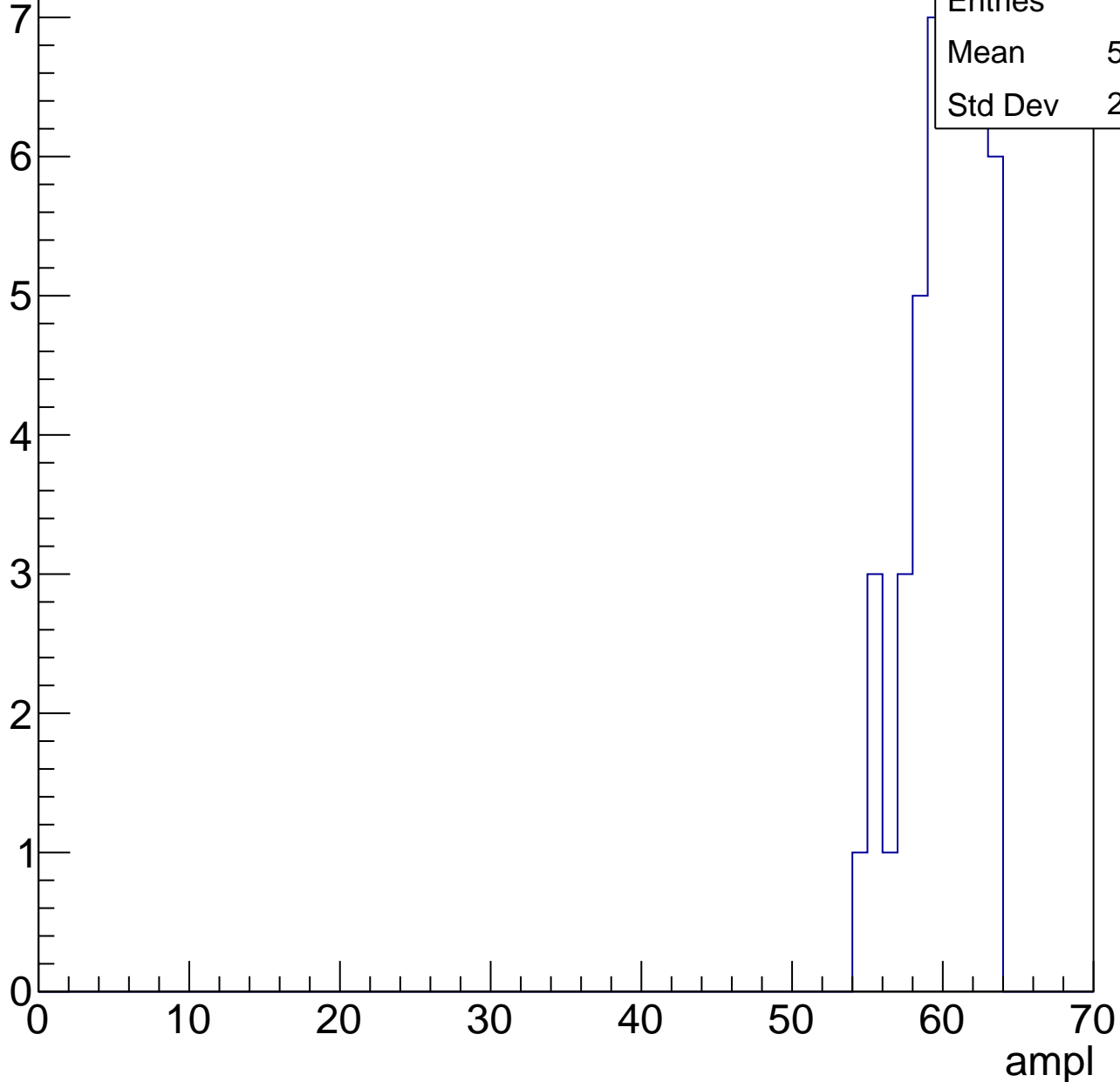
Entries	73
Mean	54.73
Std Dev	3.115



B1L003S, U20-ch124, adc5

calib_packv5_042523_0143.root, FC#13, port D2

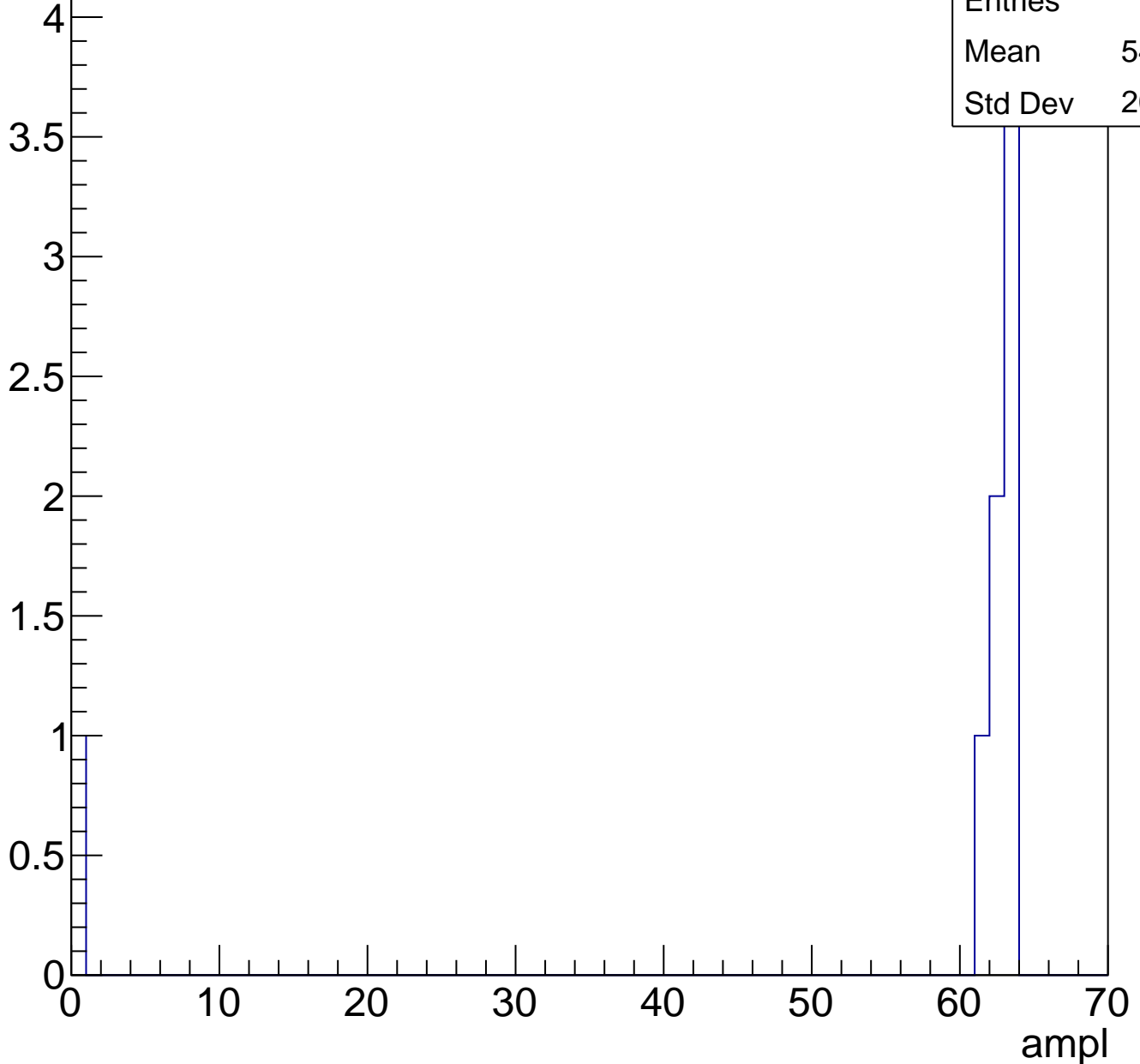
Entry



B1L003S, U20-ch124, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U20-ch124, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch125, adc0

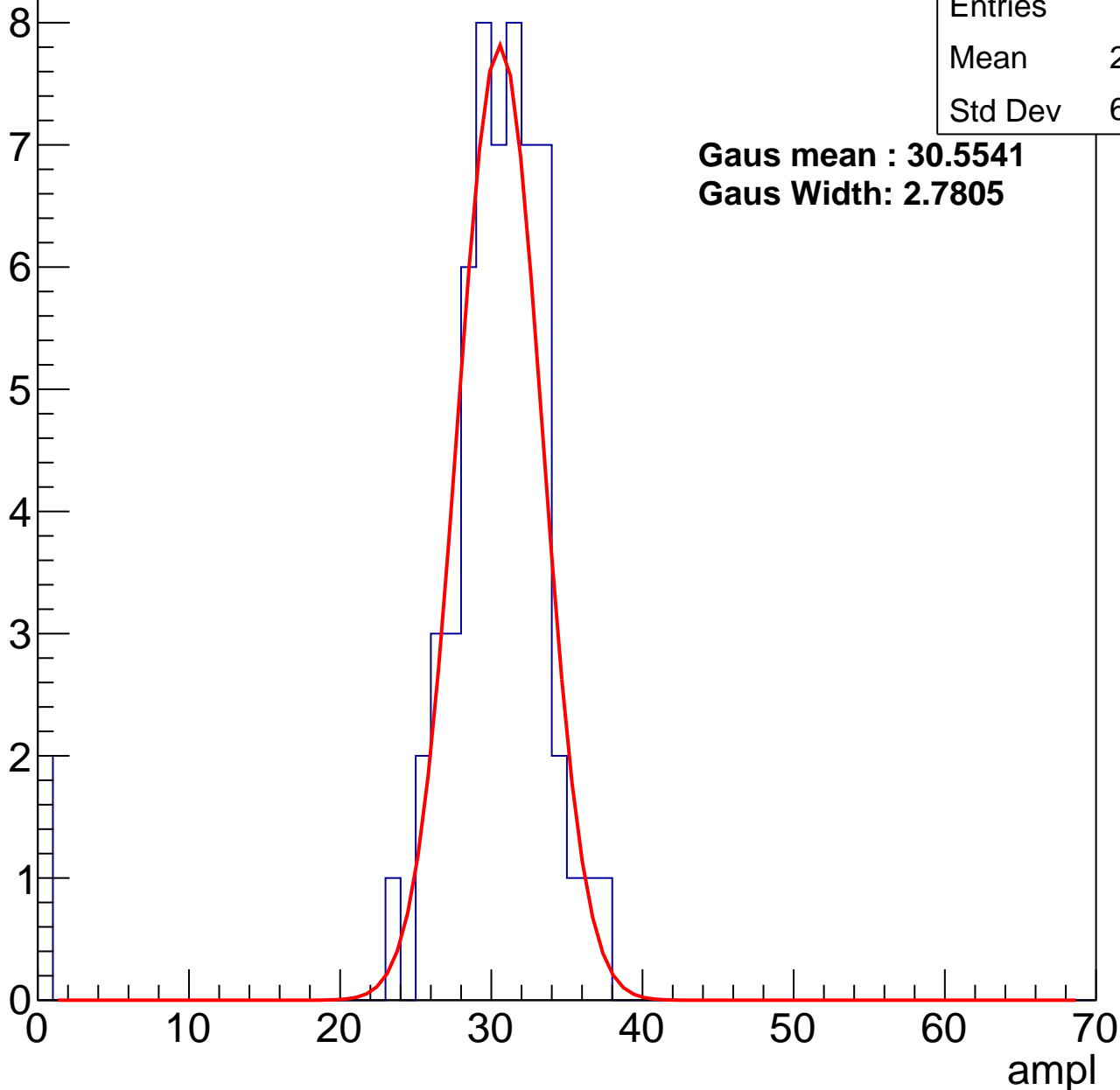
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.17
Std Dev	6.115

Gaus mean : 30.5541

Gaus Width: 2.7805



B1L003S, U20-ch125, adc1

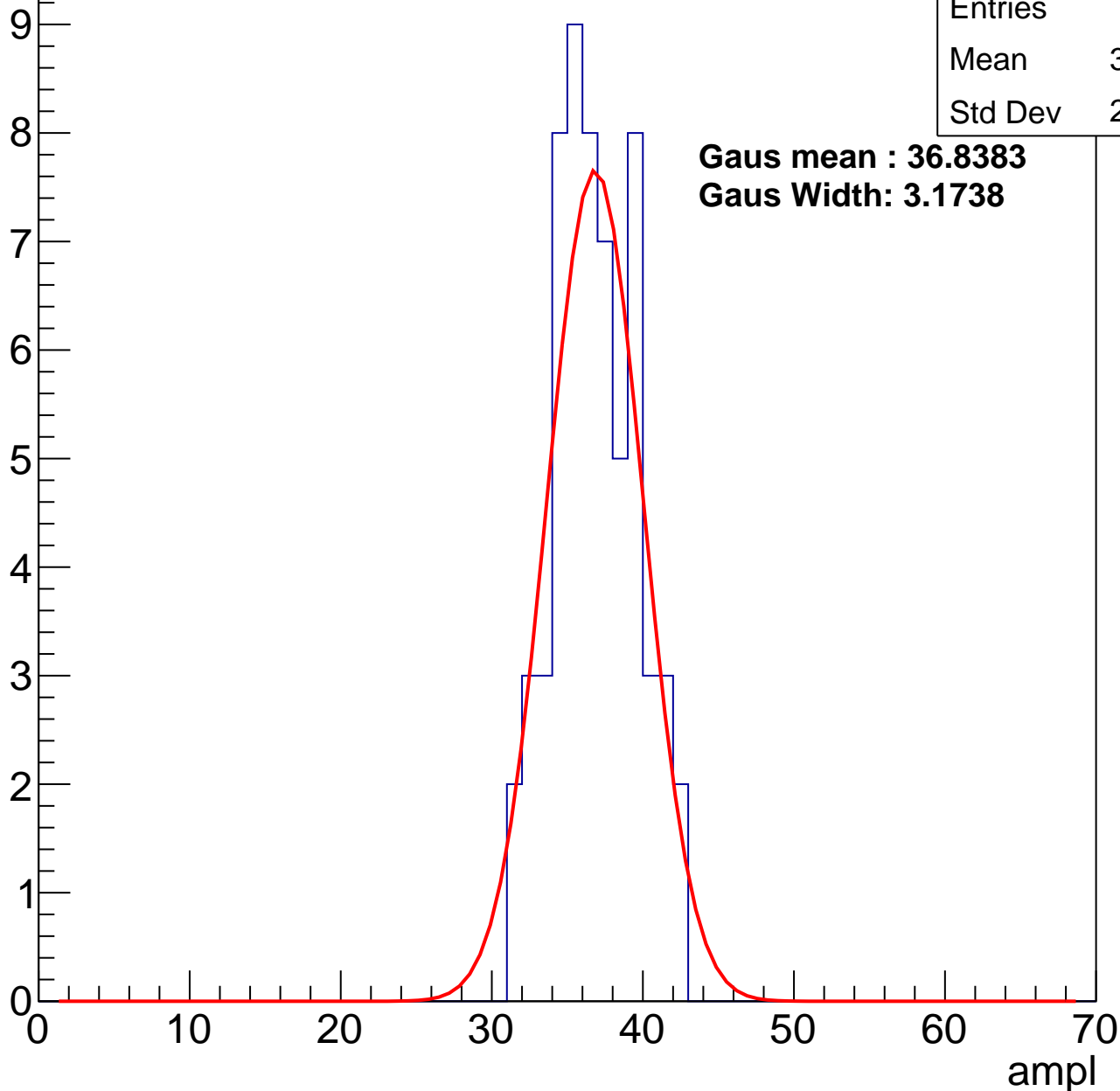
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.39
Std Dev	2.718

Gaus mean : 36.8383

Gaus Width: 3.1738



B1L003S, U20-ch125, adc2

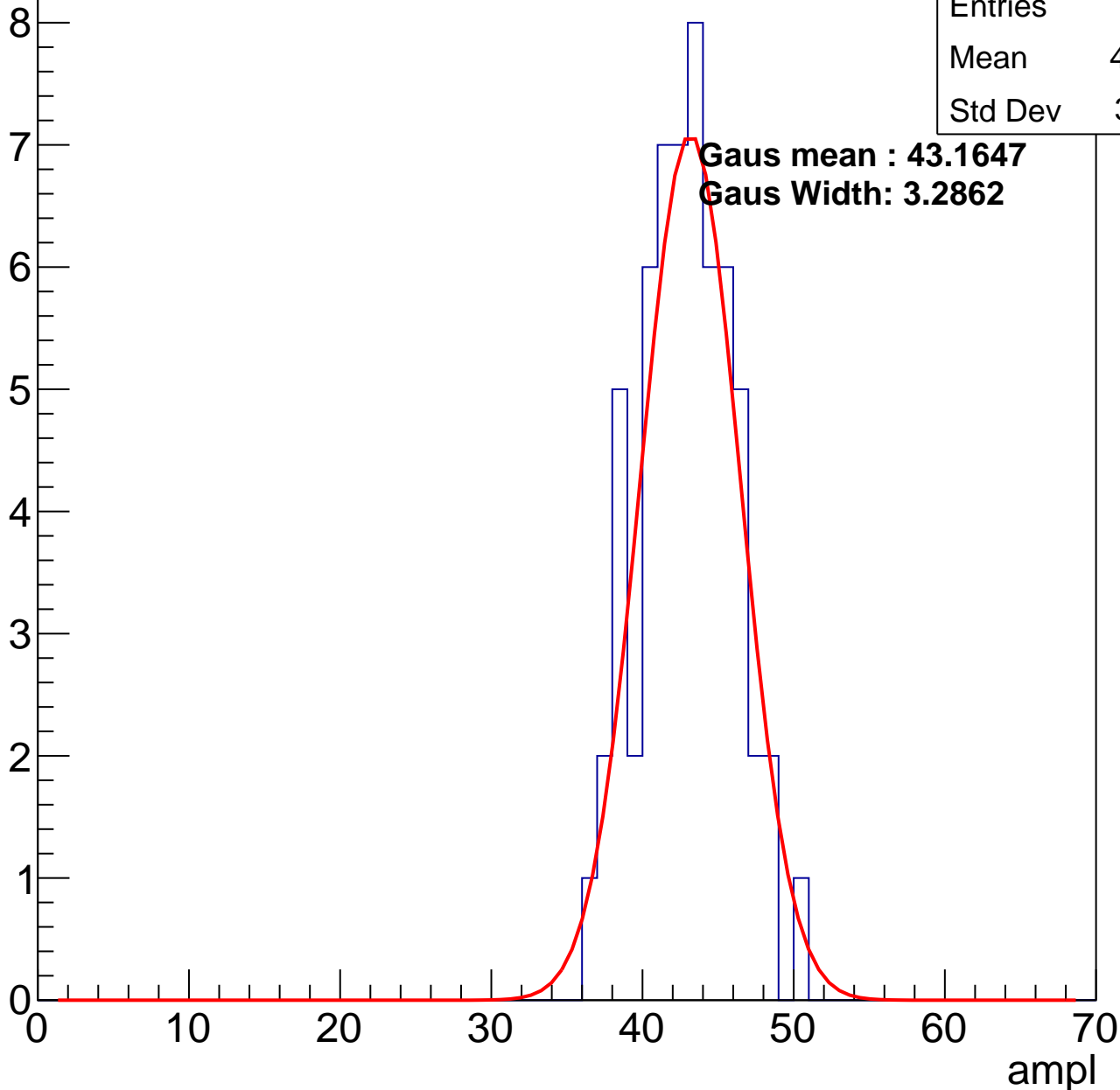
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.45
Std Dev	3.041

Gaus mean : 43.1647

Gaus Width: 3.2862



B1L003S, U20-ch125, adc3

calib_packv5_042523_0143.root, FC#13, port D2

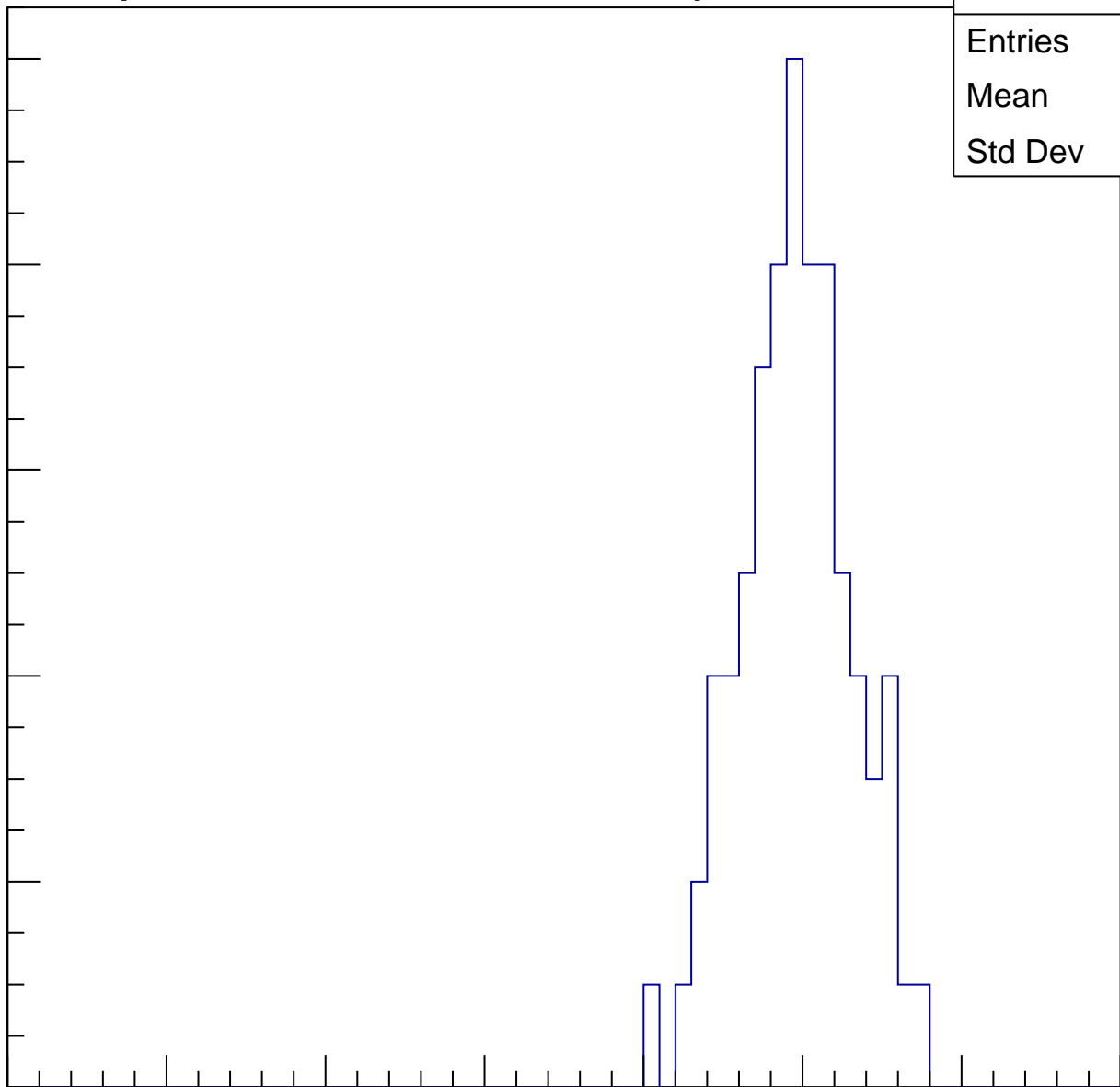
Entries	76
Mean	49.11
Std Dev	3.493

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

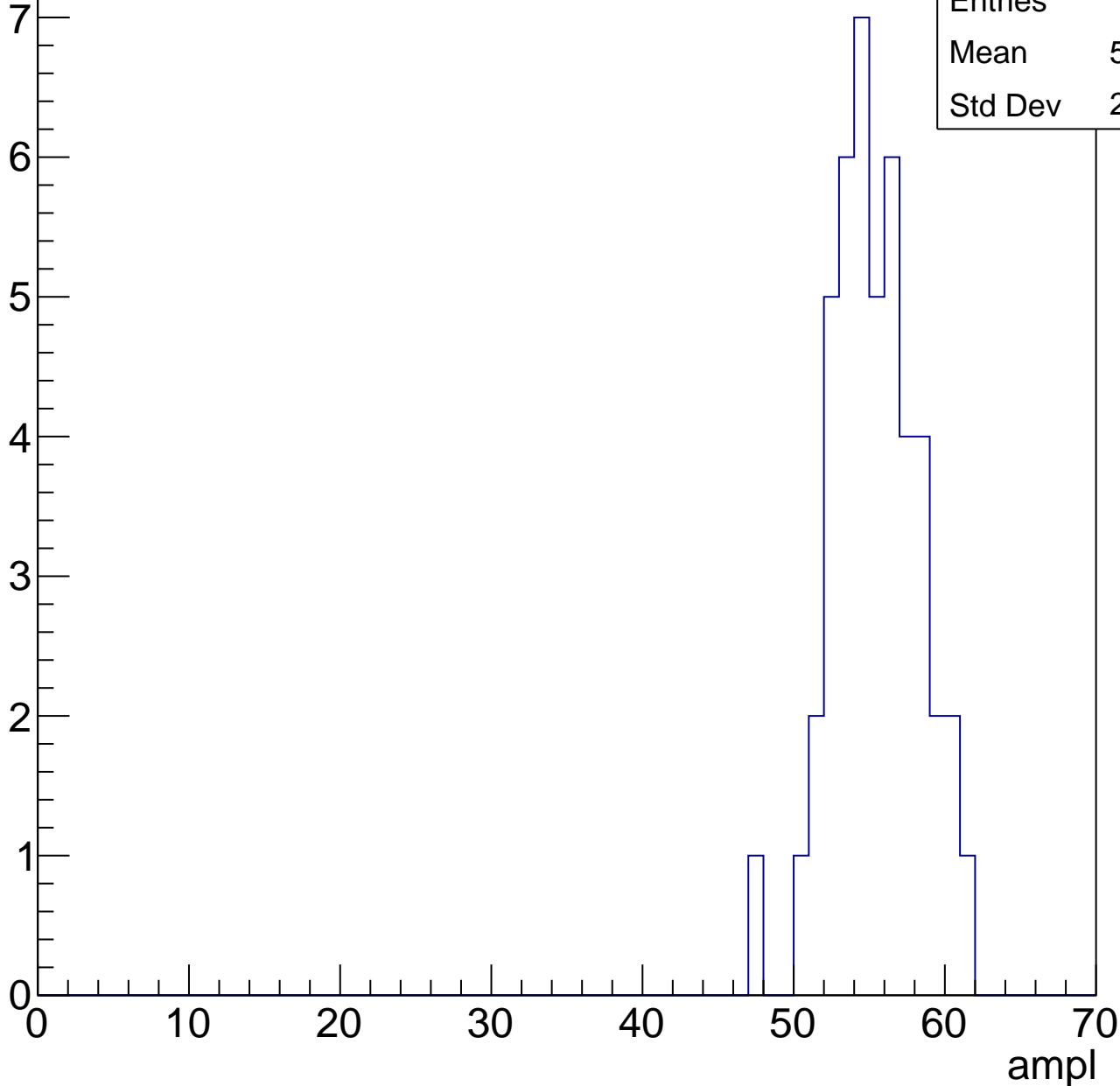


B1L003S, U20-ch125, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	54.89
Std Dev	2.846

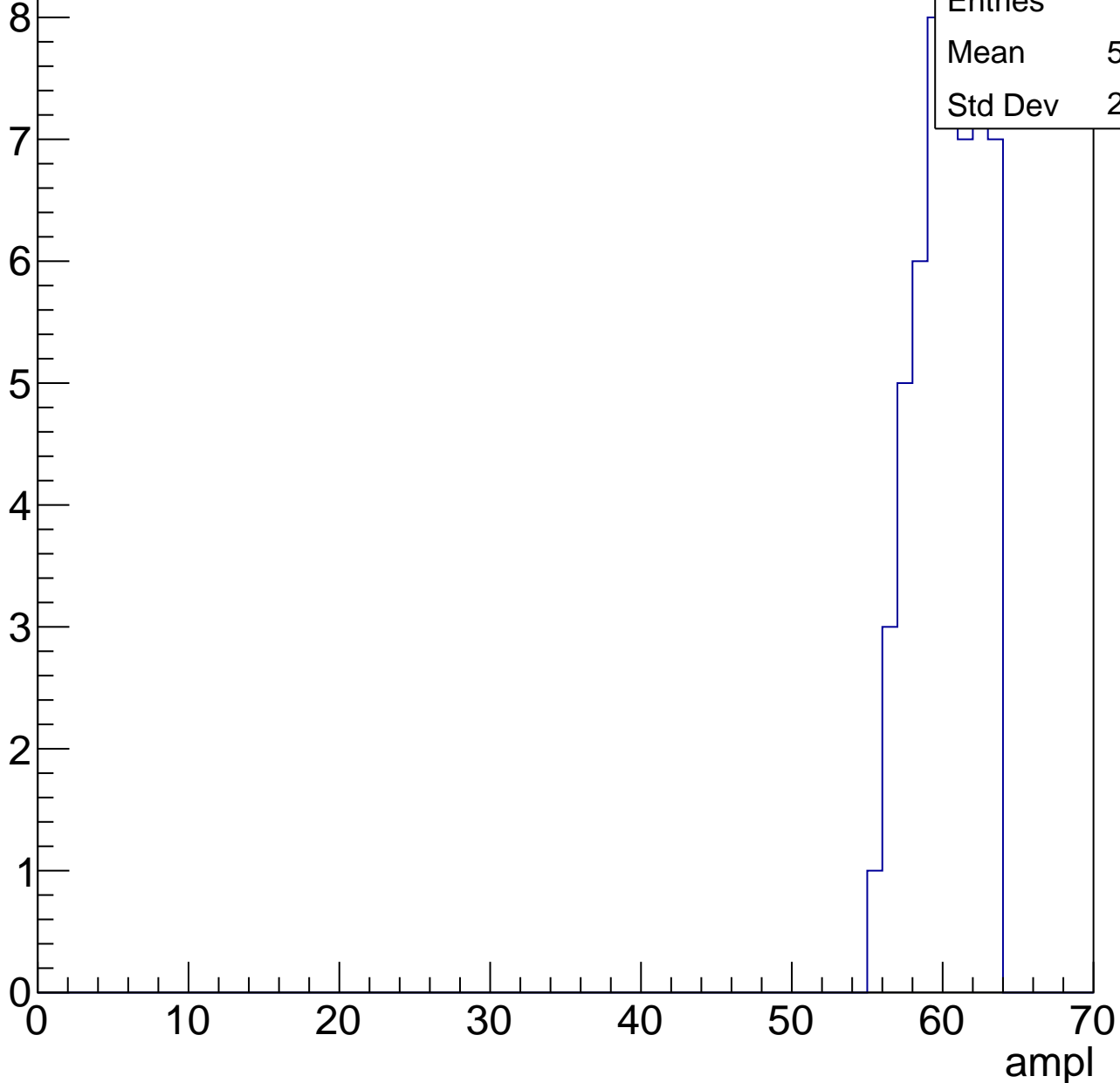


B1L003S, U20-ch125, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.85
Std Dev	2.175



B1L003S, U20-ch125, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

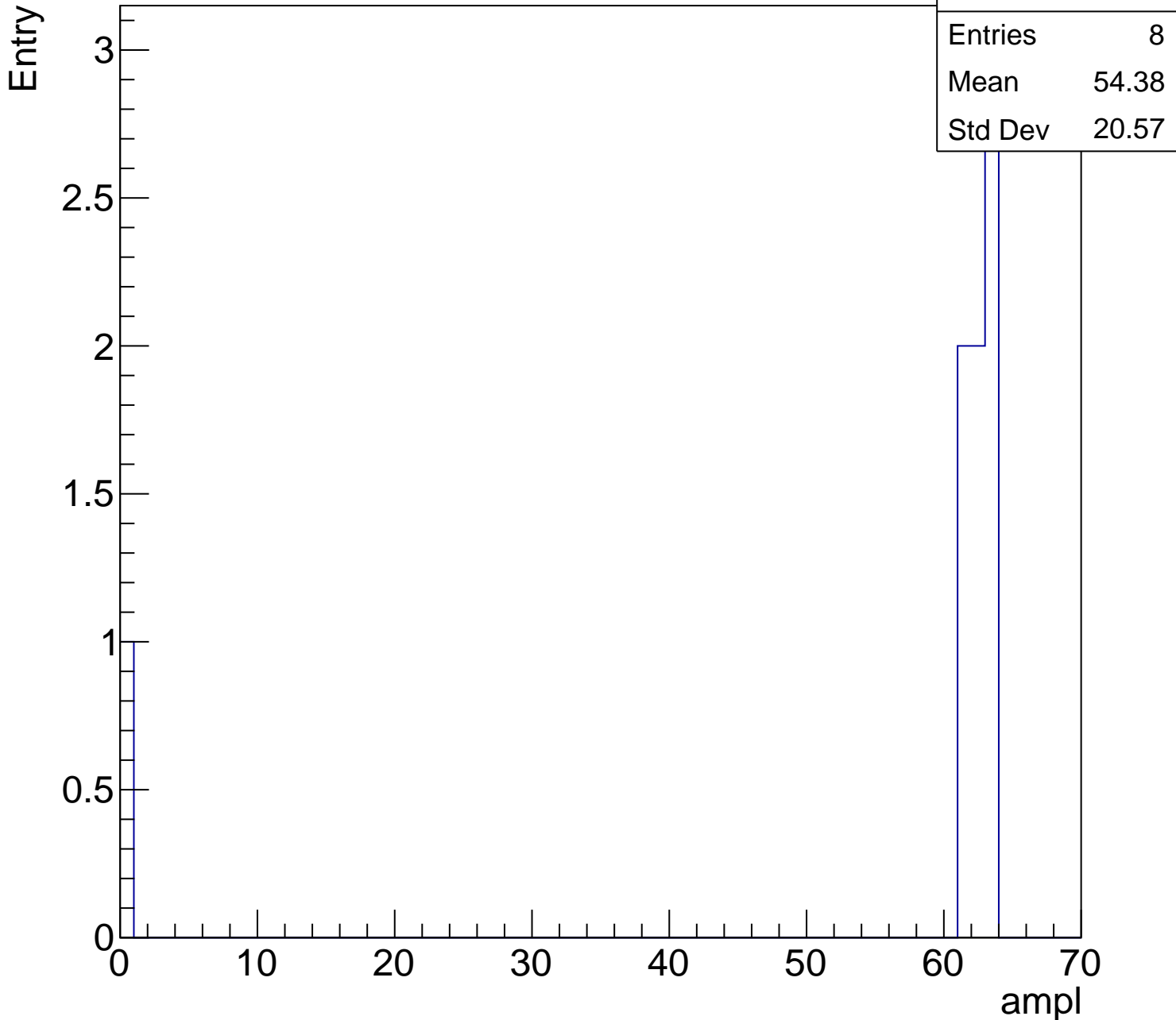
1

0.5

0

ampl

Entries	8
Mean	54.38
Std Dev	20.57



B1L003S, U20-ch125, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U20-ch126, adc0

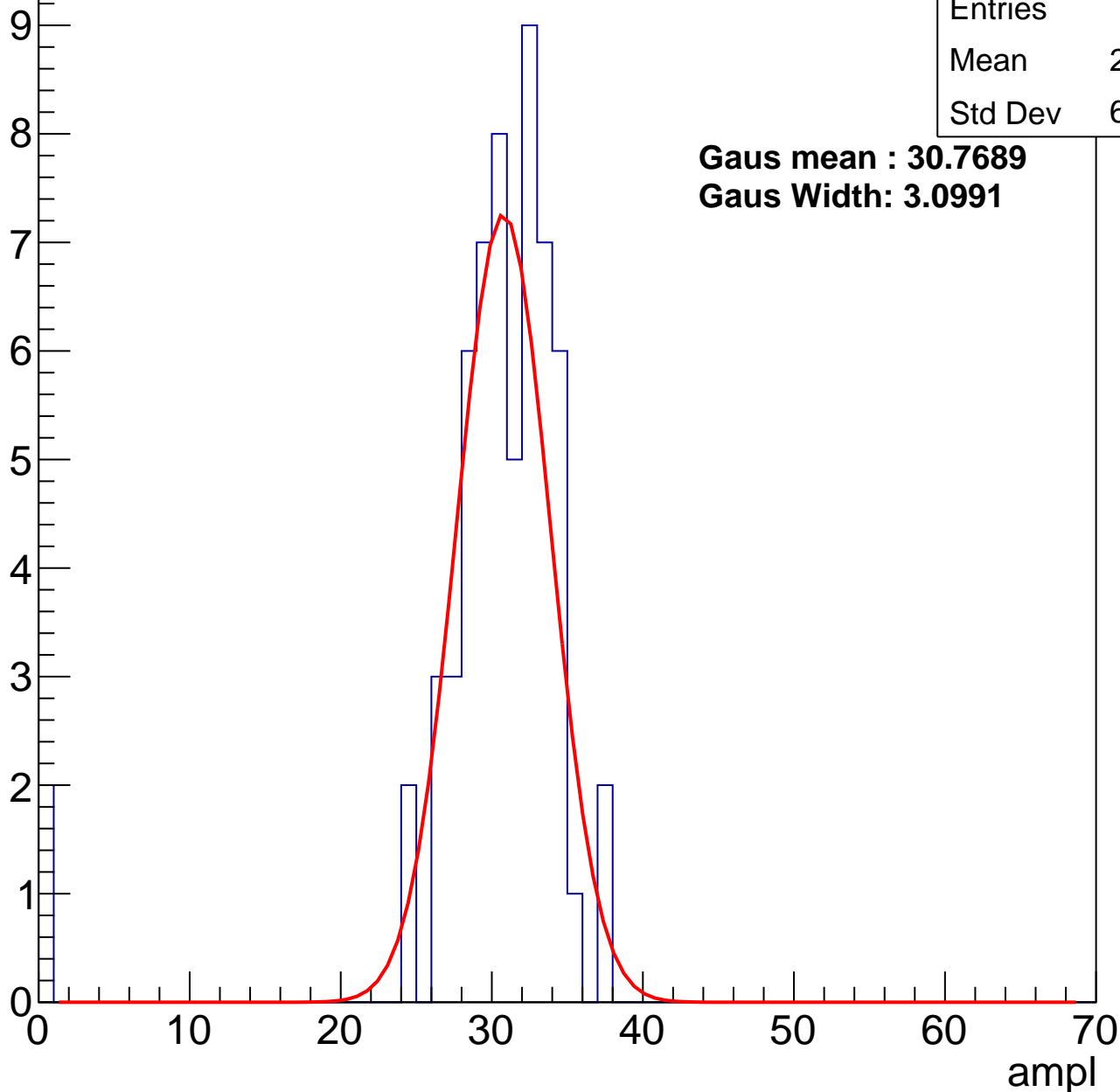
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.59
Std Dev	6.128

Gaus mean : 30.7689

Gaus Width: 3.0991



B1L003S, U20-ch126, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	37.97
Std Dev	3.604

Gaus mean : 38.6006

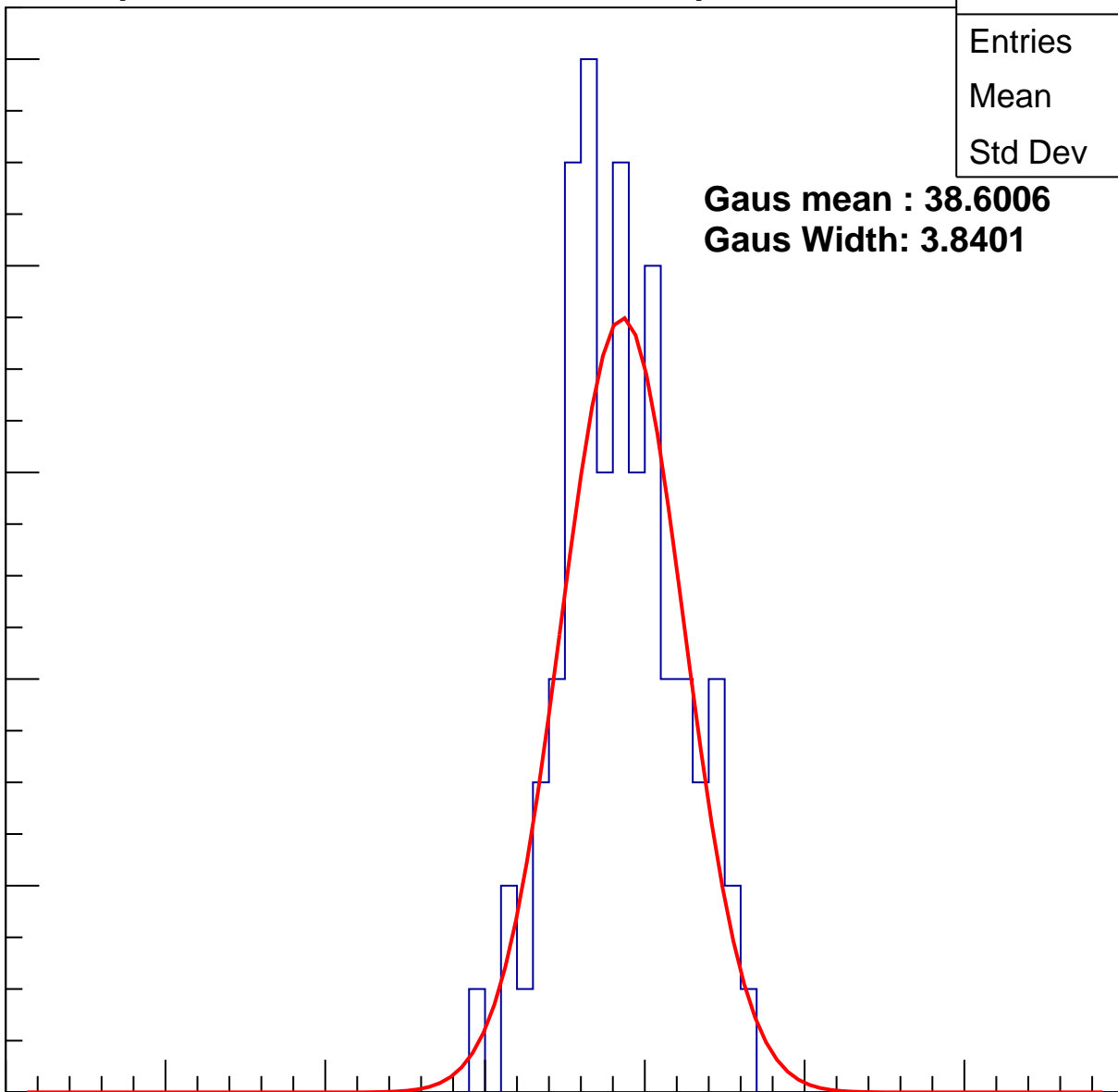
Gaus Width: 3.8401

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U20-ch126, adc2

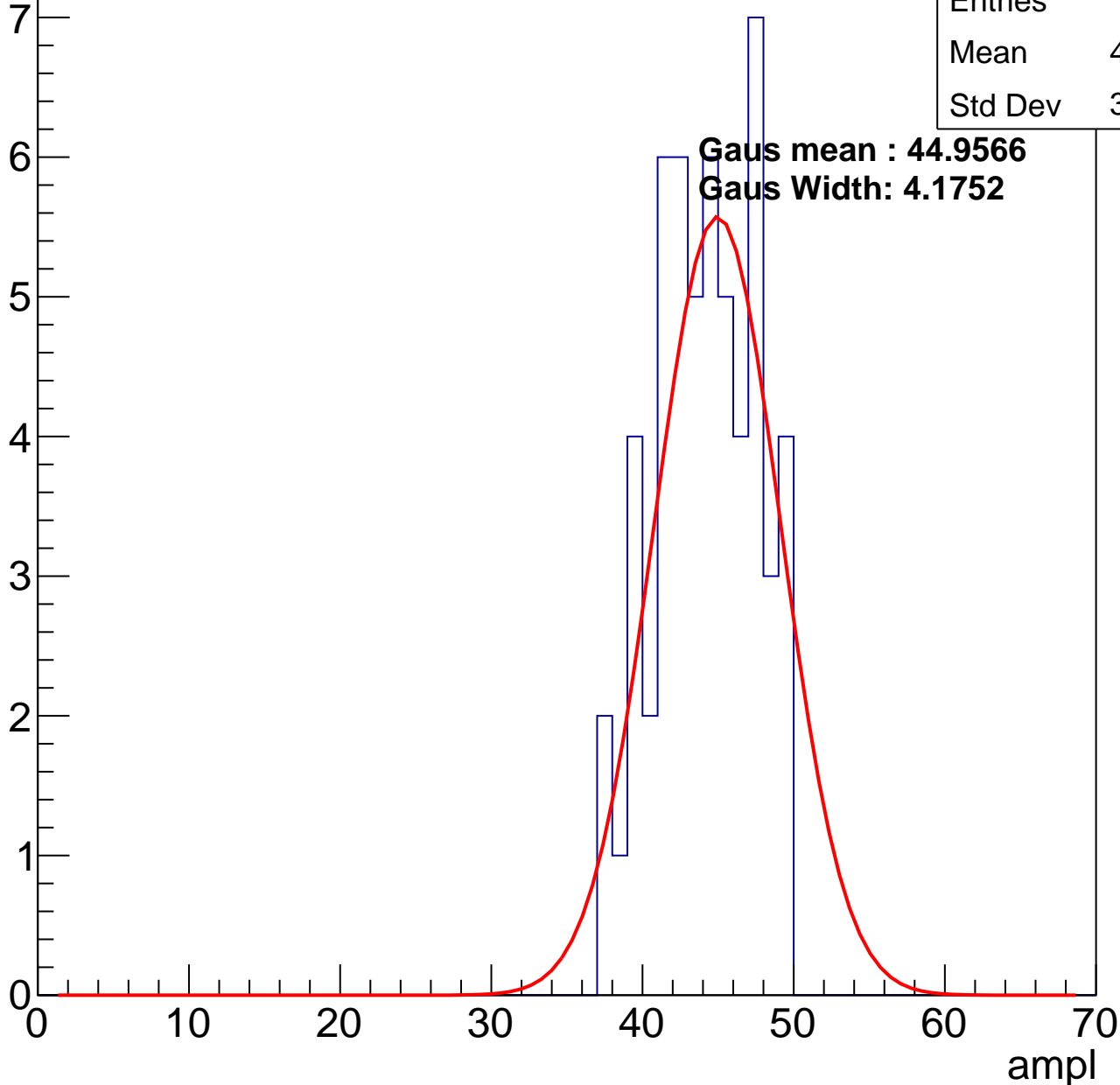
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.69
Std Dev	3.235

Gaus mean : 44.9566

Gaus Width: 4.1752

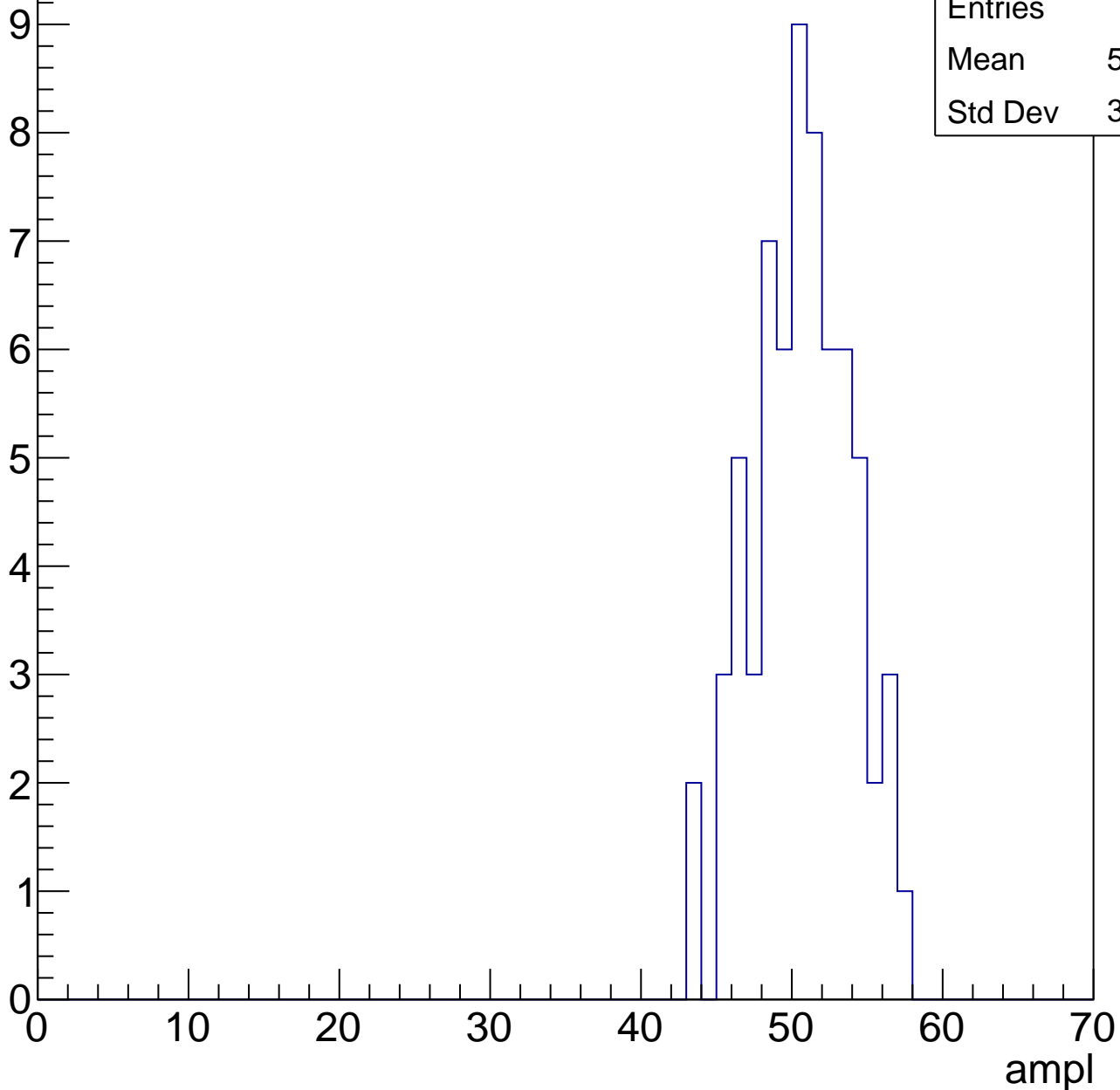


B1L003S, U20-ch126, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	50.23
Std Dev	3.218

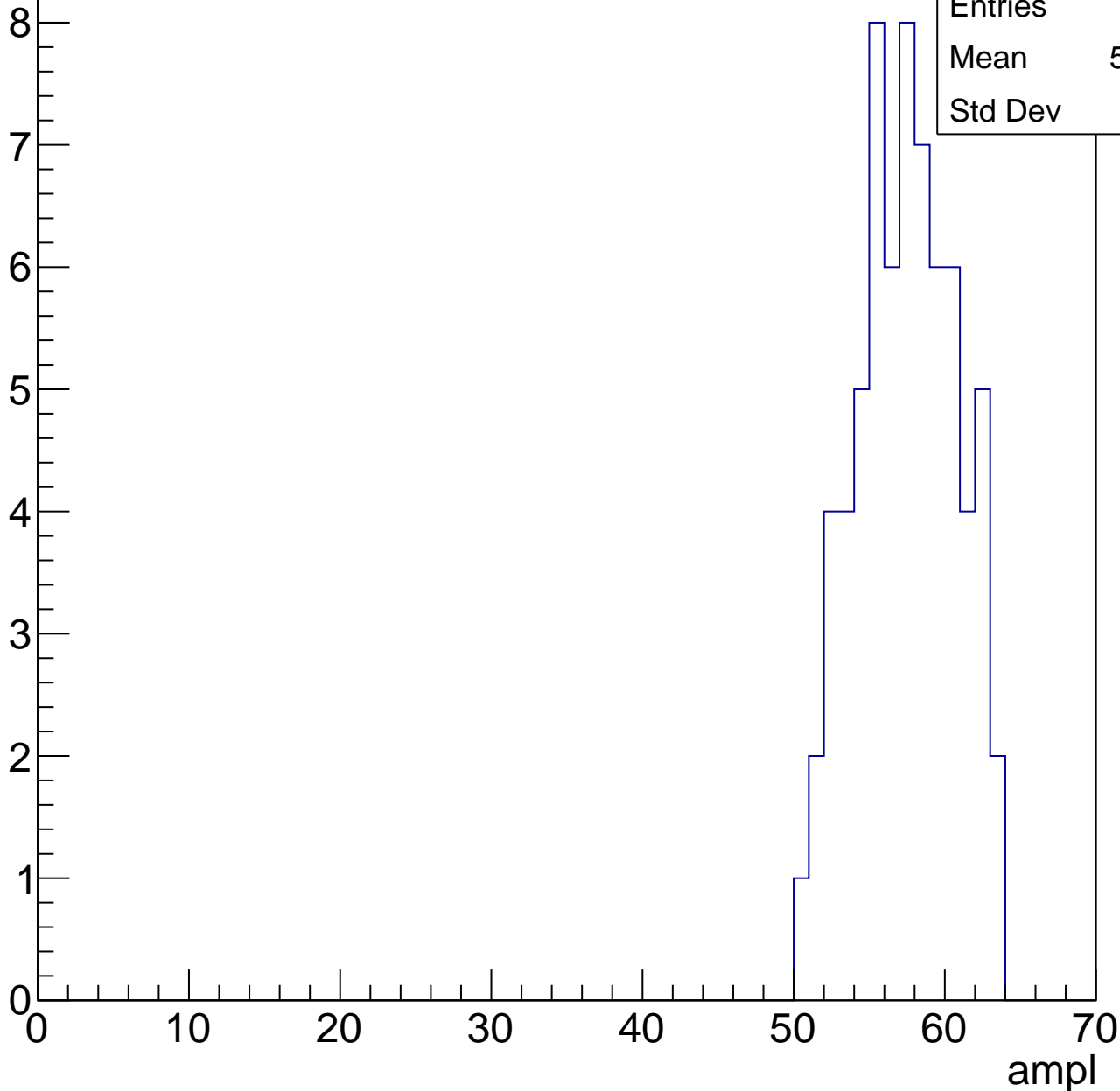


B1L003S, U20-ch126, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	56.97
Std Dev	3.24

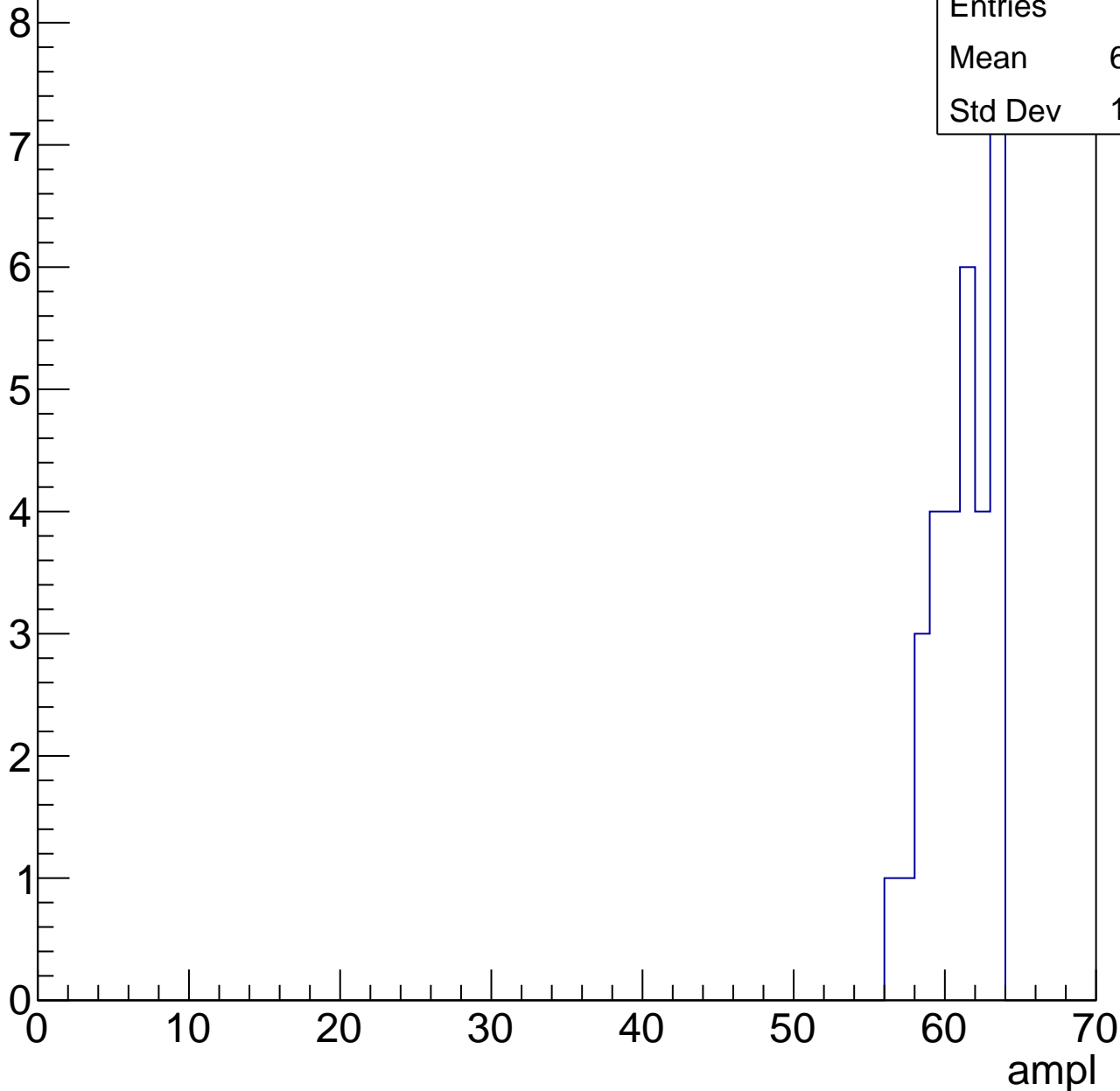


B1L003S, U20-ch126, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	31
Mean	60.68
Std Dev	1.974



B1L003S, U20-ch126, adc6

calib_packv5_042523_0143.root, FC#13, port D2

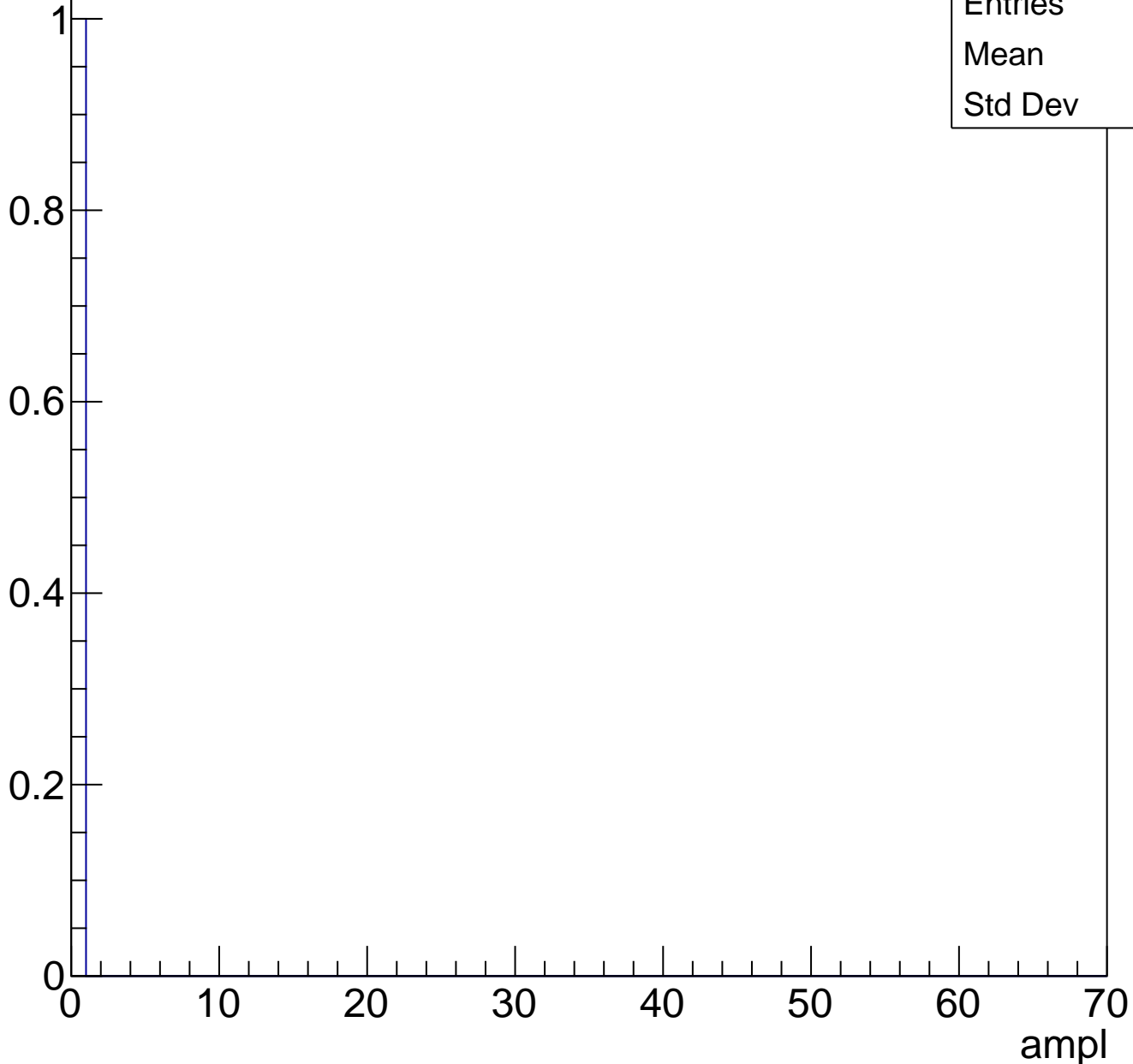
Entry



B1L003S, U20-ch126, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U20-ch127, adc0

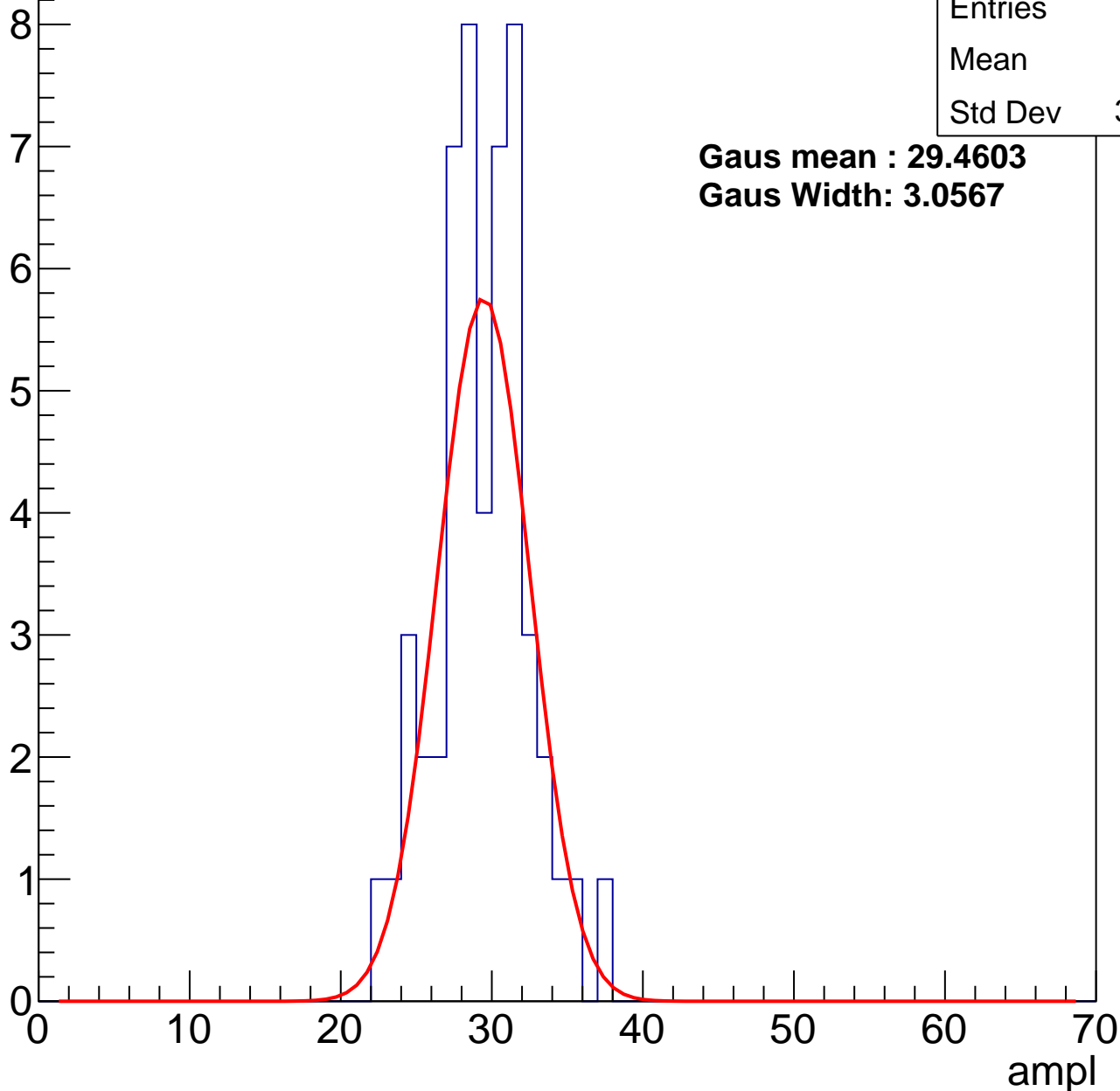
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	28.9
Std Dev	3.031

Gaus mean : 29.4603

Gaus Width: 3.0567



B1L003S, U20-ch127, adc1

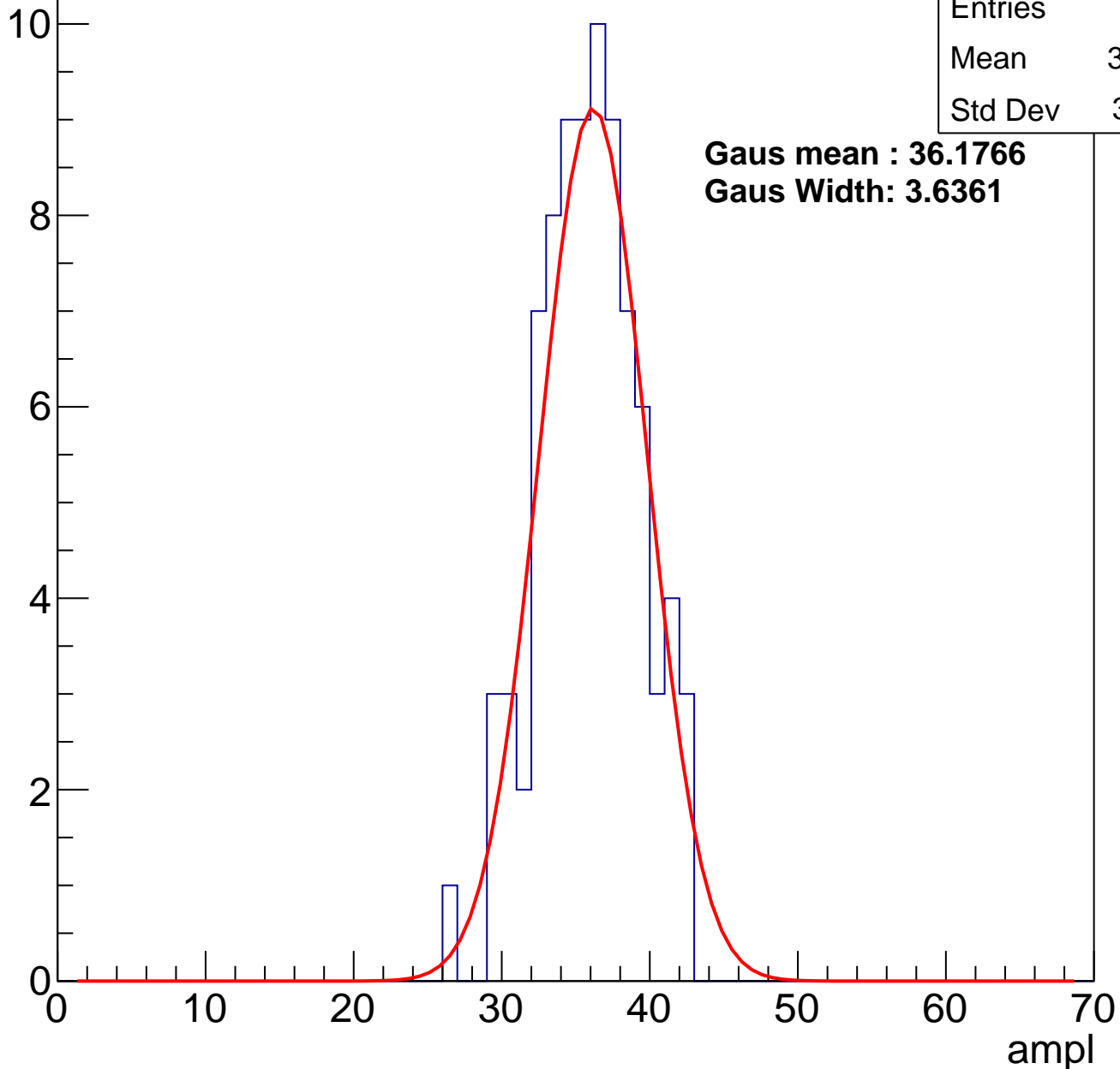
calib_packv5_042523_0143.root, FC#13, port D2

Entries	84
Mean	35.44
Std Dev	3.371

Gaus mean : 36.1766

Gaus Width: 3.6361

Entry



B1L003S, U20-ch127, adc2

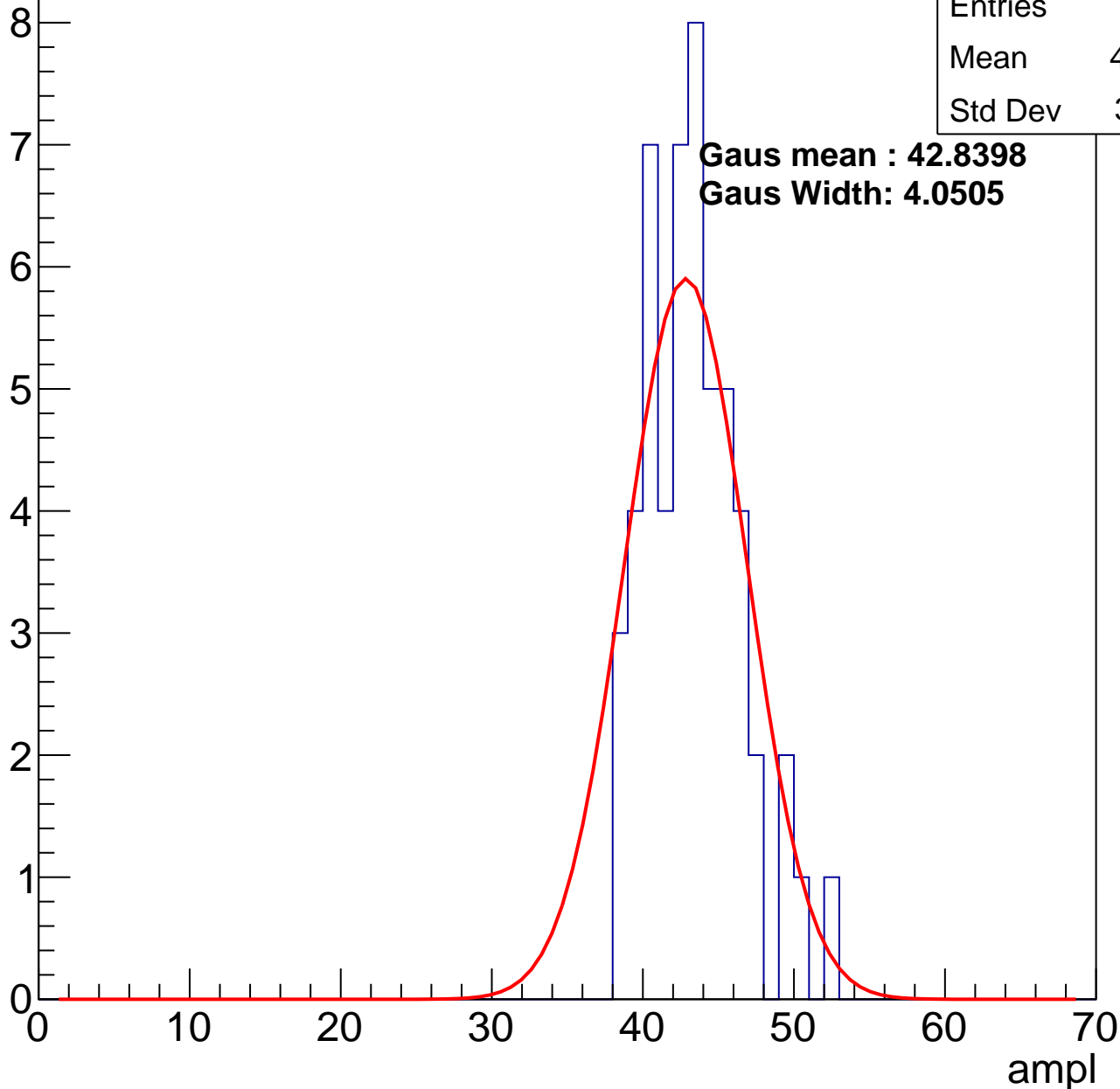
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.92
Std Dev	3.131

Gaus mean : 42.8398

Gaus Width: 4.0505

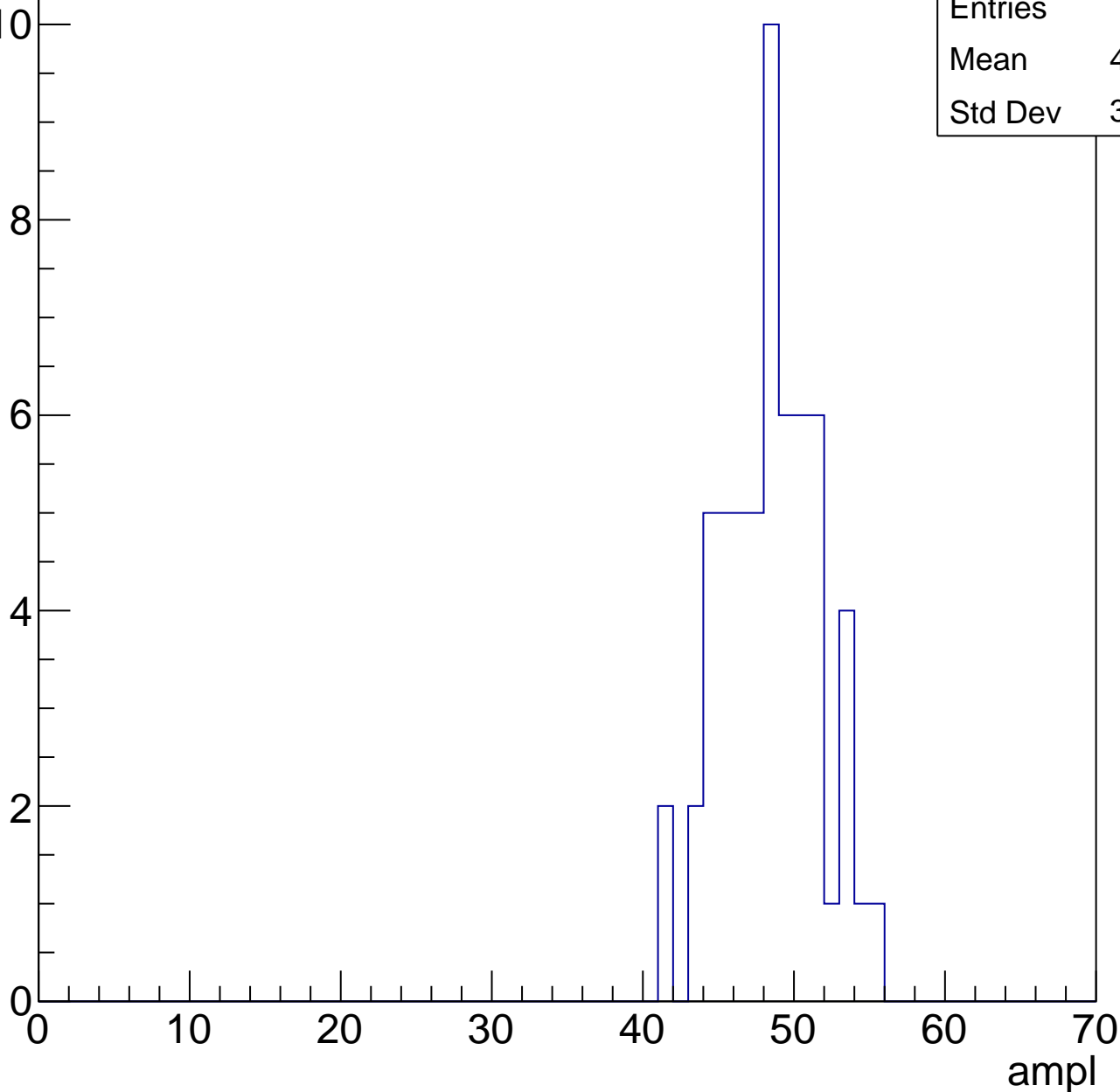


B1L003S, U20-ch127, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	47.98
Std Dev	3.143

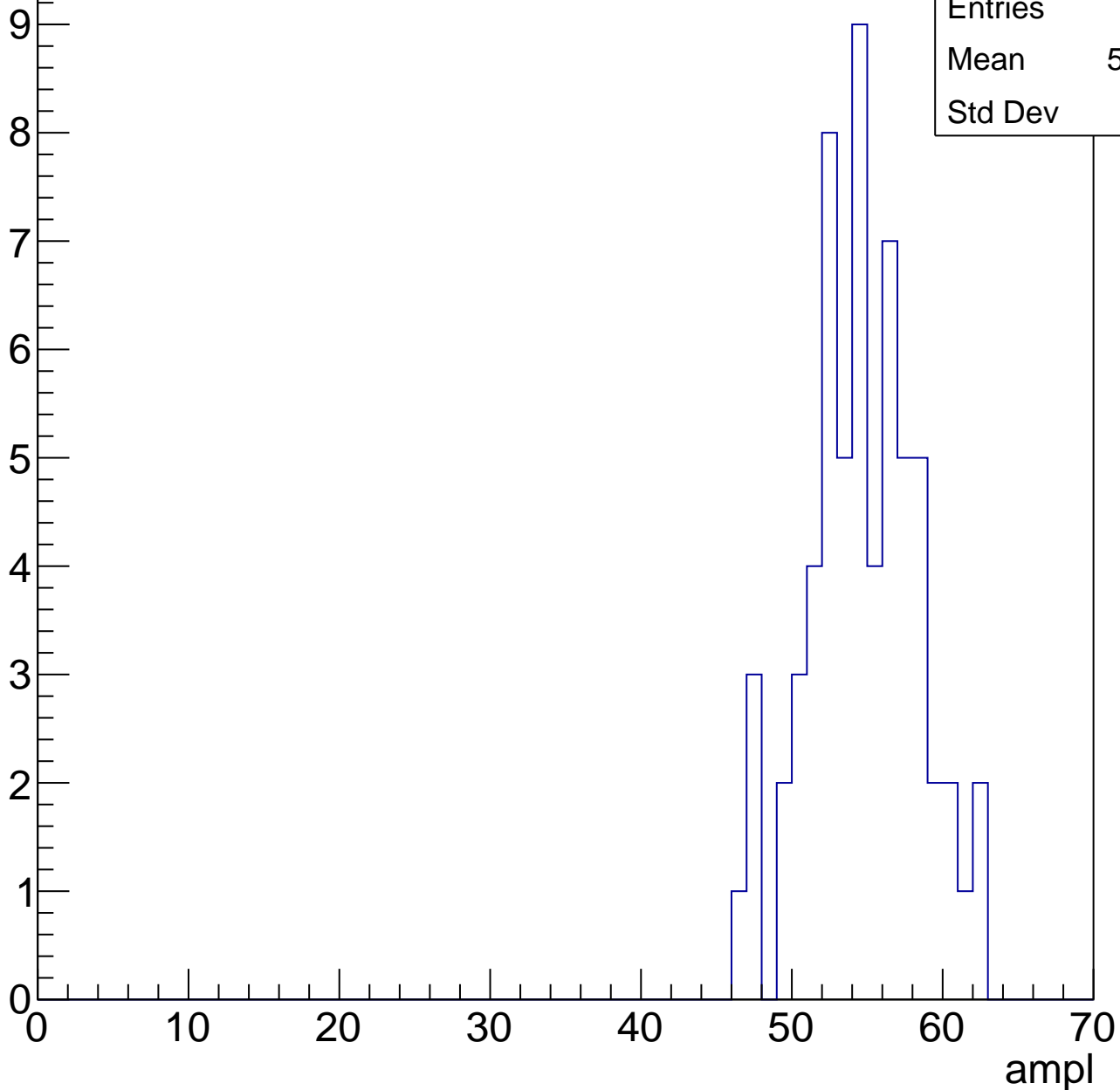


B1L003S, U20-ch127, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	54.22
Std Dev	3.64

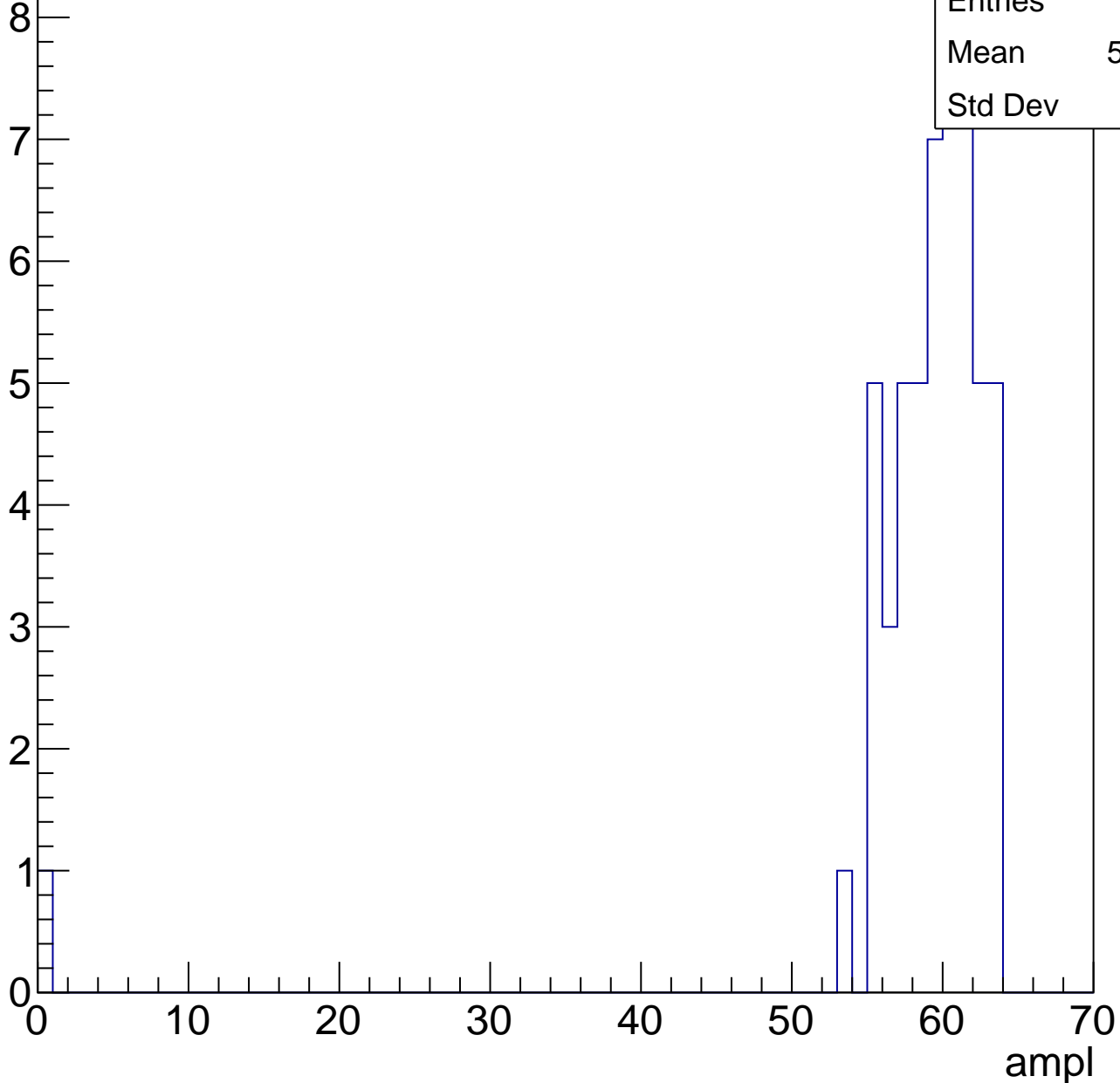


B1L003S, U20-ch127, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

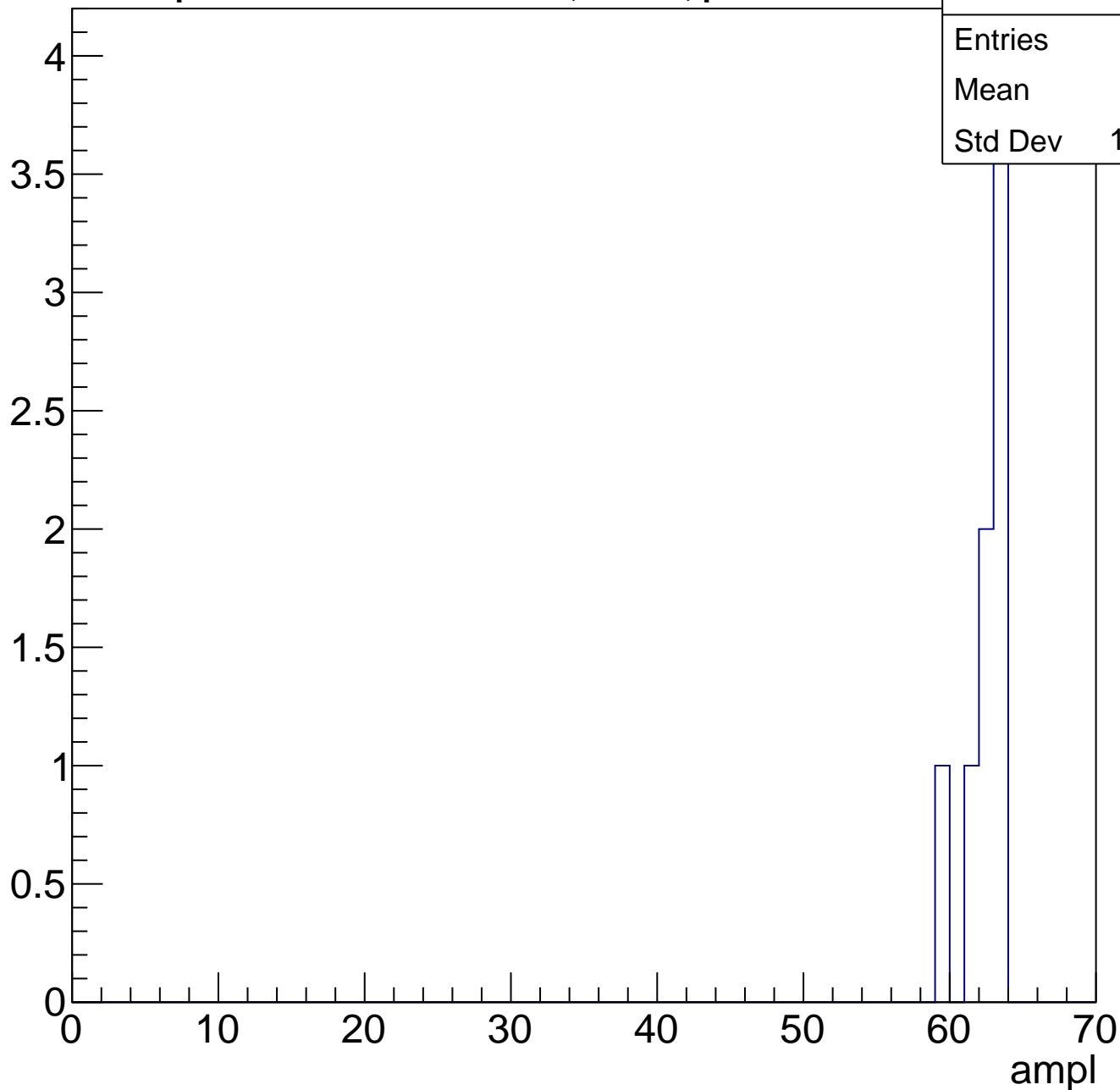
Entries	53
Mean	58.06
Std Dev	8.43



B1L003S, U20-ch127, adc6

calib_packv5_042523_0143.root, FC#13, port D2

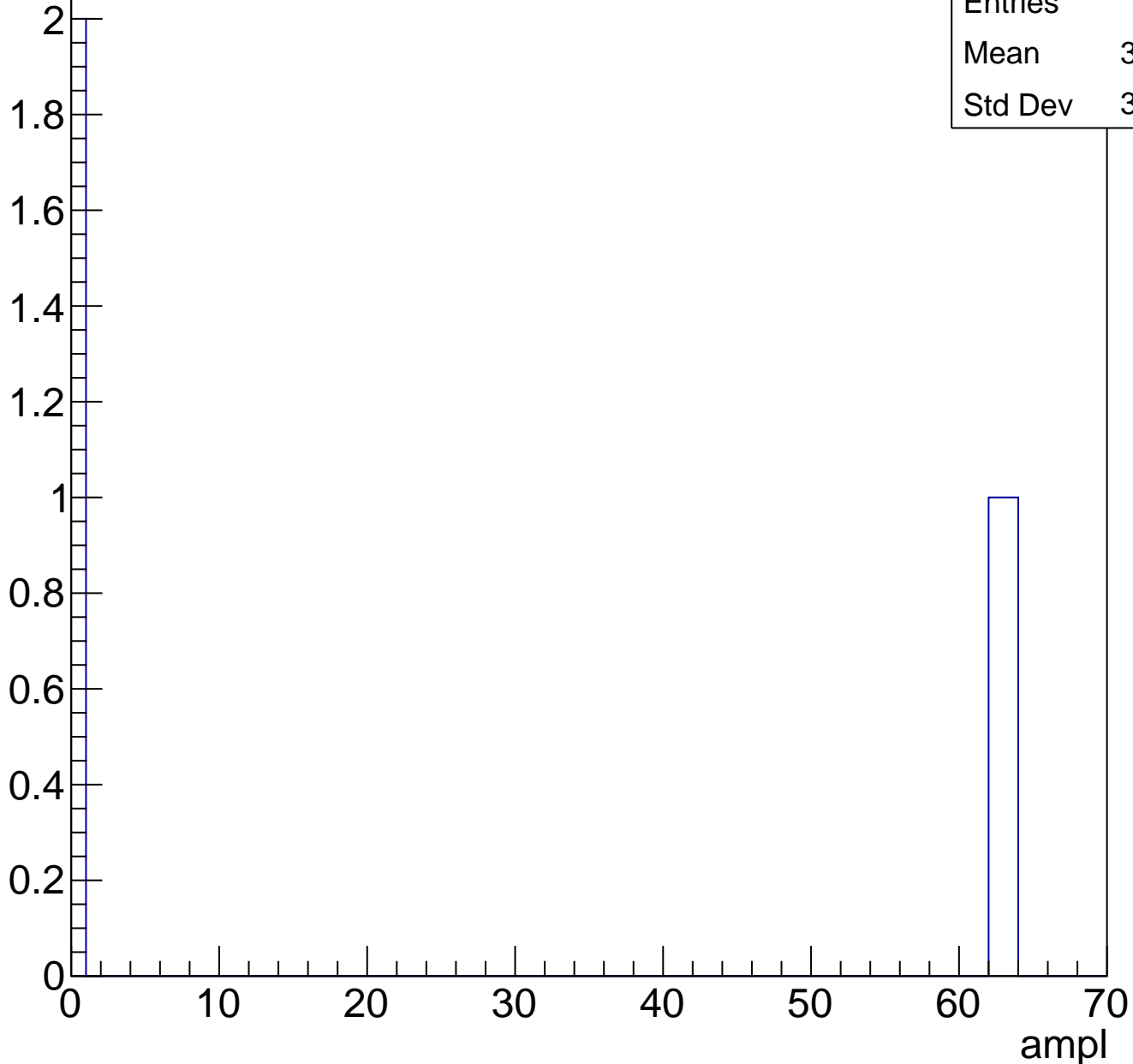
Entry



B1L003S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

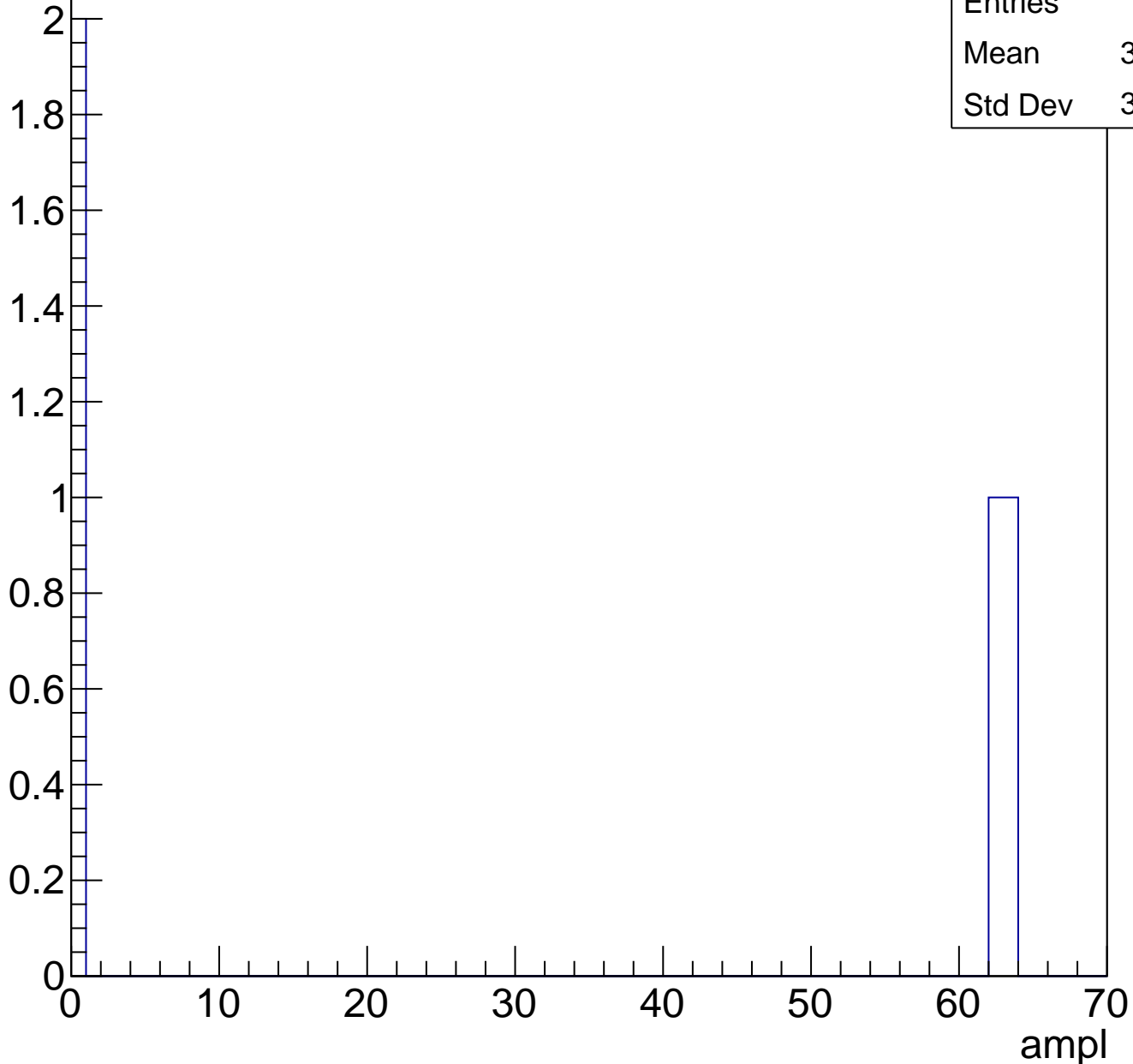


Entries	4
Mean	31.25
Std Dev	31.25

B1L003S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	31.25
Std Dev	31.25