

B1L000S, U15-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.23
Std Dev	12.11

Turn on : 23.7483

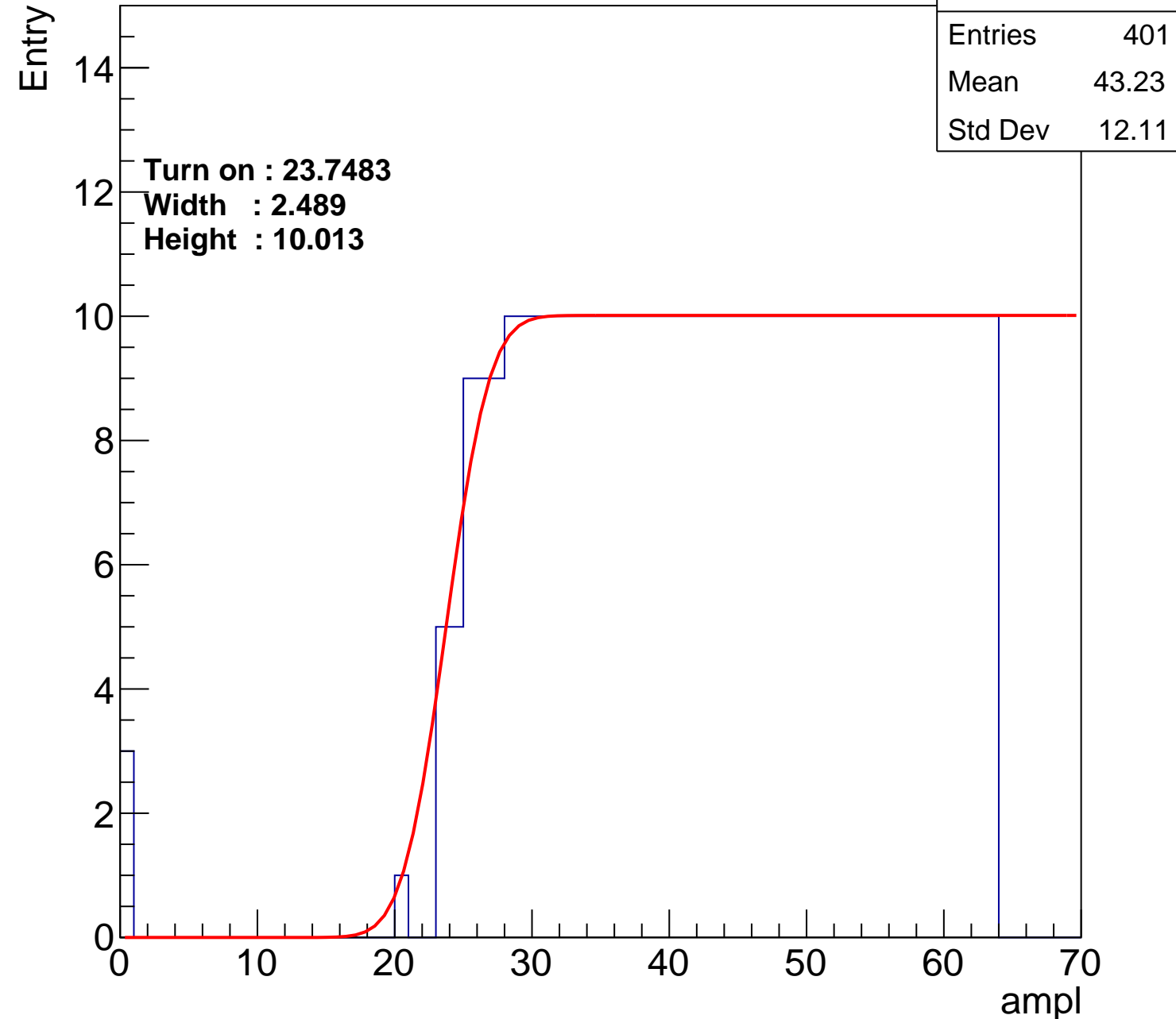
Width : 2.489

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.1336

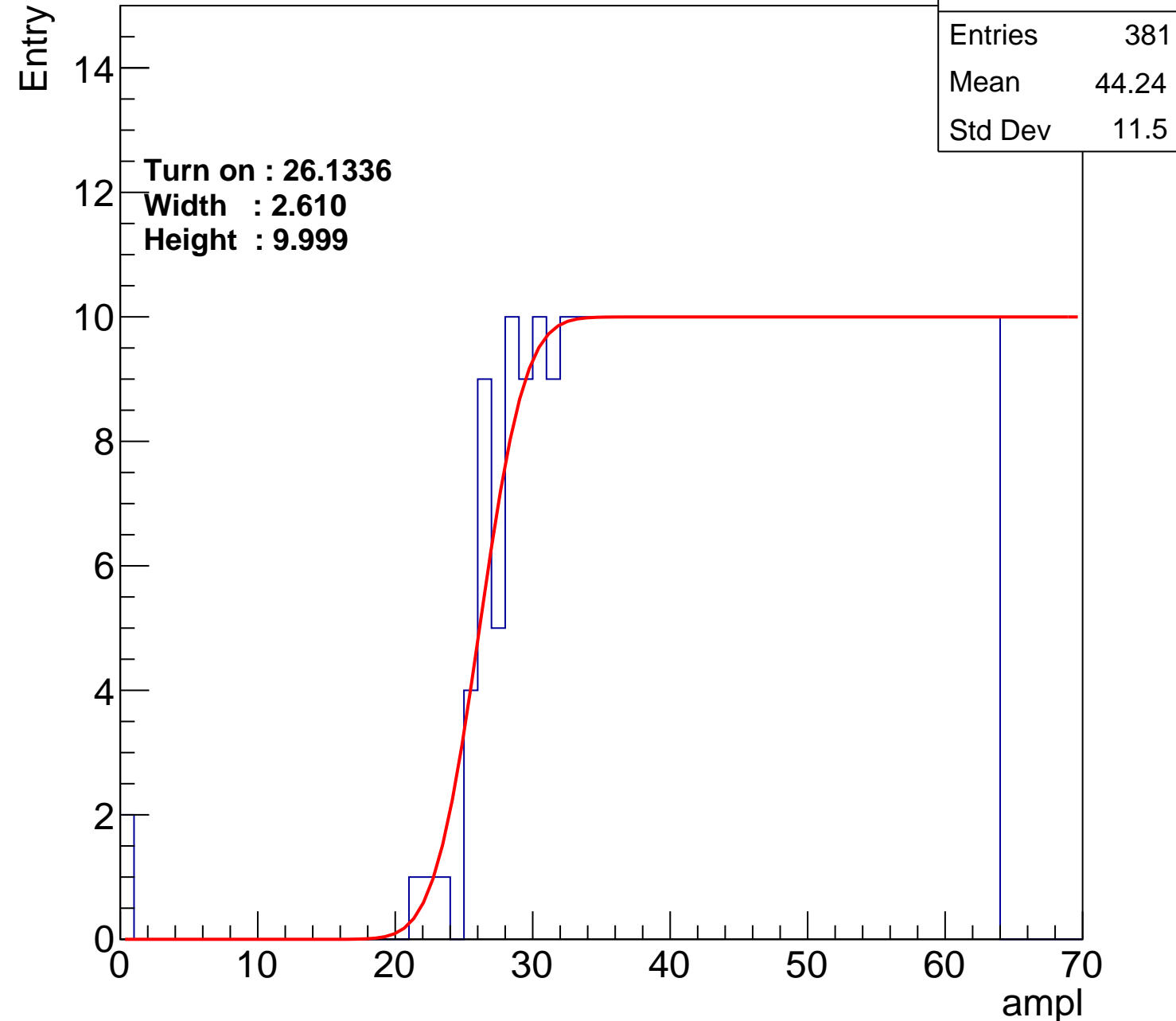
Width : 2.610

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.98
Std Dev	10.97

Turn on : 27.6937

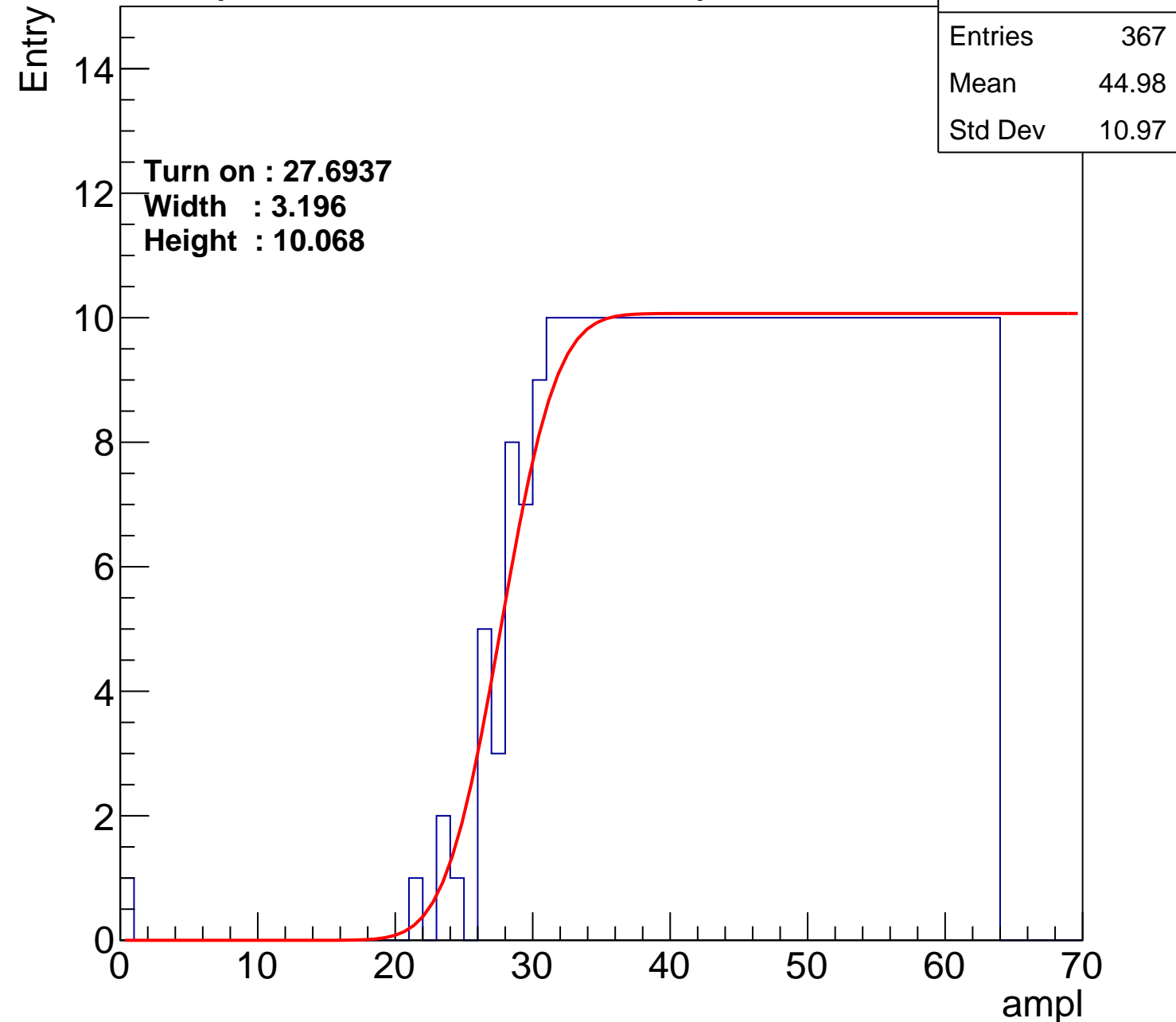
Width : 3.196

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.8485

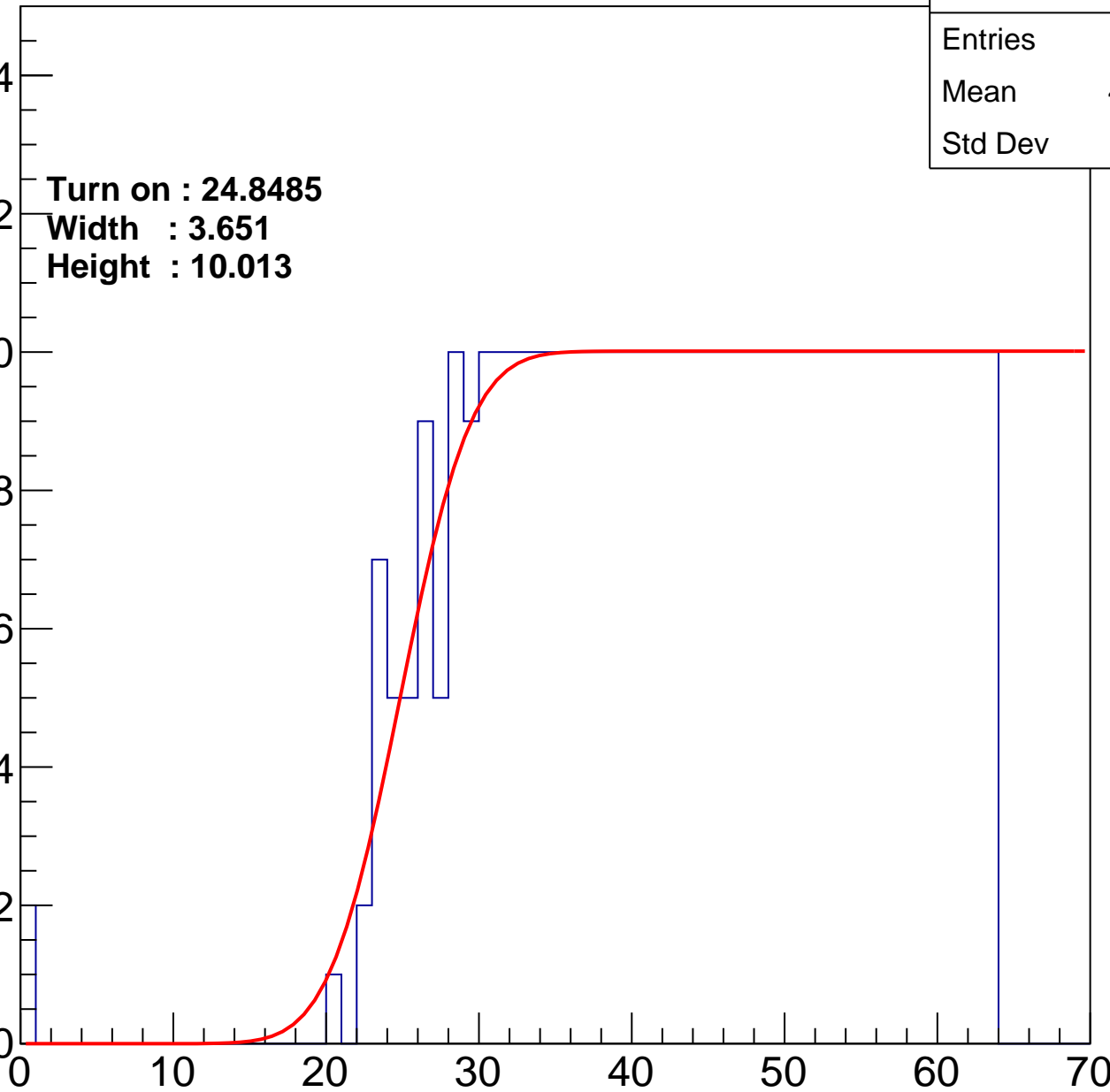
Width : 3.651

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch4

calib_packv5_042523_0143.root, FC#3, port B3

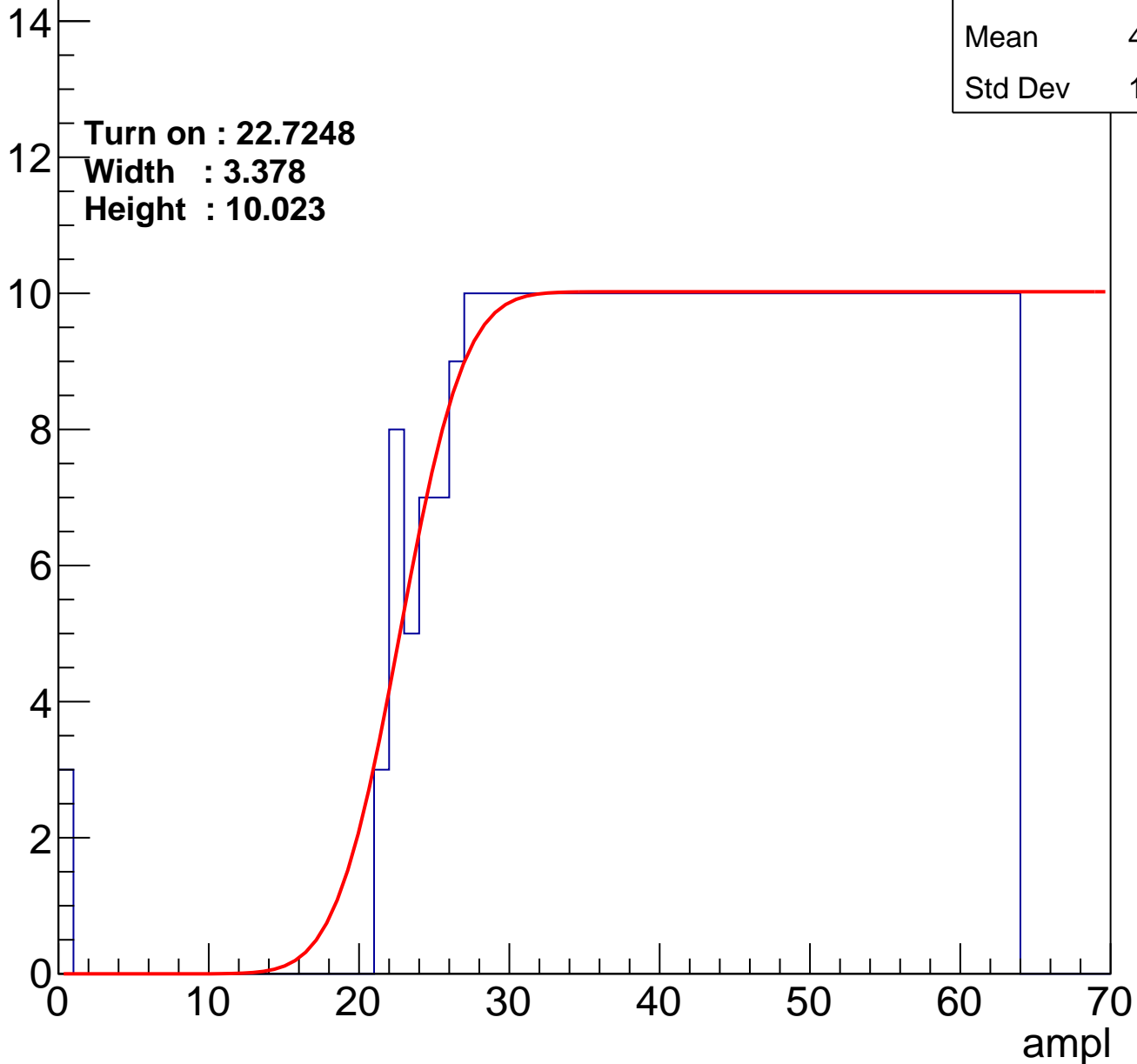
Entries	412
Mean	42.67
Std Dev	12.42

Turn on : 22.7248

Width : 3.378

Height : 10.023

Entry



B1L000S, U15-ch5

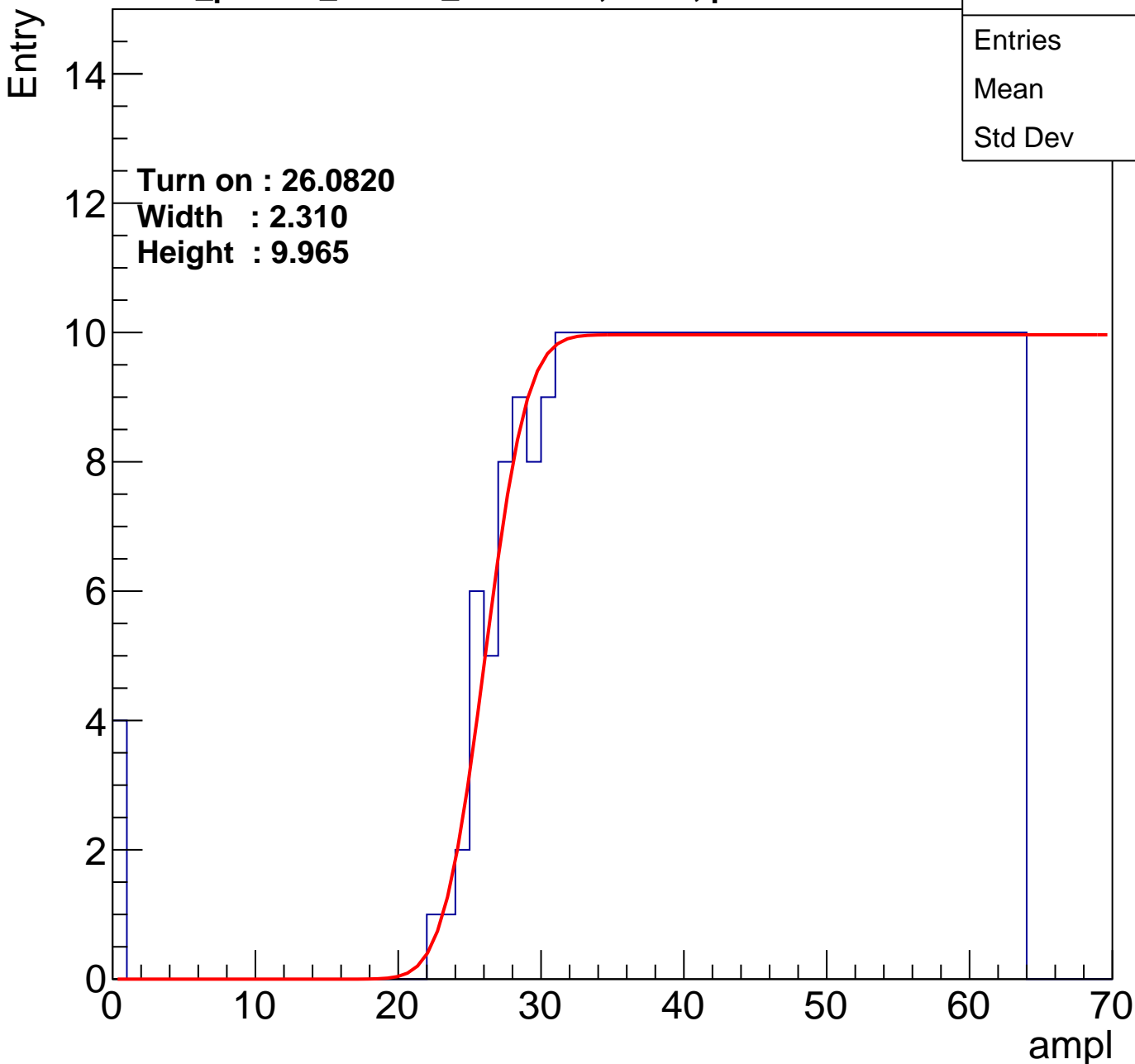
calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44
Std Dev	11.91

Turn on : 26.0820

Width : 2.310

Height : 9.965



B1L000S, U15-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.4809

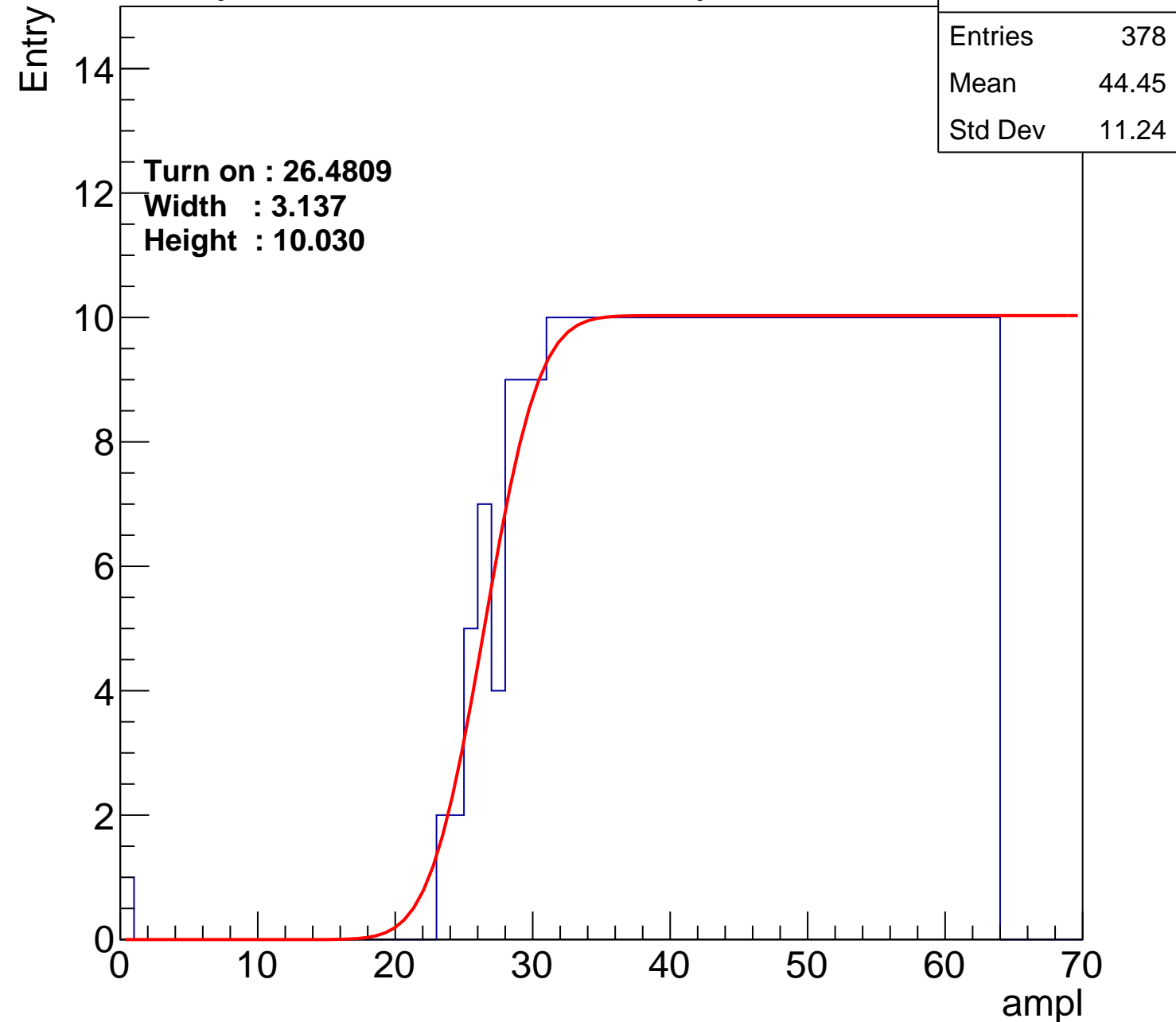
Width : 3.137

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.77
Std Dev	11.62

Turn on : 28.1559

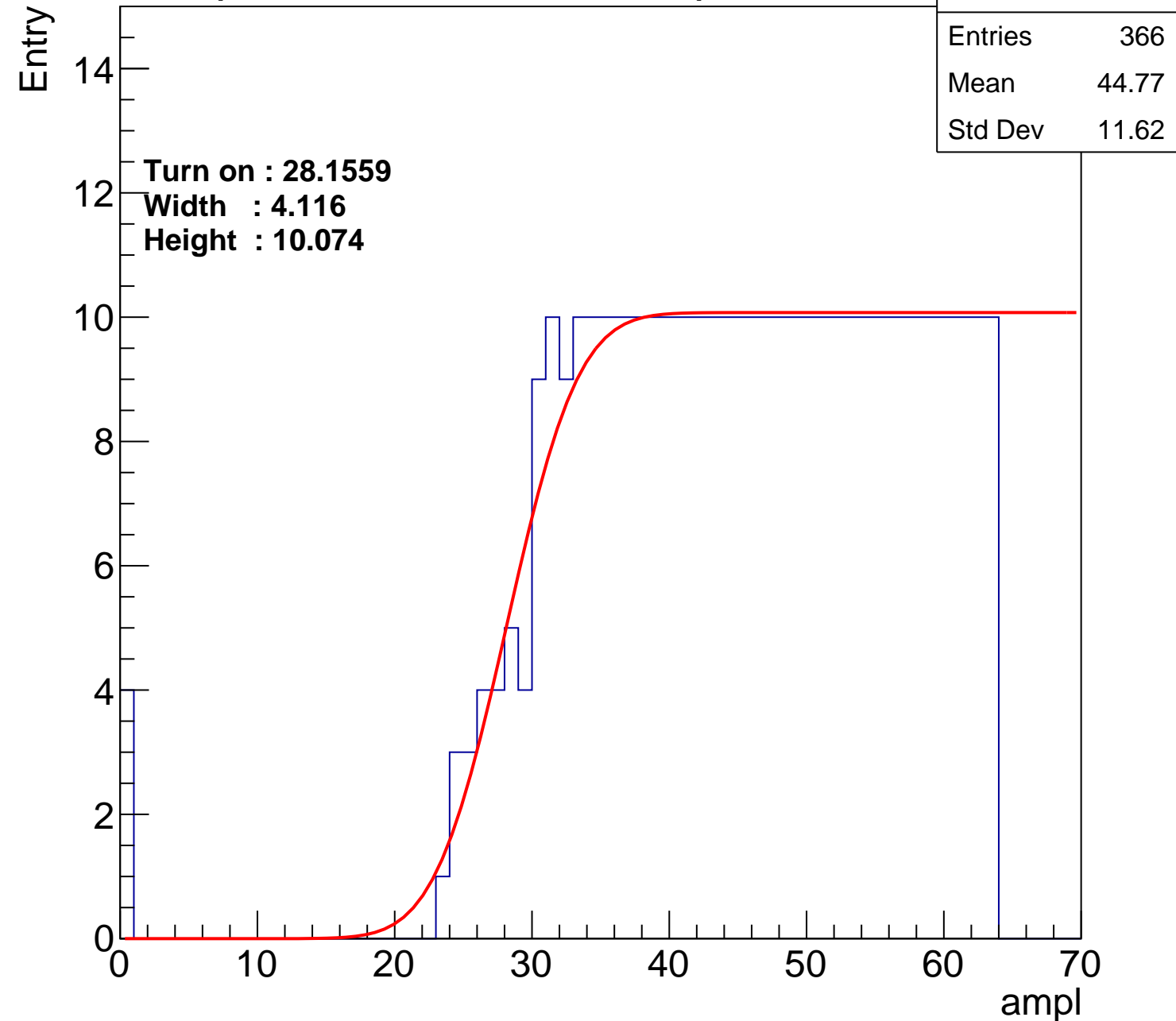
Width : 4.116

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 25.0626

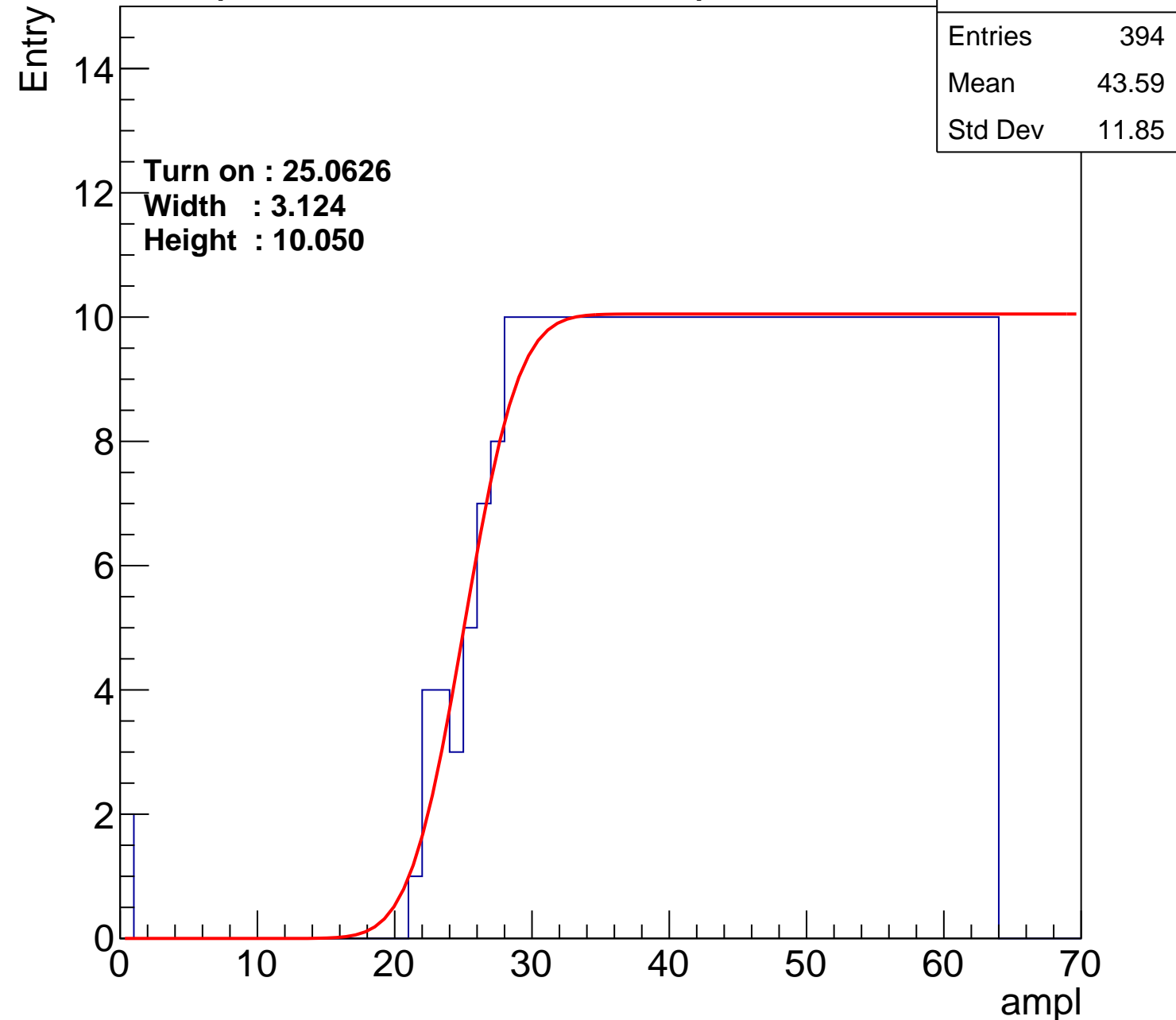
Width : 3.124

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.42
Std Dev	11.23

Turn on : 26.3221

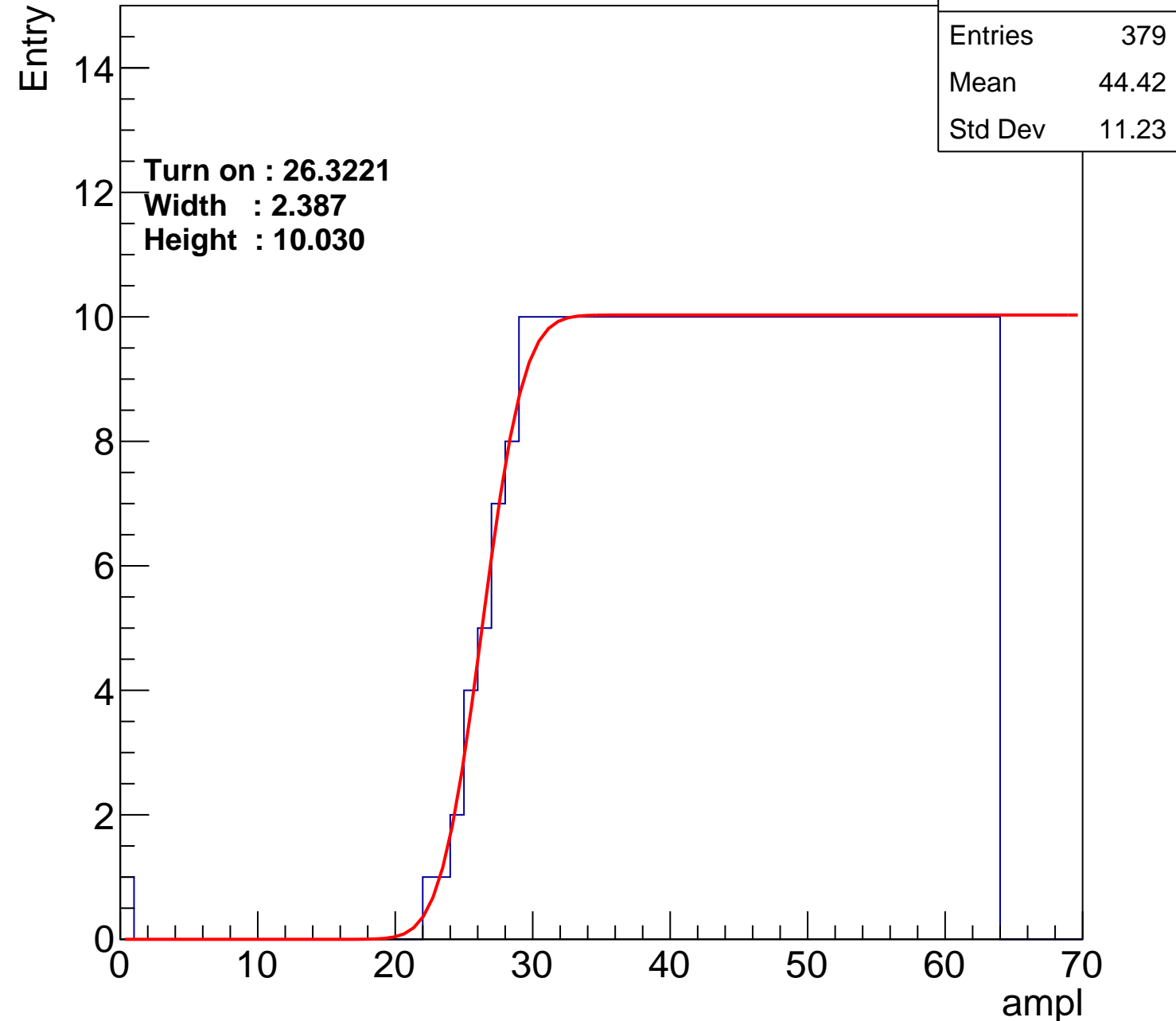
Width : 2.387

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch10

calib_packv5_042523_0143.root, FC#3, port B3

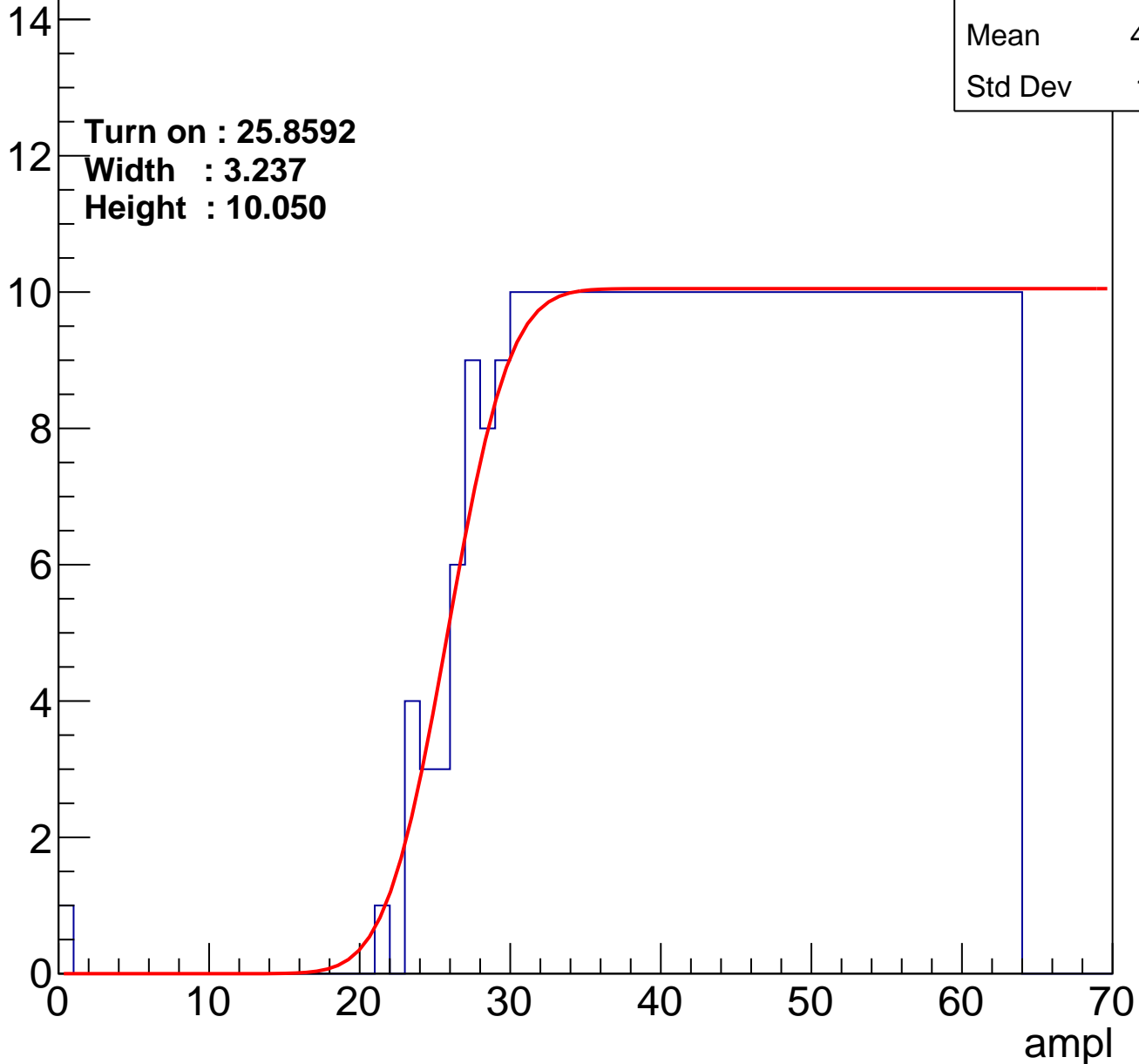
Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.8592

Width : 3.237

Height : 10.050

Entry



B1L000S, U15-ch11

calib_packv5_042523_0143.root, FC#3, port B3

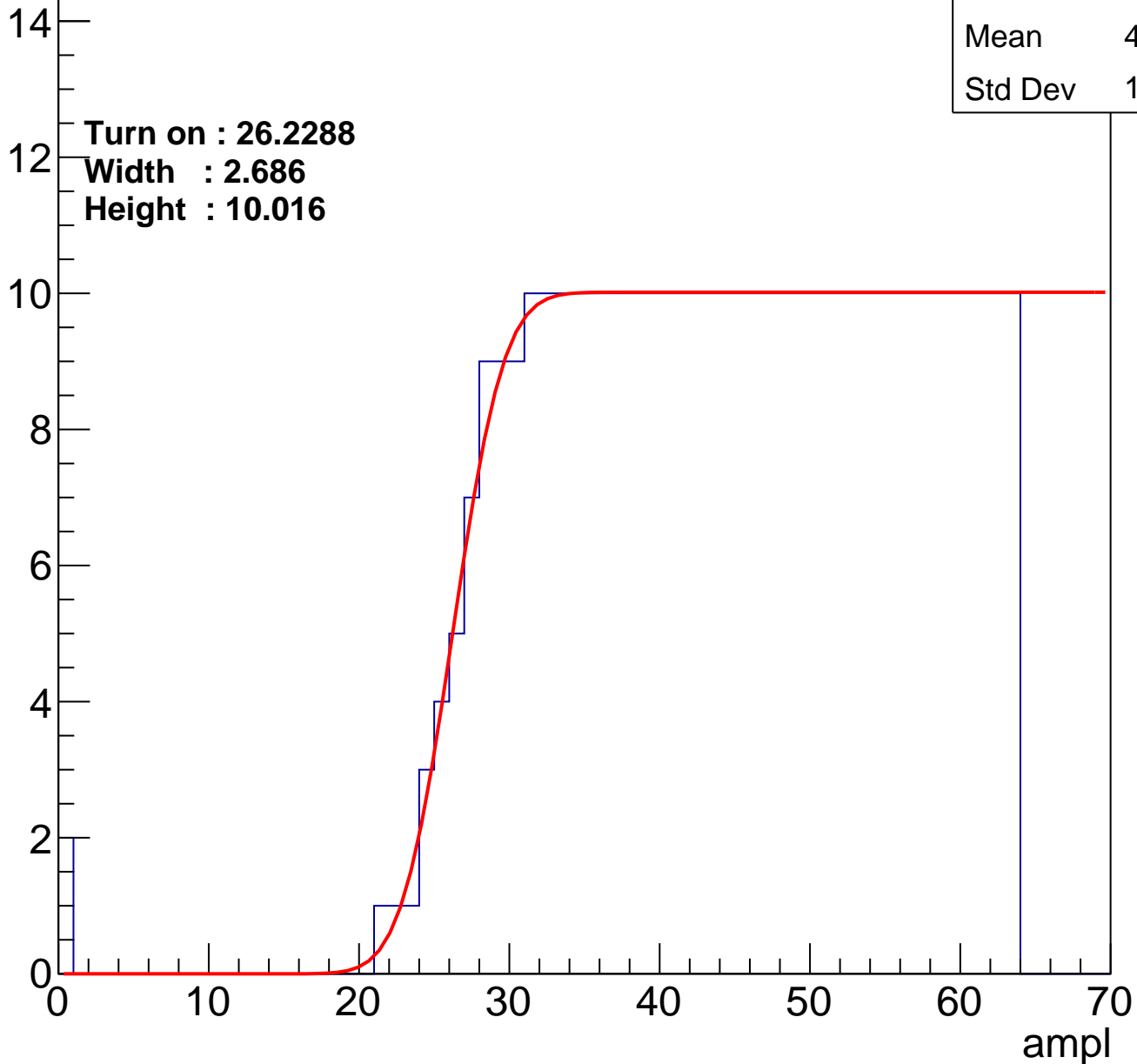
Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.2288

Width : 2.686

Height : 10.016

Entry



B1L000S, U15-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.88
Std Dev	10.97

Turn on : 27.7455

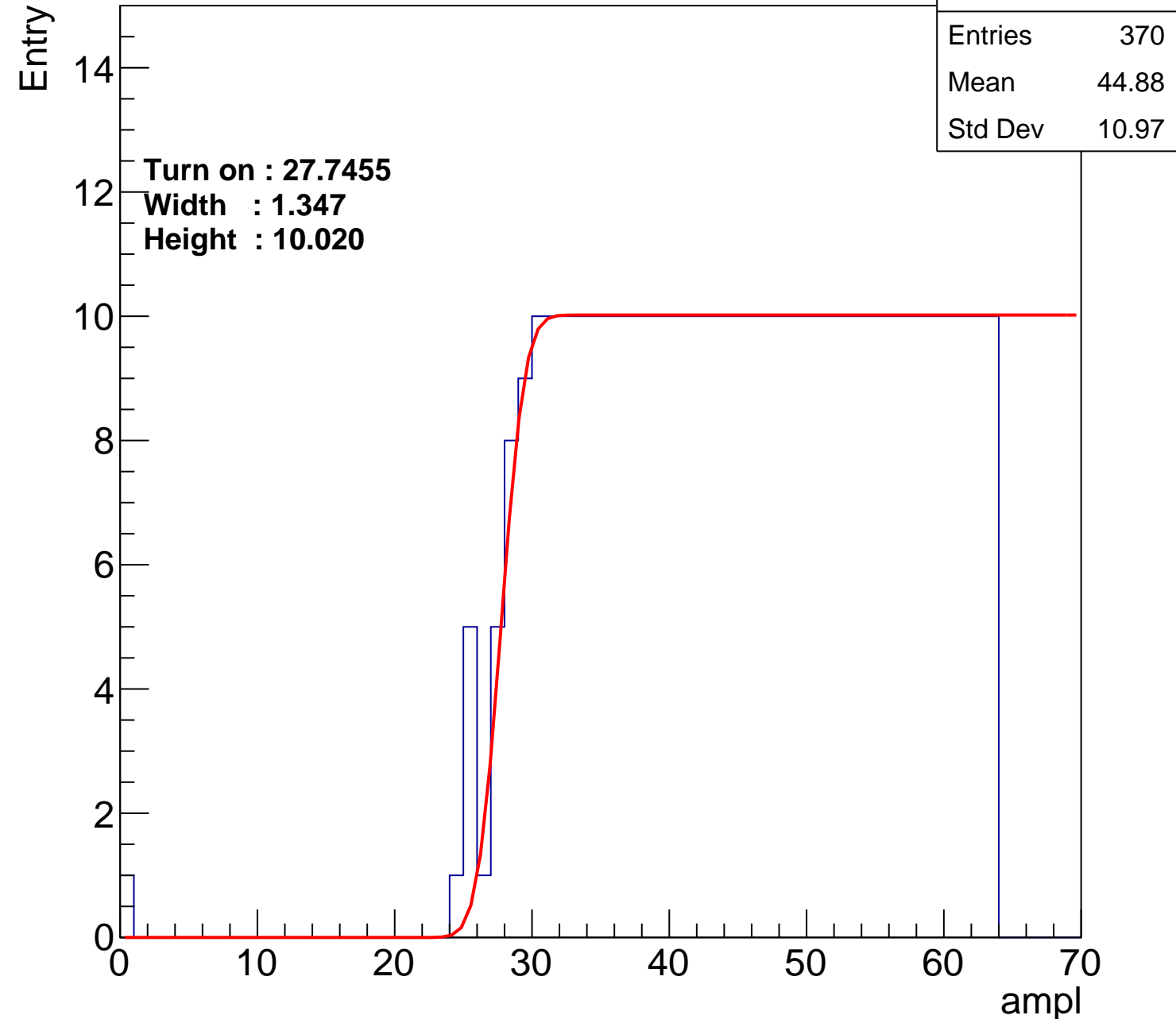
Width : 1.347

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.27
Std Dev	11.62

Turn on : 26.3417

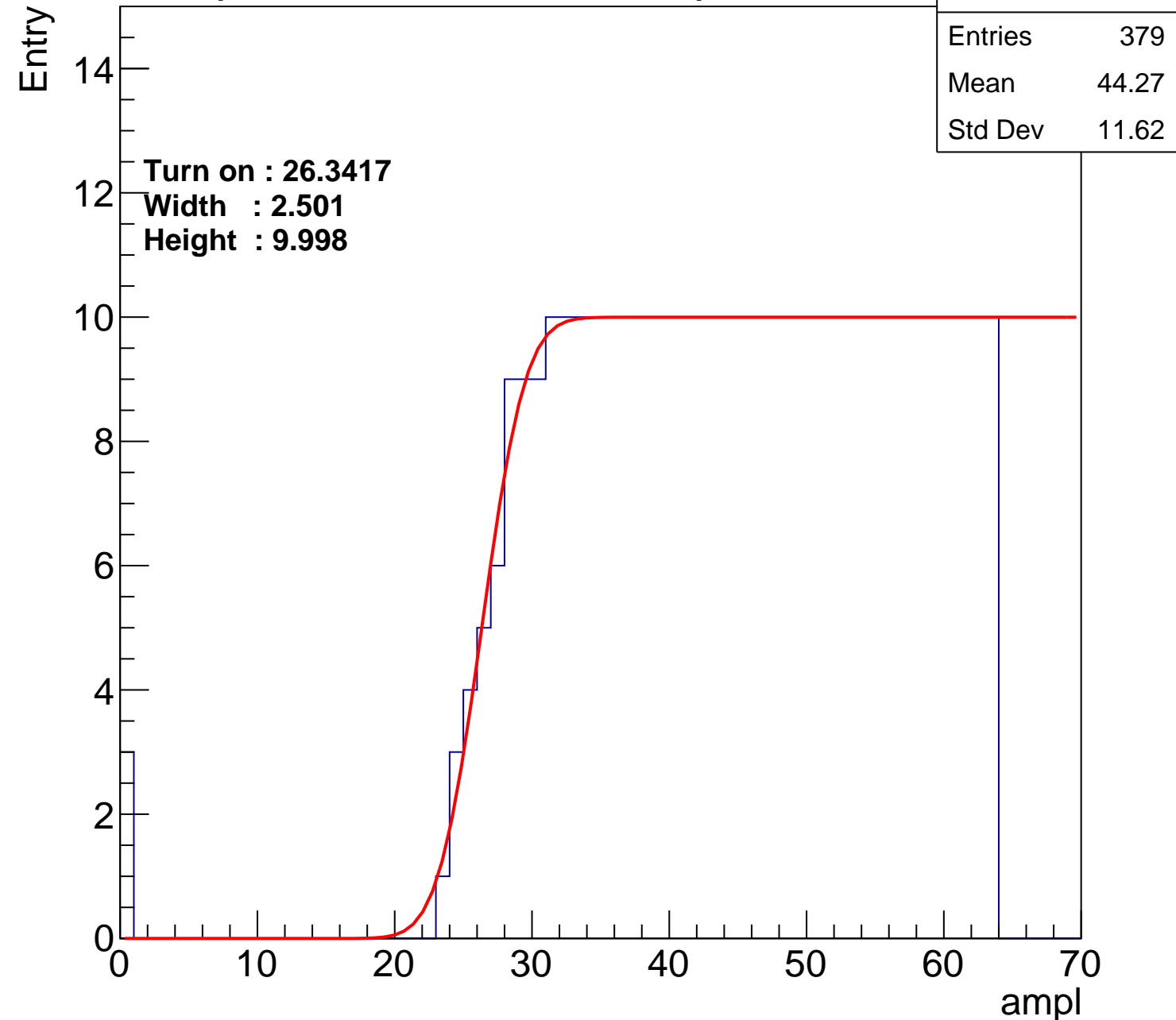
Width : 2.501

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.45
Std Dev	11.38

Turn on : 26.0628

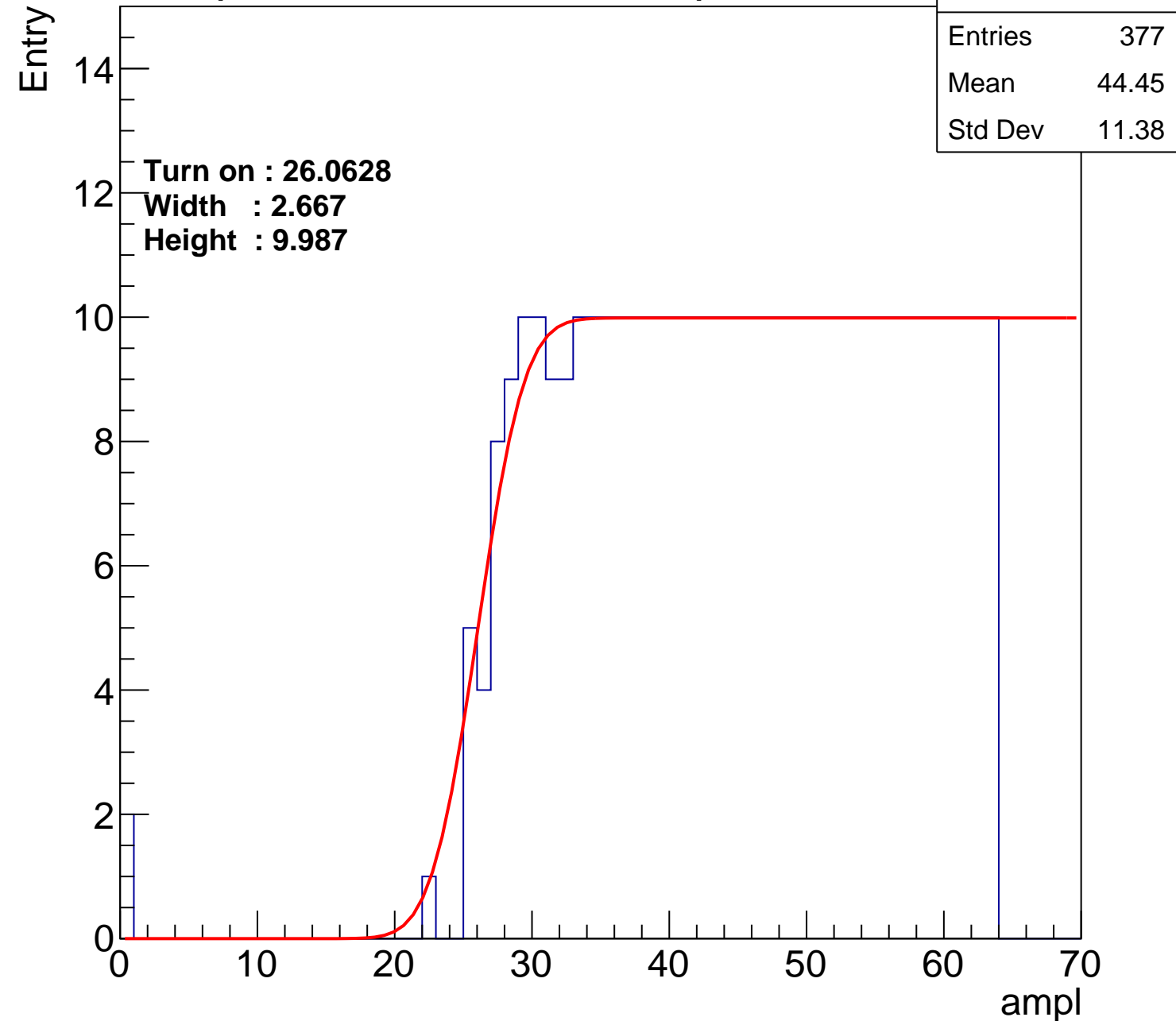
Width : 2.667

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch15

calib_packv5_042523_0143.root, FC#3, port B3

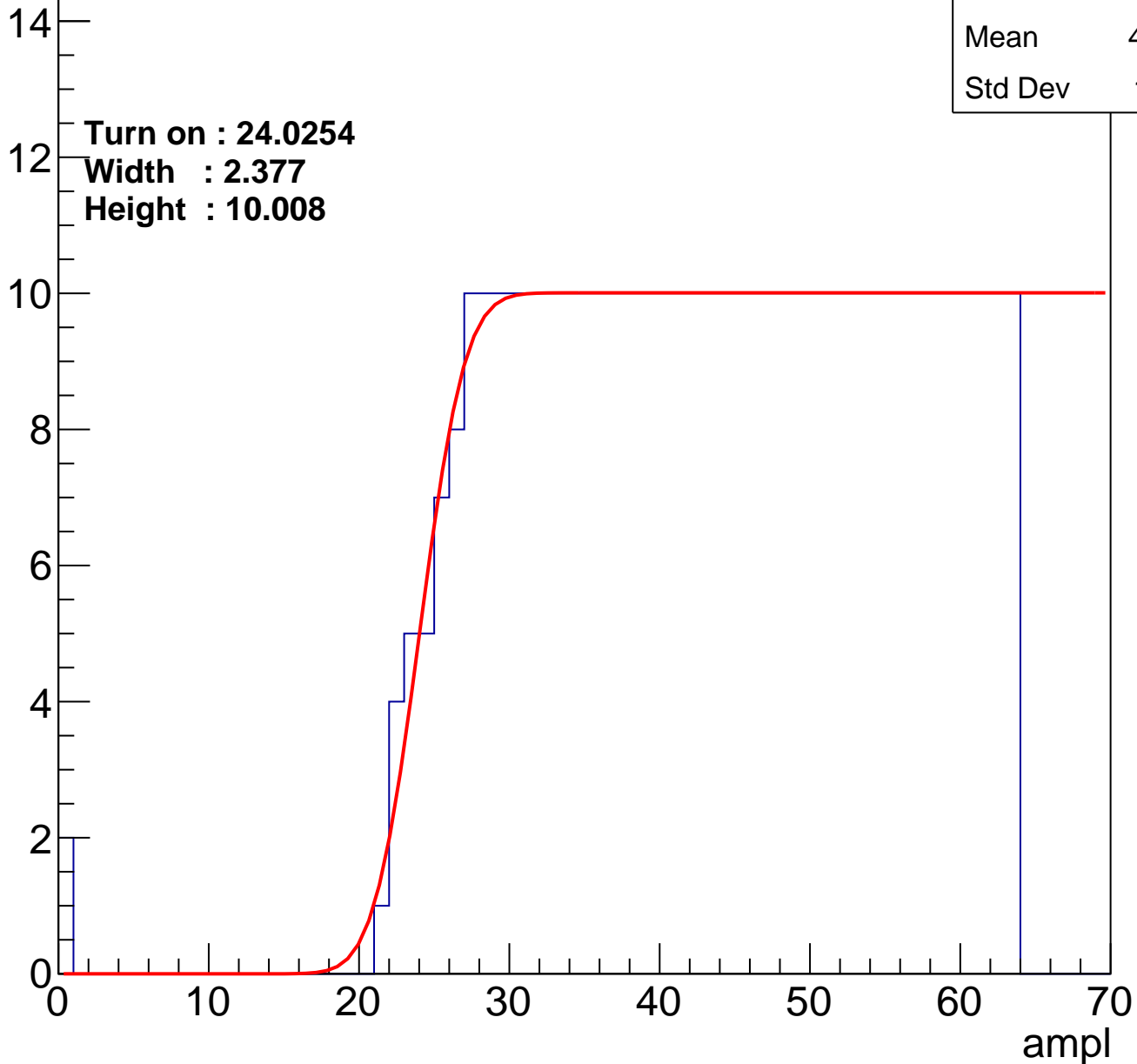
Entries	402
Mean	43.23
Std Dev	12.01

Turn on : 24.0254

Width : 2.377

Height : 10.008

Entry



B1L000S, U15-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.82
Std Dev	11.28

Turn on : 28.0302

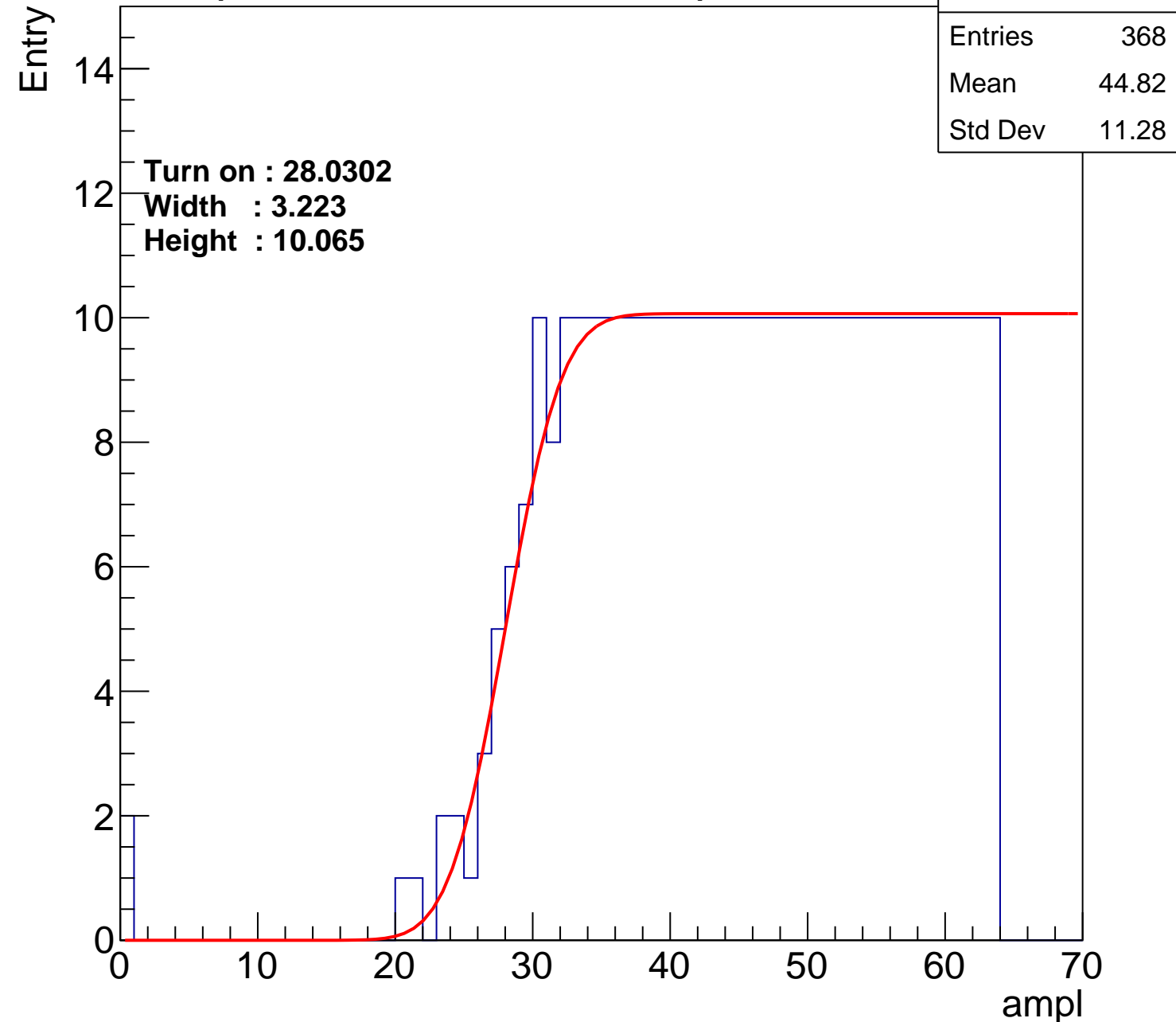
Width : 3.223

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch17

calib_packv5_042523_0143.root, FC#3, port B3

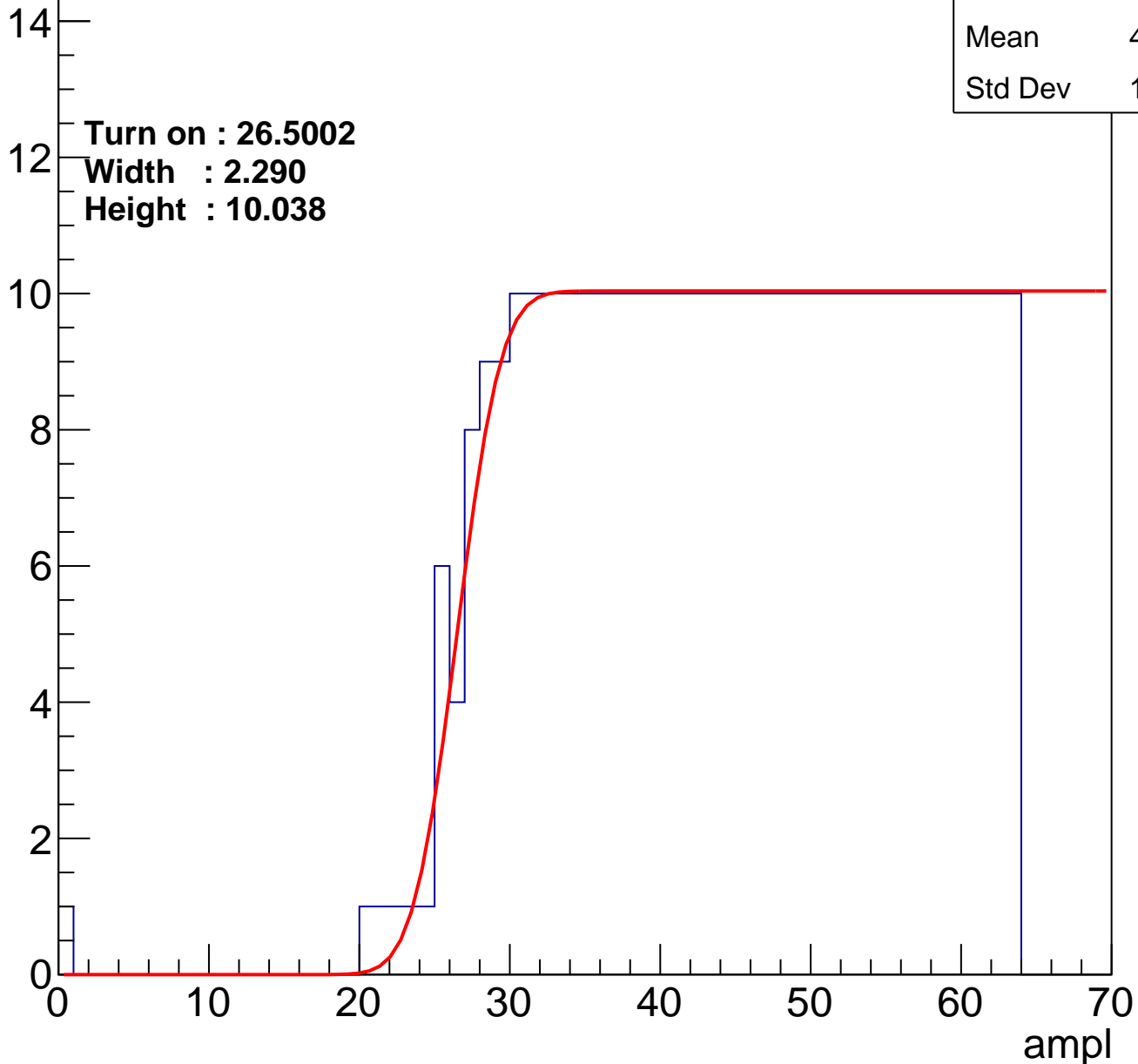
Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.5002

Width : 2.290

Height : 10.038

Entry



B1L000S, U15-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 26.2748

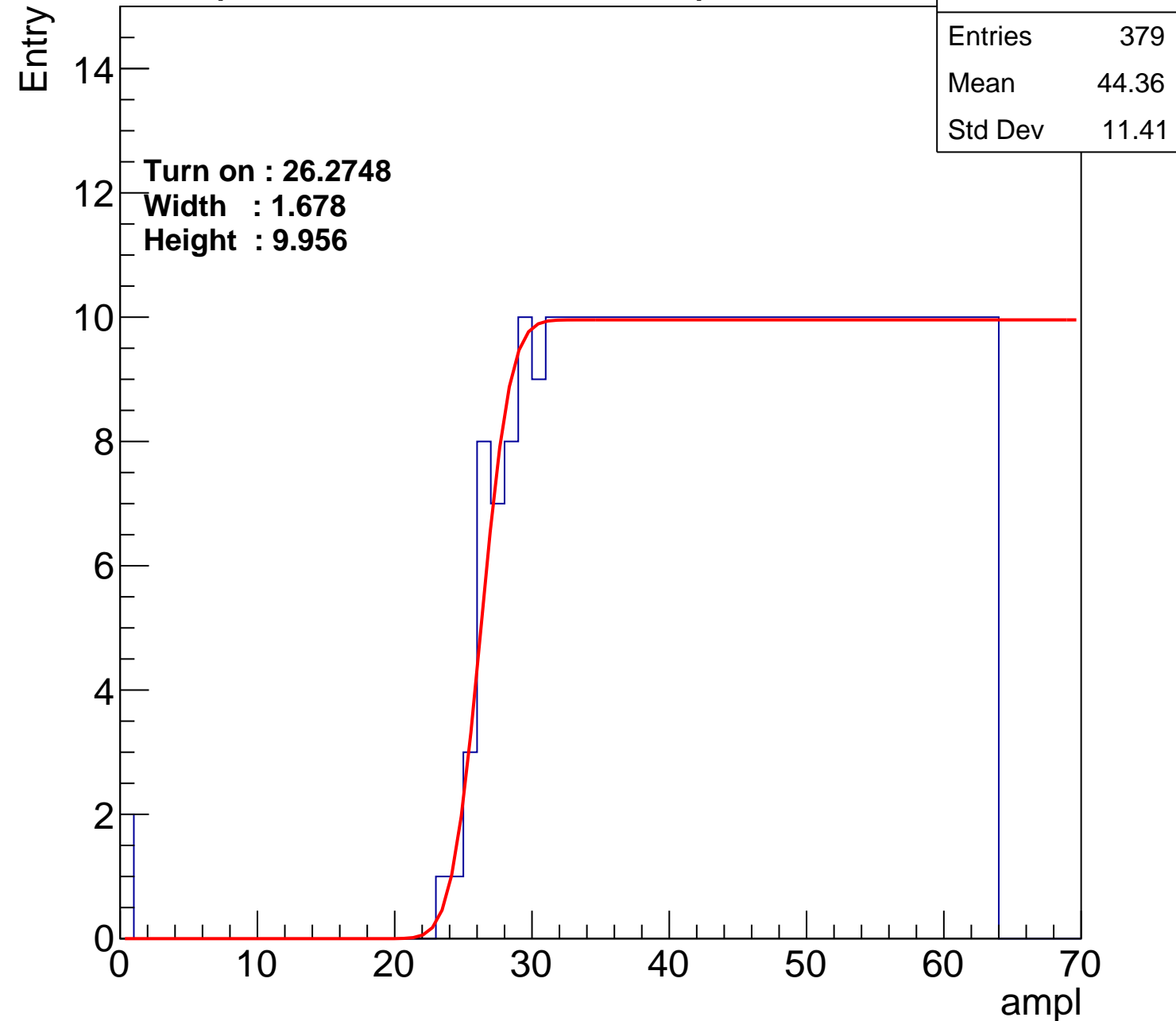
Width : 1.678

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch19

calib_packv5_042523_0143.root, FC#3, port B3

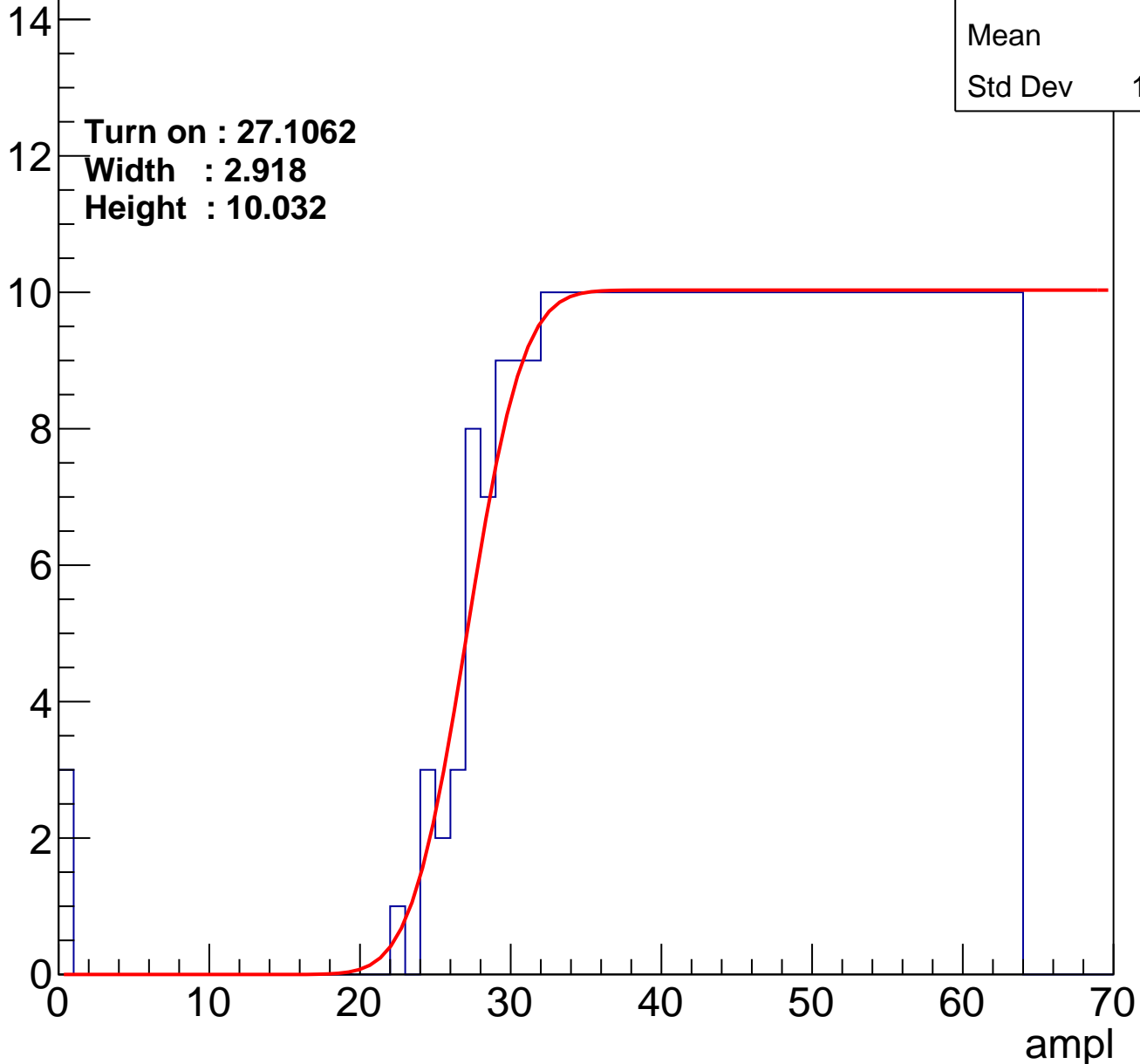
Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.1062

Width : 2.918

Height : 10.032

Entry



B1L000S, U15-ch20

calib_packv5_042523_0143.root, FC#3, port B3

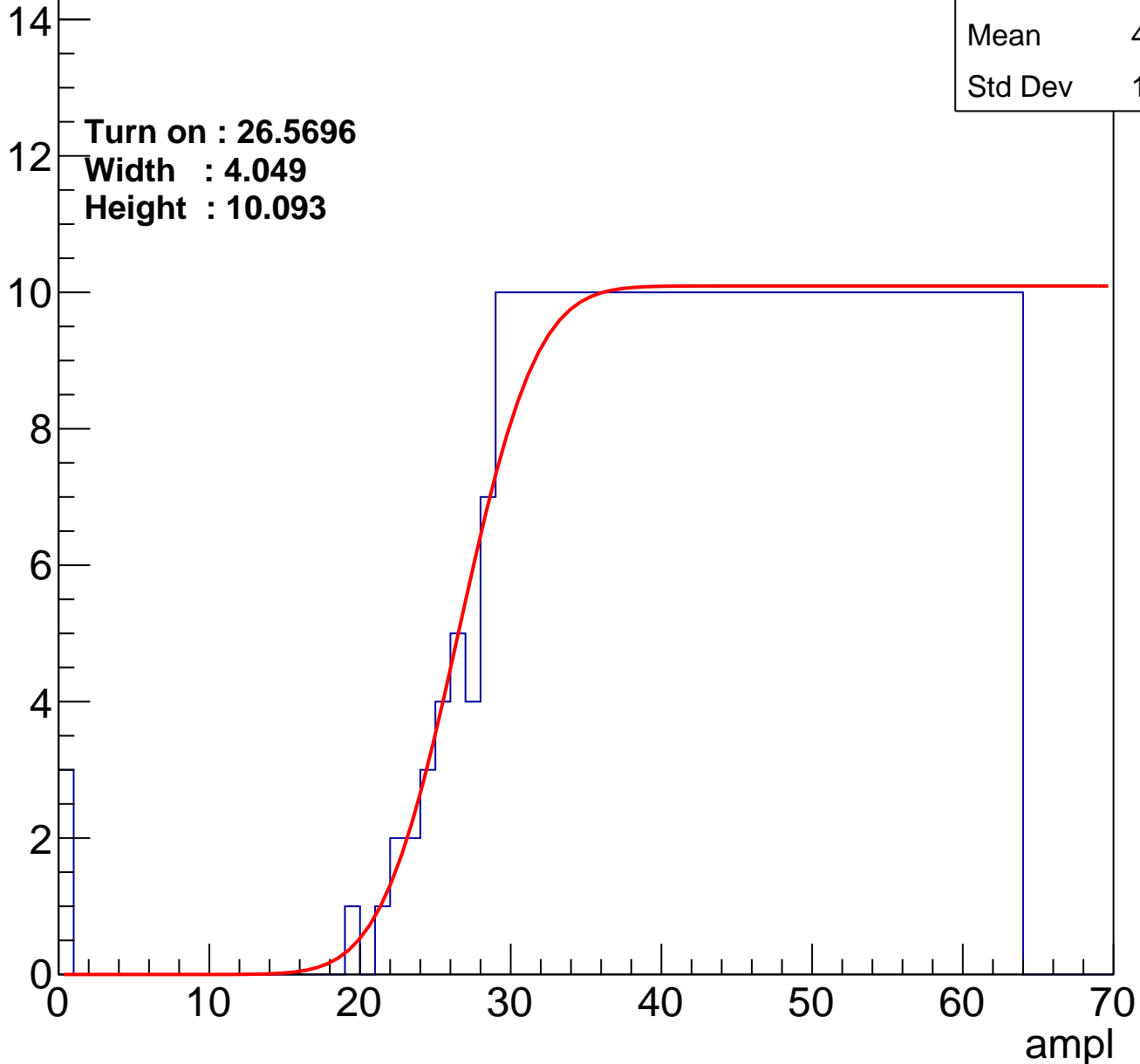
Entries	382
Mean	44.07
Std Dev	11.79

Turn on : 26.5696

Width : 4.049

Height : 10.093

Entry



B1L000S, U15-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.09
Std Dev	12.22

Turn on : 23.9950

Width : 2.431

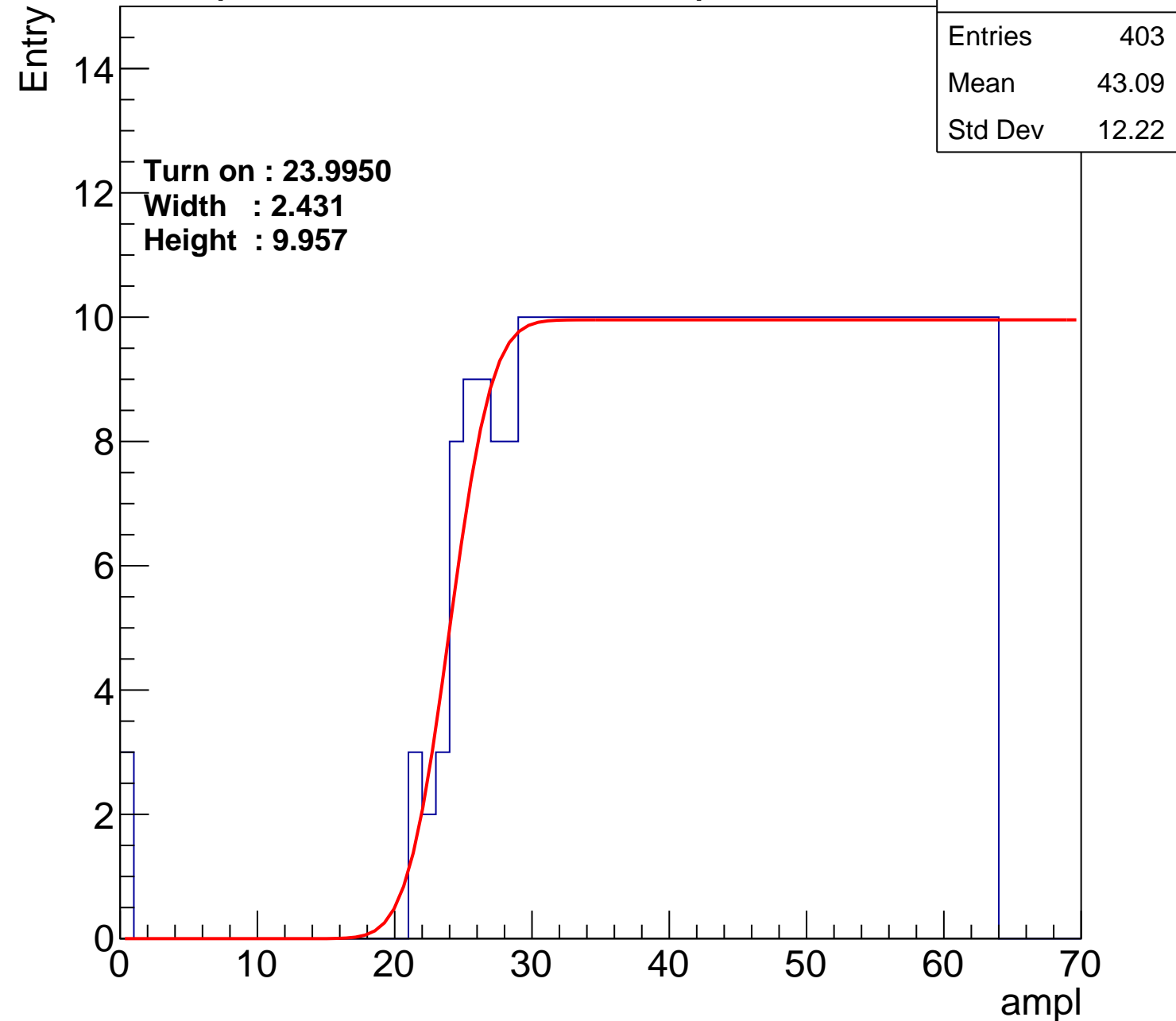
Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U15-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.9
Std Dev	11.85

Turn on : 25.8700

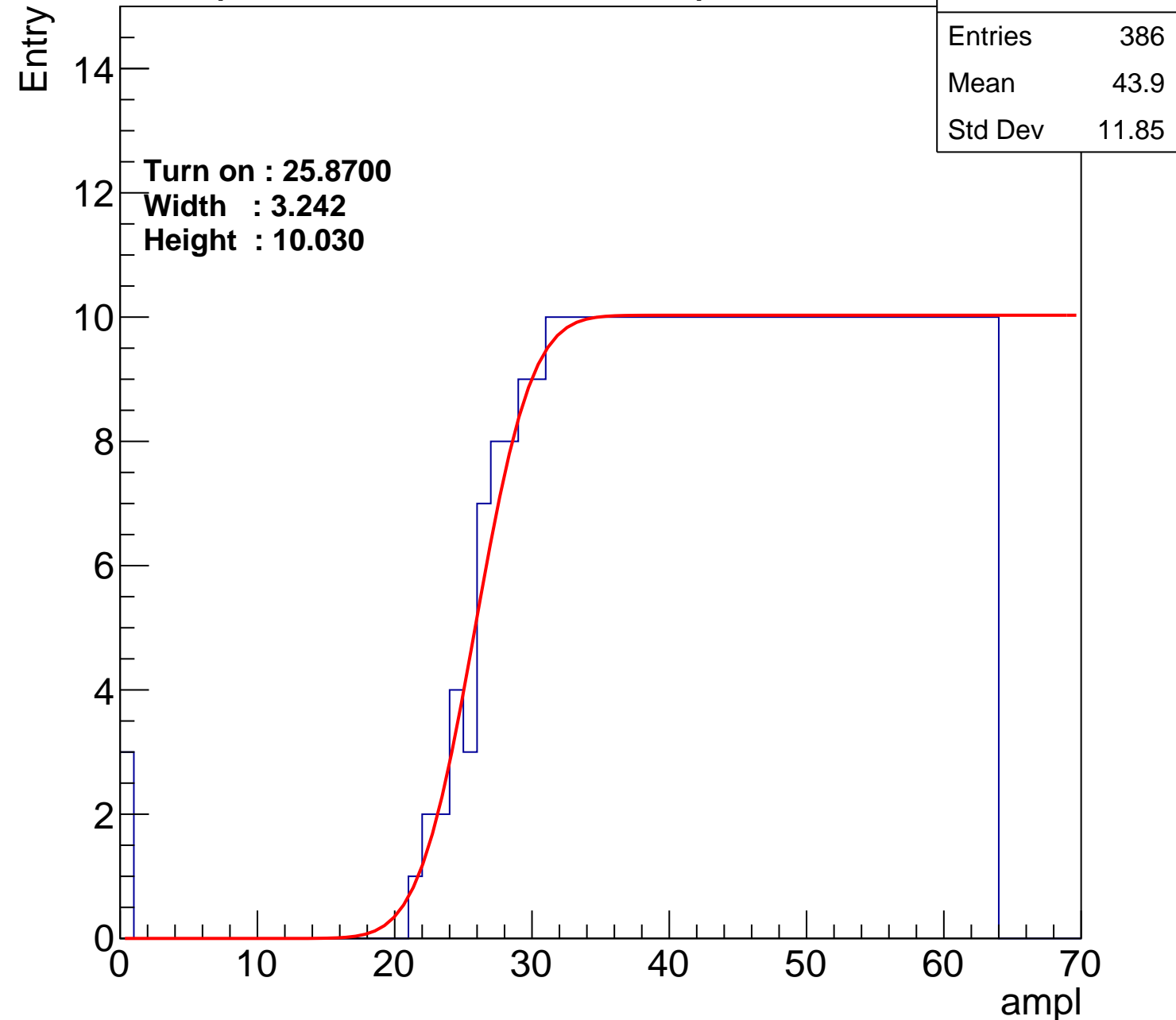
Width : 3.242

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.43
Std Dev	11.4

Turn on : 26.7578

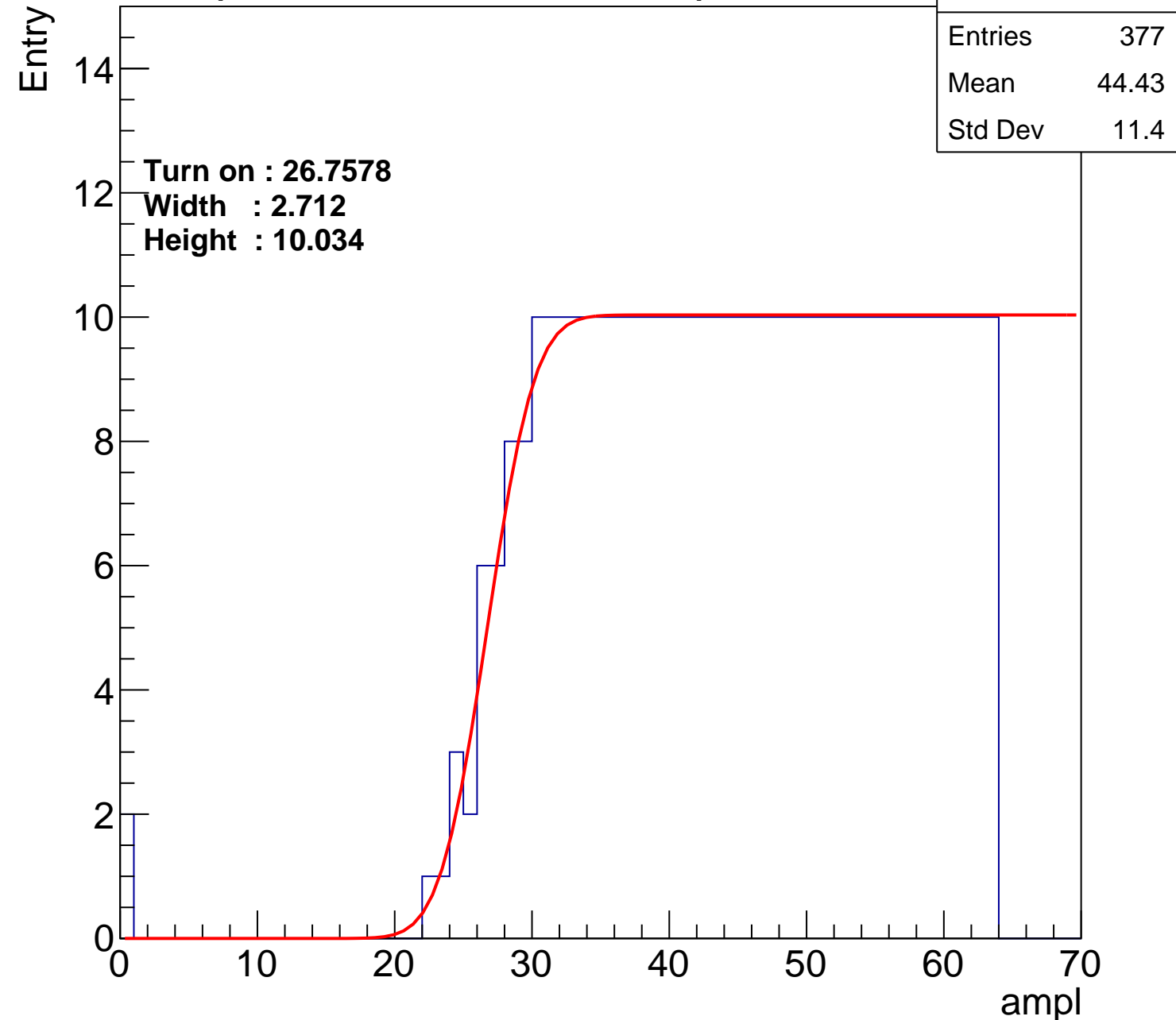
Width : 2.712

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch24

calib_packv5_042523_0143.root, FC#3, port B3

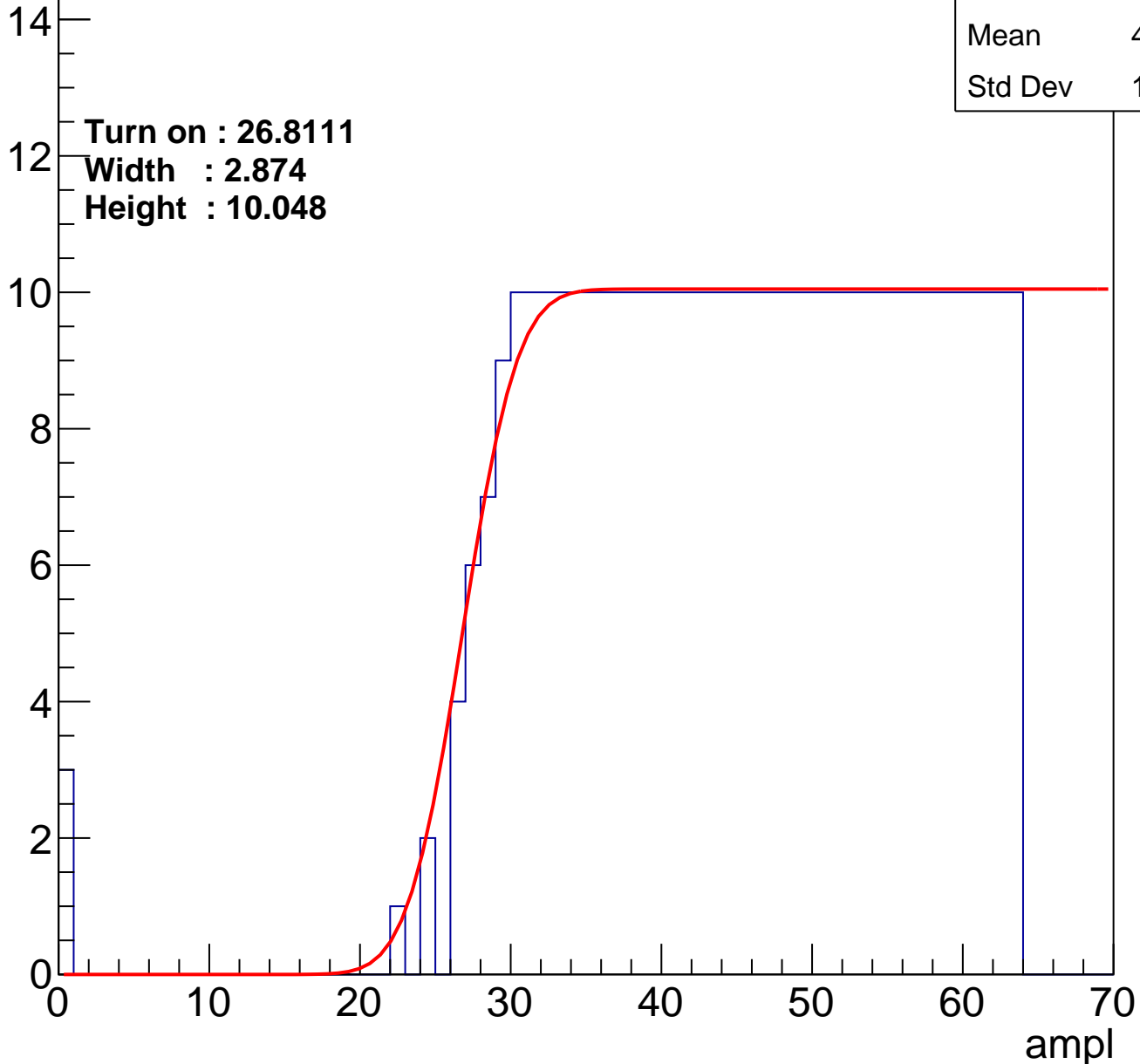
Entries	372
Mean	44.63
Std Dev	11.43

Turn on : 26.8111

Width : 2.874

Height : 10.048

Entry



B1L000S, U15-ch25

calib_packv5_042523_0143.root, FC#3, port B3

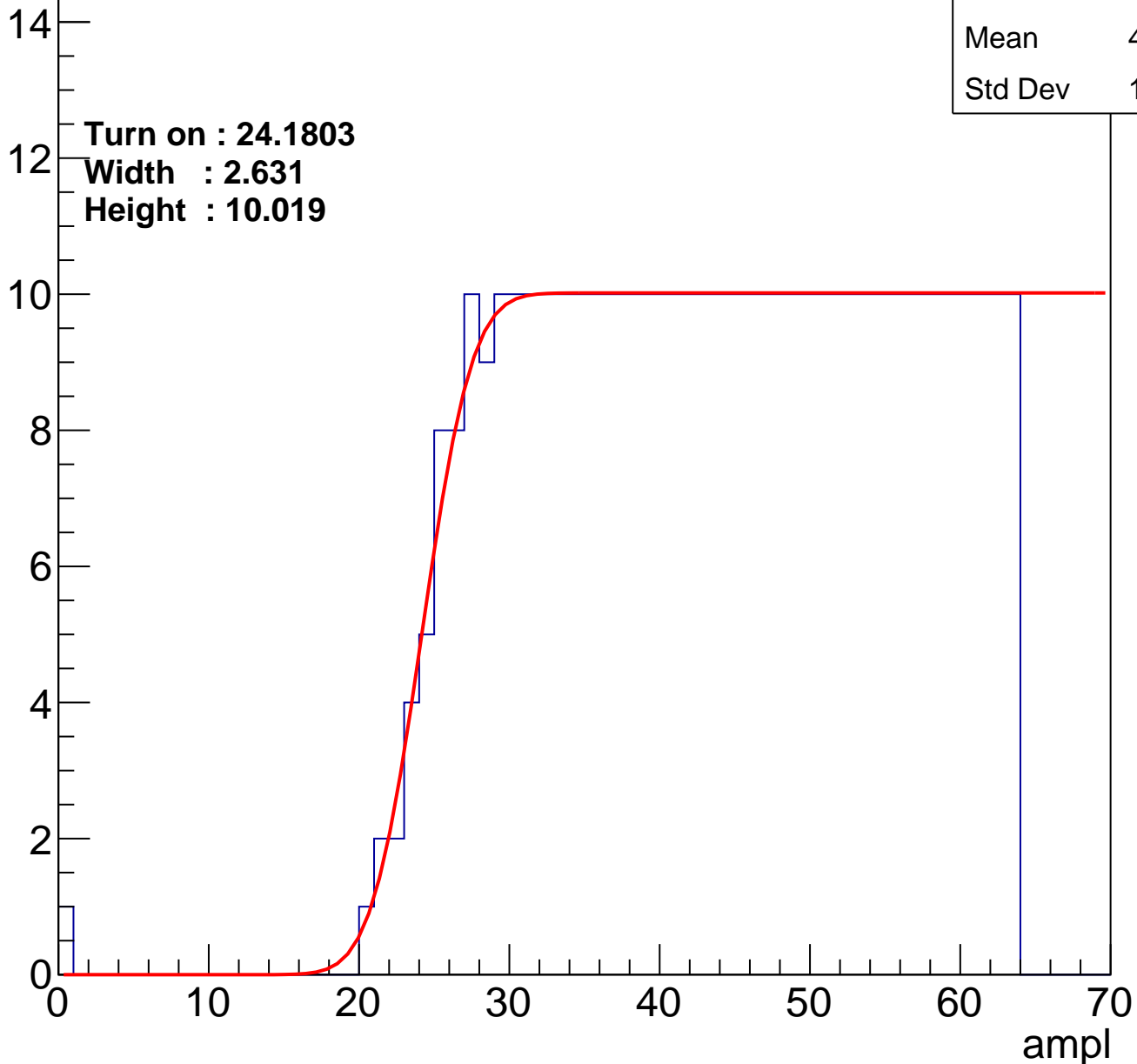
Entries	400
Mean	43.37
Std Dev	11.82

Turn on : 24.1803

Width : 2.631

Height : 10.019

Entry



B1L000S, U15-ch26

calib_packv5_042523_0143.root, FC#3, port B3

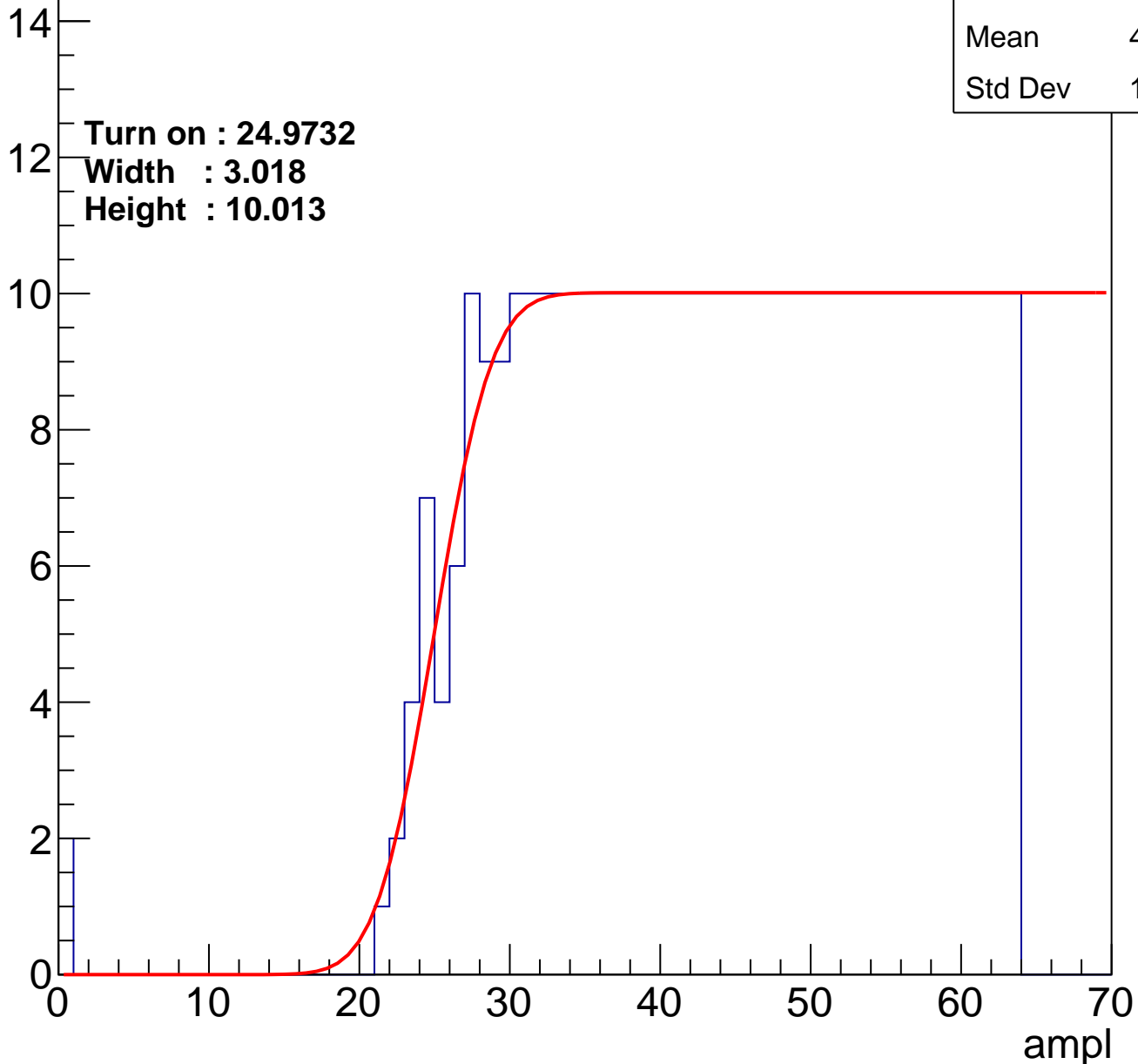
Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 24.9732

Width : 3.018

Height : 10.013

Entry



B1L000S, U15-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 25.6049

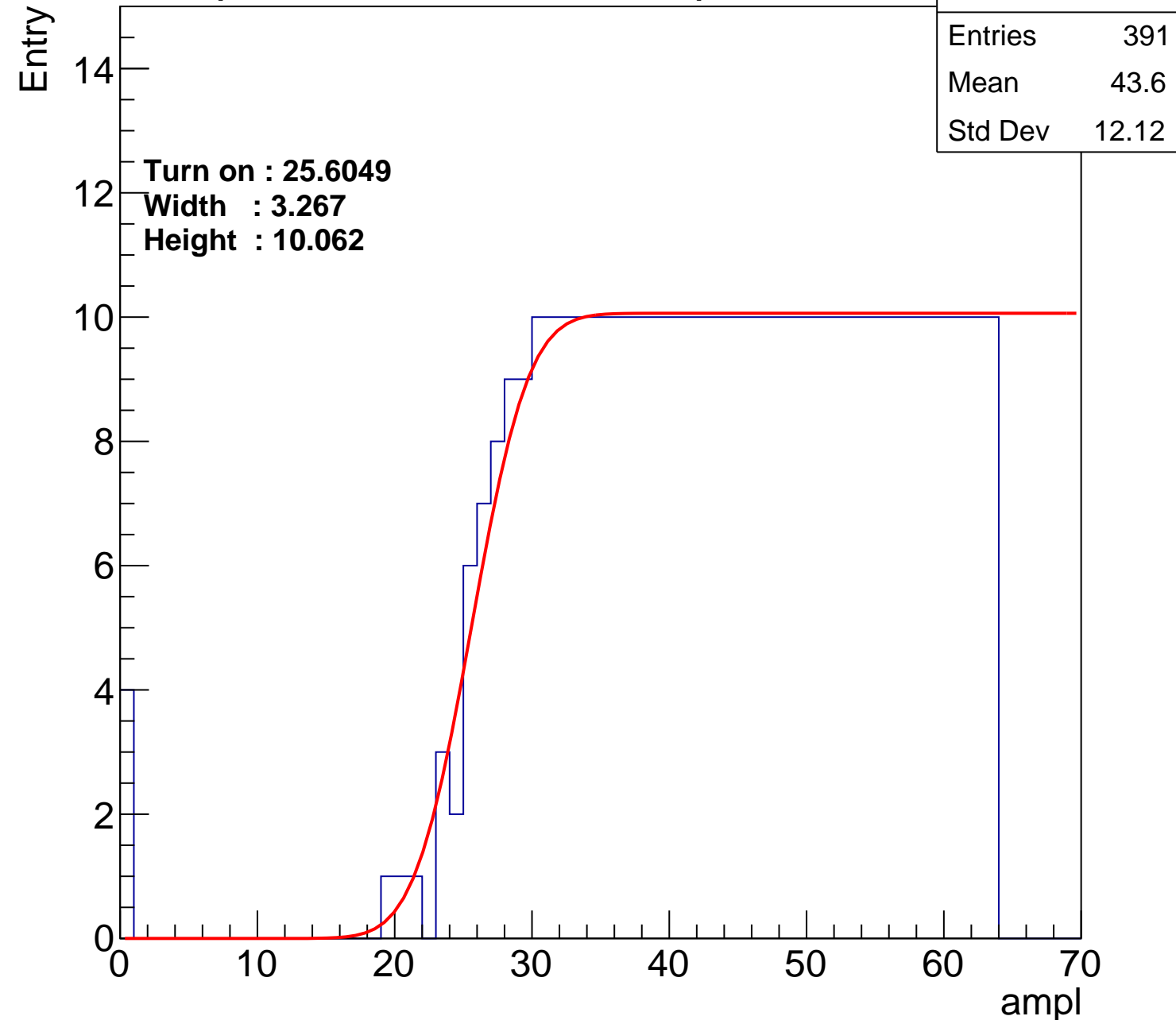
Width : 3.267

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch28

calib_packv5_042523_0143.root, FC#3, port B3

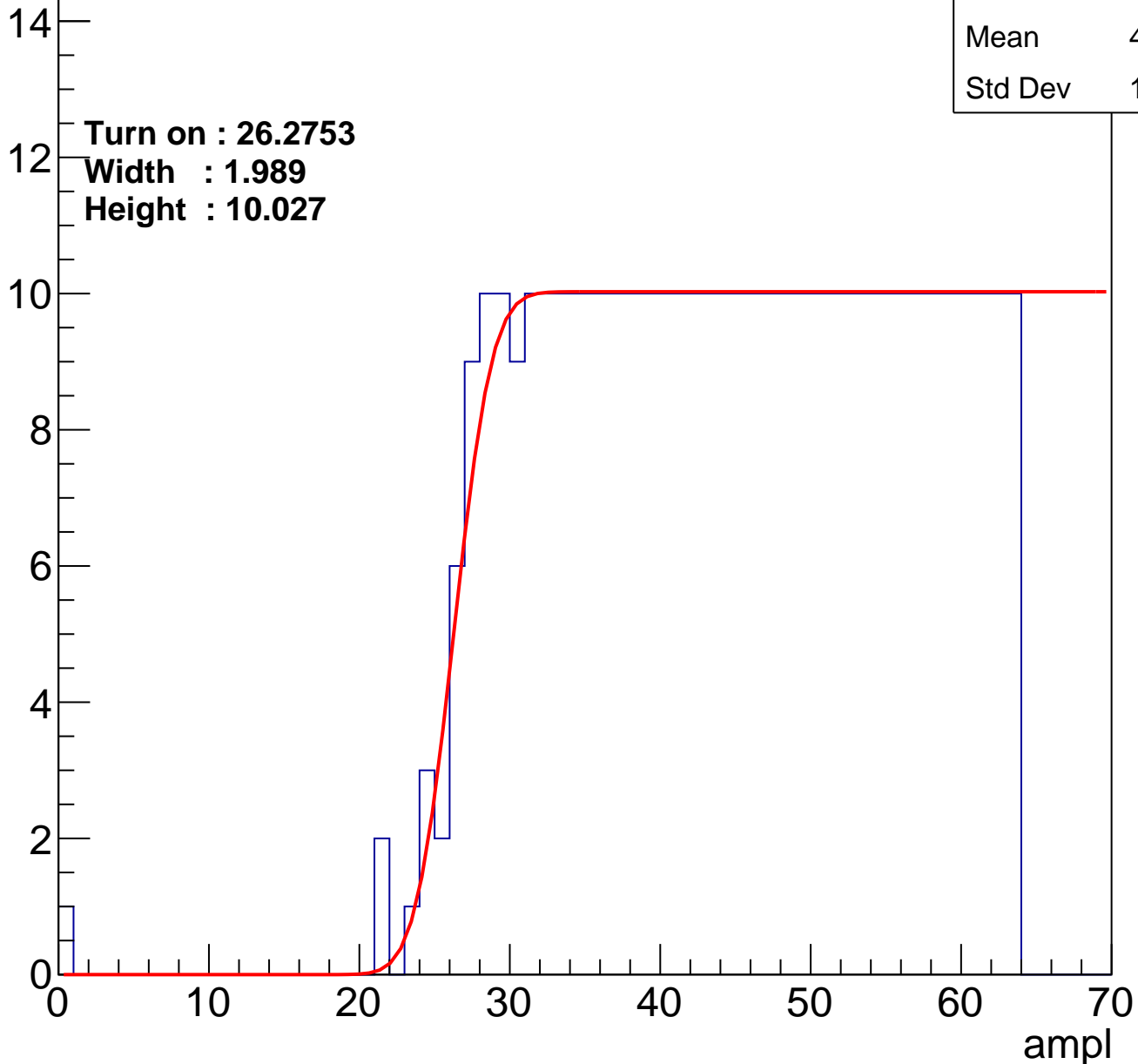
Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 26.2753

Width : 1.989

Height : 10.027

Entry



B1L000S, U15-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.47
Std Dev	12.16

Turn on : 25.1763

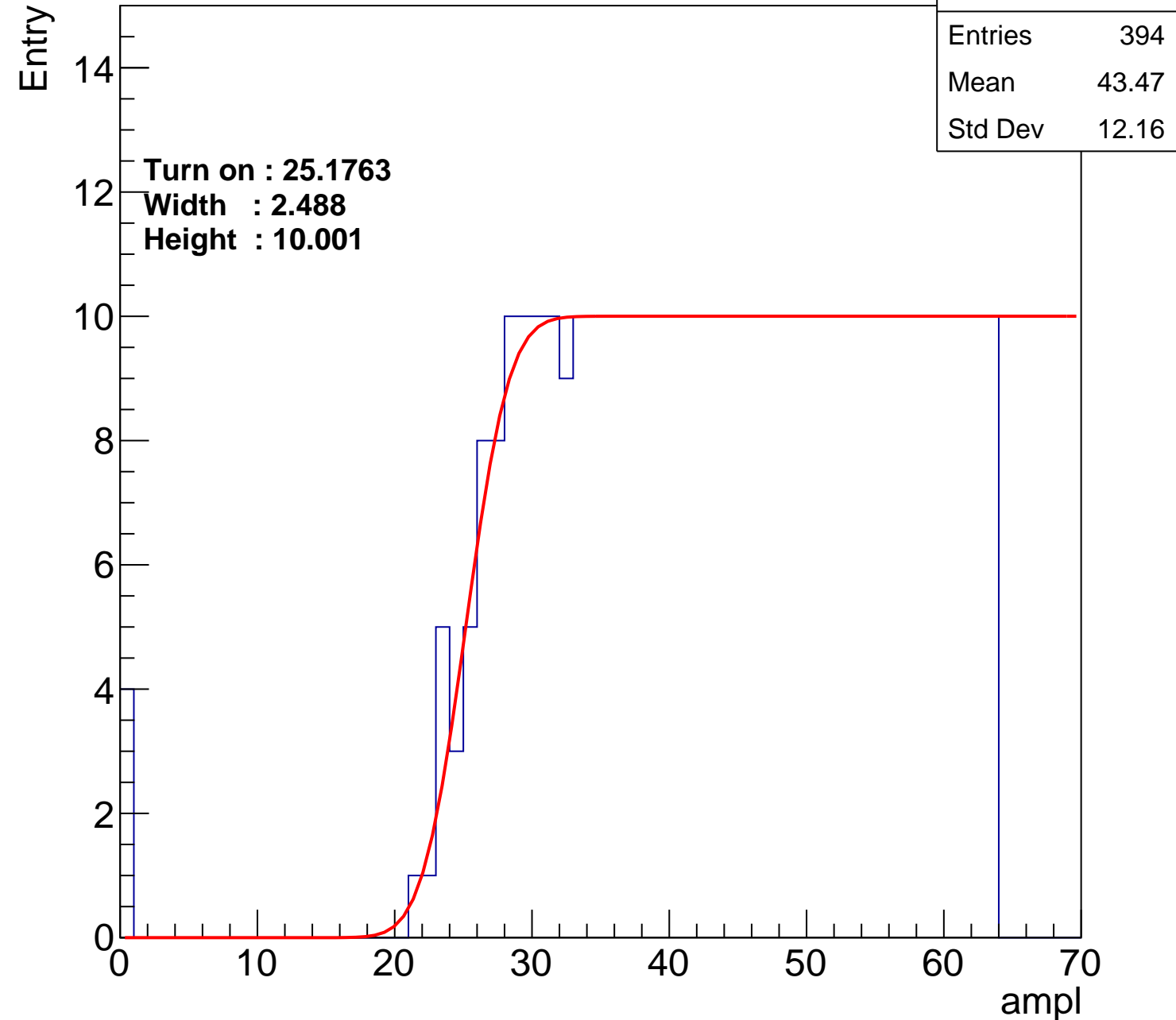
Width : 2.488

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.99
Std Dev	11.62

Turn on : 25.7518

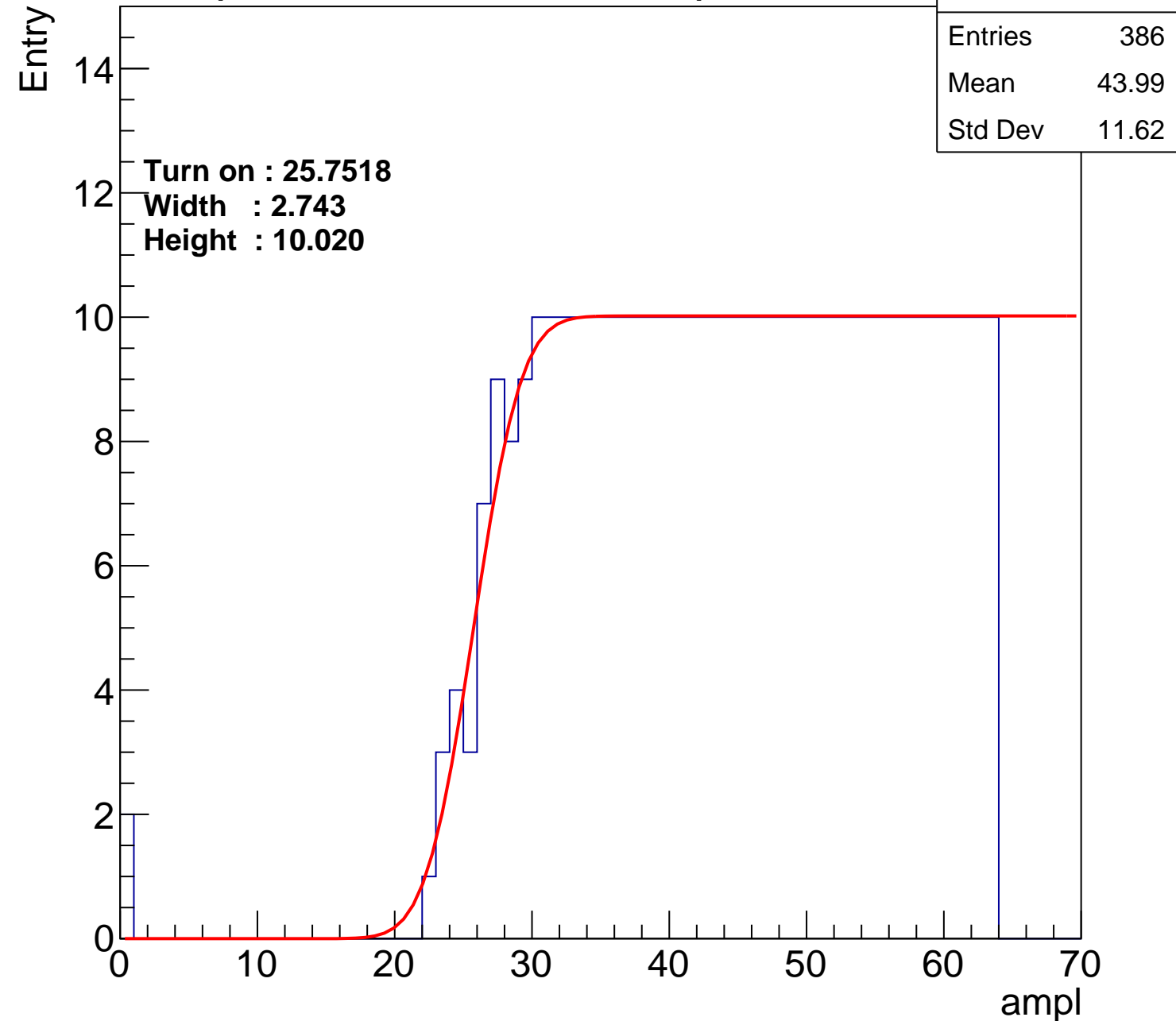
Width : 2.743

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch31

calib_packv5_042523_0143.root, FC#3, port B3

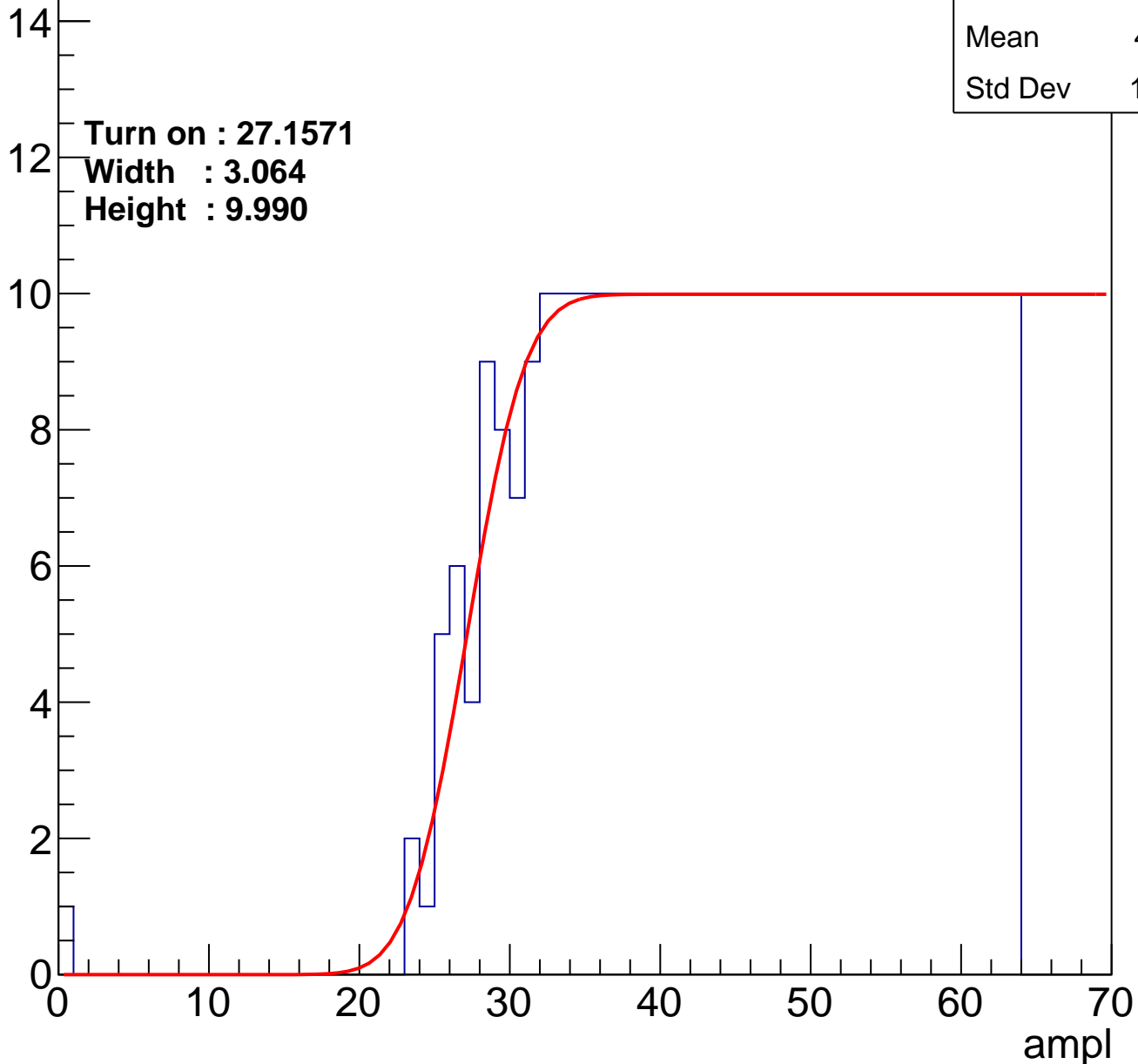
Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.1571

Width : 3.064

Height : 9.990

Entry



B1L000S, U15-ch32

calib_packv5_042523_0143.root, FC#3, port B3

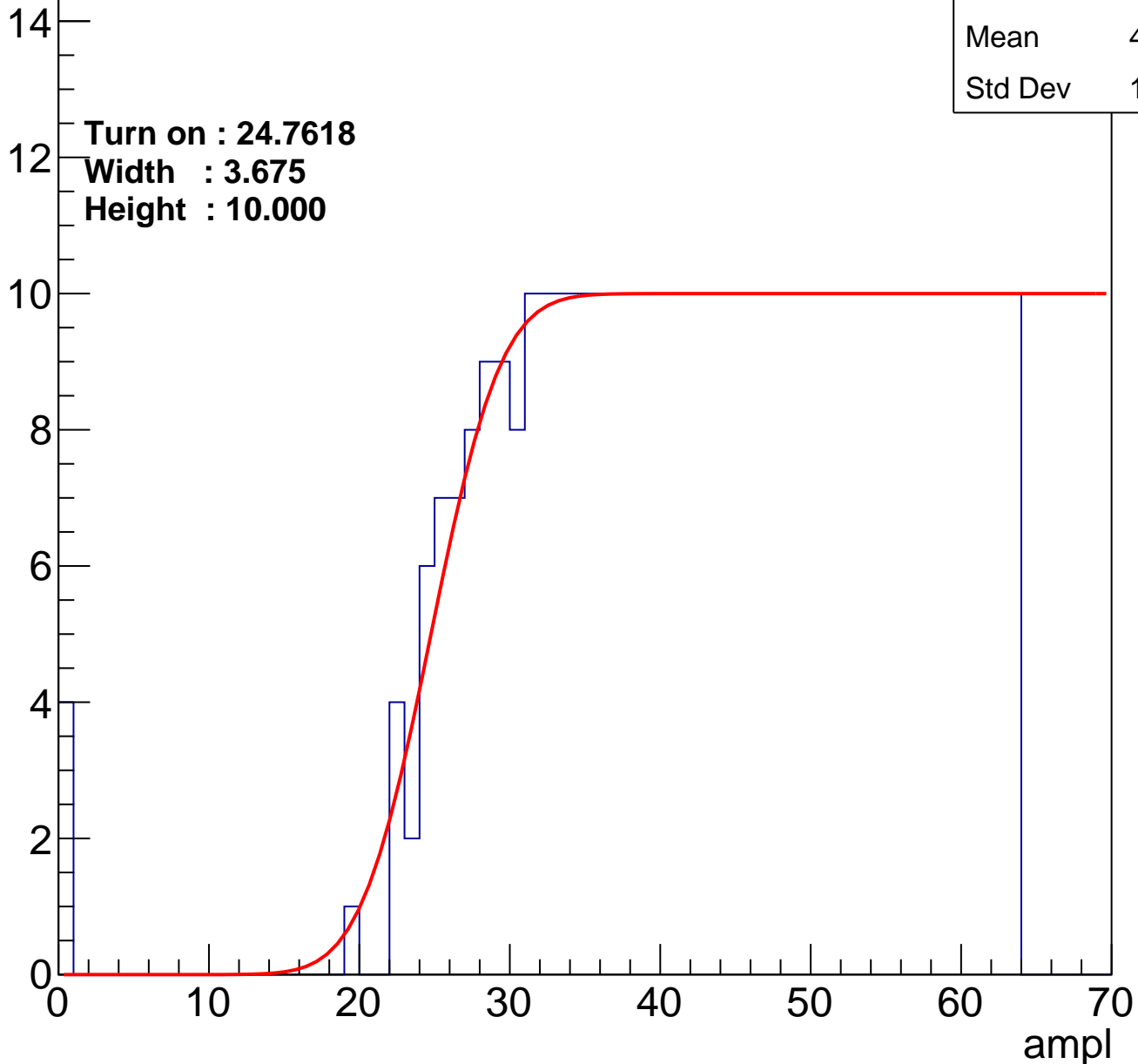
Entries	395
Mean	43.37
Std Dev	12.26

Turn on : 24.7618

Width : 3.675

Height : 10.000

Entry



B1L000S, U15-ch33

calib_packv5_042523_0143.root, FC#3, port B3

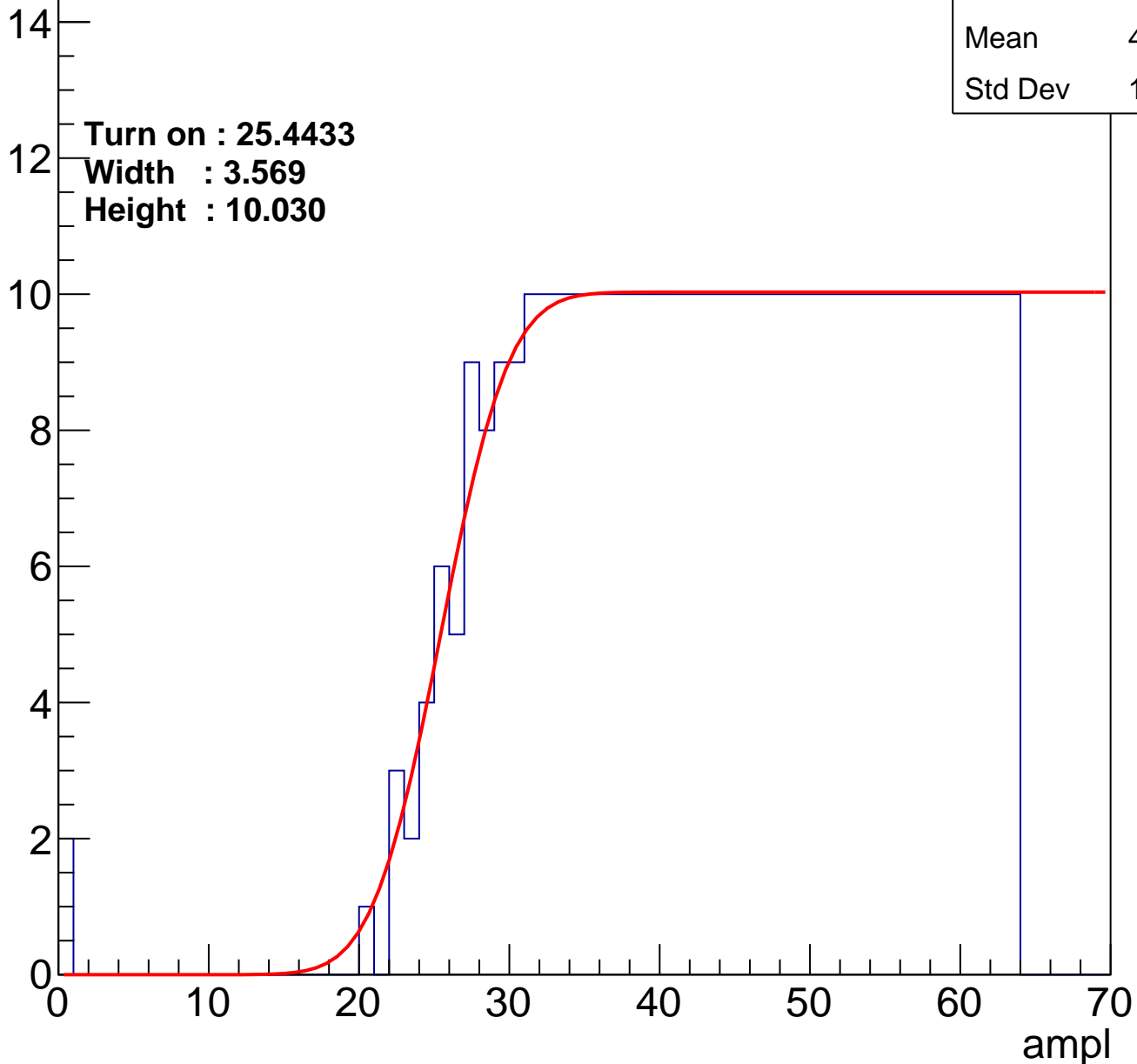
Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 25.4433

Width : 3.569

Height : 10.030

Entry



B1L000S, U15-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.67
Std Dev	11.1

Turn on : 27.1941

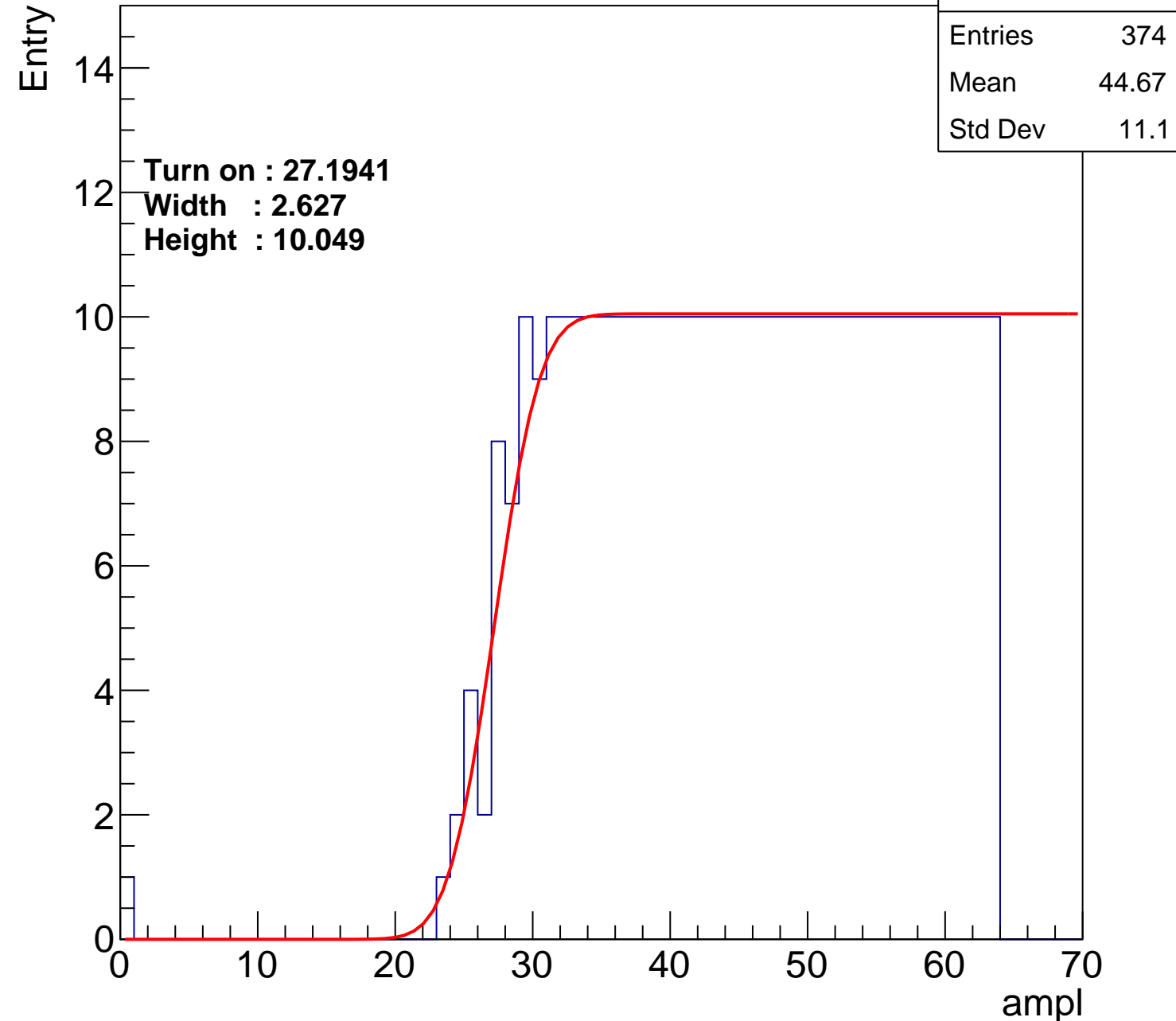
Width : 2.627

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.77
Std Dev	11.03

Turn on : 26.5310

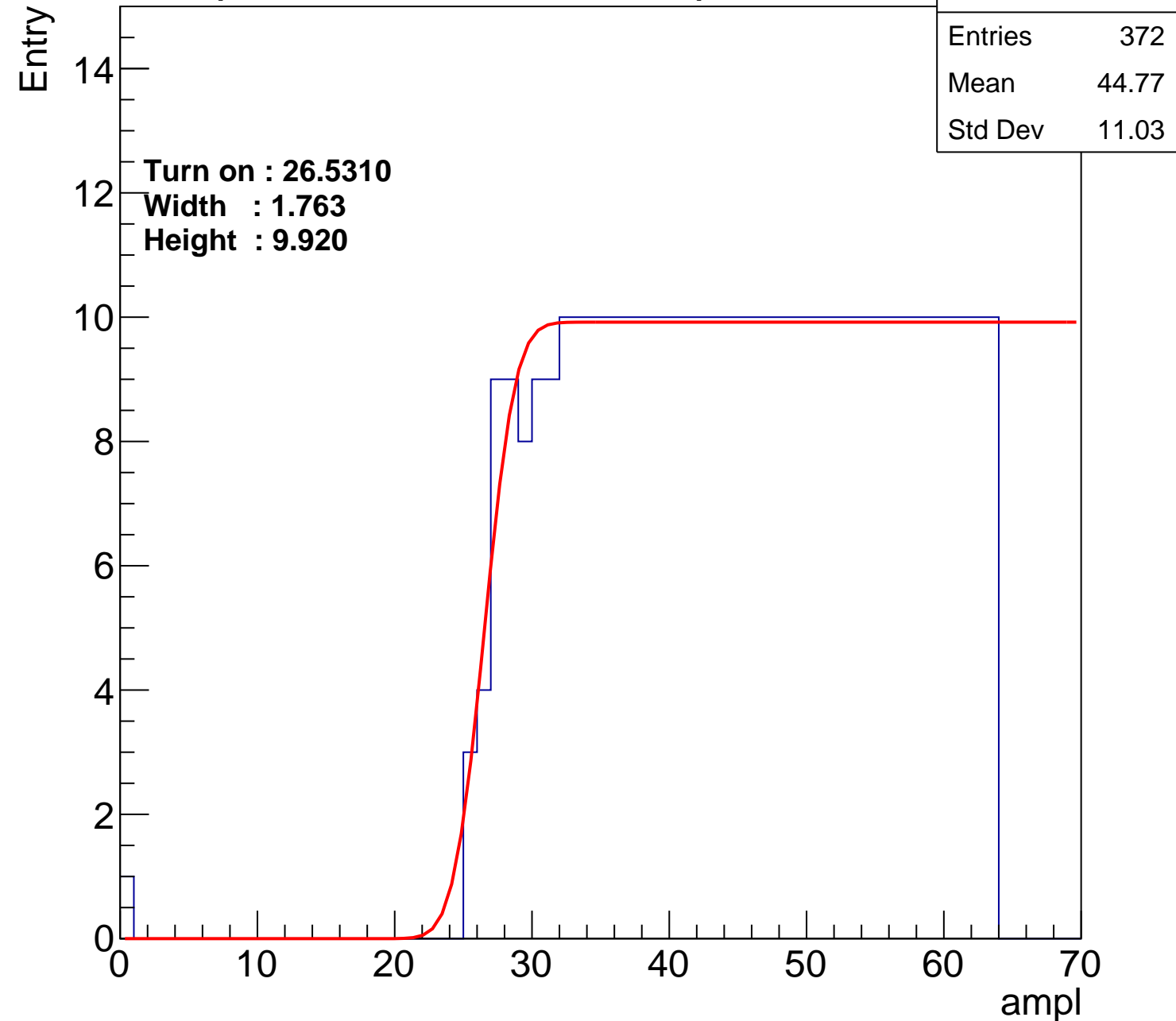
Width : 1.763

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch36

calib_packv5_042523_0143.root, FC#3, port B3

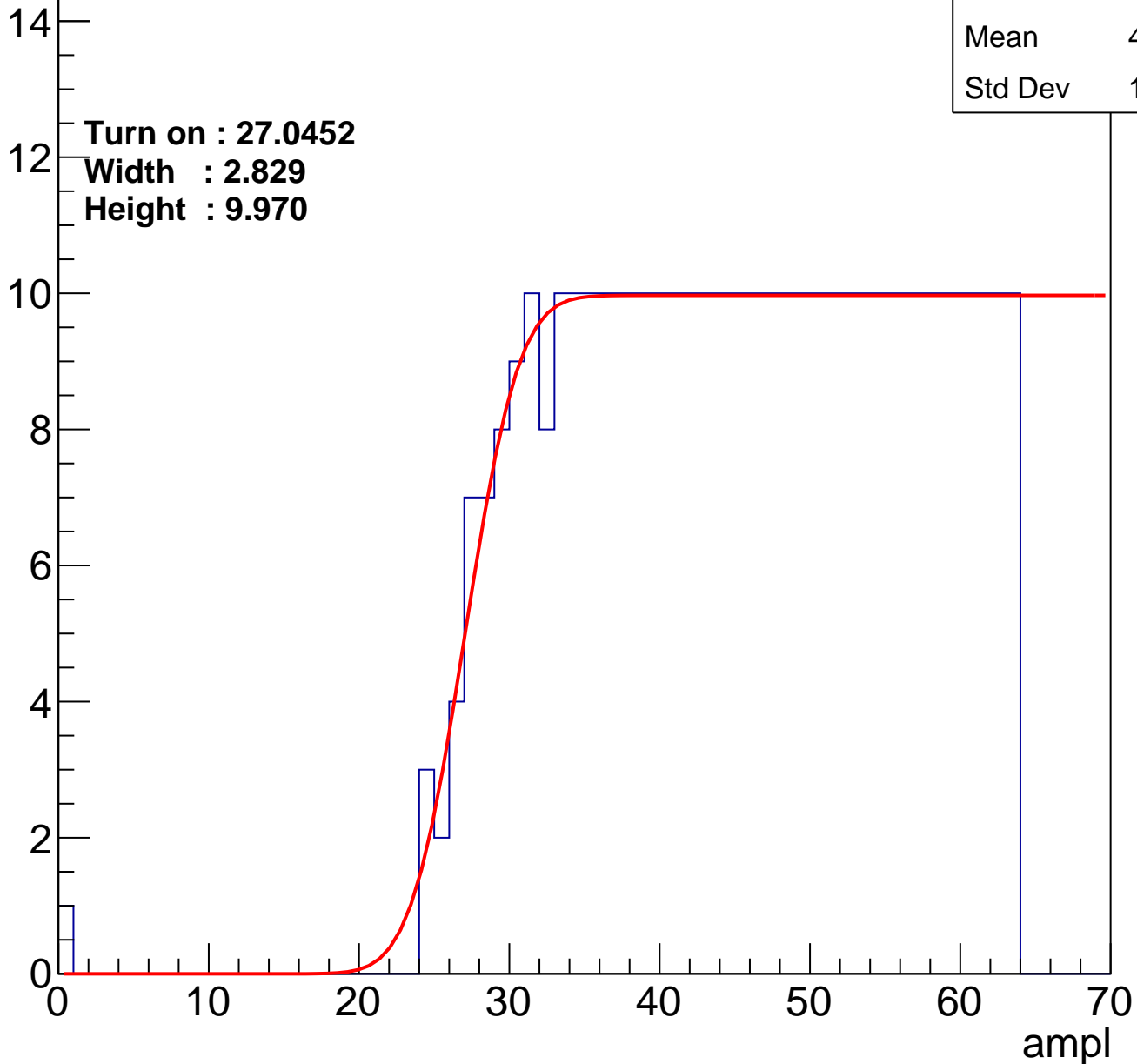
Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.0452

Width : 2.829

Height : 9.970

Entry



B1L000S, U15-ch37

calib_packv5_042523_0143.root, FC#3, port B3

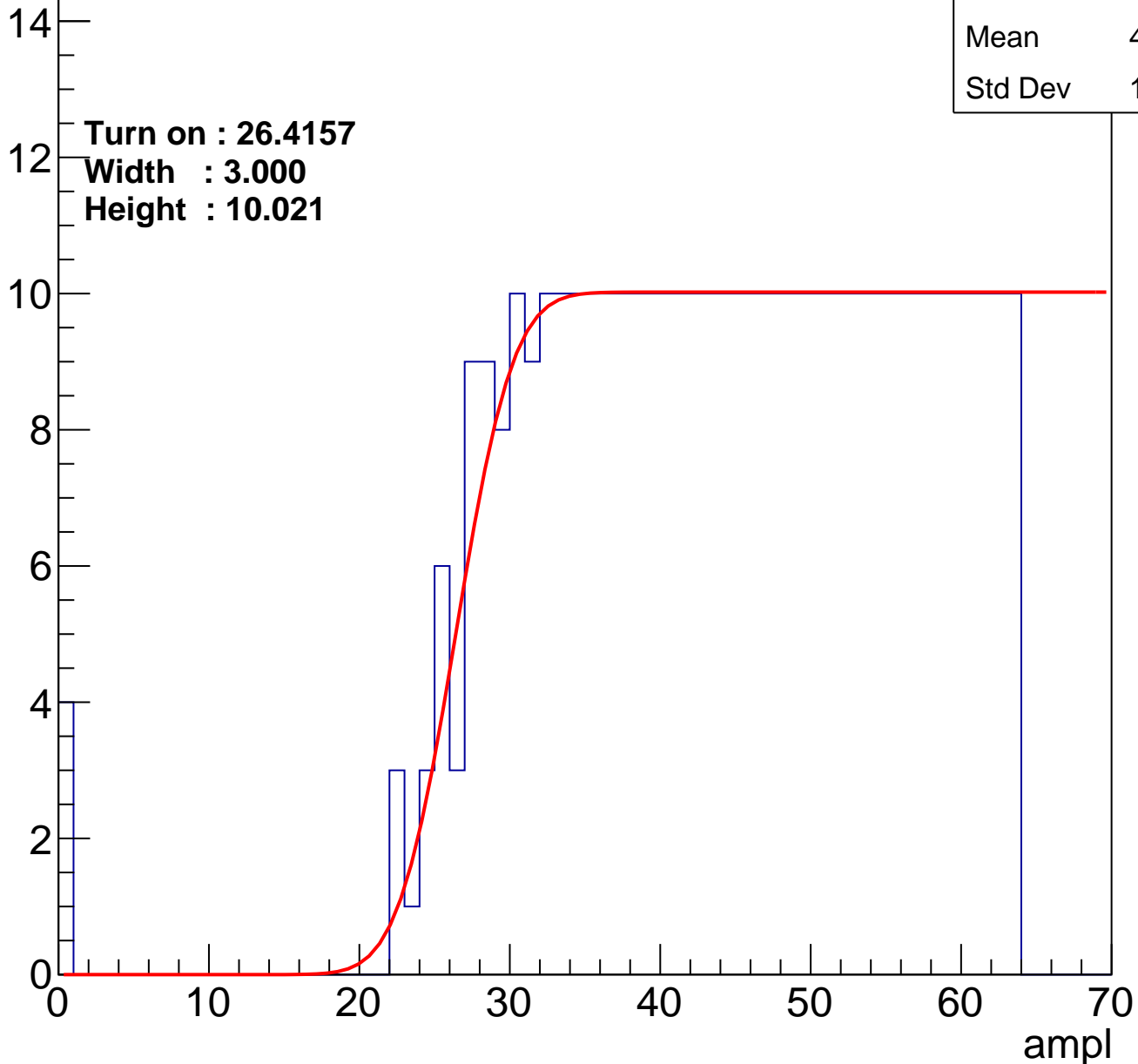
Entries	385
Mean	43.88
Std Dev	11.99

Turn on : 26.4157

Width : 3.000

Height : 10.021

Entry



B1L000S, U15-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	43.03
Std Dev	12.16

Turn on : 23.6785

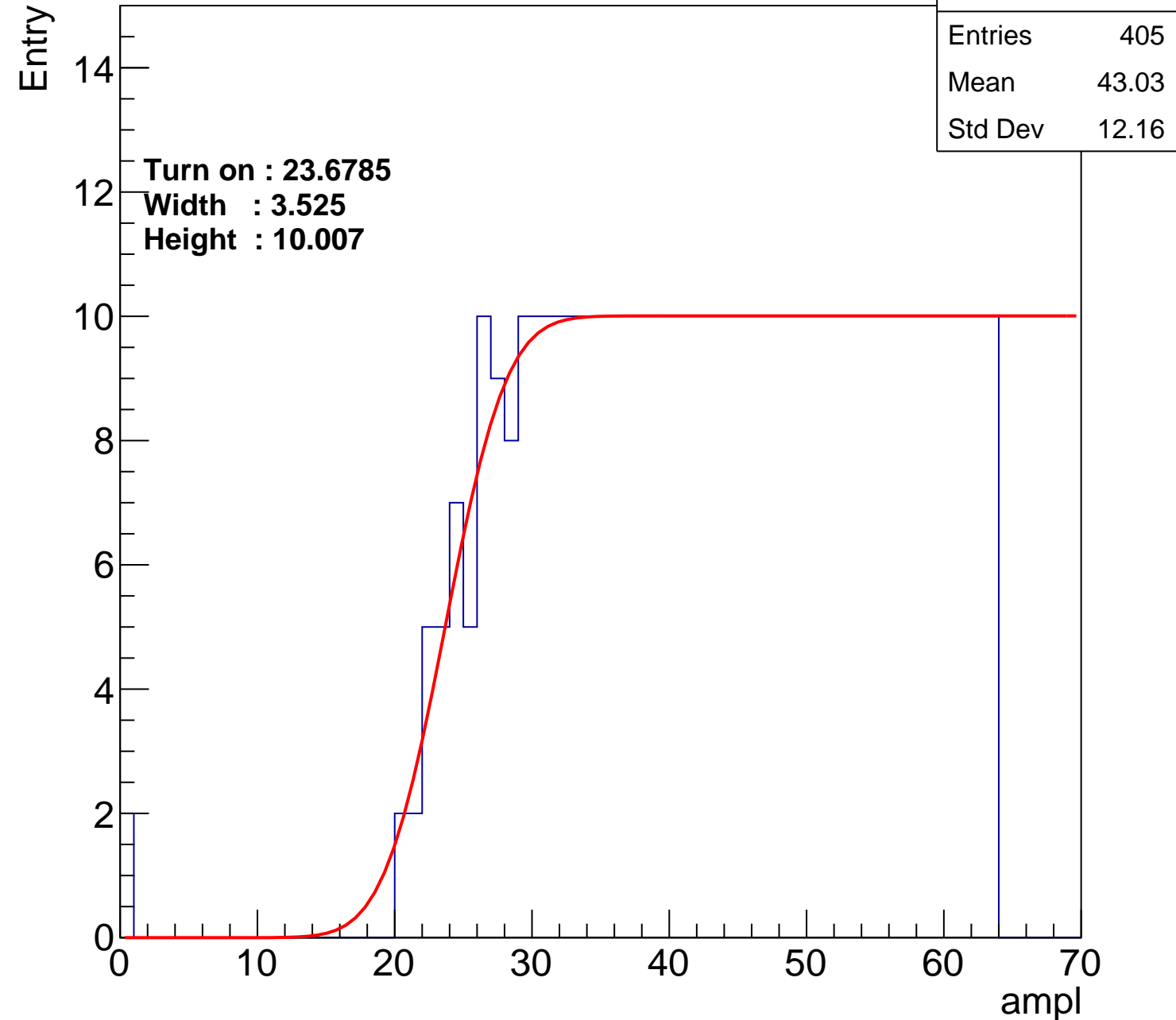
Width : 3.525

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch39

calib_packv5_042523_0143.root, FC#3, port B3

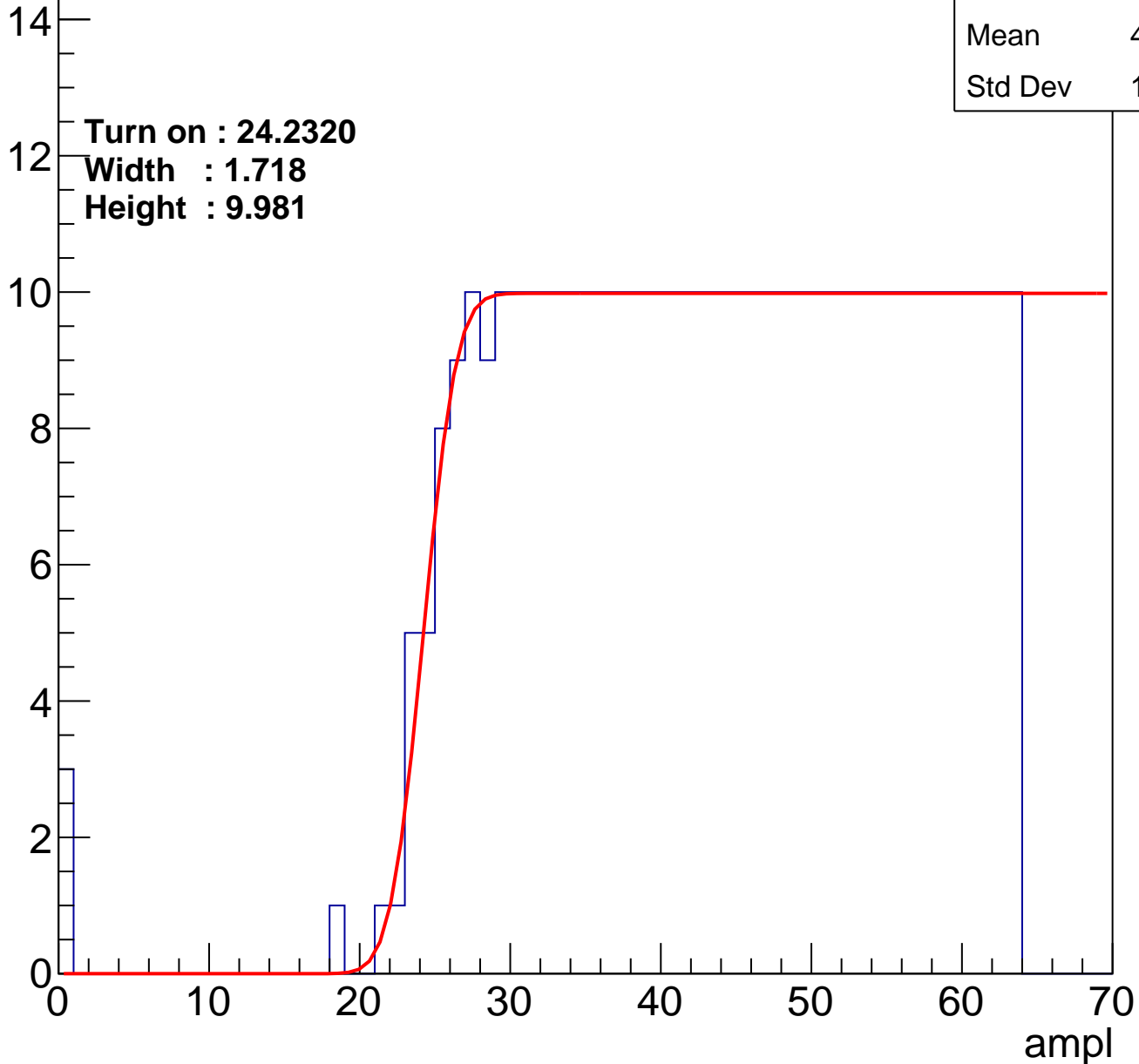
Entries	402
Mean	43.16
Std Dev	12.17

Turn on : 24.2320

Width : 1.718

Height : 9.981

Entry



B1L000S, U15-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.87
Std Dev	11.71

Turn on : 25.4913

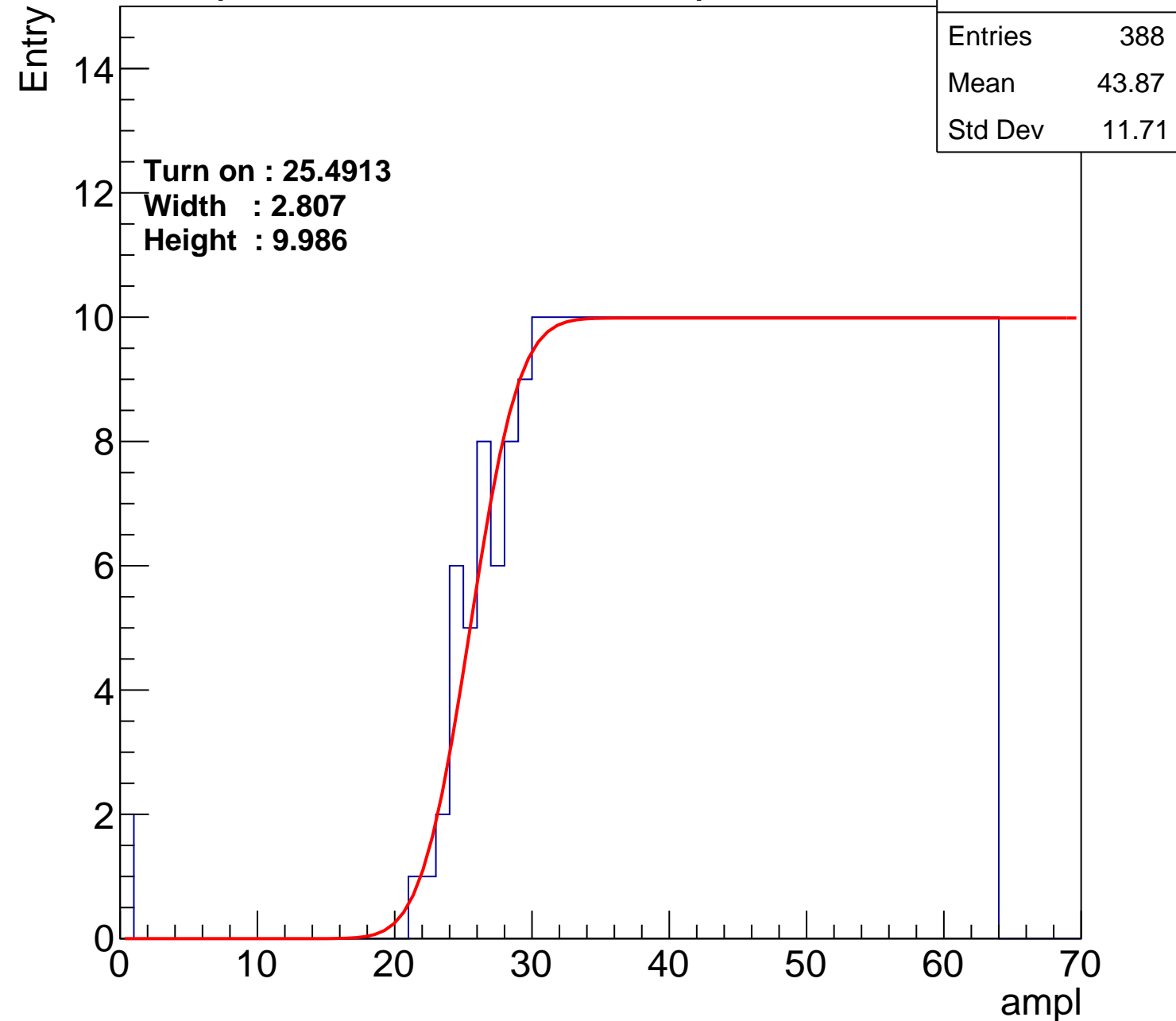
Width : 2.807

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.3161

Width : 2.799

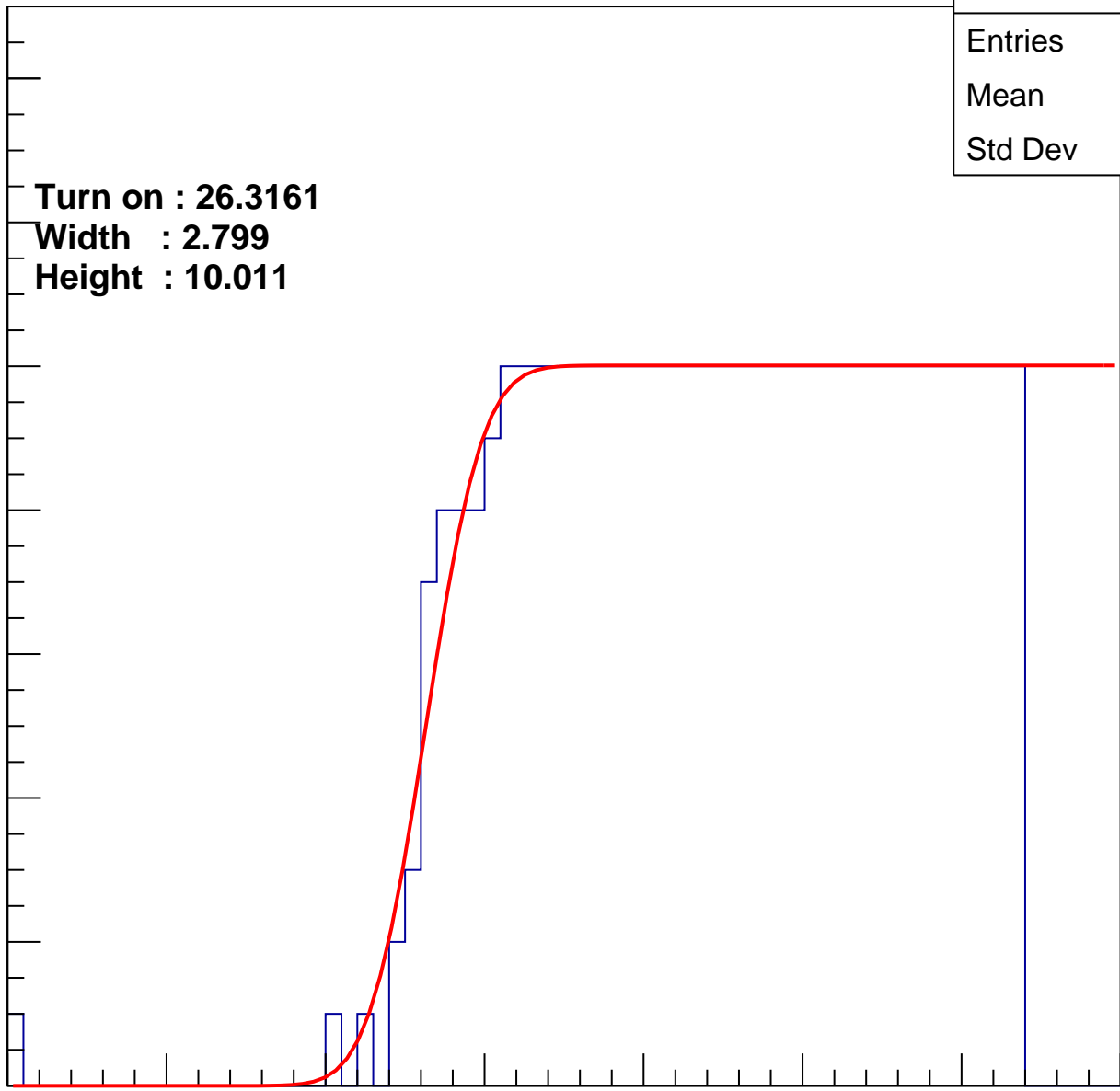
Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U15-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
---------	-----

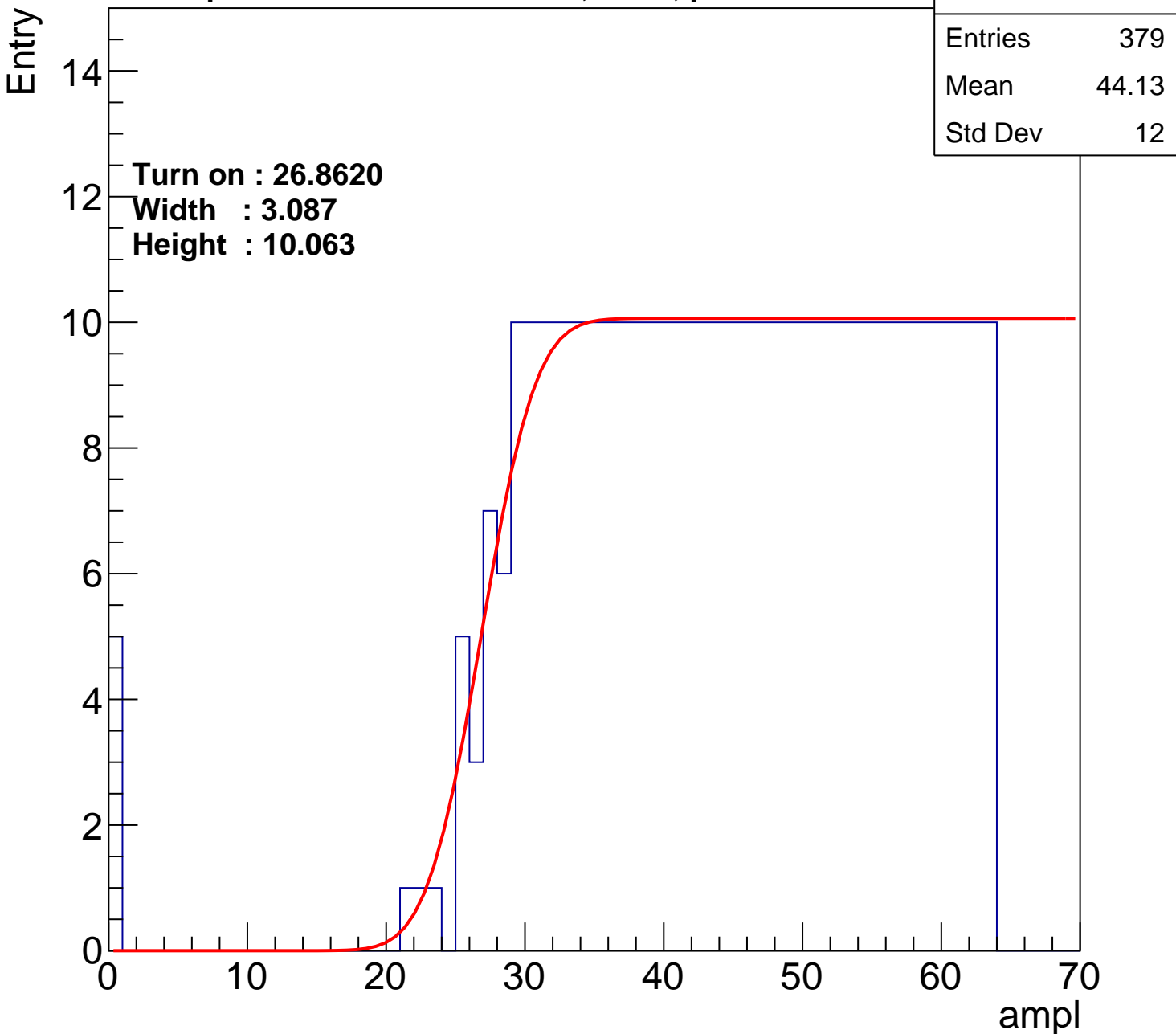
Mean	44.13
------	-------

Std Dev	12
---------	----

Turn on : 26.8620

Width : 3.087

Height : 10.063



B1L000S, U15-ch43

calib_packv5_042523_0143.root, FC#3, port B3

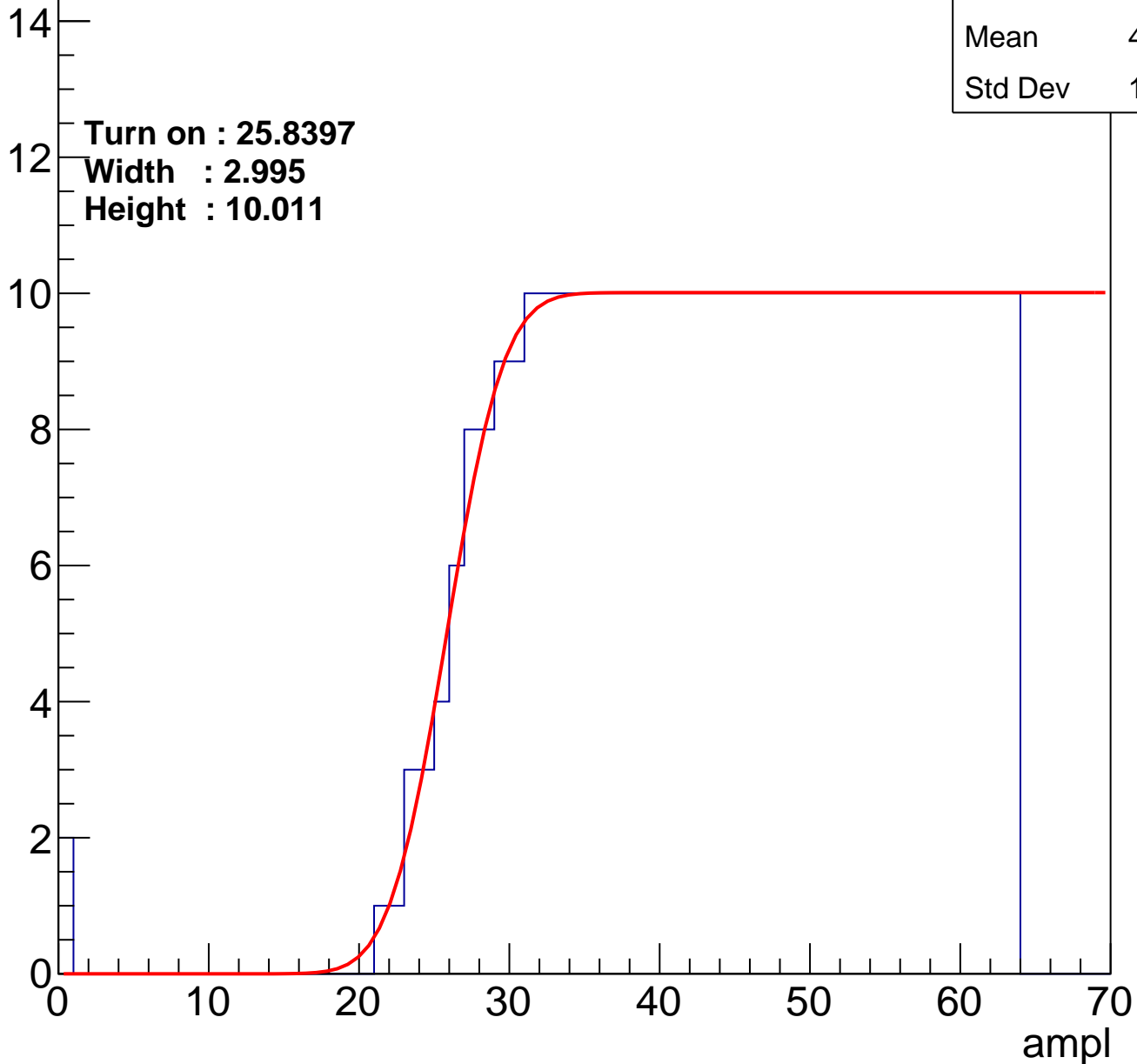
Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 25.8397

Width : 2.995

Height : 10.011

Entry



B1L000S, U15-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.9
Std Dev	11

Turn on : 27.5121

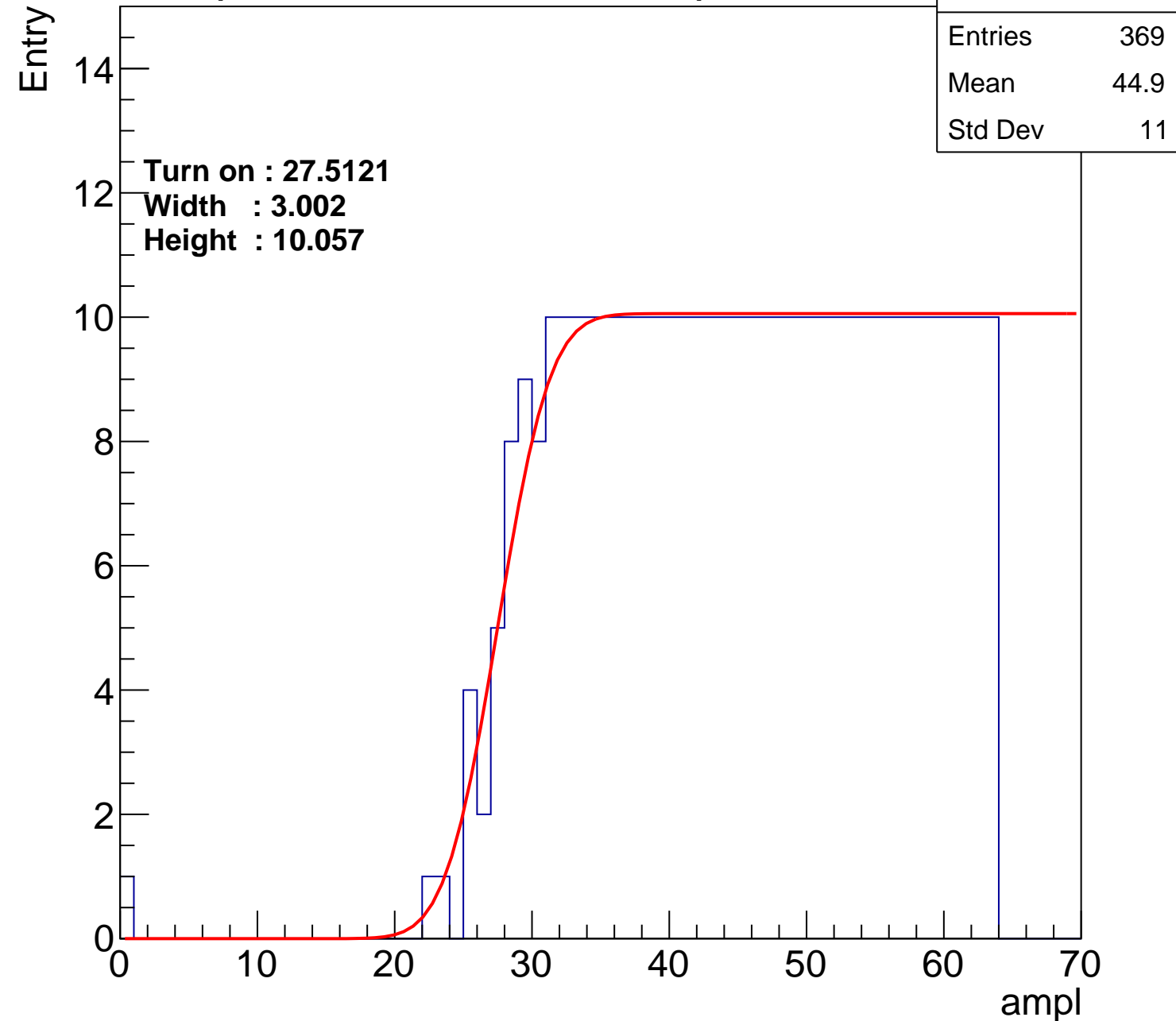
Width : 3.002

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch45

calib_packv5_042523_0143.root, FC#3, port B3

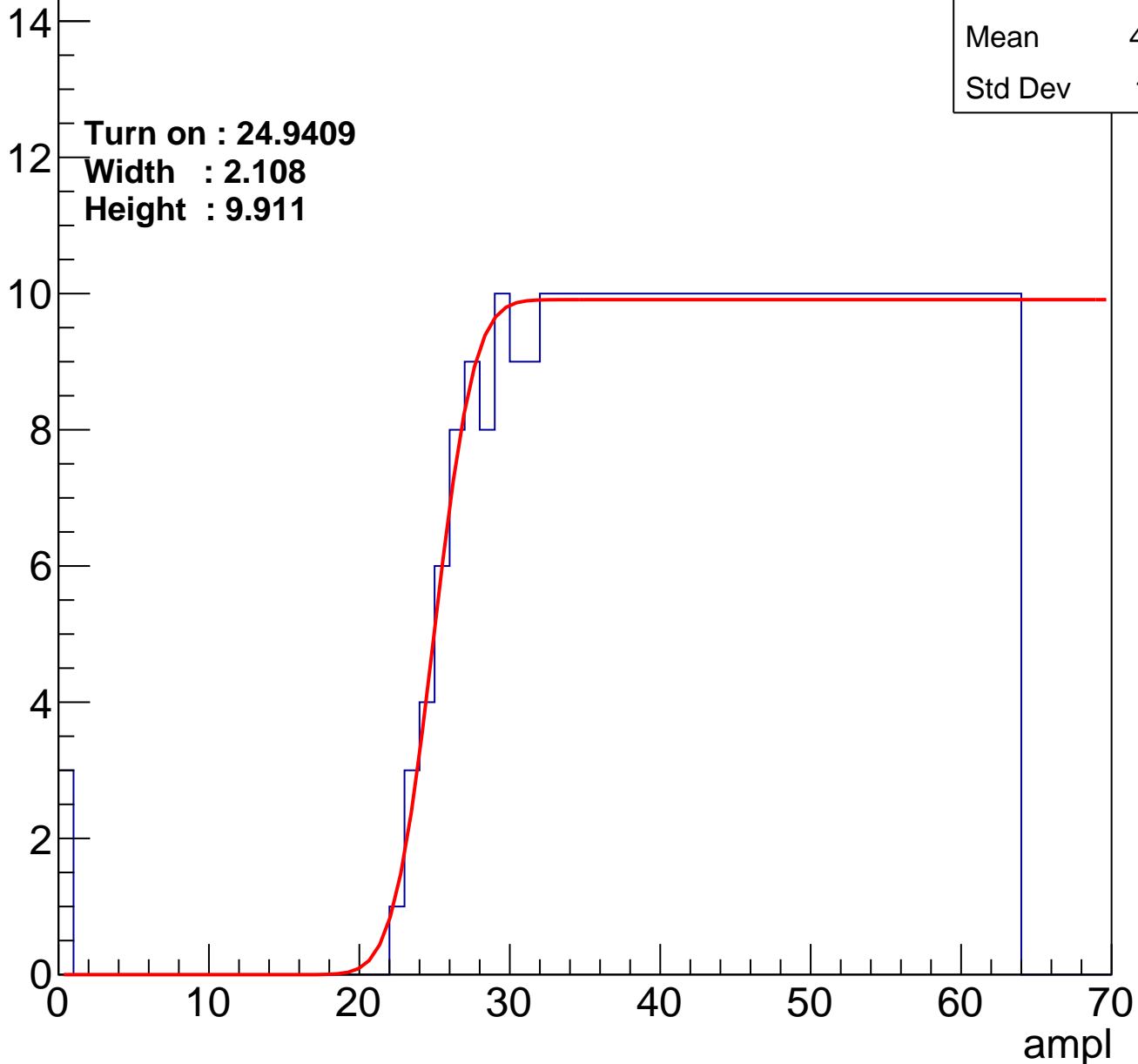
Entries	390
Mean	43.72
Std Dev	11.91

Turn on : 24.9409

Width : 2.108

Height : 9.911

Entry



B1L000S, U15-ch46

calib_packv5_042523_0143.root, FC#3, port B3

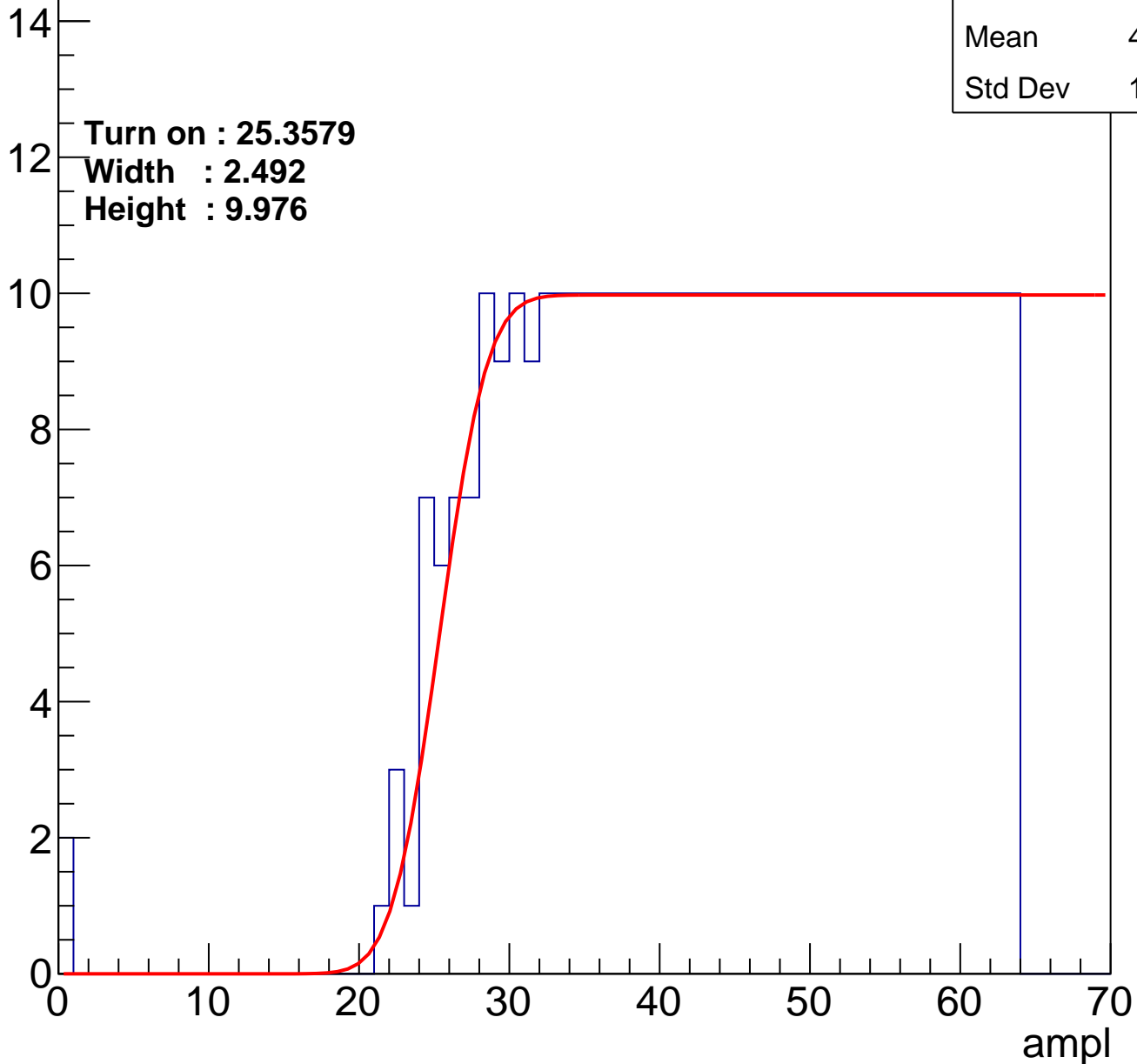
Entries	392
Mean	43.67
Std Dev	11.82

Turn on : 25.3579

Width : 2.492

Height : 9.976

Entry



B1L000S, U15-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.18
Std Dev	11.42

Turn on : 26.3895

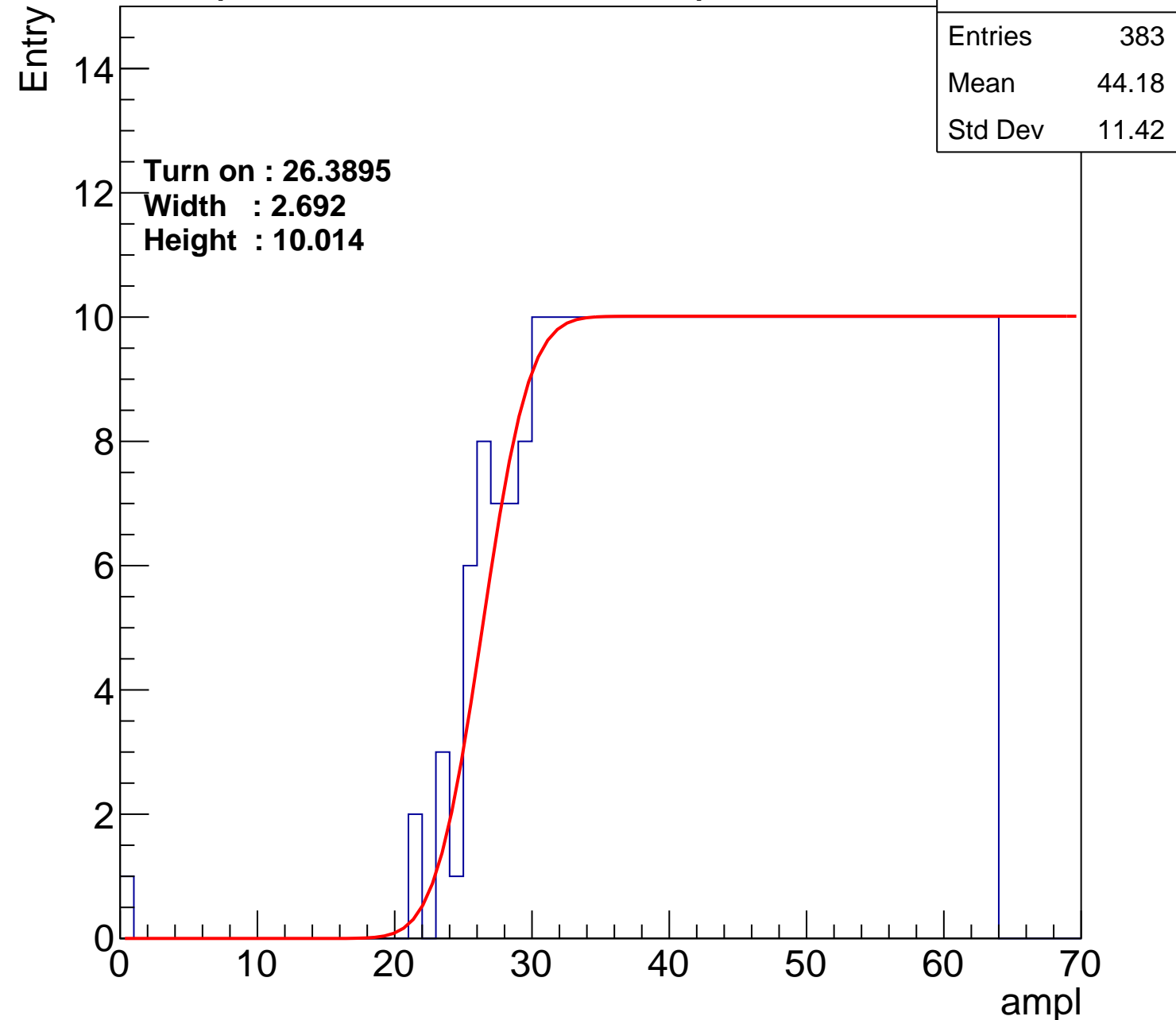
Width : 2.692

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.3444

Width : 1.986

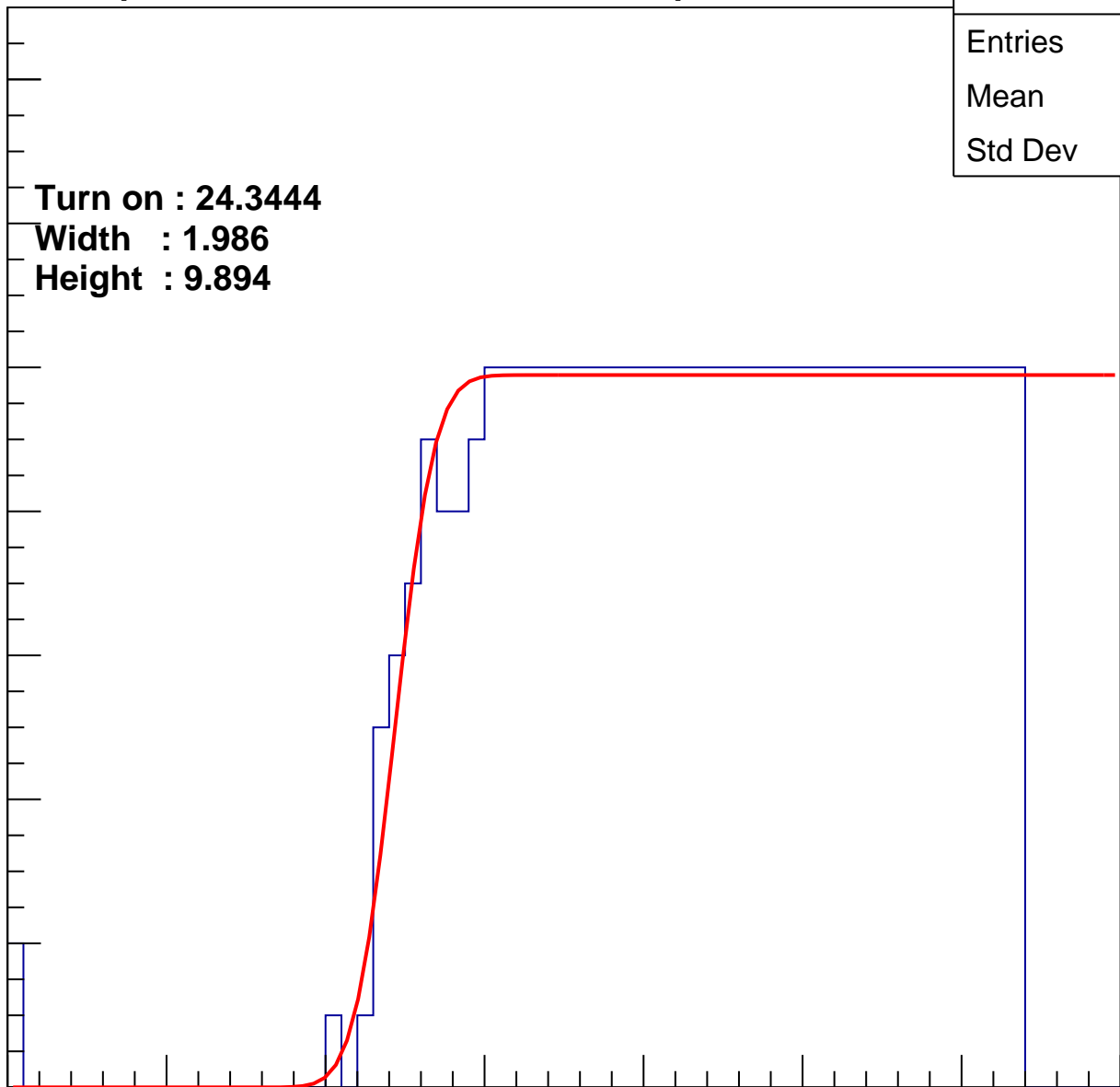
Height : 9.894

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U15-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.77
Std Dev	11.72

Turn on : 25.3305

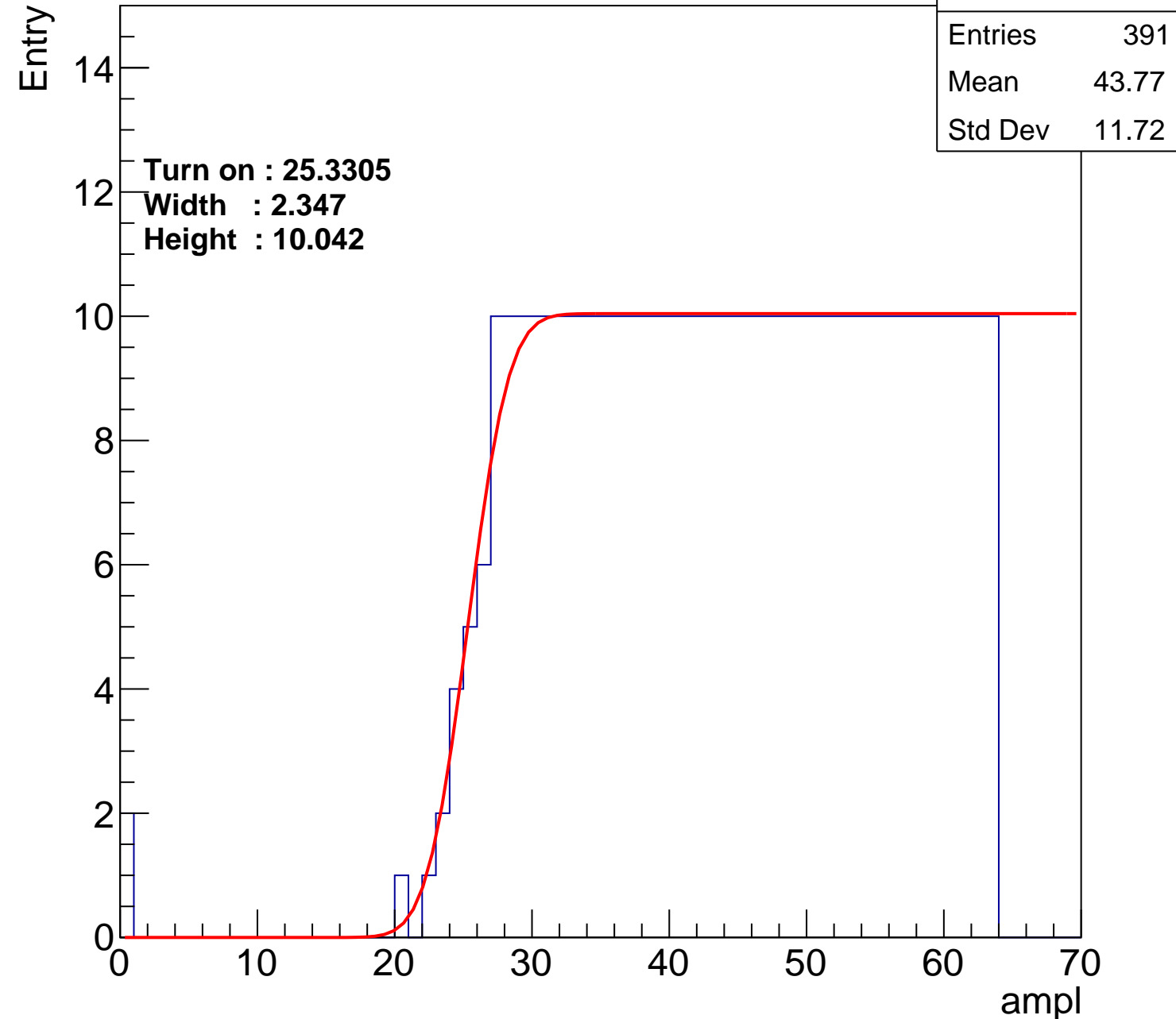
Width : 2.347

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.5565

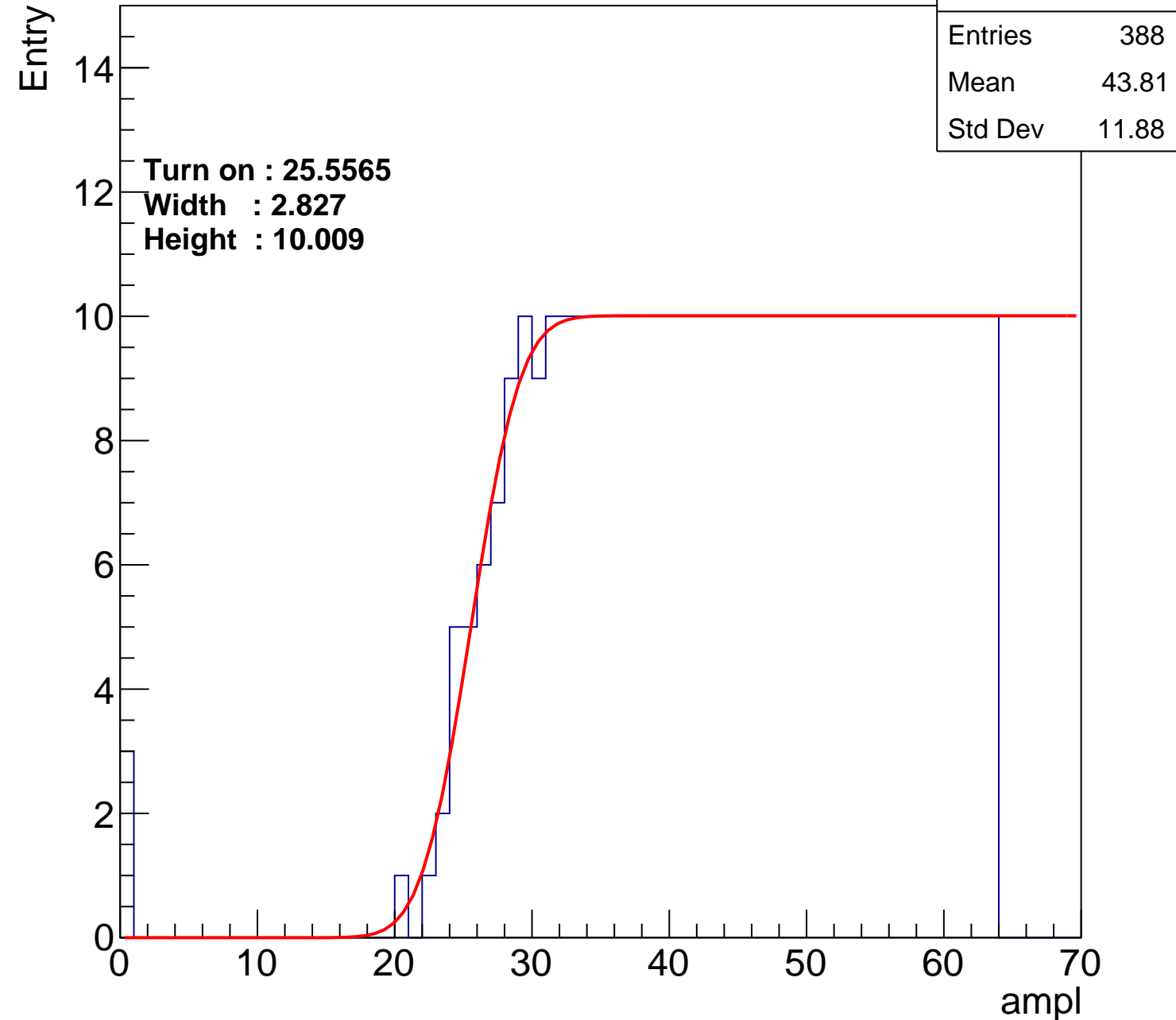
Width : 2.827

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.57

Turn on : 25.8558

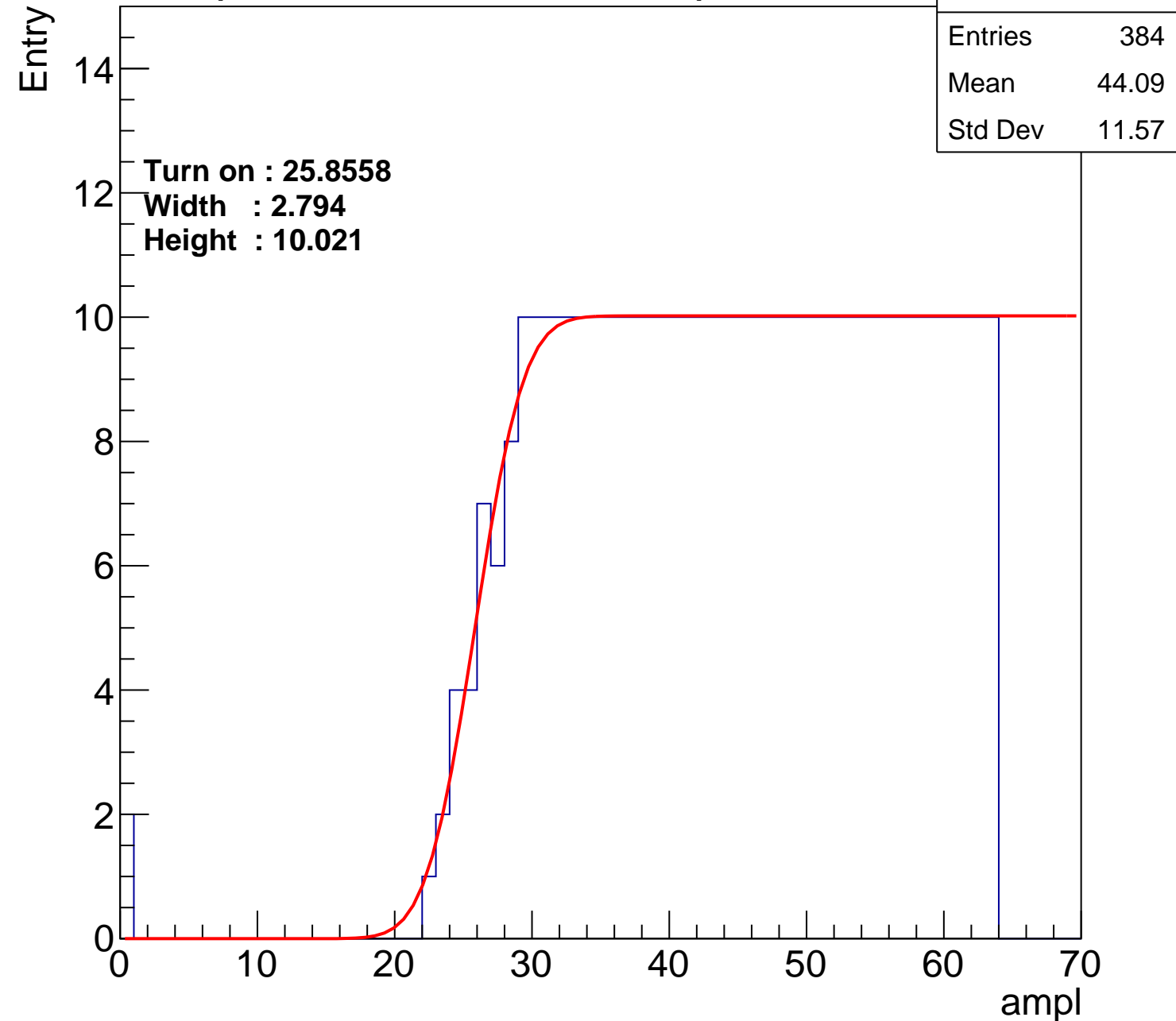
Width : 2.794

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch52

calib_packv5_042523_0143.root, FC#3, port B3

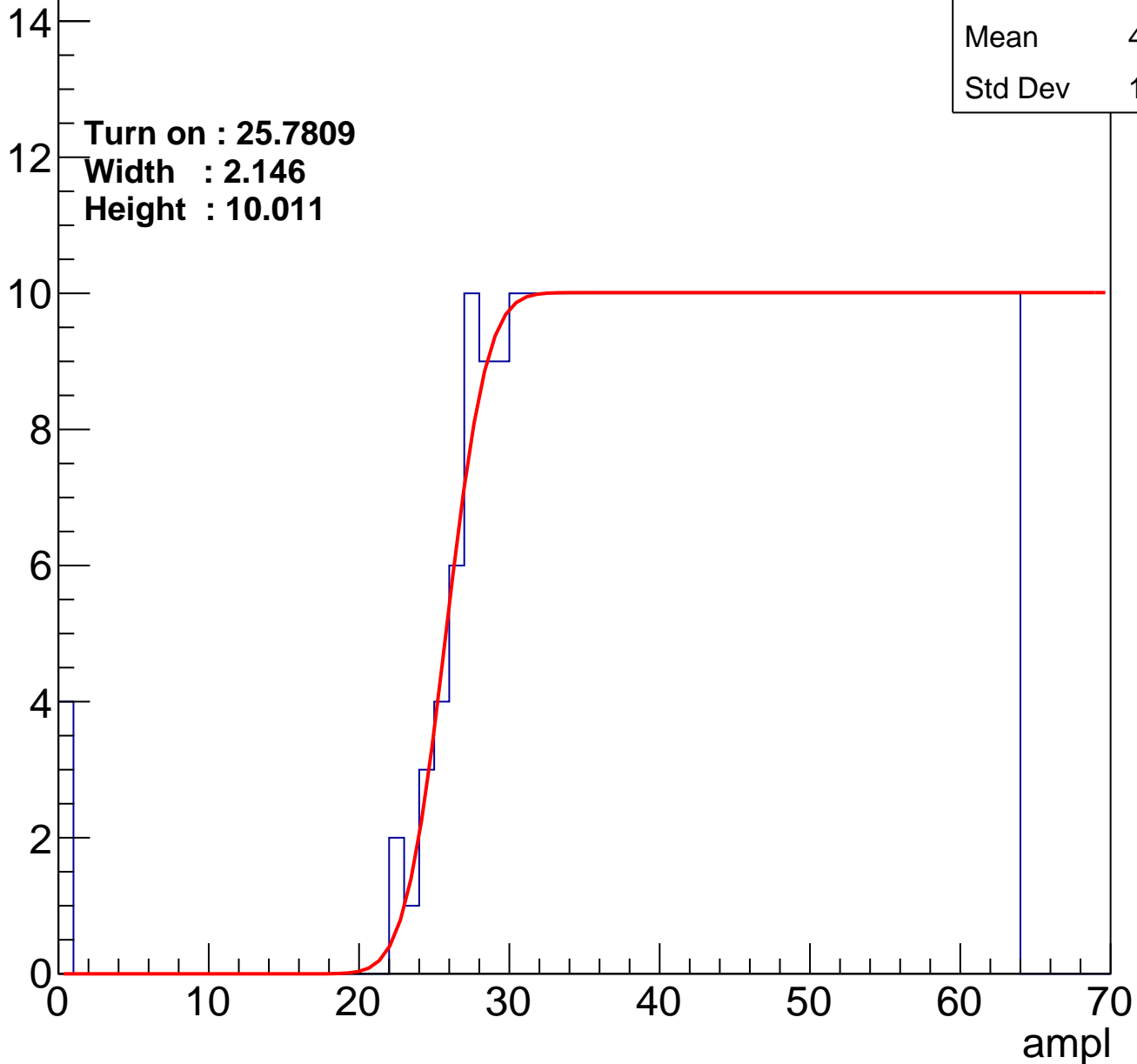
Entries	388
Mean	43.78
Std Dev	11.99

Turn on : 25.7809

Width : 2.146

Height : 10.011

Entry



B1L000S, U15-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.6
Std Dev	11.2

Turn on : 26.7297

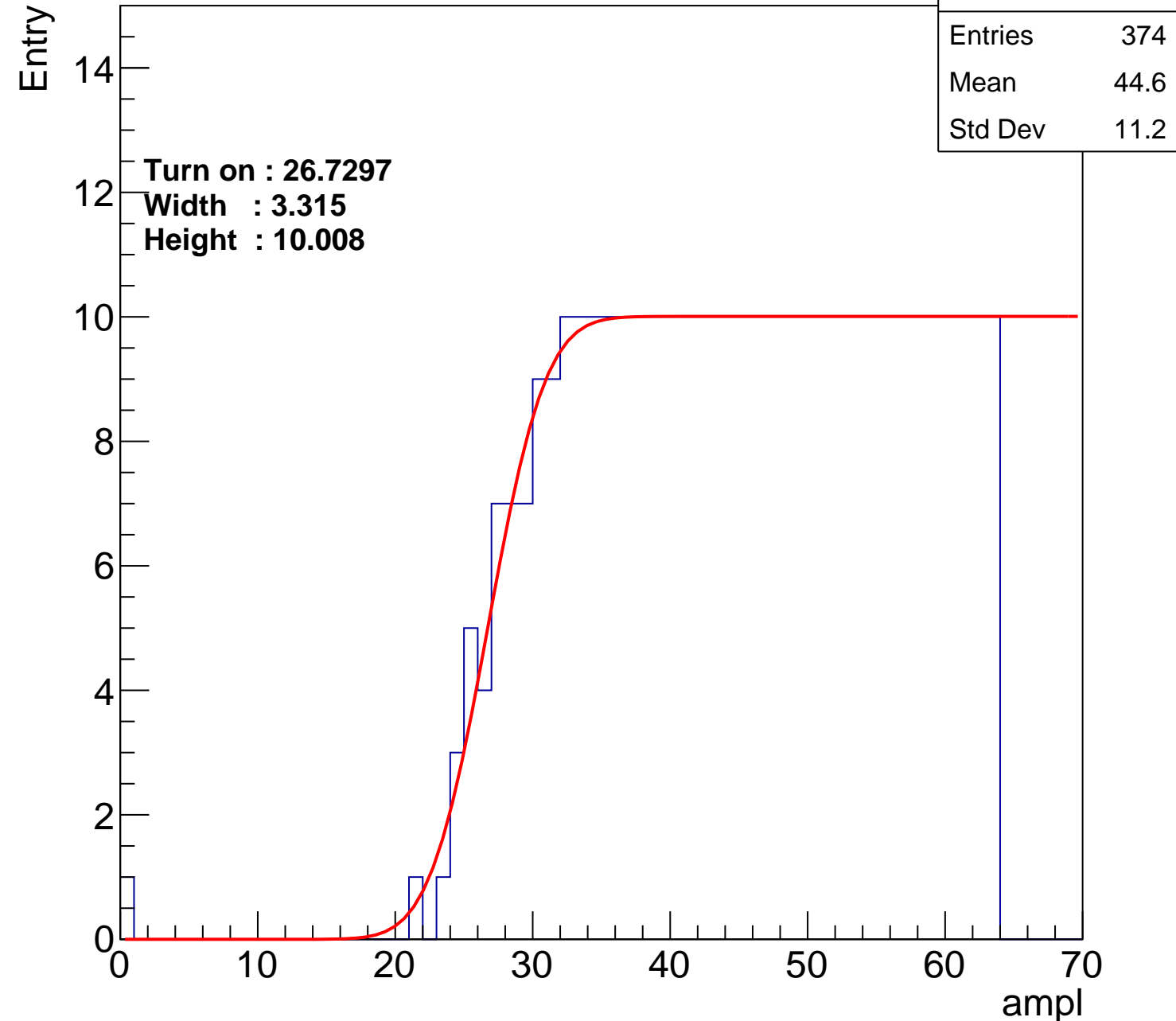
Width : 3.315

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.18
Std Dev	12.31

Turn on : 24.6033

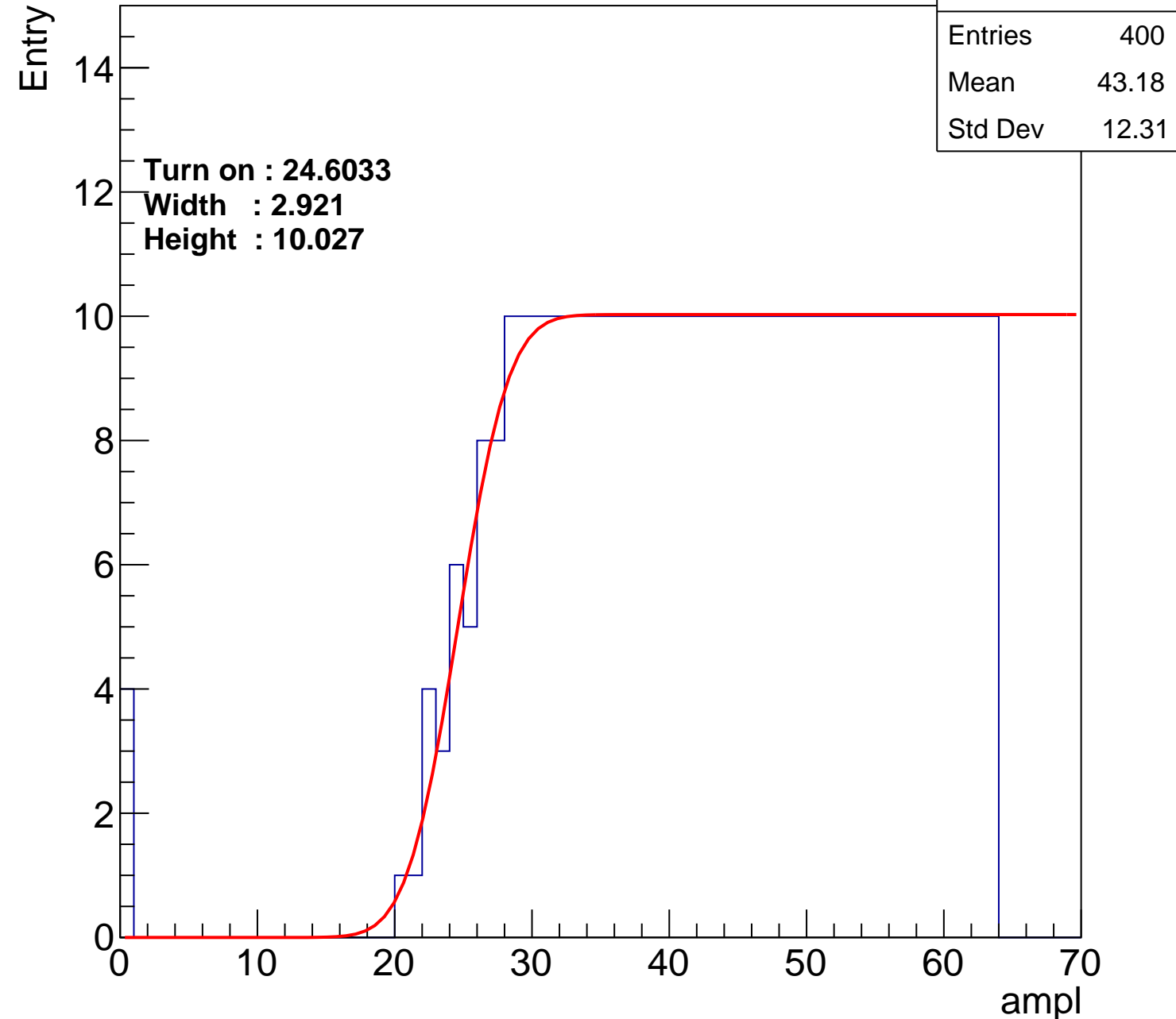
Width : 2.921

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.8210

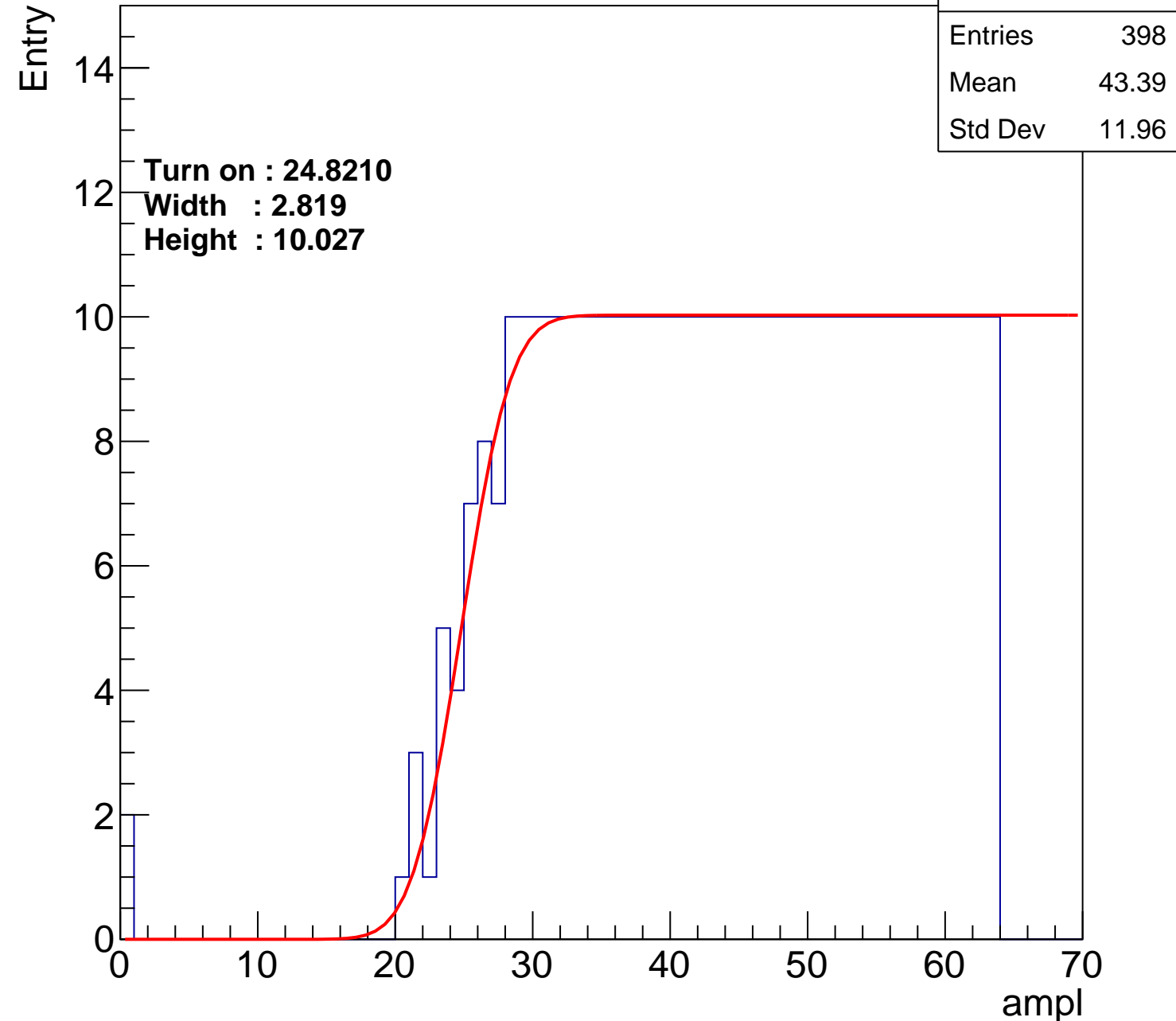
Width : 2.819

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.78
Std Dev	11.59

Turn on : 24.8090

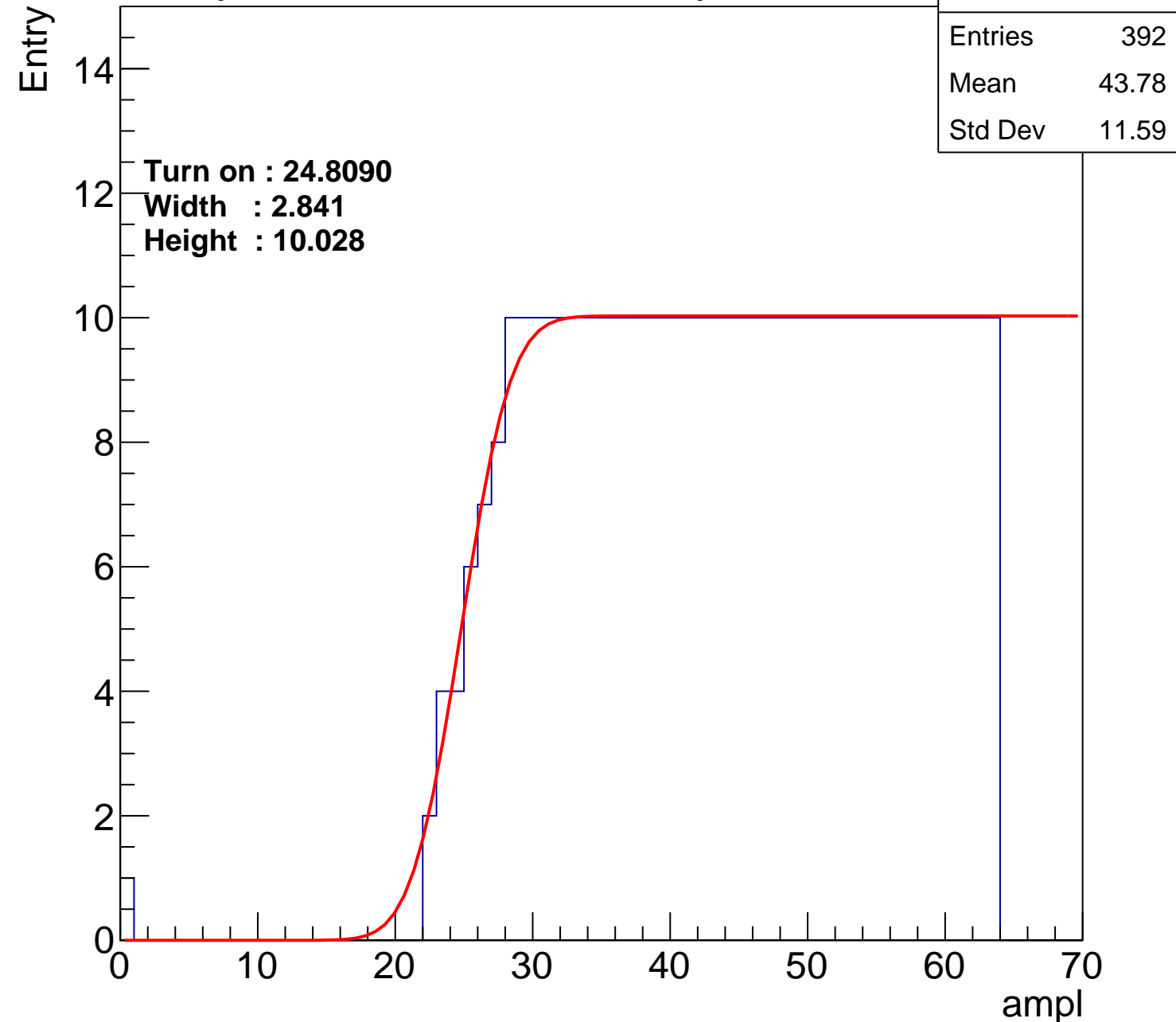
Width : 2.841

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch57

calib_packv5_042523_0143.root, FC#3, port B3

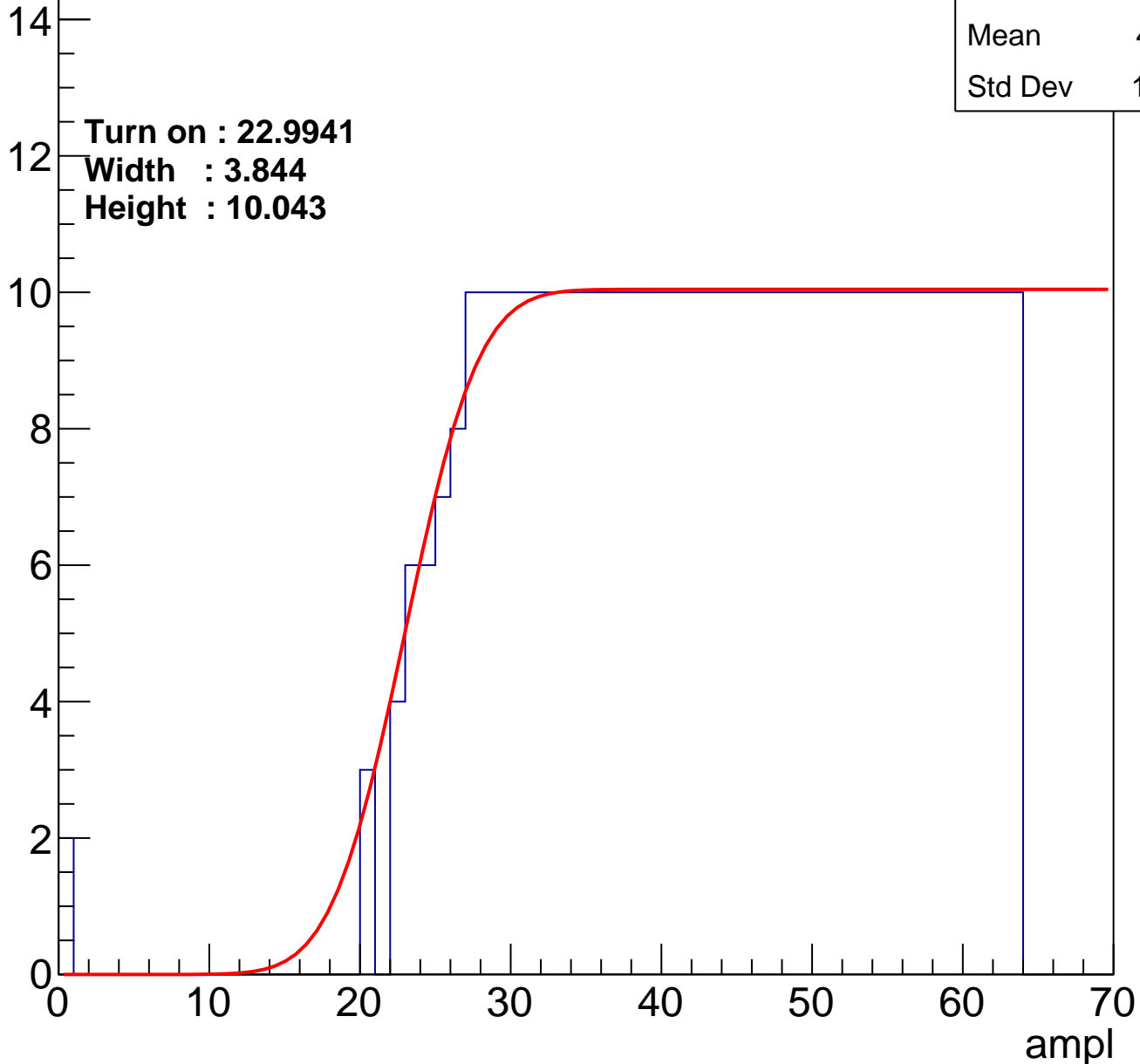
Entries	406
Mean	43.01
Std Dev	12.14

Turn on : 22.9941

Width : 3.844

Height : 10.043

Entry



B1L000S, U15-ch58

calib_packv5_042523_0143.root, FC#3, port B3

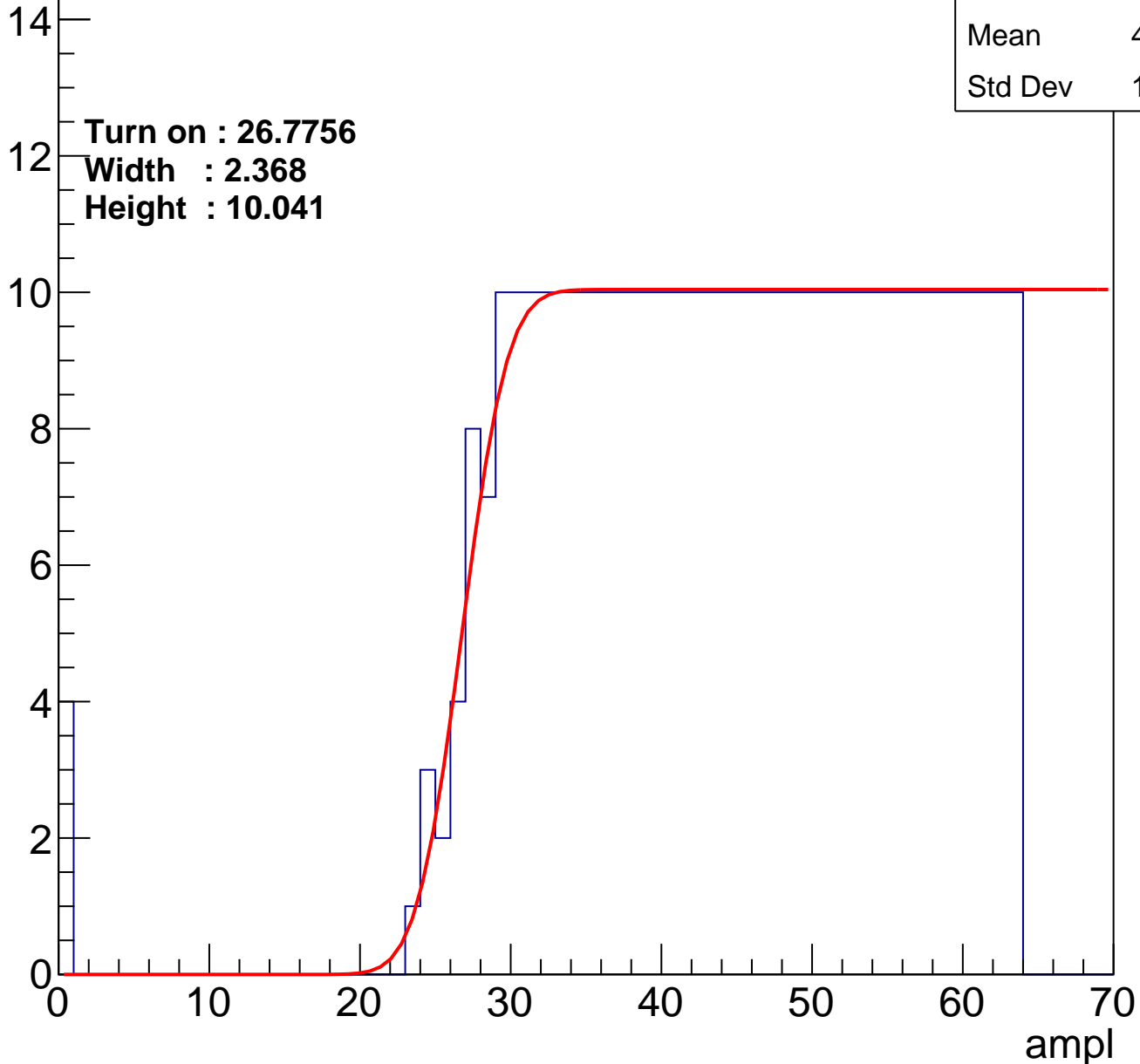
Entries	379
Mean	44.22
Std Dev	11.78

Turn on : 26.7756

Width : 2.368

Height : 10.041

Entry



B1L000S, U15-ch59

calib_packv5_042523_0143.root, FC#3, port B3

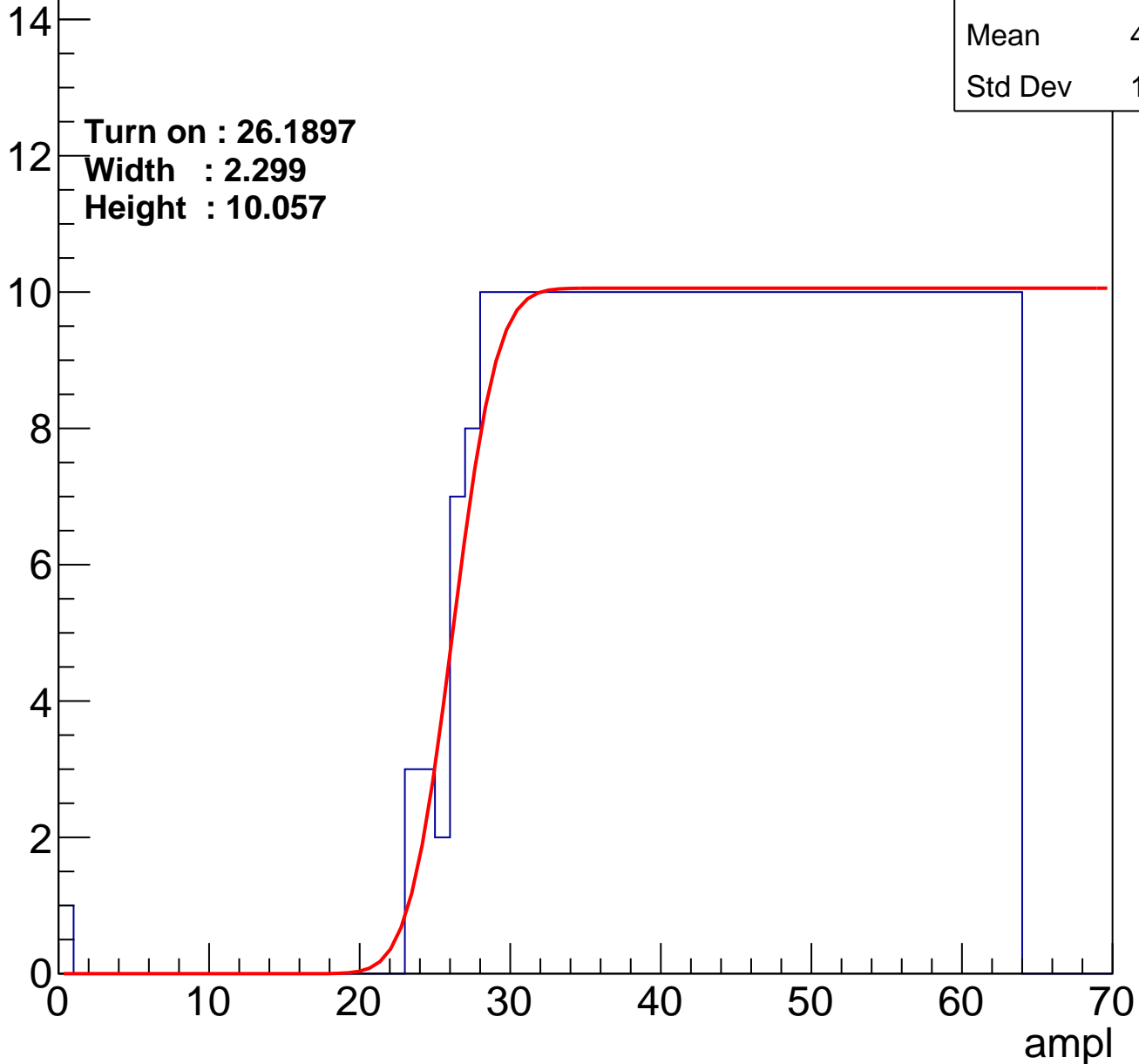
Entries	384
Mean	44.19
Std Dev	11.34

Turn on : 26.1897

Width : 2.299

Height : 10.057

Entry



B1L000S, U15-ch60

calib_packv5_042523_0143.root, FC#3, port B3

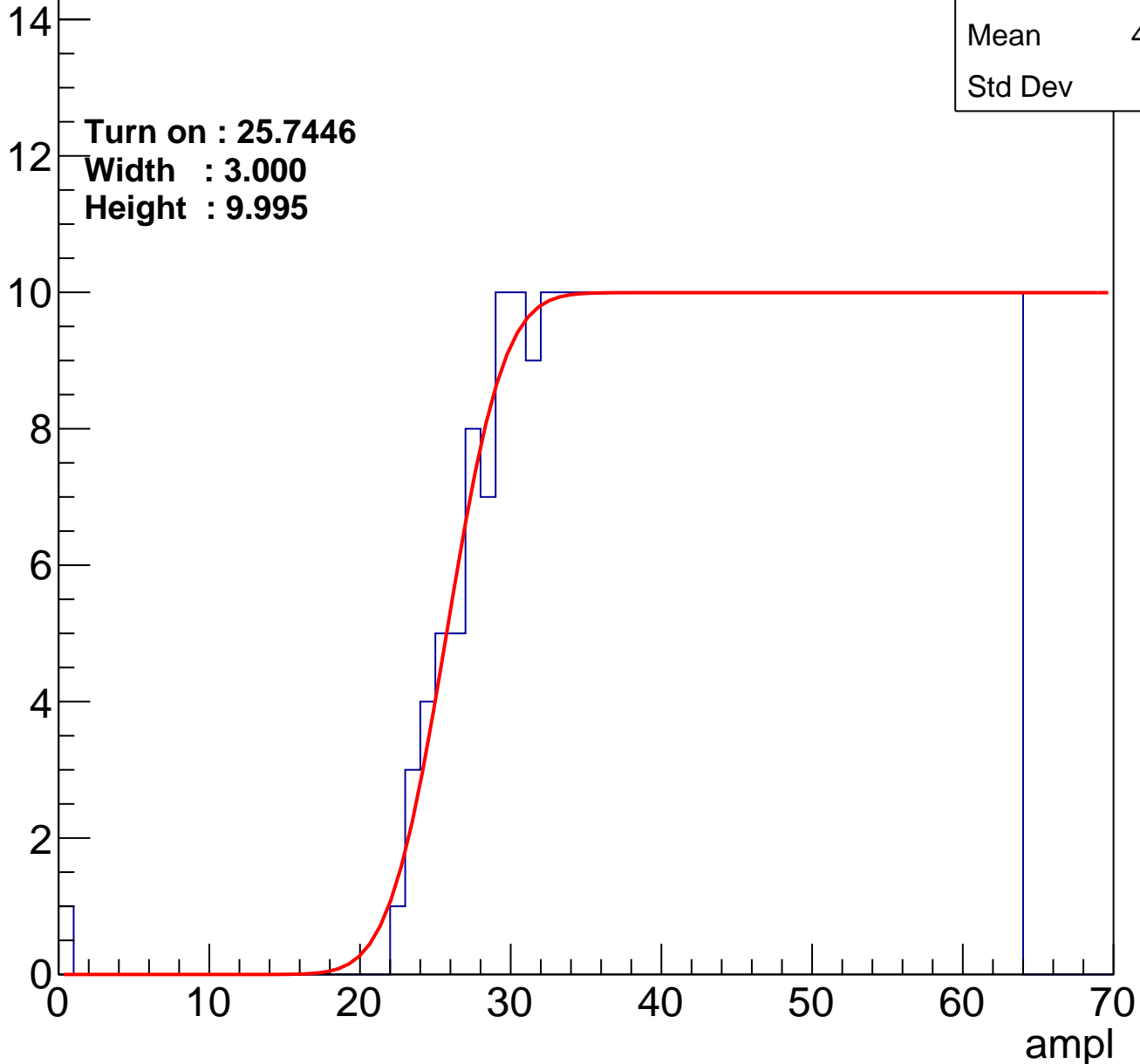
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.7446

Width : 3.000

Height : 9.995

Entry



B1L000S, U15-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 27.9464

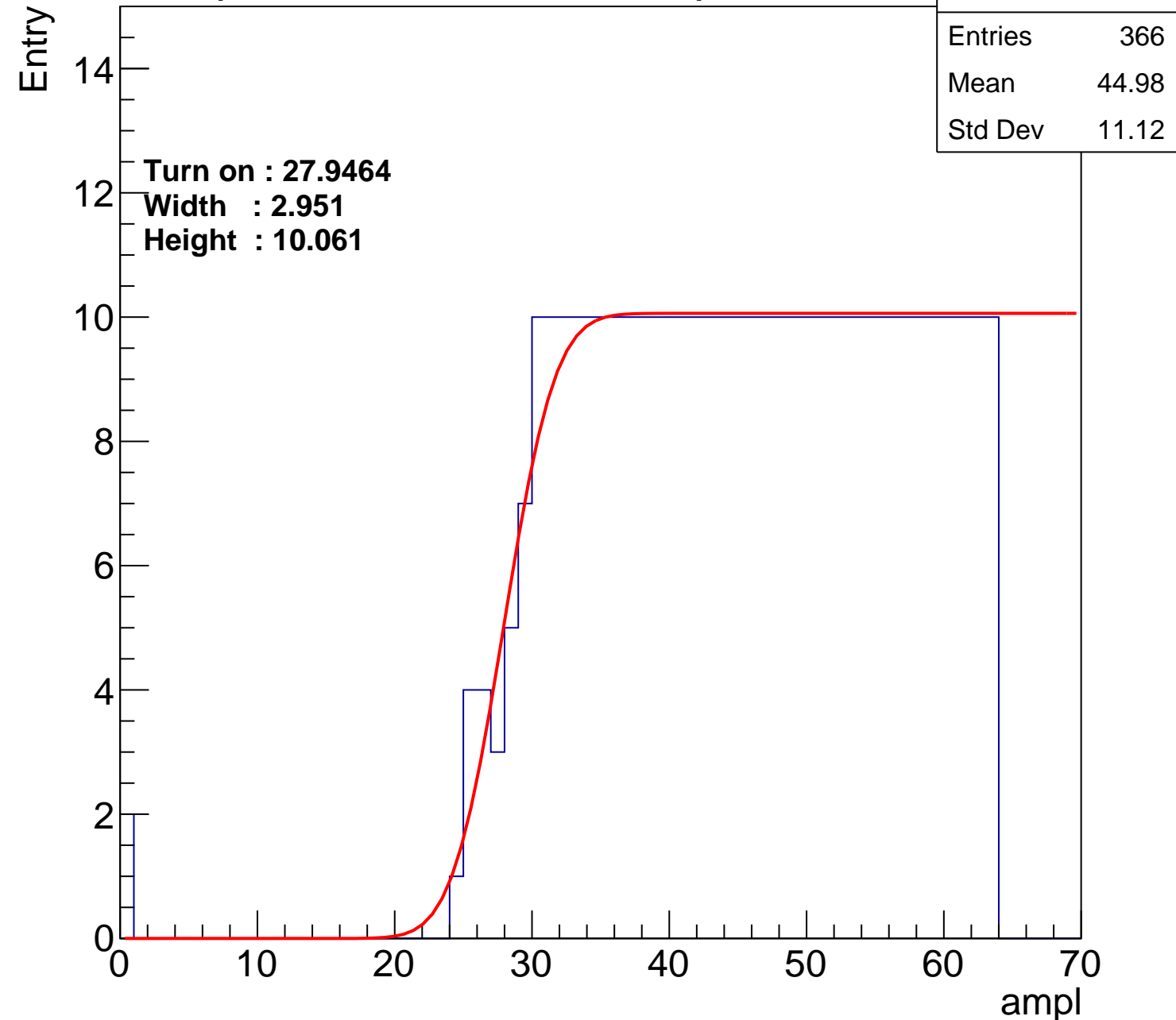
Width : 2.951

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch62

calib_packv5_042523_0143.root, FC#3, port B3

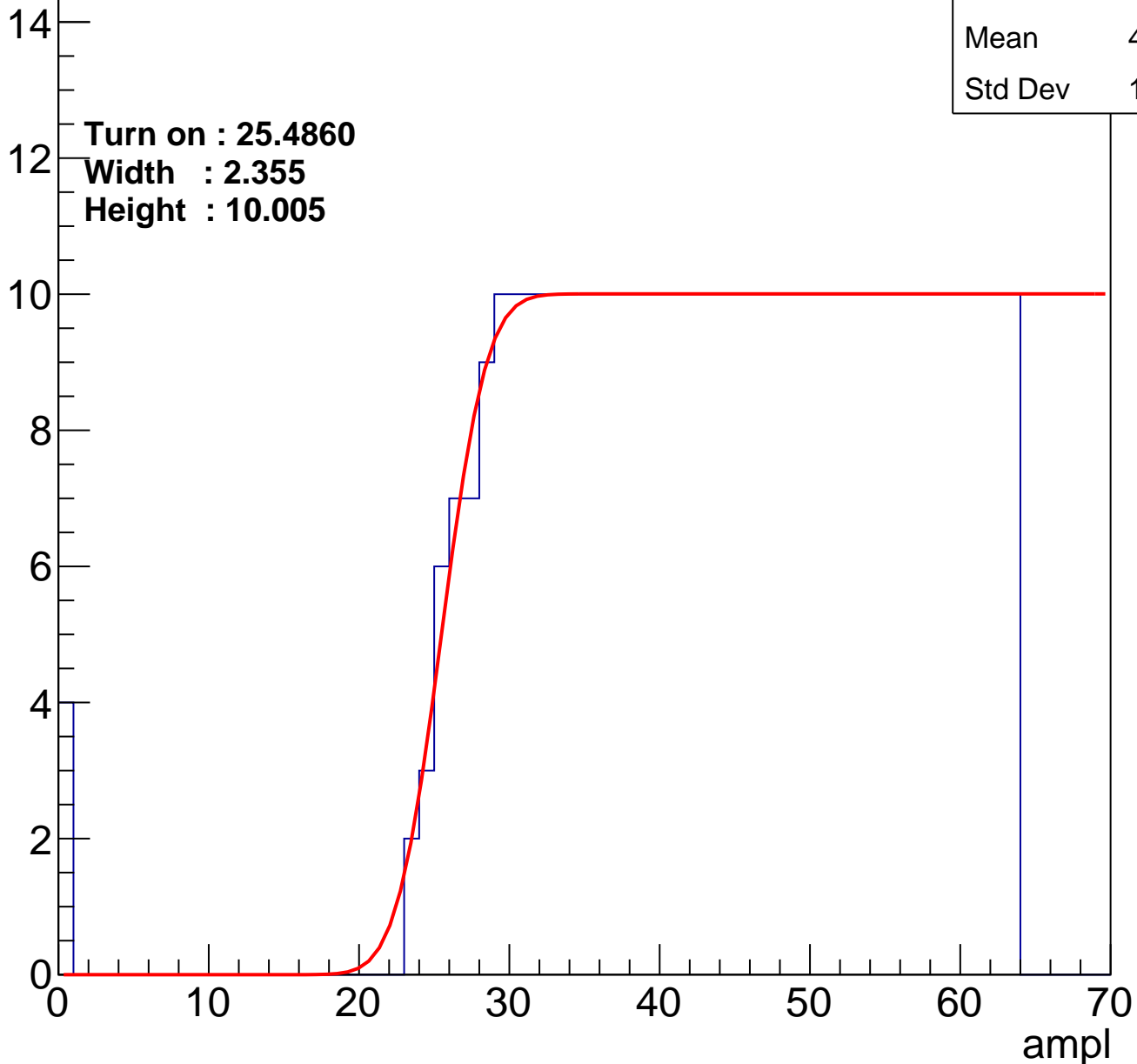
Entries	388
Mean	43.79
Std Dev	11.98

Turn on : 25.4860

Width : 2.355

Height : 10.005

Entry



B1L000S, U15-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.4
Std Dev	11.76

Turn on : 26.8054

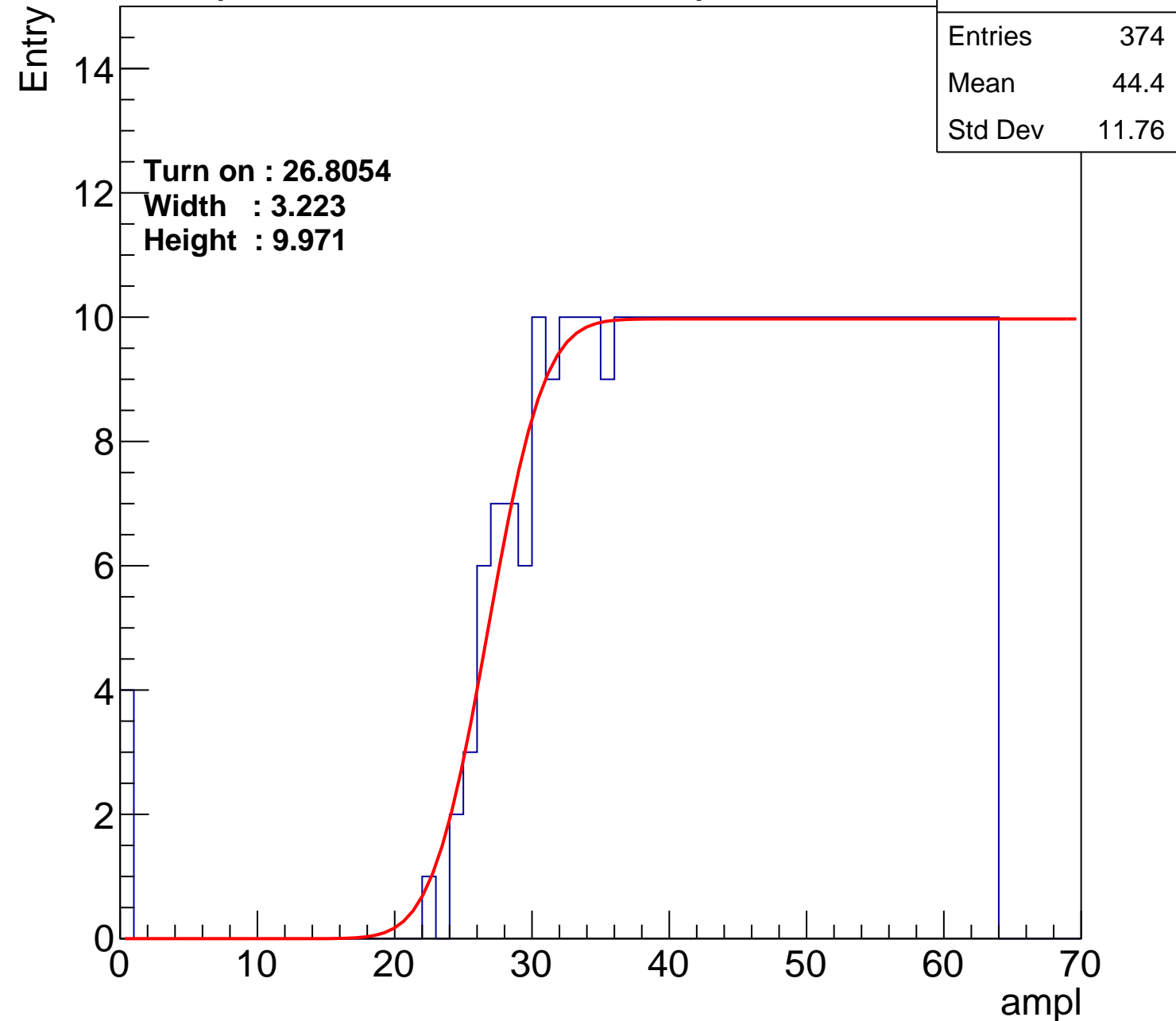
Width : 3.223

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9627

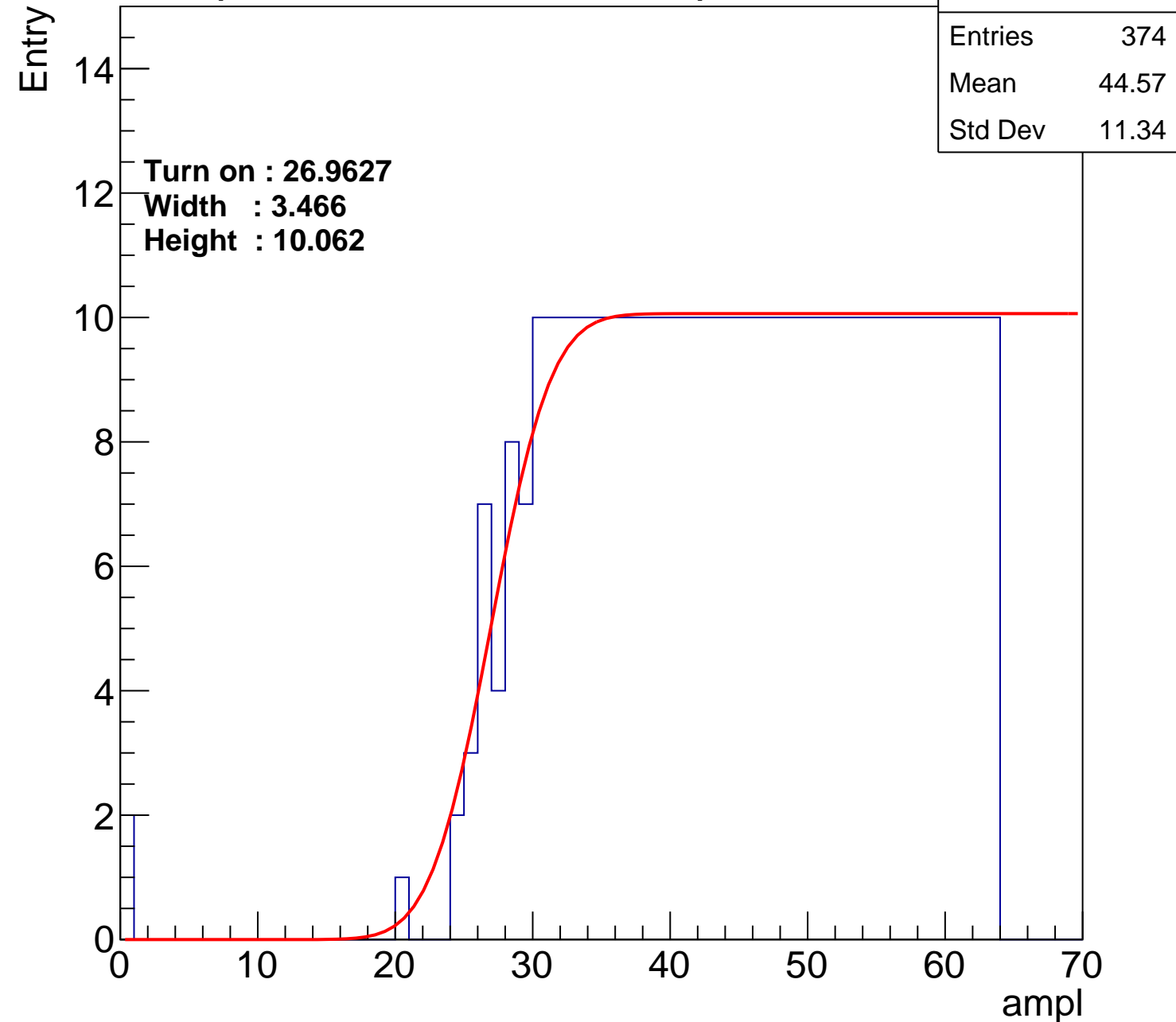
Width : 3.466

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch65

calib_packv5_042523_0143.root, FC#3, port B3

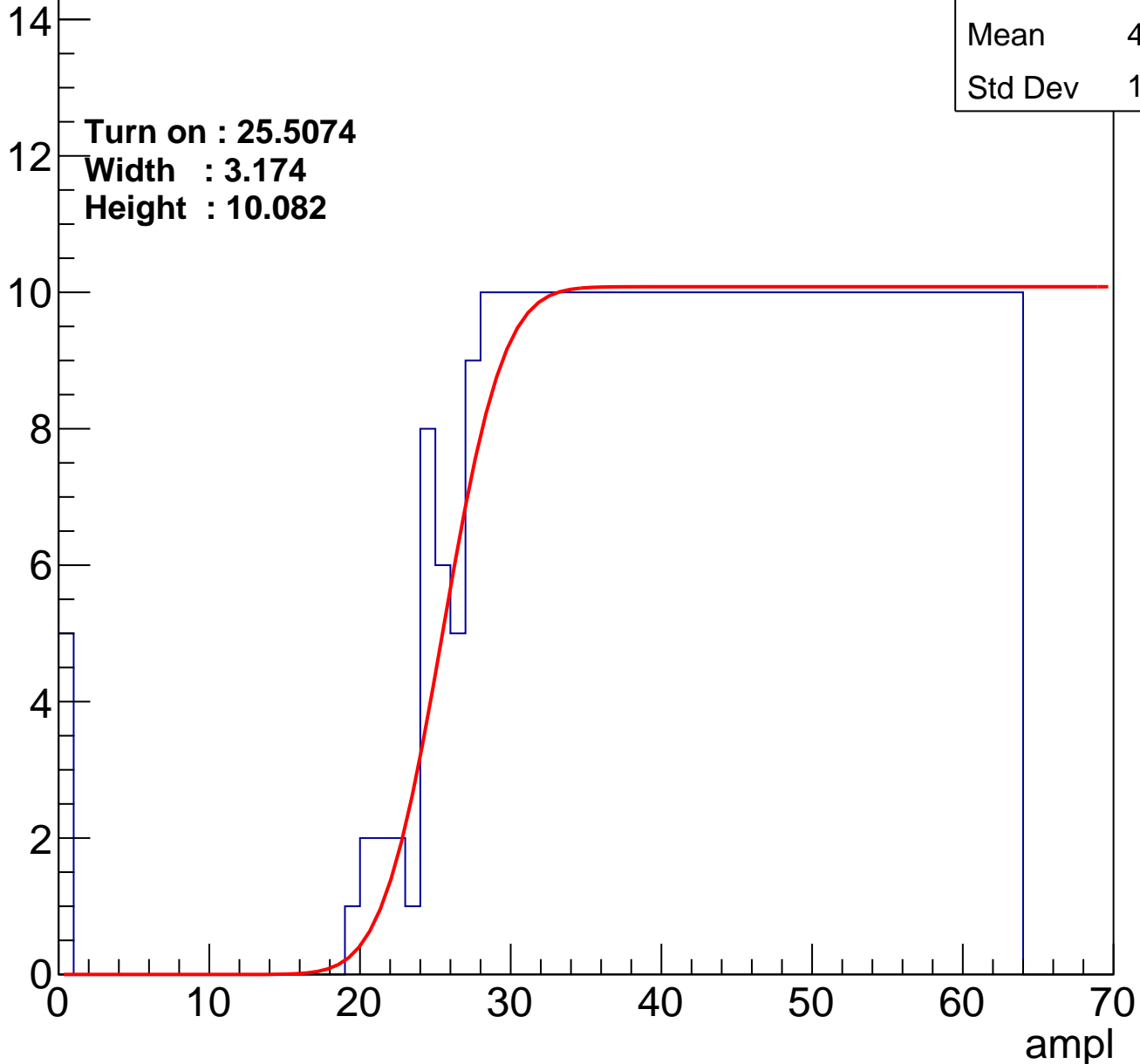
Entries	401
Mean	43.05
Std Dev	12.52

Turn on : 25.5074

Width : 3.174

Height : 10.082

Entry



B1L000S, U15-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 26.3019

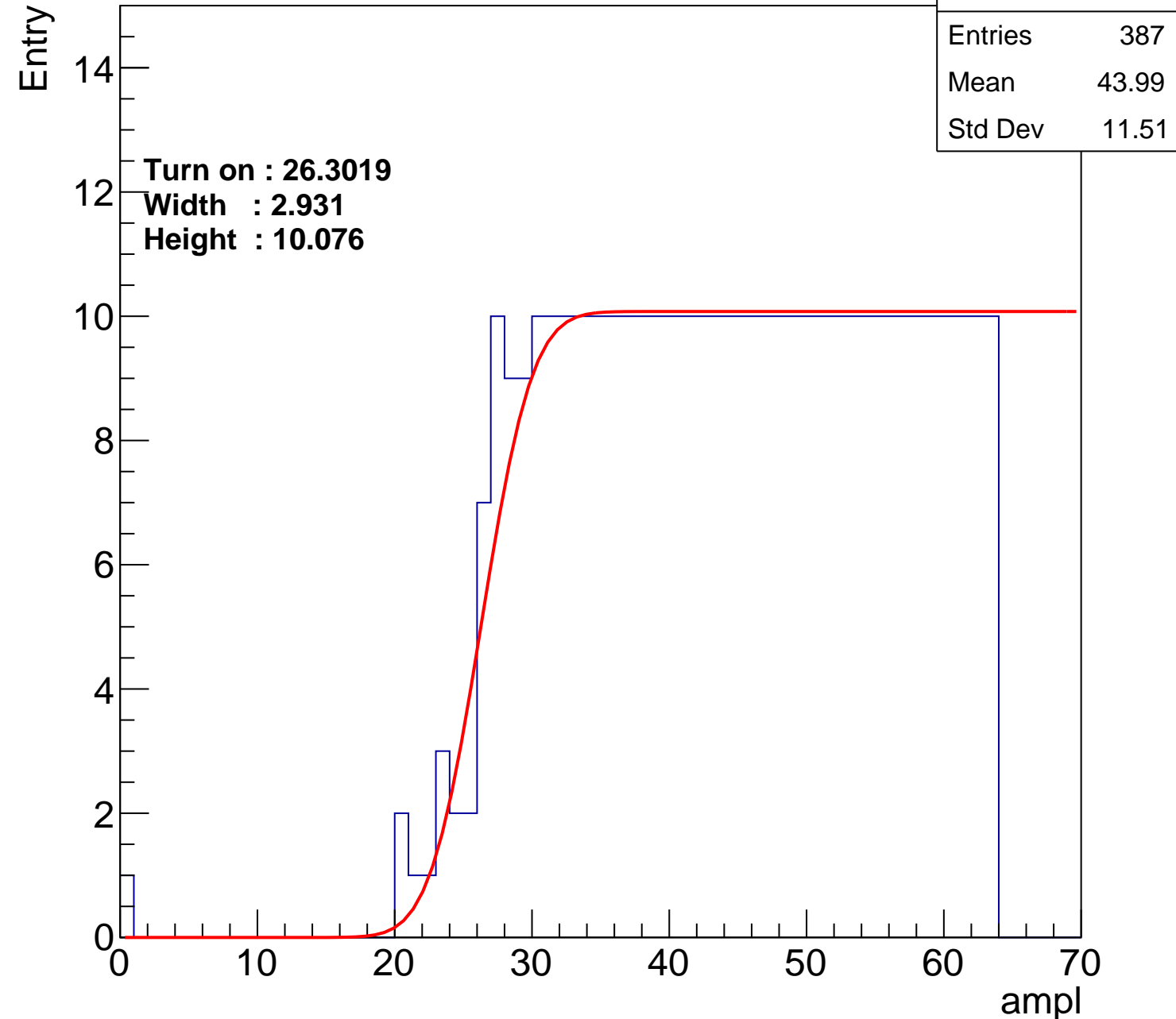
Width : 2.931

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.53
Std Dev	11.81

Turn on : 27.8507

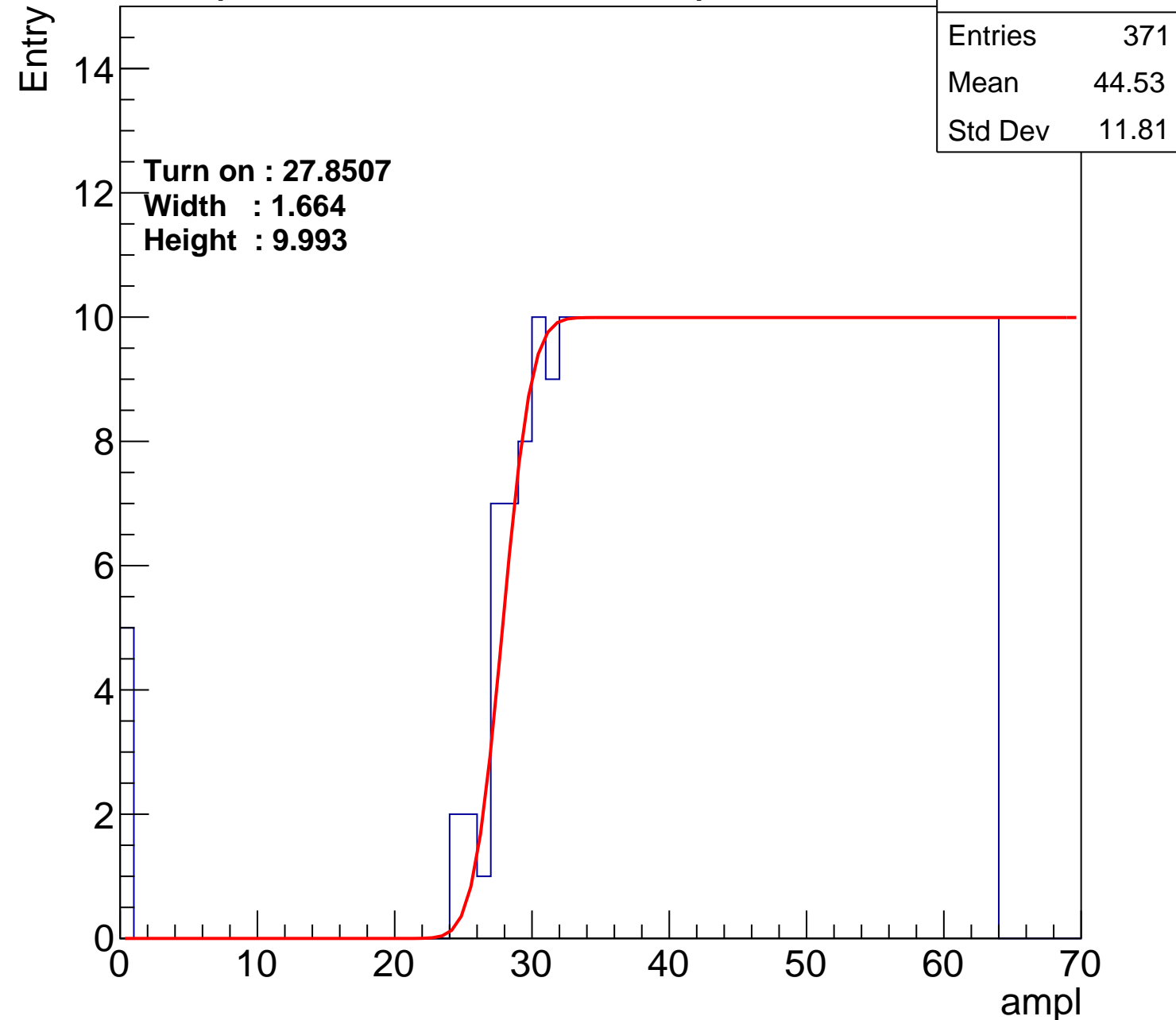
Width : 1.664

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.7784

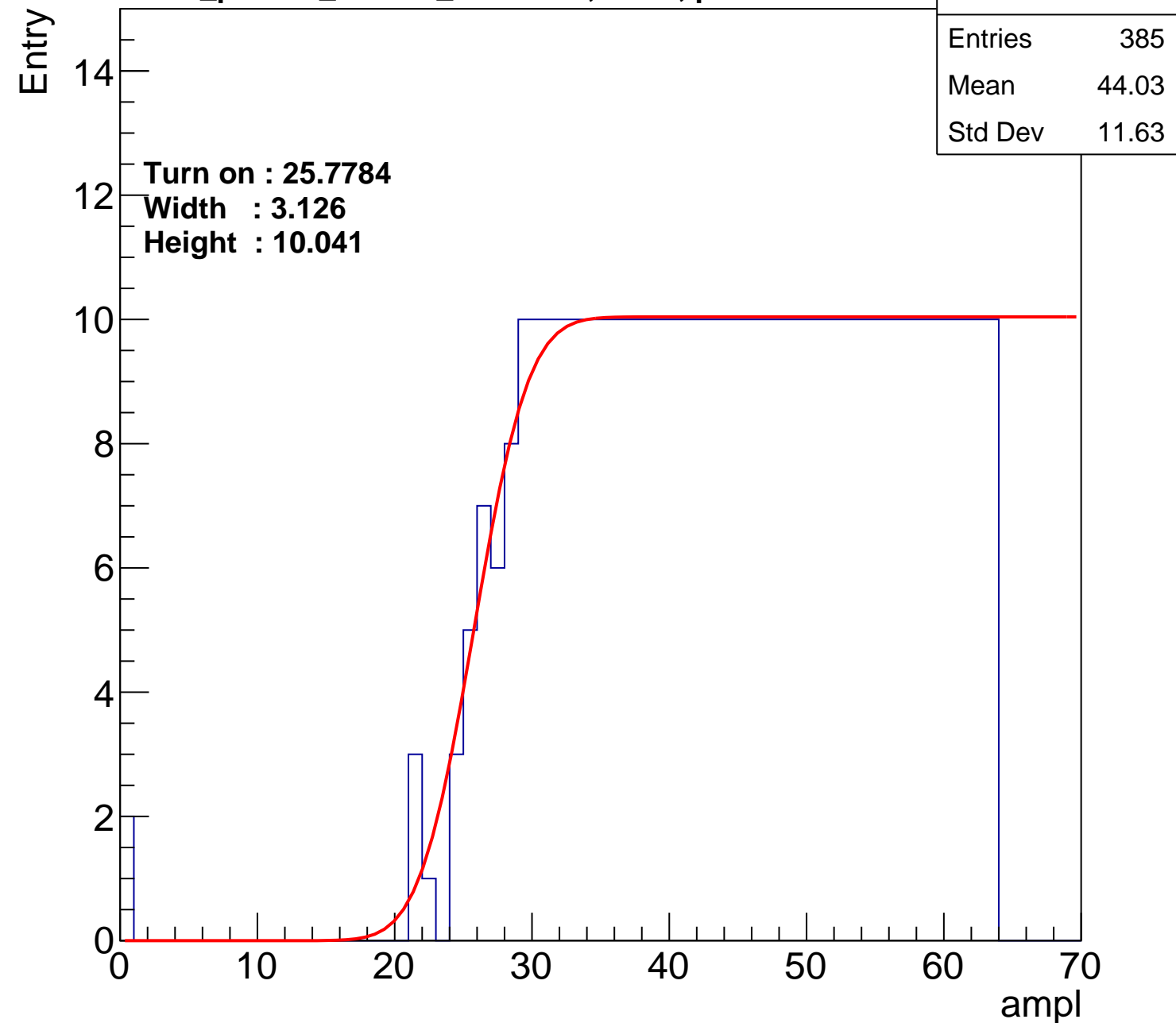
Width : 3.126

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch69

calib_packv5_042523_0143.root, FC#3, port B3

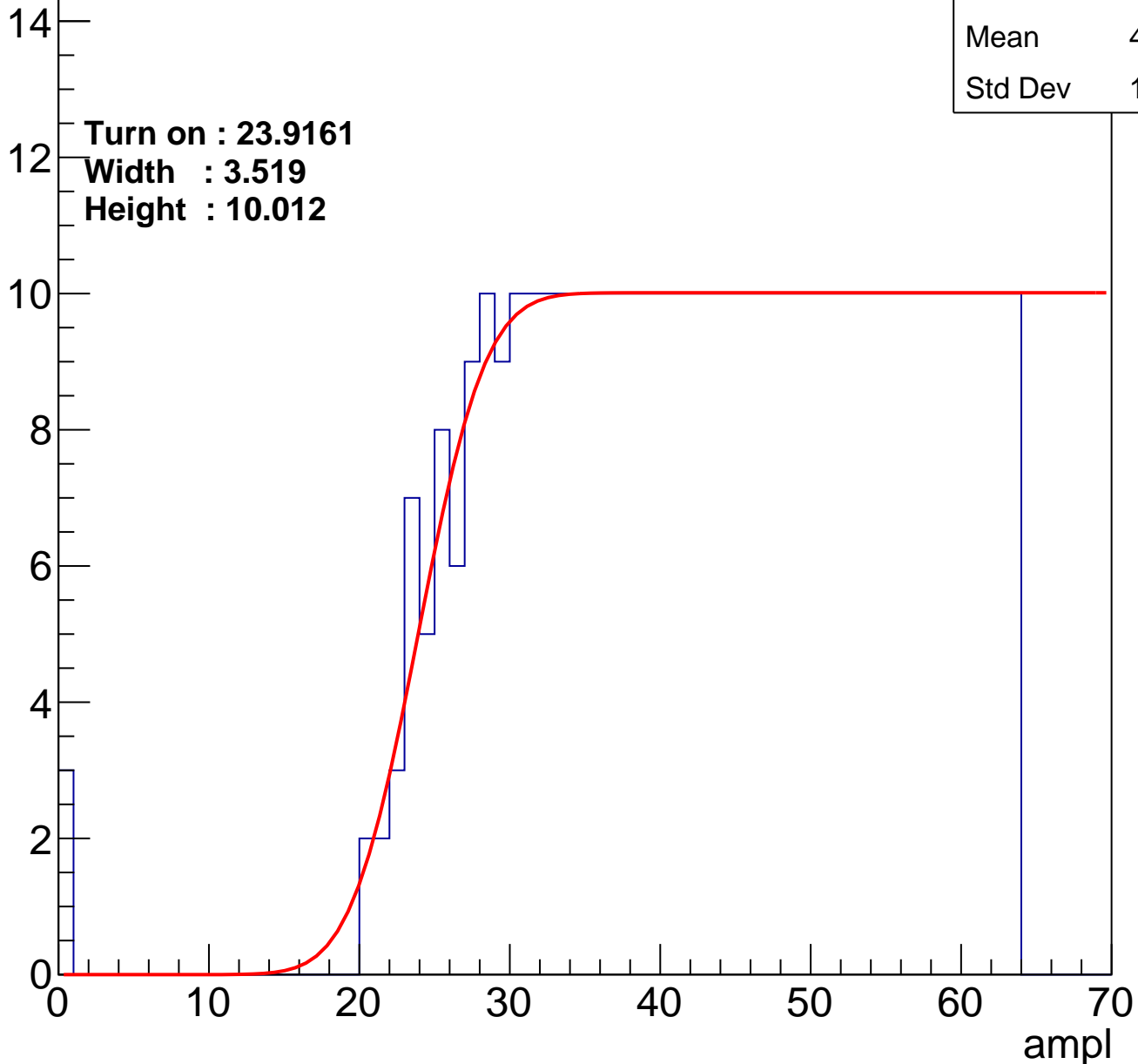
Entries	404
Mean	43.02
Std Dev	12.29

Turn on : 23.9161

Width : 3.519

Height : 10.012

Entry



B1L000S, U15-ch70

calib_packv5_042523_0143.root, FC#3, port B3

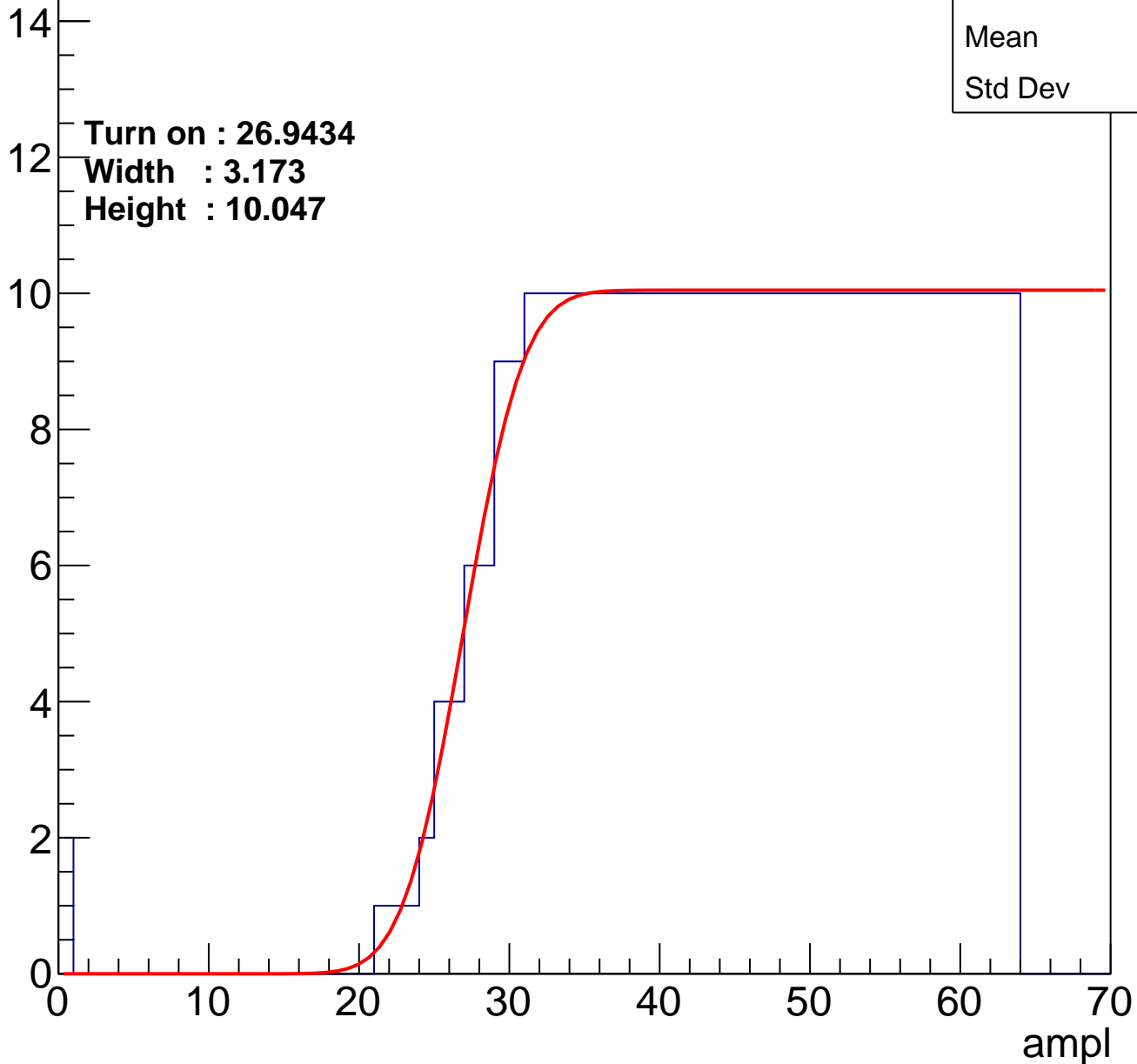
Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.9434

Width : 3.173

Height : 10.047

Entry



B1L000S, U15-ch71

calib_packv5_042523_0143.root, FC#3, port B3

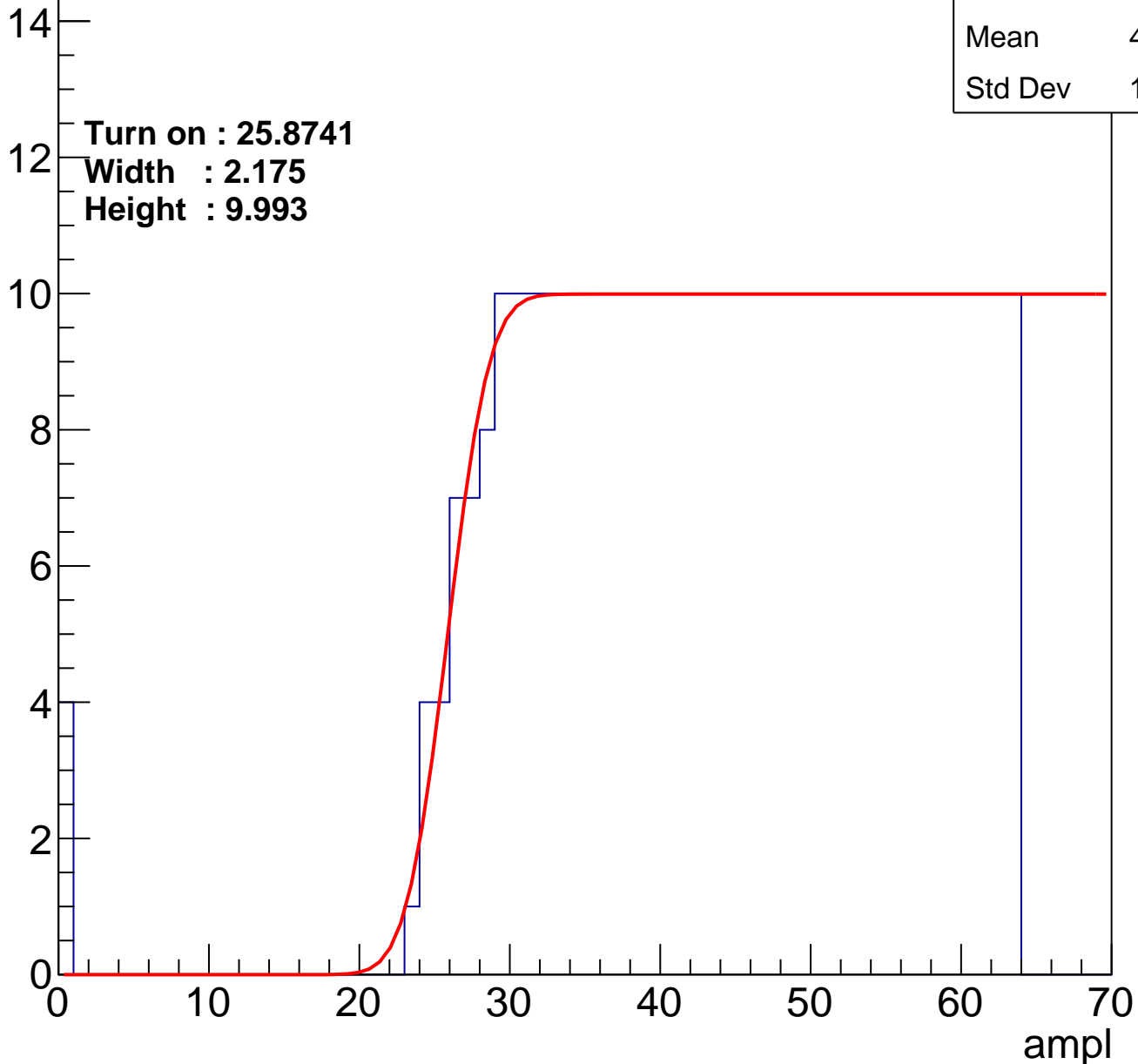
Entries	385
Mean	43.93
Std Dev	11.92

Turn on : 25.8741

Width : 2.175

Height : 9.993

Entry



B1L000S, U15-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.73
Std Dev	11.91

Turn on : 25.7550

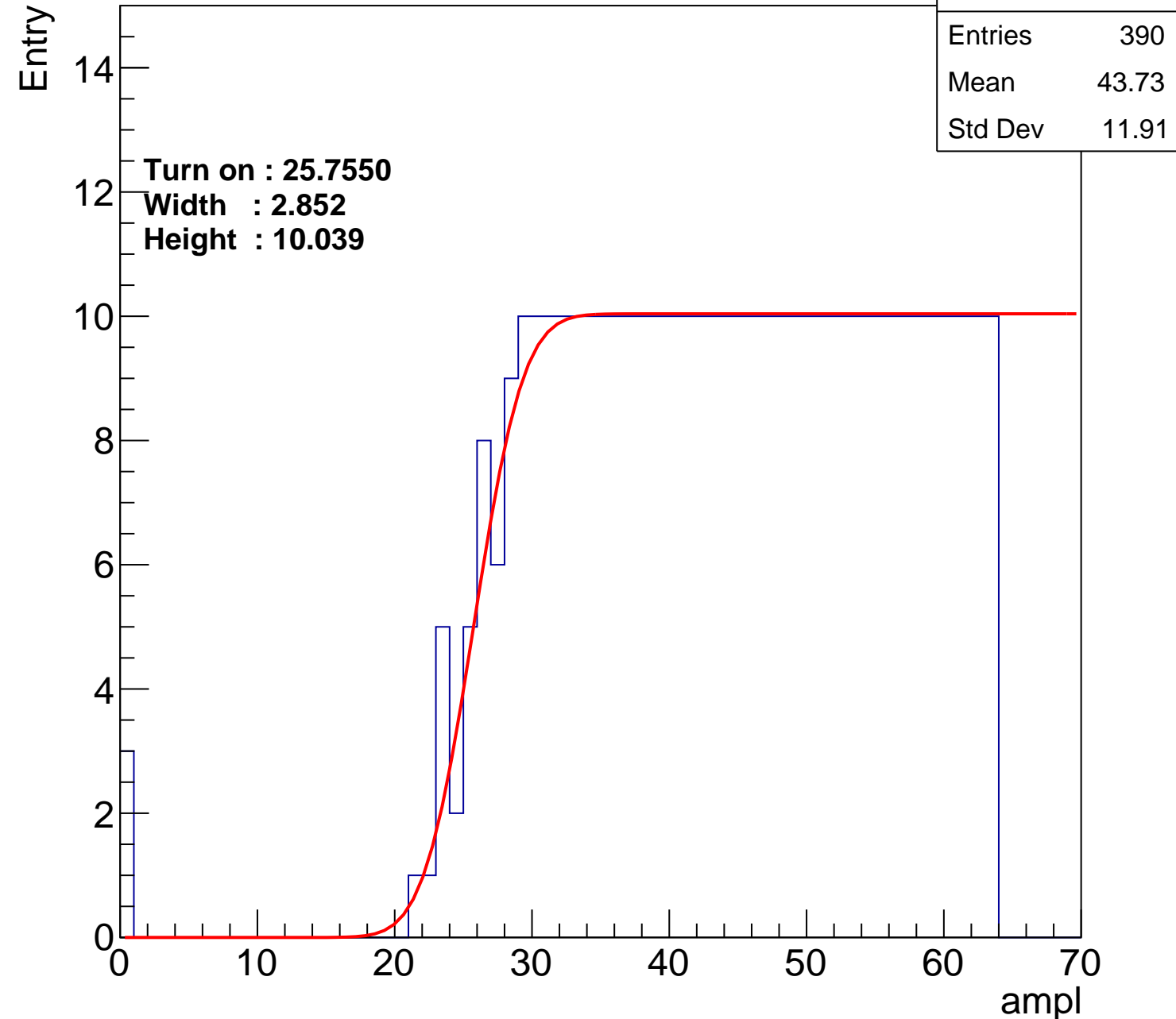
Width : 2.852

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.14
Std Dev	11.99

Turn on : 26.7923

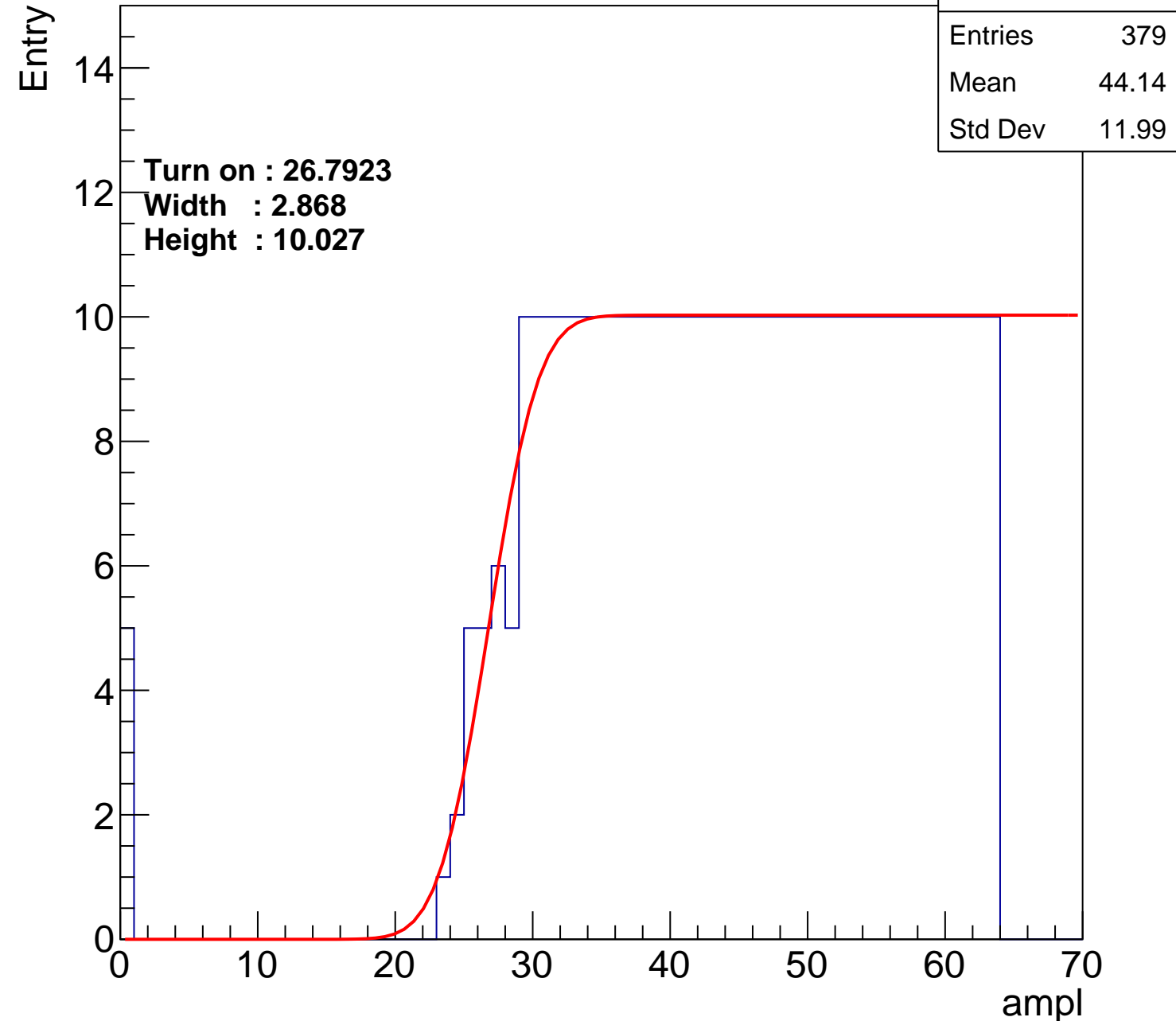
Width : 2.868

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 26.0968

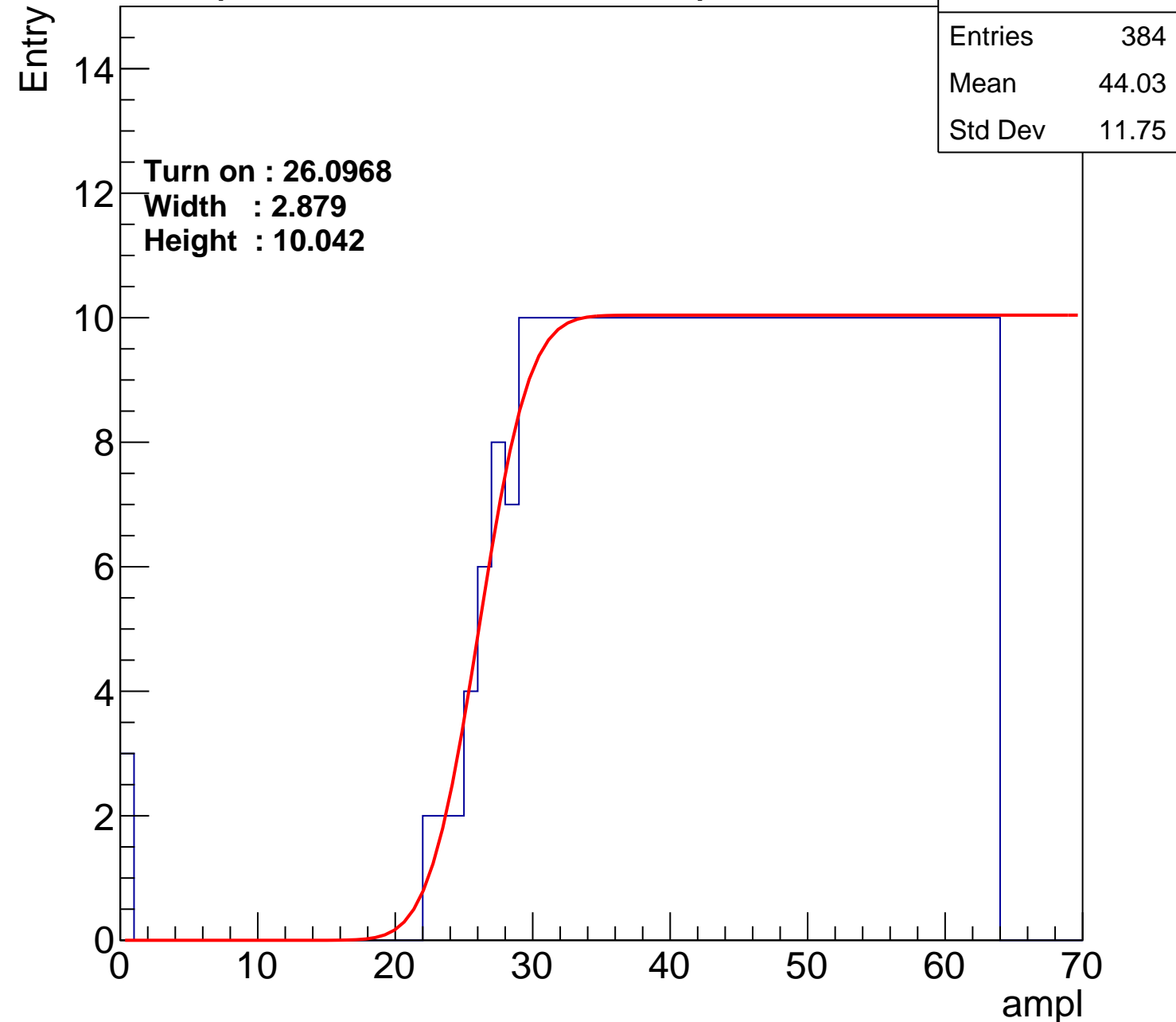
Width : 2.879

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch75

calib_packv5_042523_0143.root, FC#3, port B3

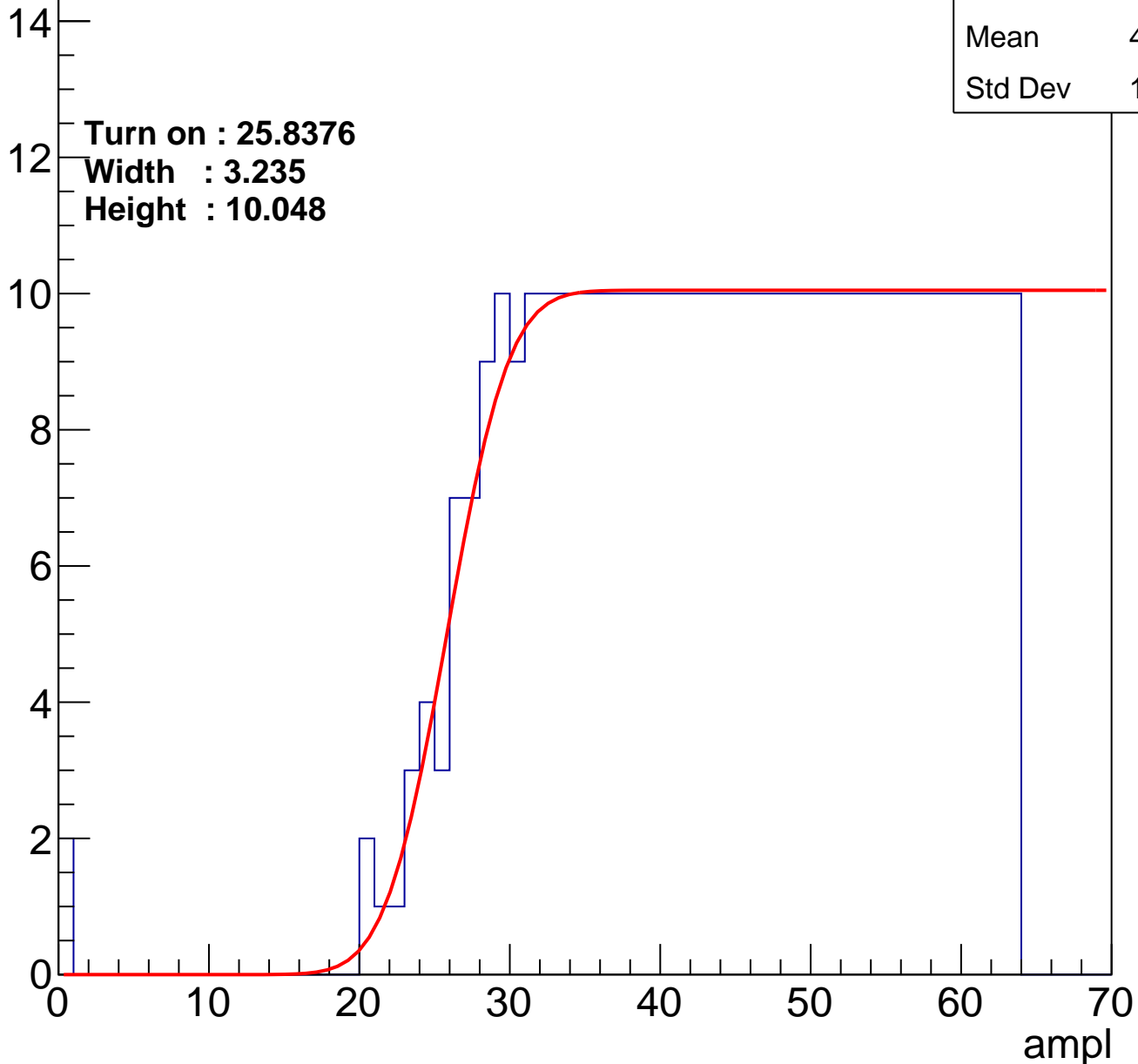
Entries	388
Mean	43.86
Std Dev	11.75

Turn on : 25.8376

Width : 3.235

Height : 10.048

Entry



B1L000S, U15-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.79
Std Dev	11.61

Turn on : 25.2693

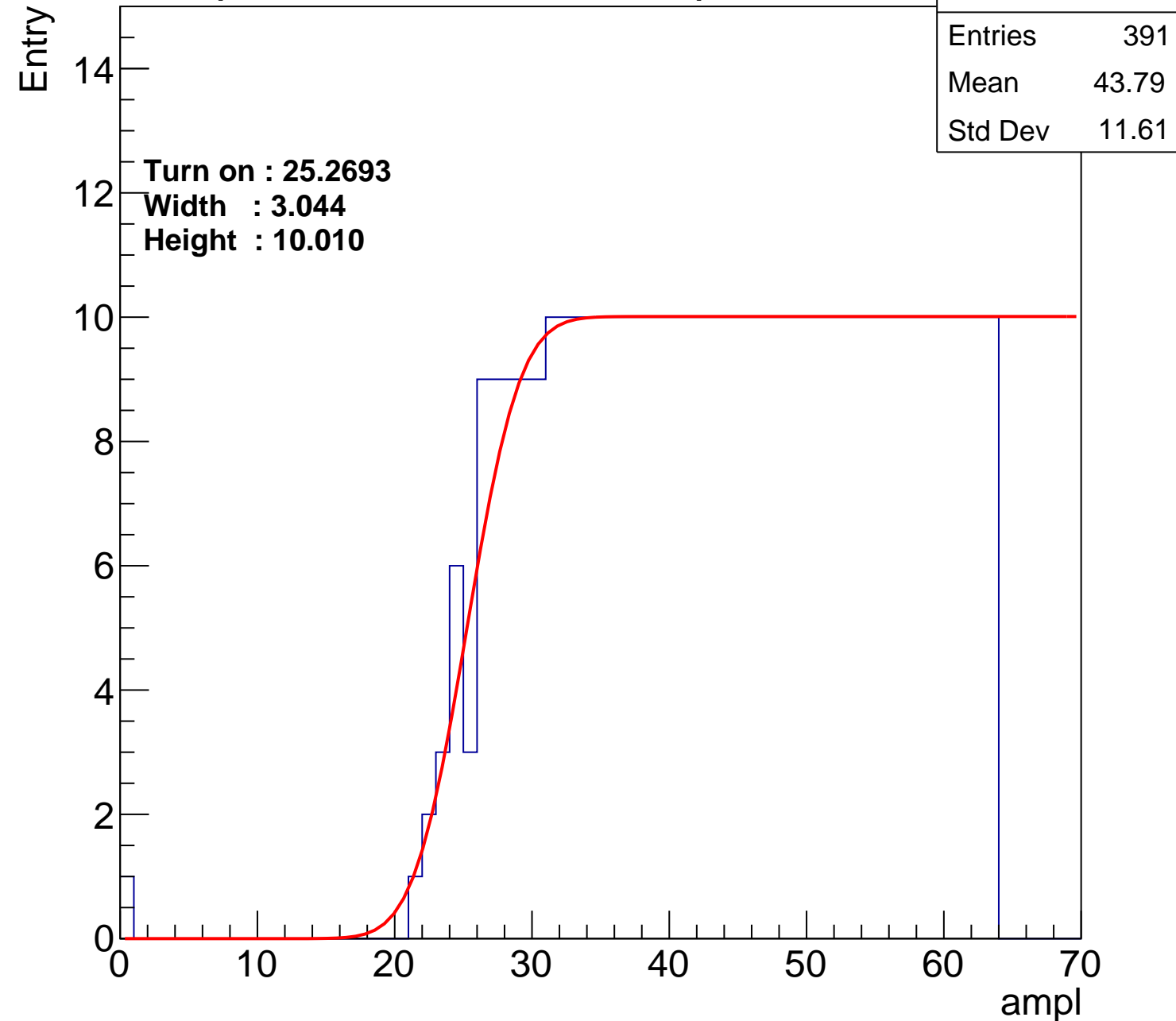
Width : 3.044

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch77

calib_packv5_042523_0143.root, FC#3, port B3

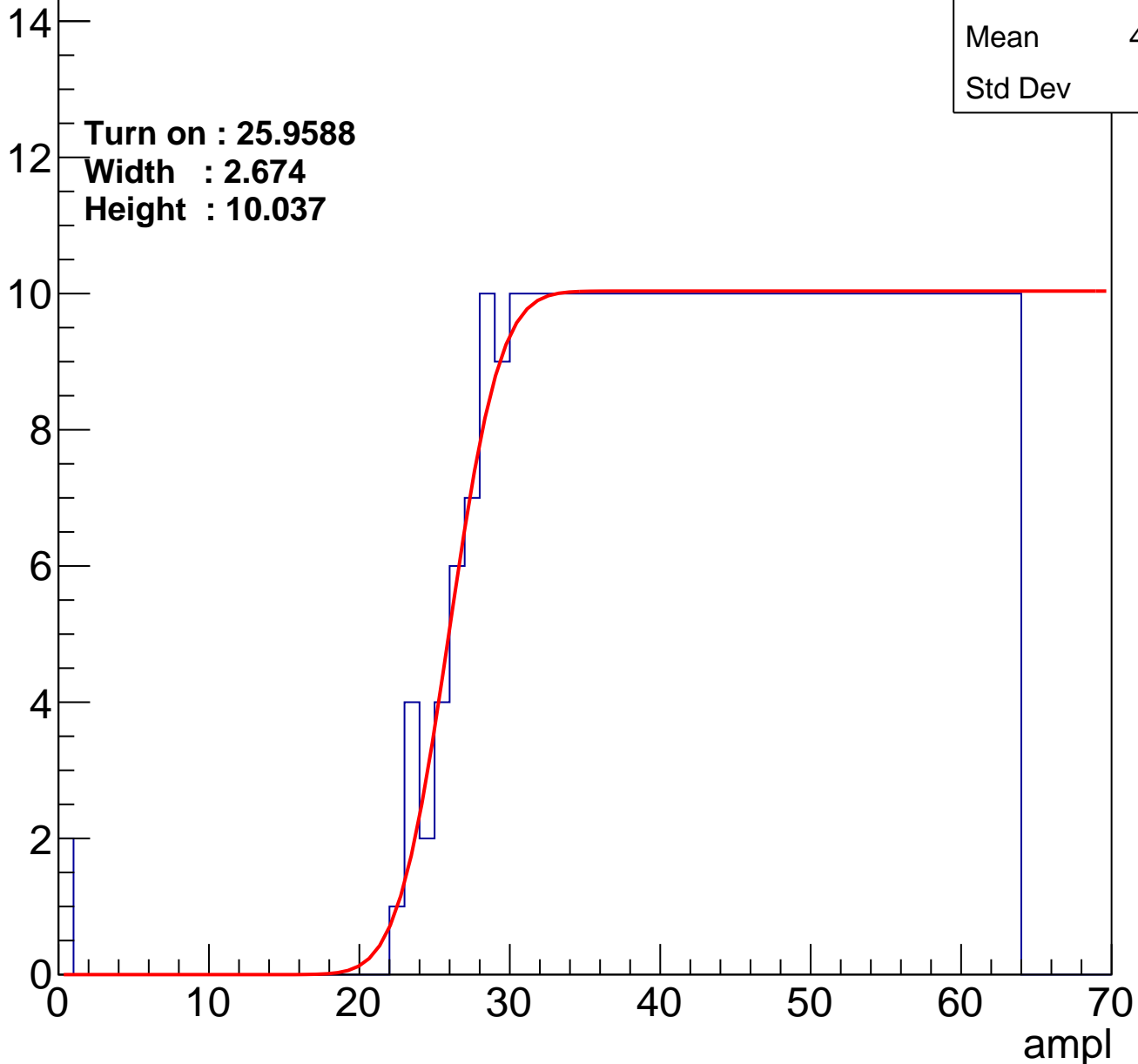
Entries	385
Mean	44.05
Std Dev	11.6

Turn on : 25.9588

Width : 2.674

Height : 10.037

Entry



B1L000S, U15-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.87
Std Dev	11.96

Turn on : 25.9025

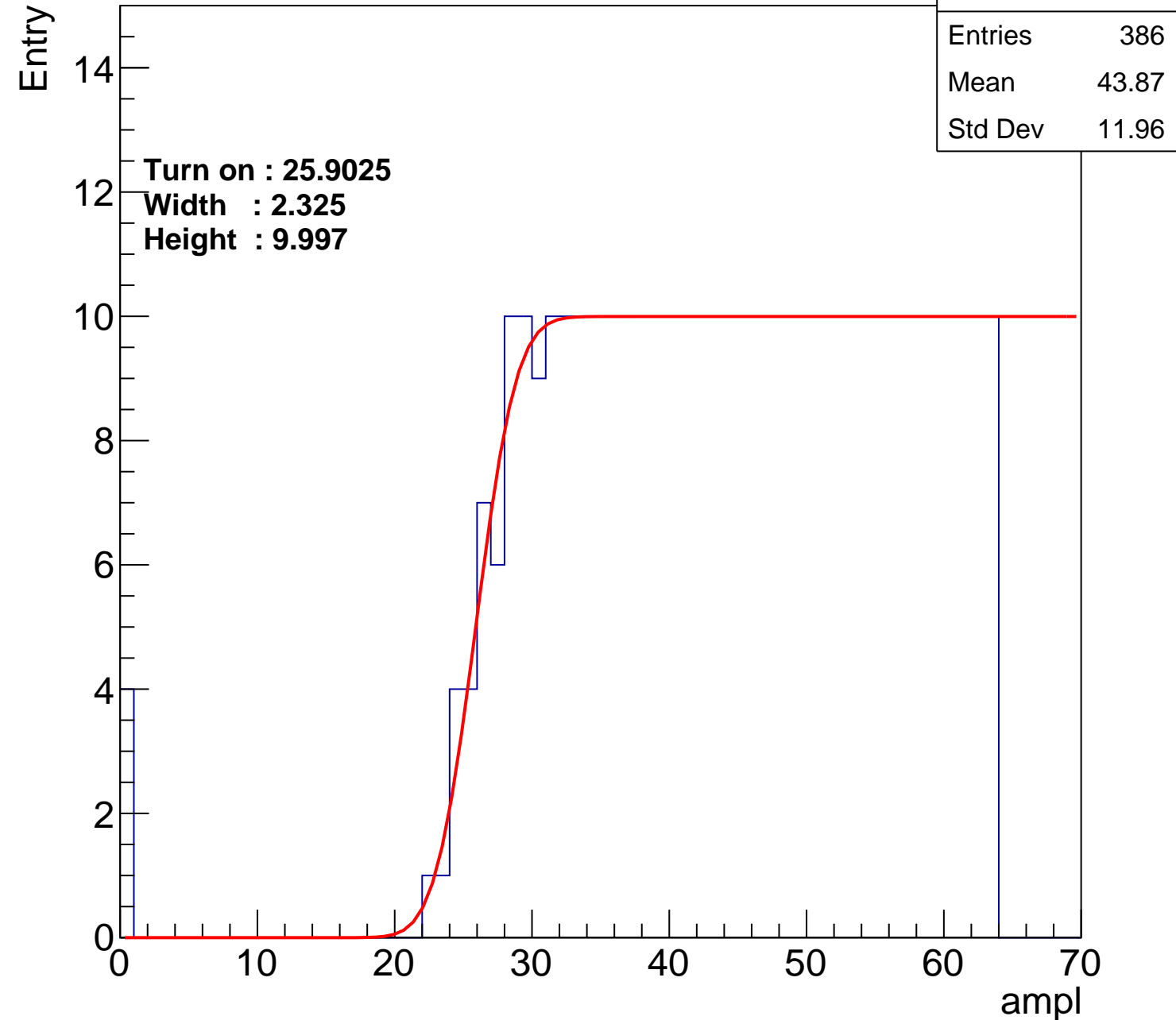
Width : 2.325

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch79

calib_packv5_042523_0143.root, FC#3, port B3

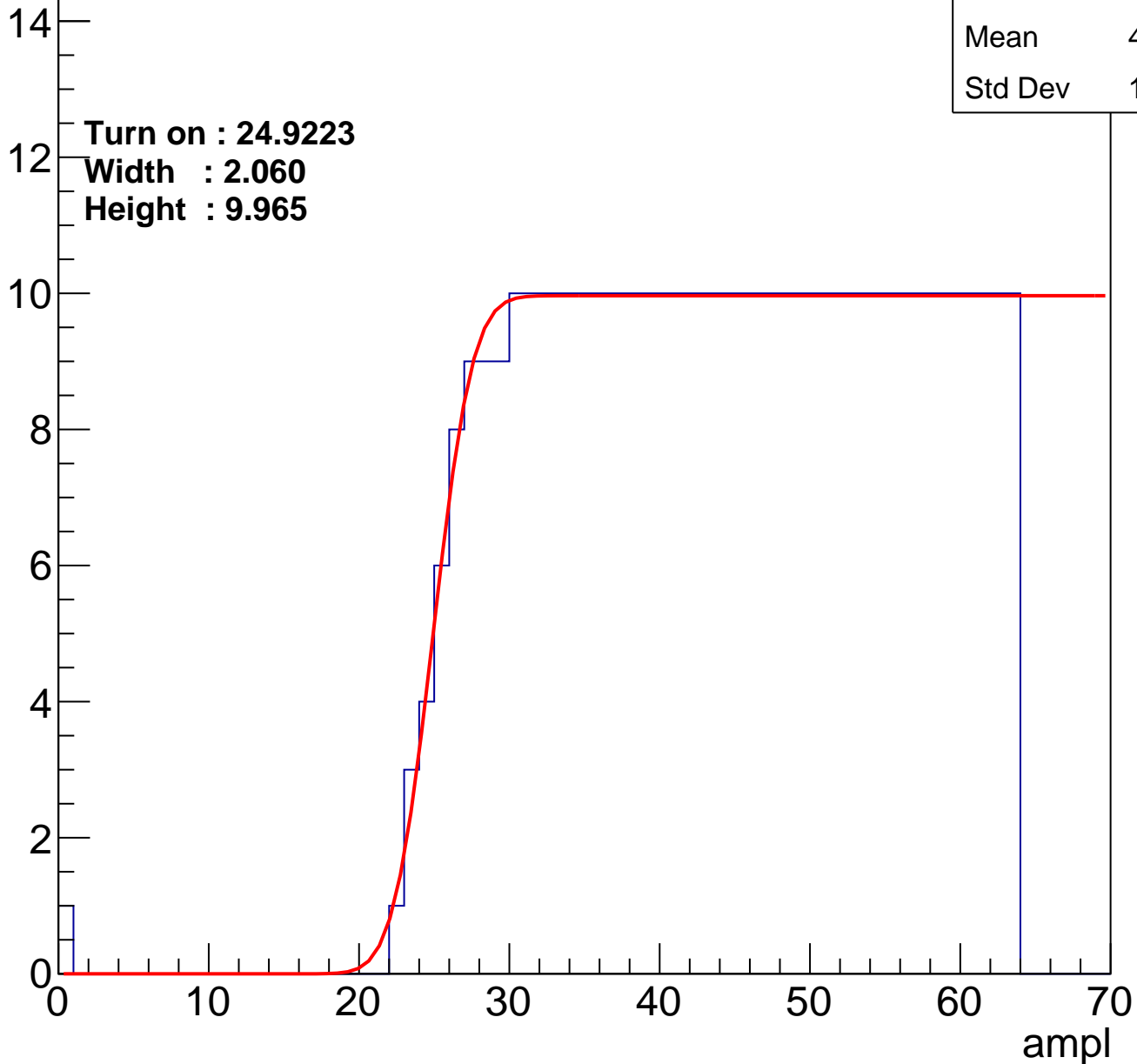
Entries	390
Mean	43.87
Std Dev	11.53

Turn on : 24.9223

Width : 2.060

Height : 9.965

Entry



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.4081
Width : 2.851
Height : 9.993



B1L000S, U15-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.1242

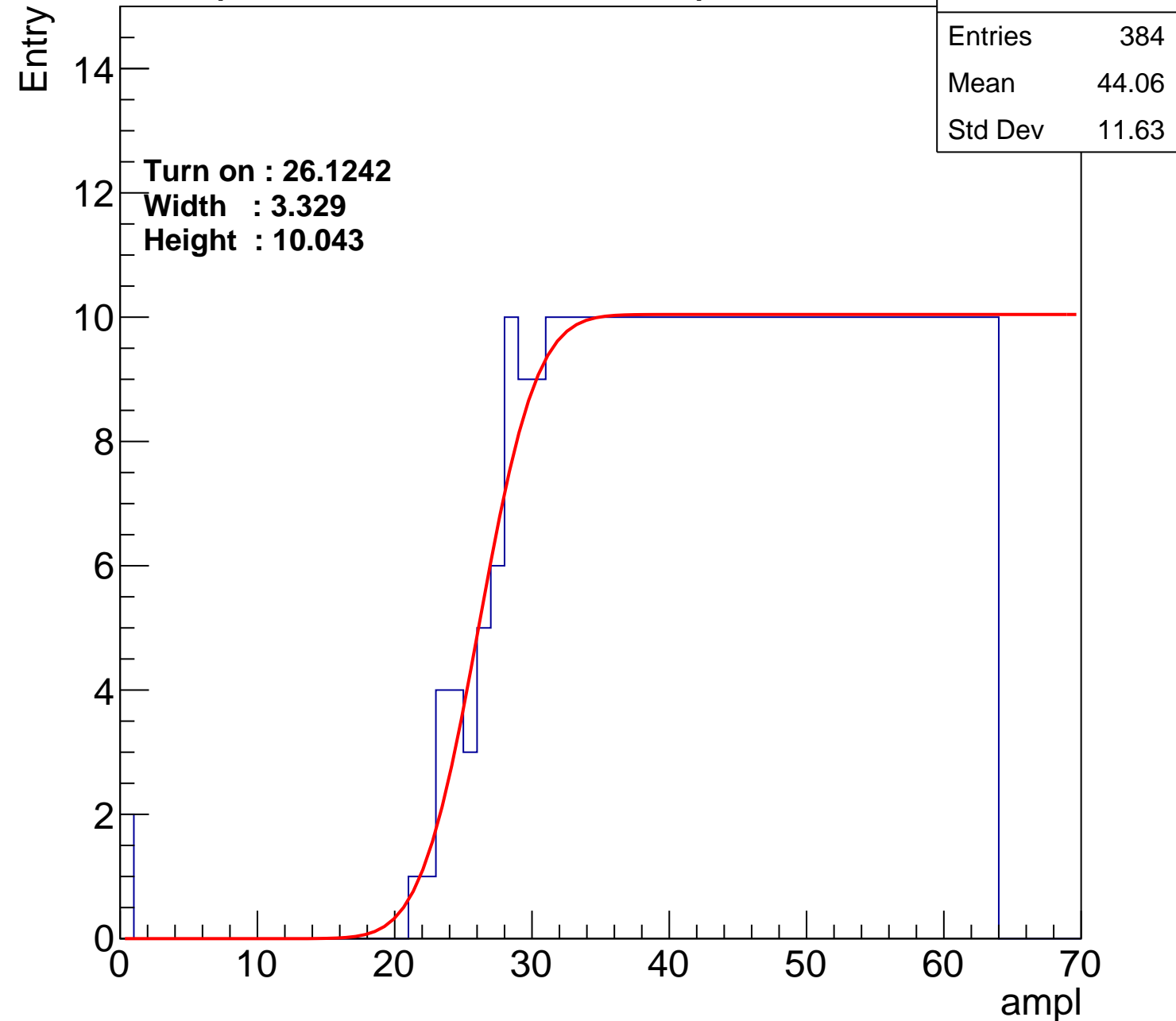
Width : 3.329

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.55
Std Dev	11.92

Turn on : 25.2399

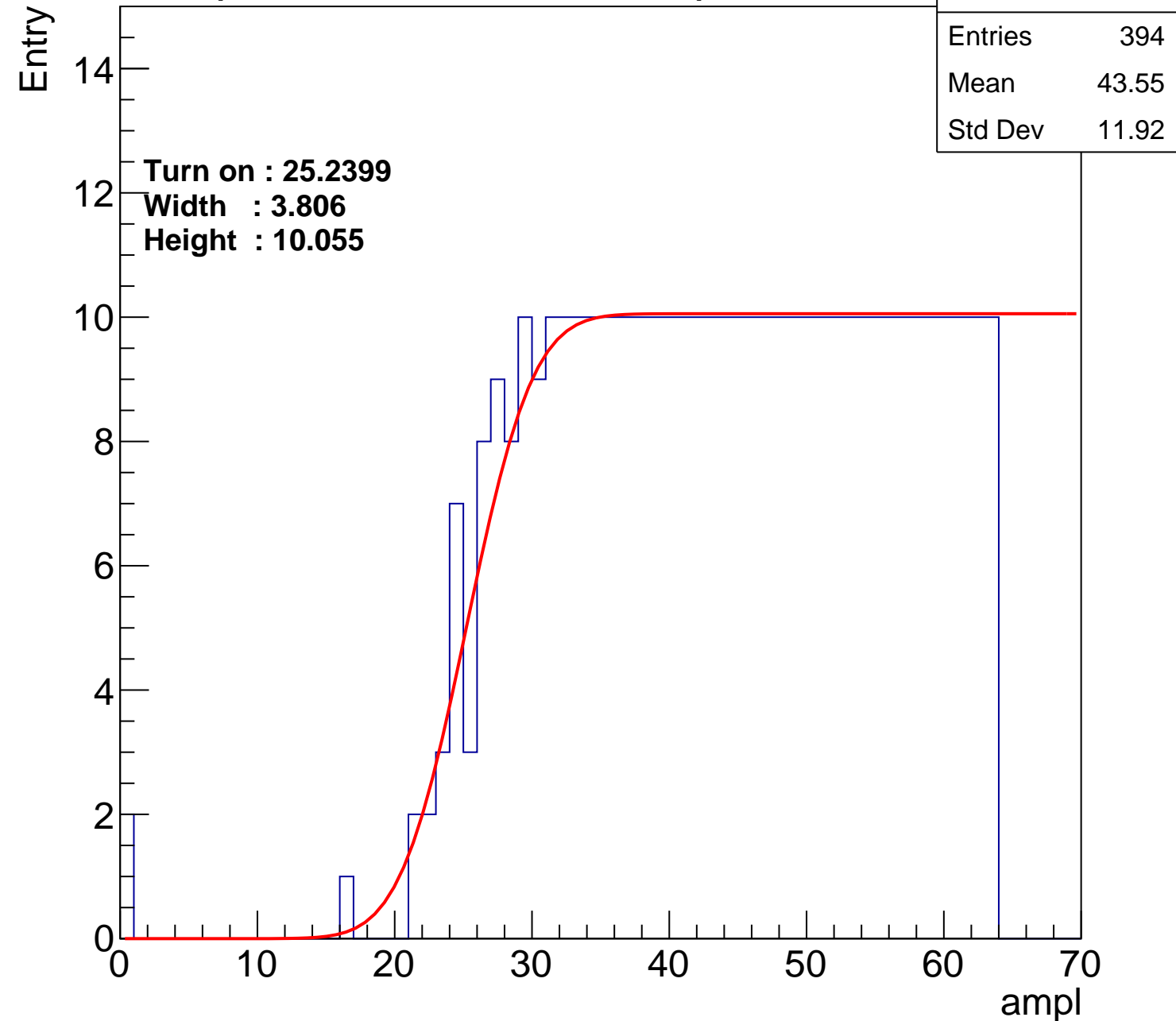
Width : 3.806

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.4121

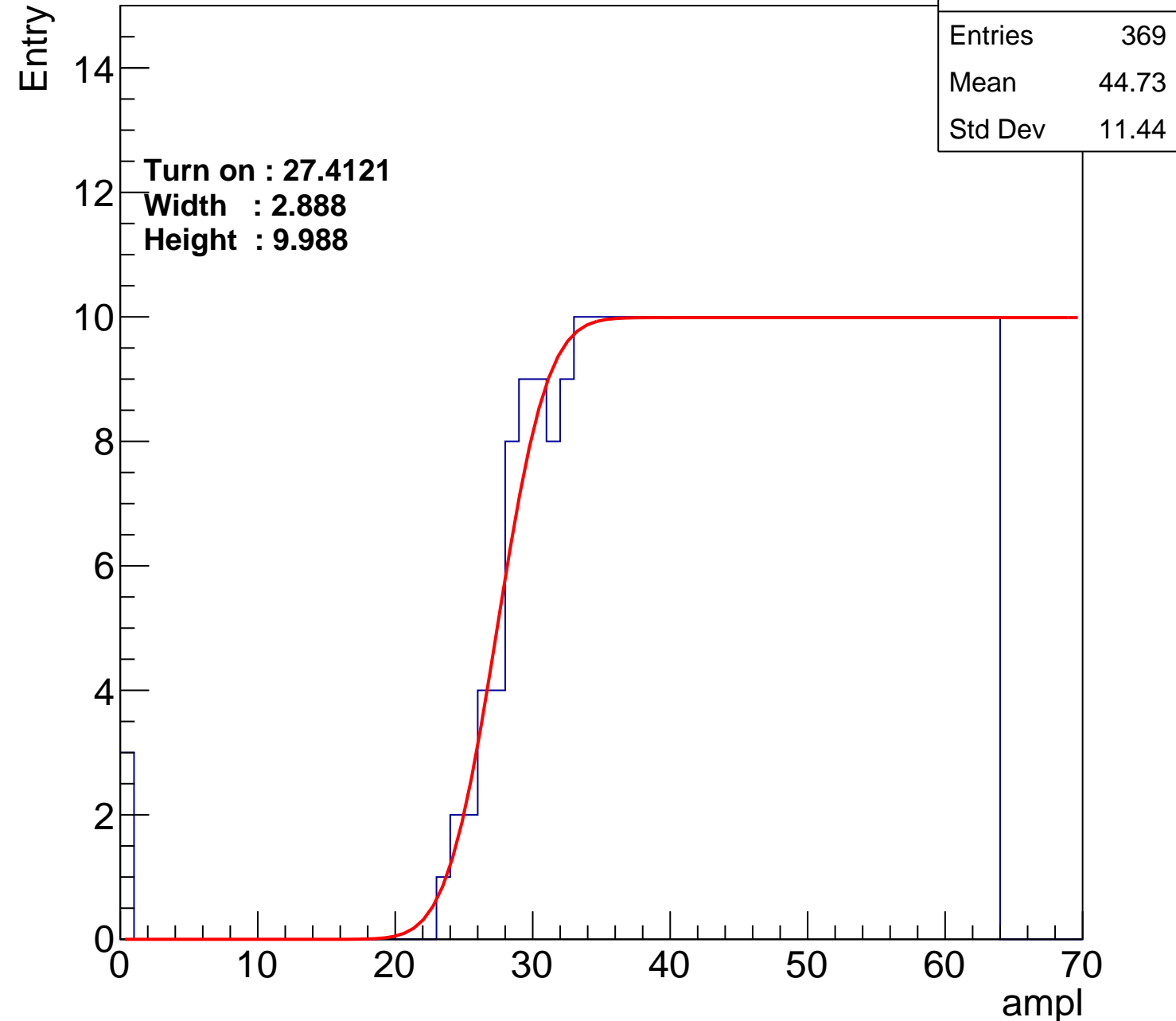
Width : 2.888

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch84

calib_packv5_042523_0143.root, FC#3, port B3

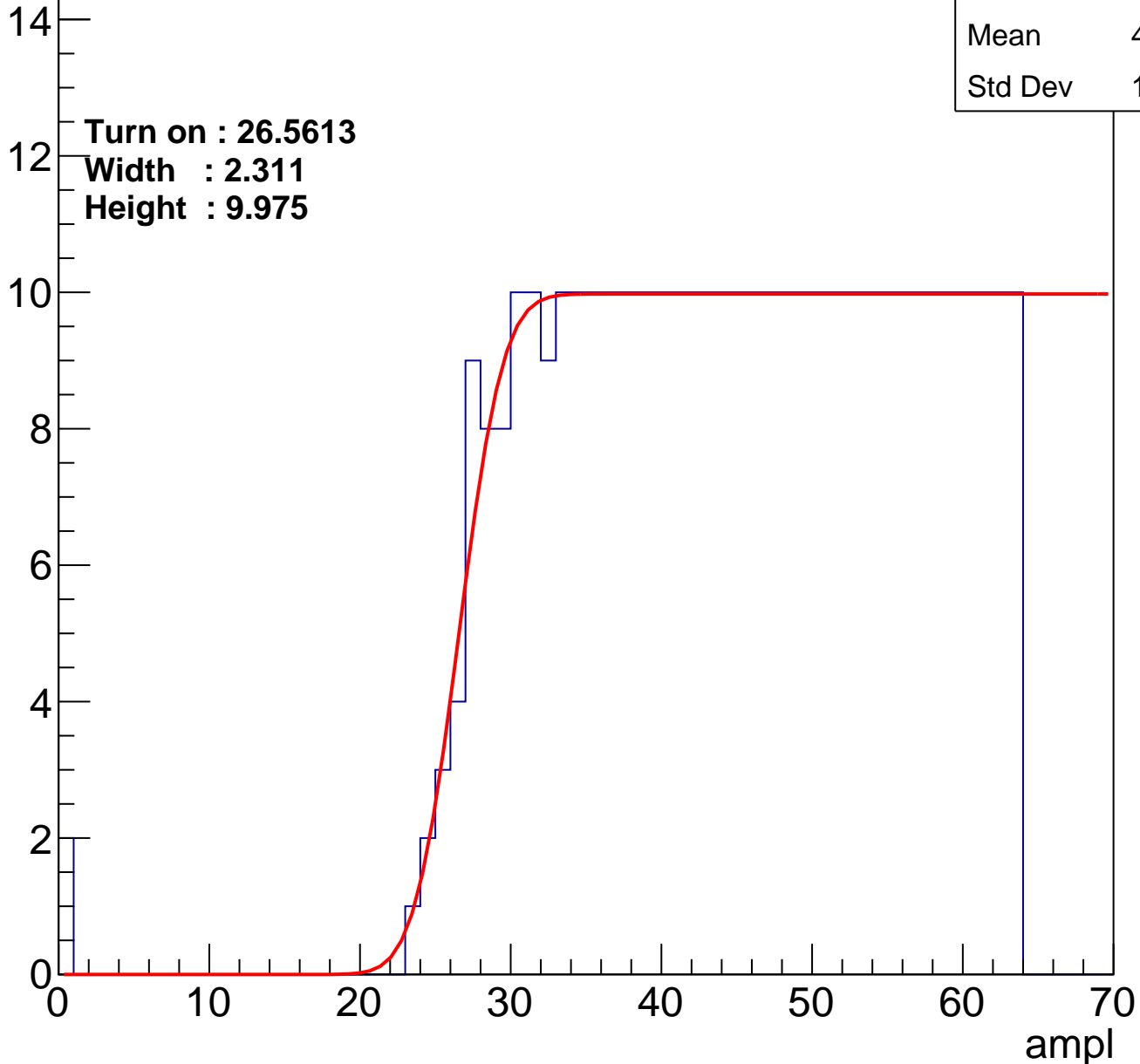
Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.5613

Width : 2.311

Height : 9.975

Entry



B1L000S, U15-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.3149

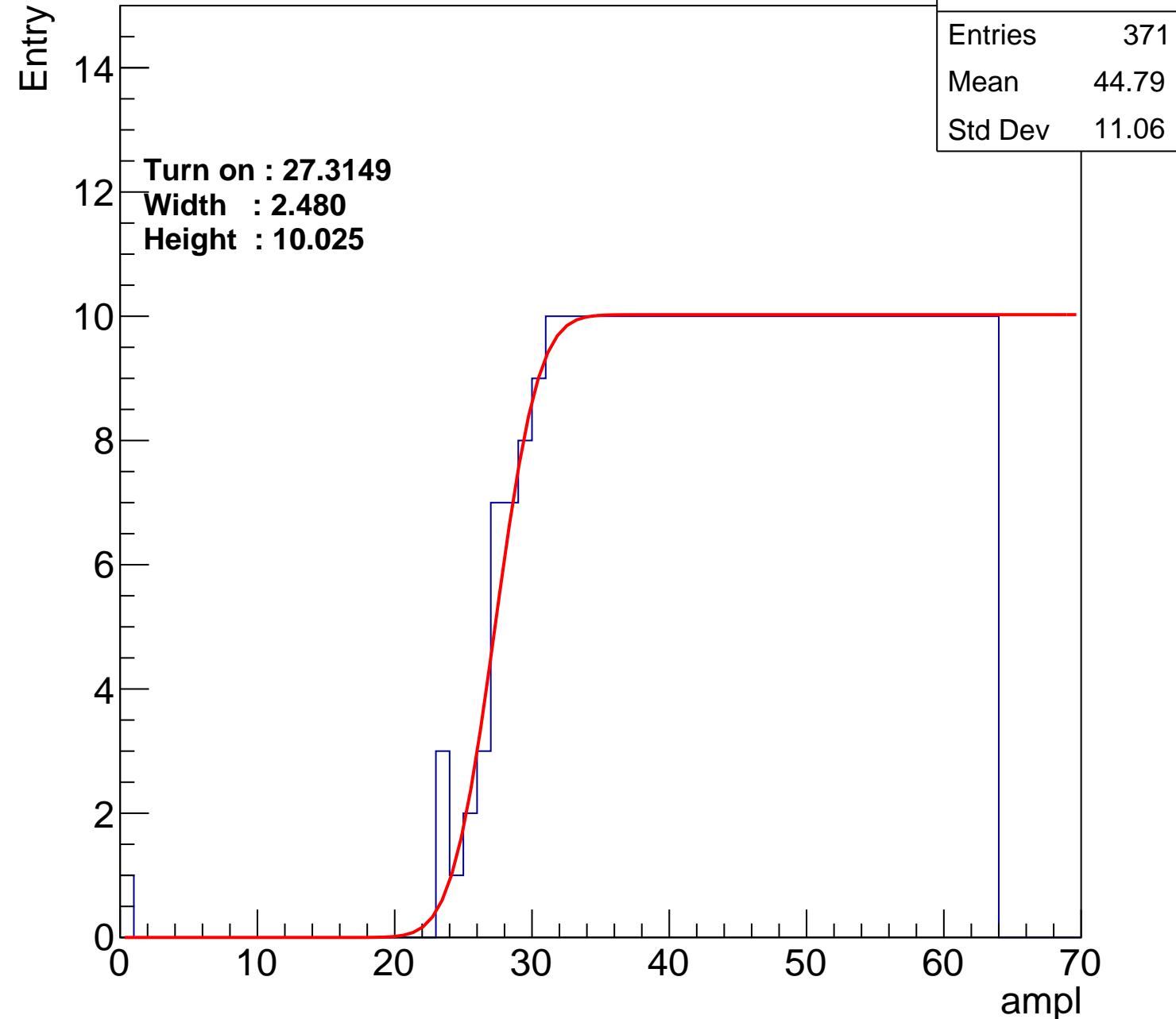
Width : 2.480

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.7
Std Dev	11.71

Turn on : 25.2398

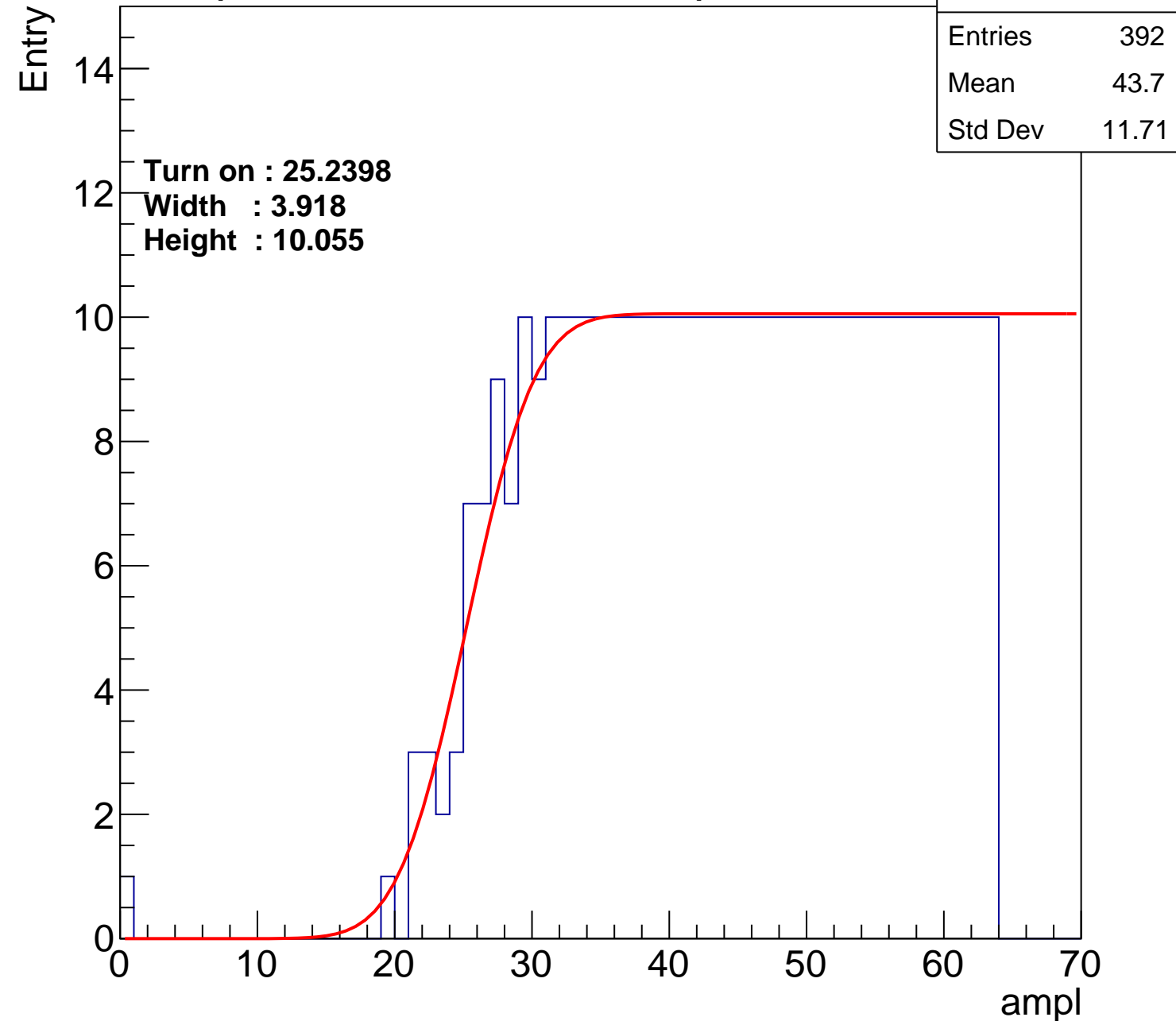
Width : 3.918

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.57
Std Dev	12

Turn on : 25.1509

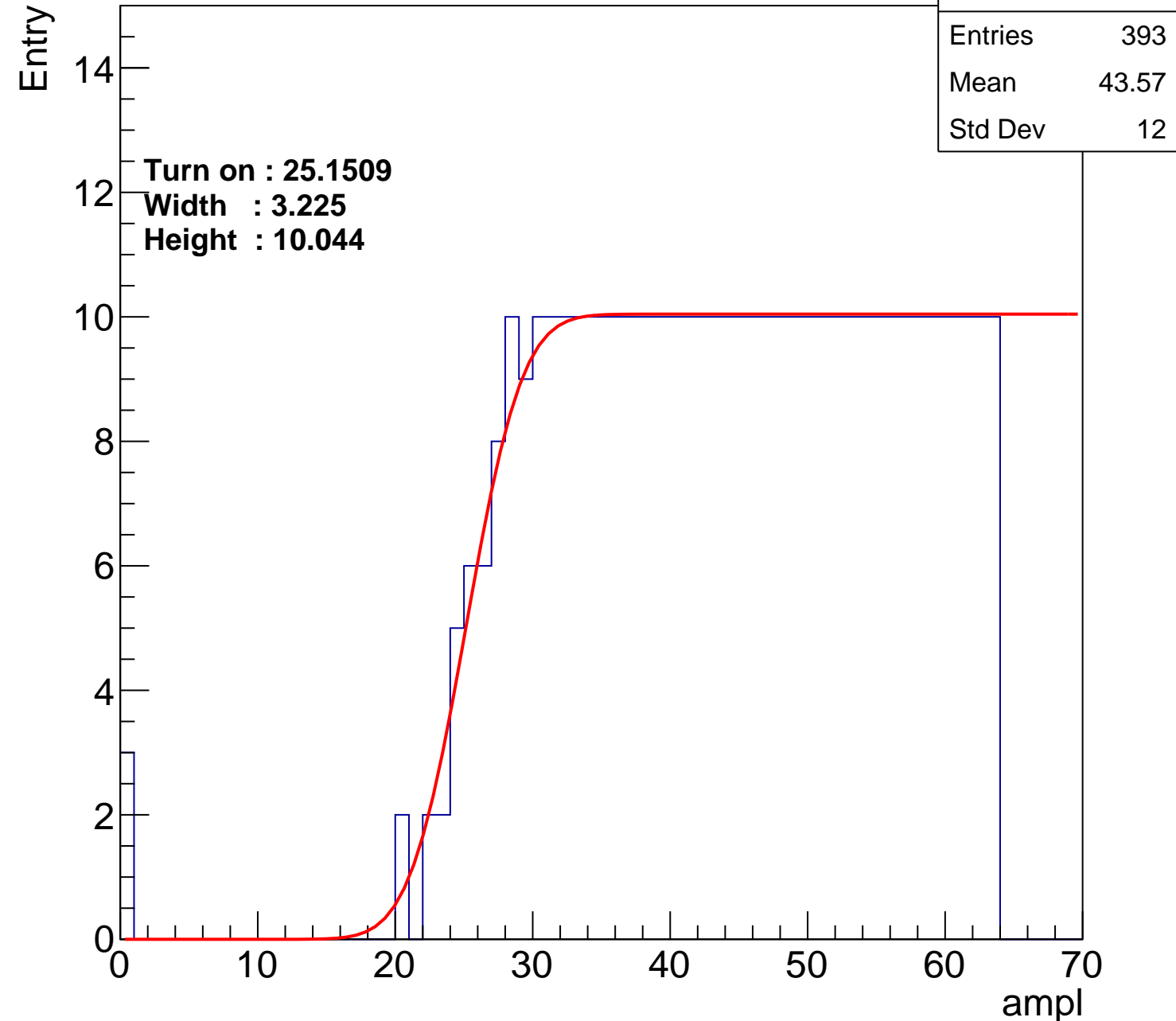
Width : 3.225

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.95
Std Dev	11.77

Turn on : 25.8332

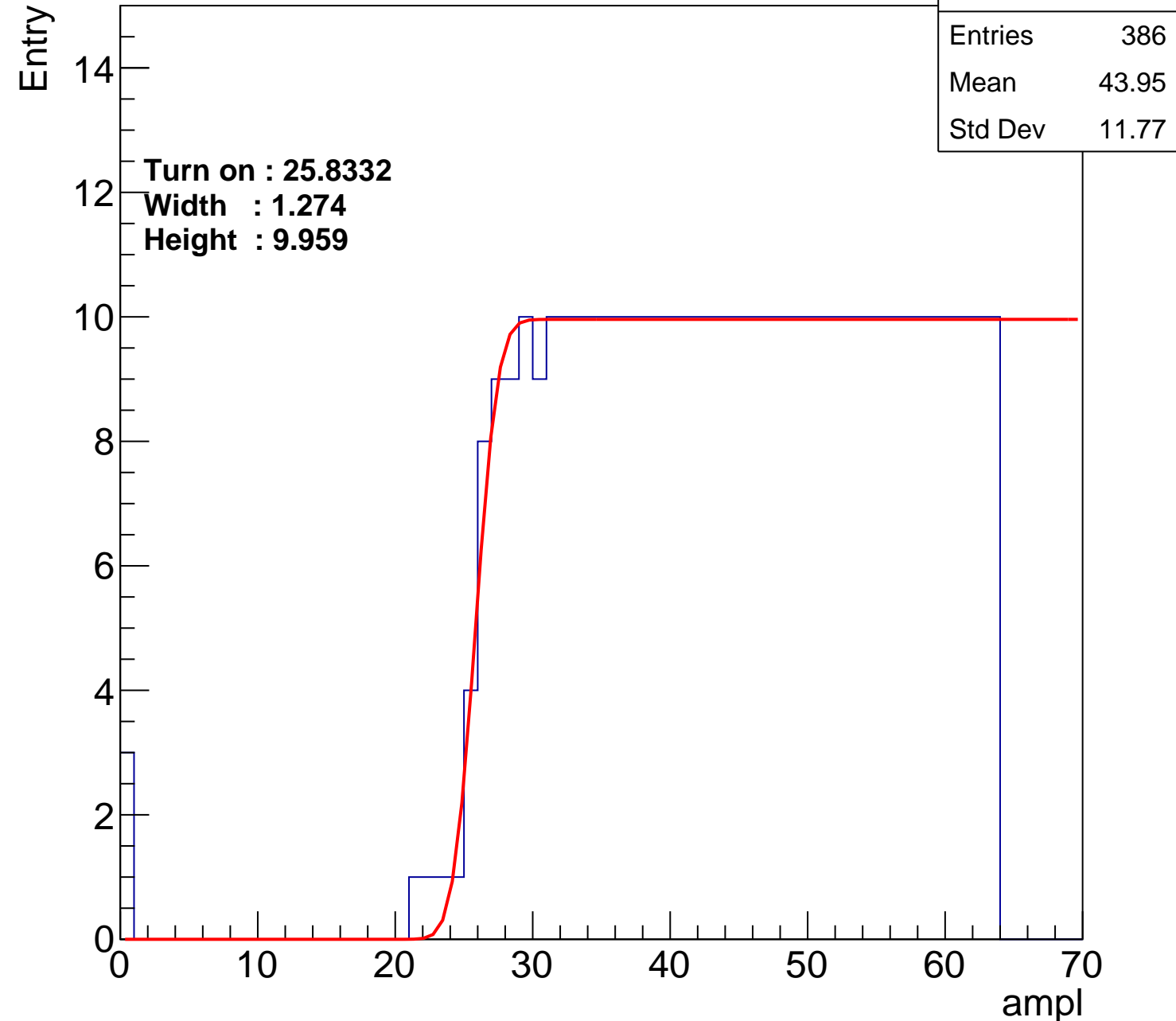
Width : 1.274

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch89

calib_packv5_042523_0143.root, FC#3, port B3

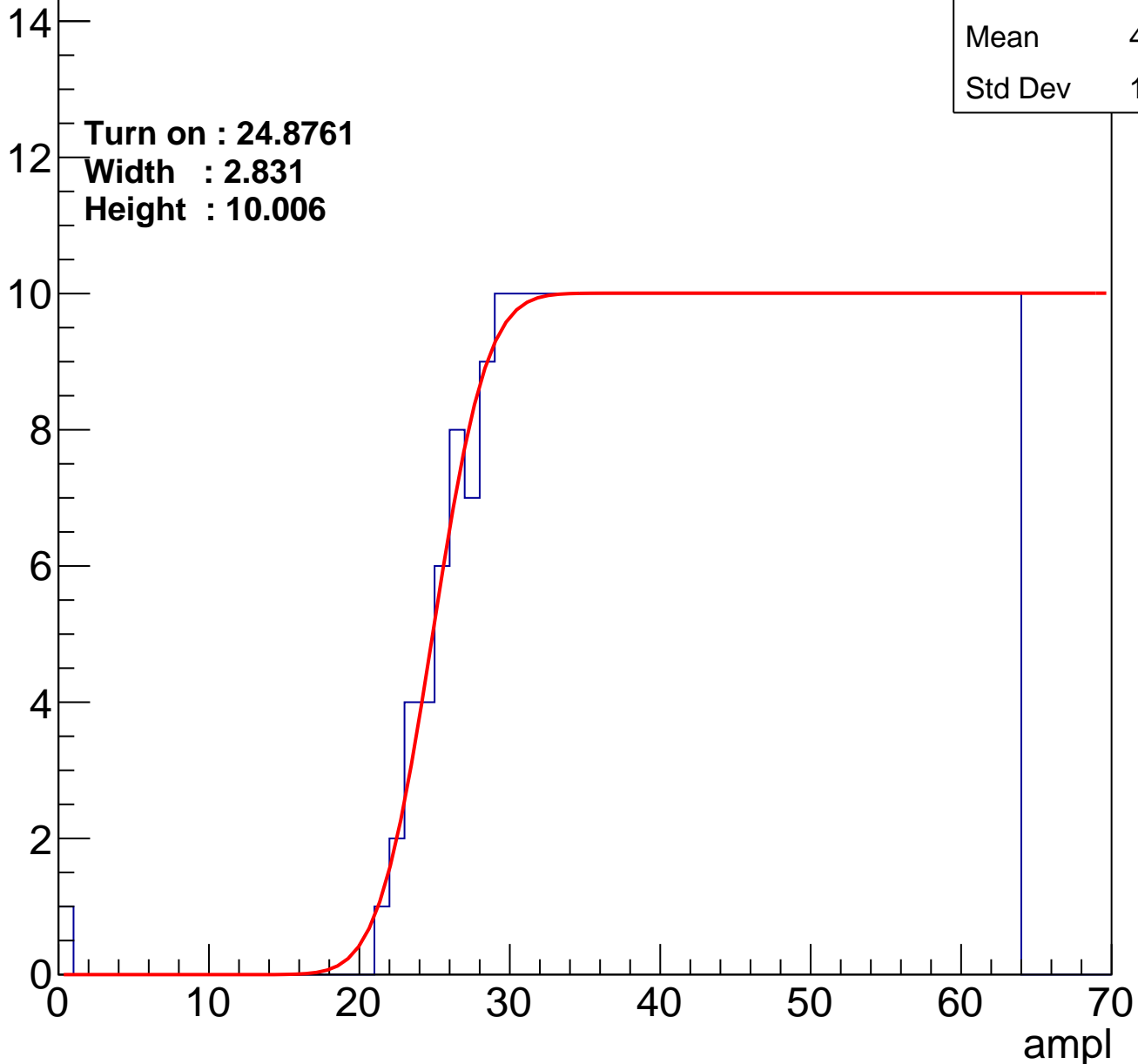
Entries	392
Mean	43.76
Std Dev	11.62

Turn on : 24.8761

Width : 2.831

Height : 10.006

Entry



B1L000S, U15-ch90

calib_packv5_042523_0143.root, FC#3, port B3

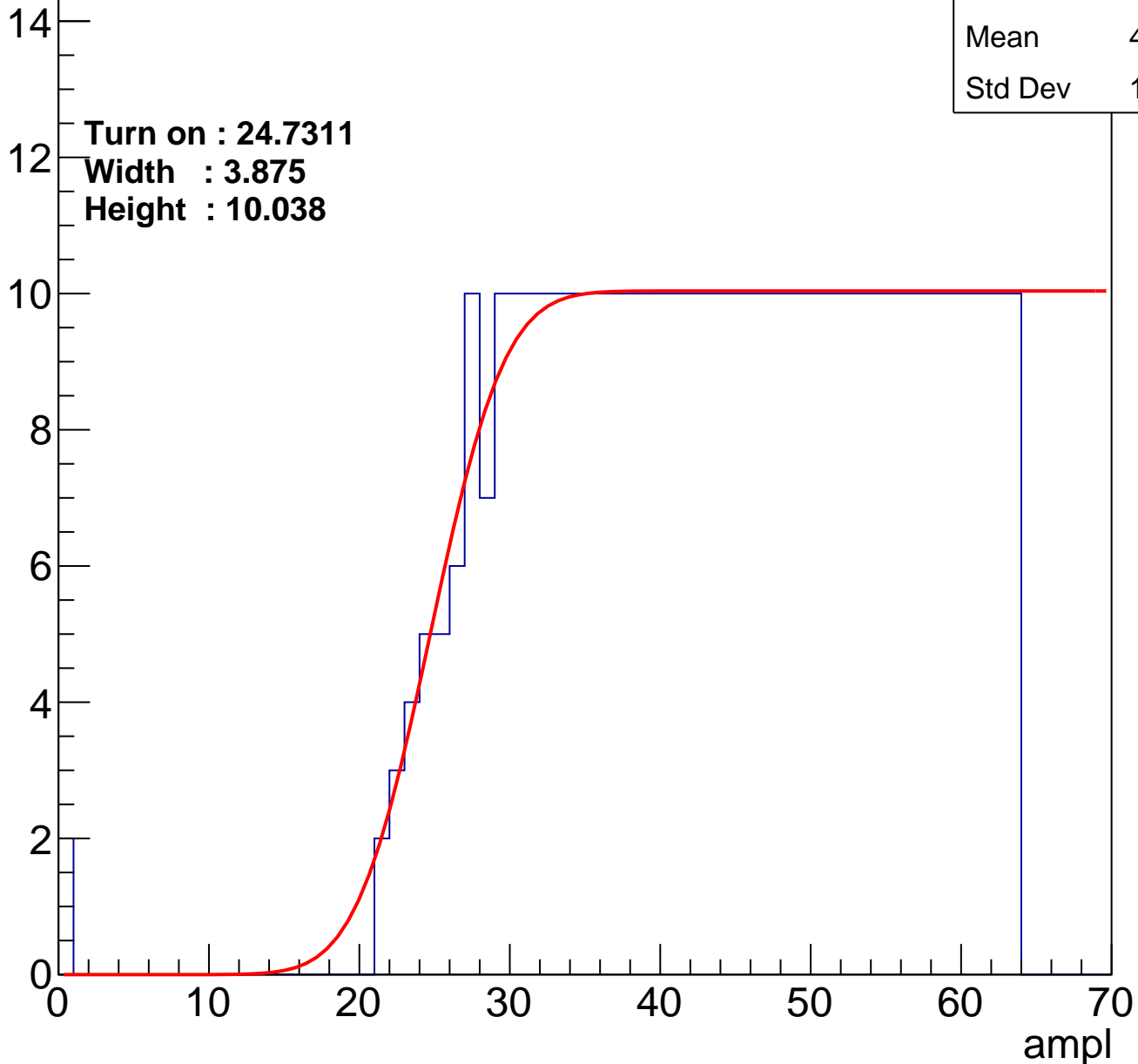
Entries	394
Mean	43.57
Std Dev	11.88

Turn on : 24.7311

Width : 3.875

Height : 10.038

Entry



B1L000S, U15-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.7823

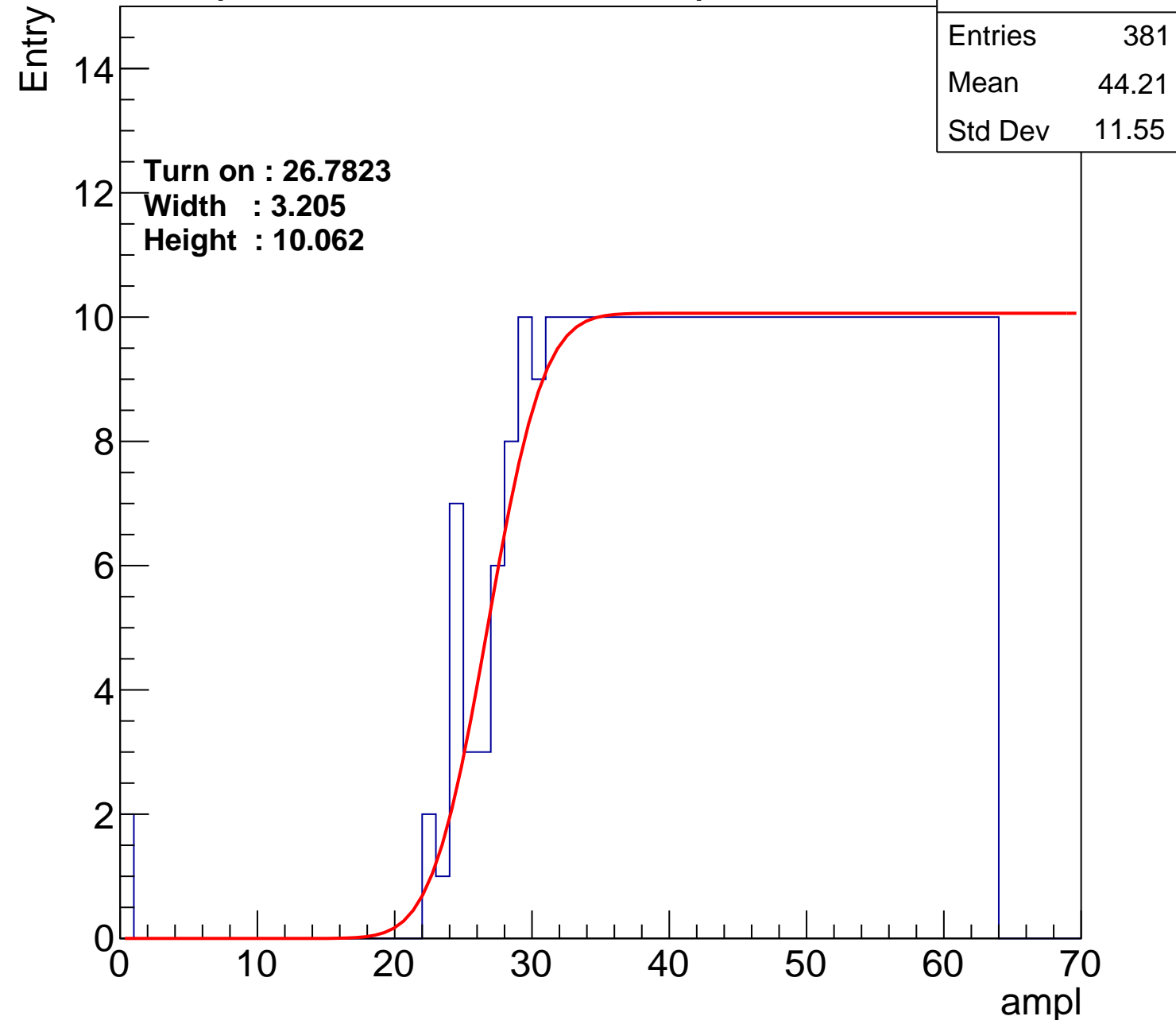
Width : 3.205

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch92

calib_packv5_042523_0143.root, FC#3, port B3

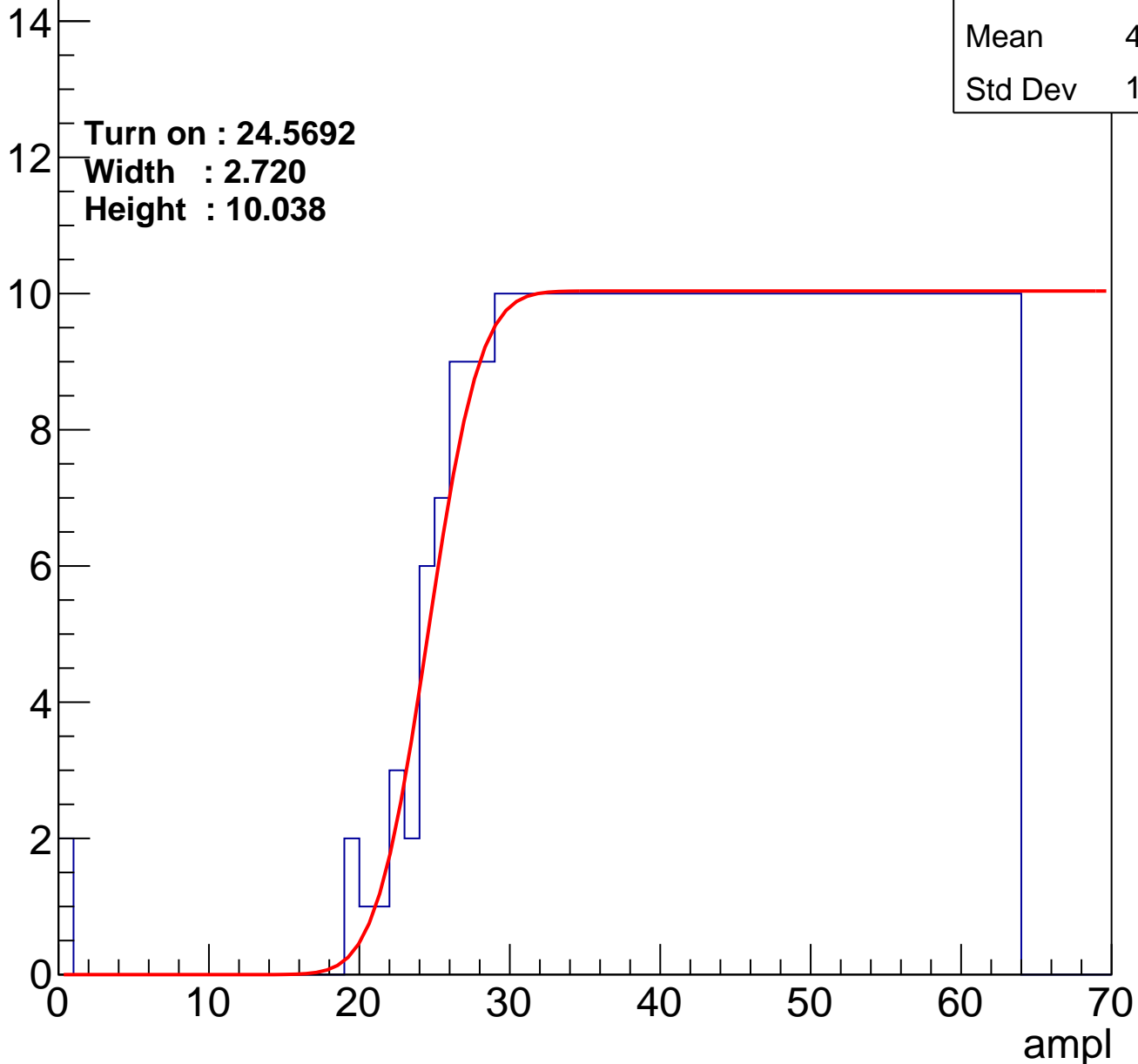
Entries	401
Mean	43.24
Std Dev	12.04

Turn on : 24.5692

Width : 2.720

Height : 10.038

Entry



B1L000S, U15-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.5375

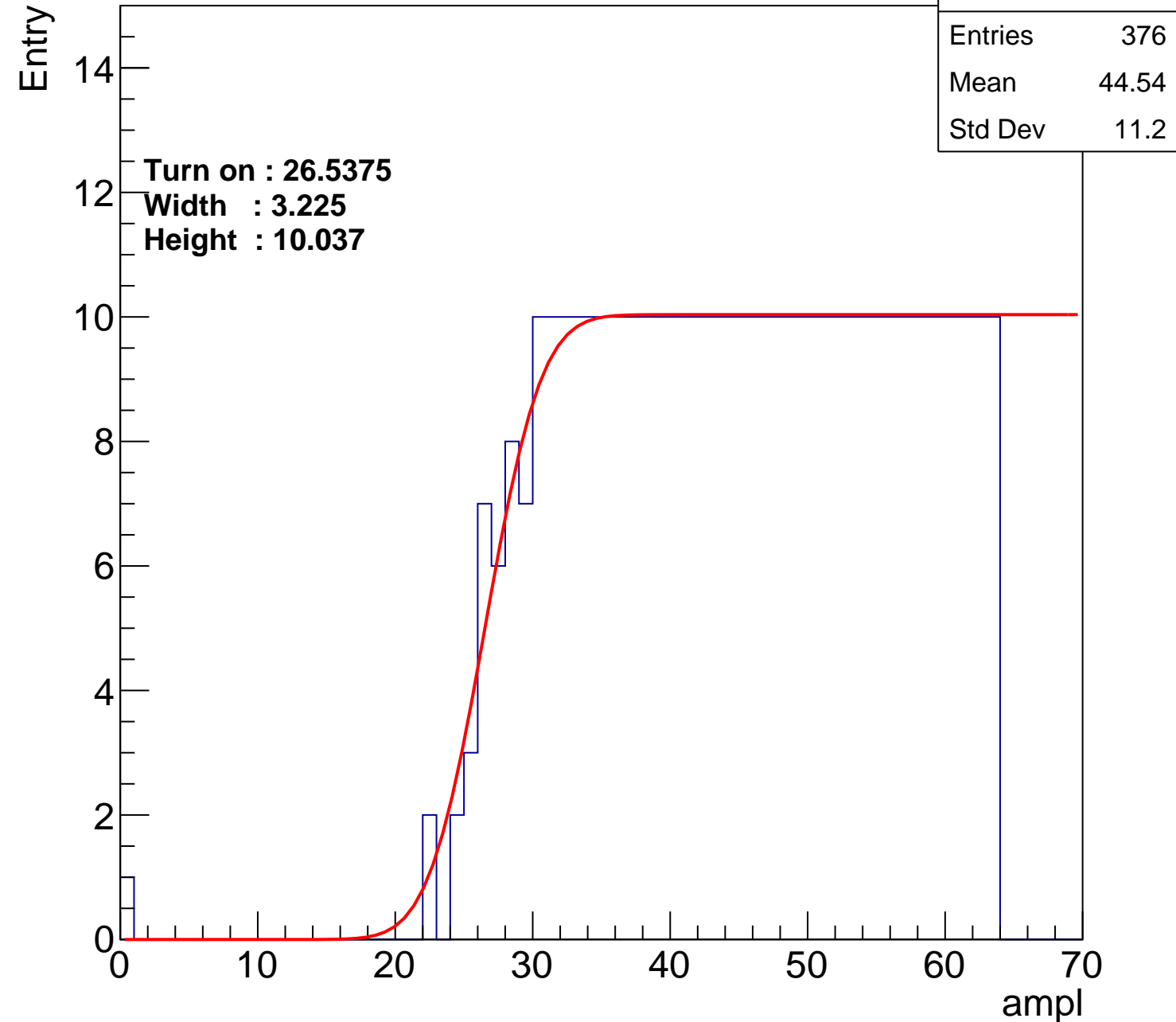
Width : 3.225

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.55
Std Dev	11.75

Turn on : 24.5664

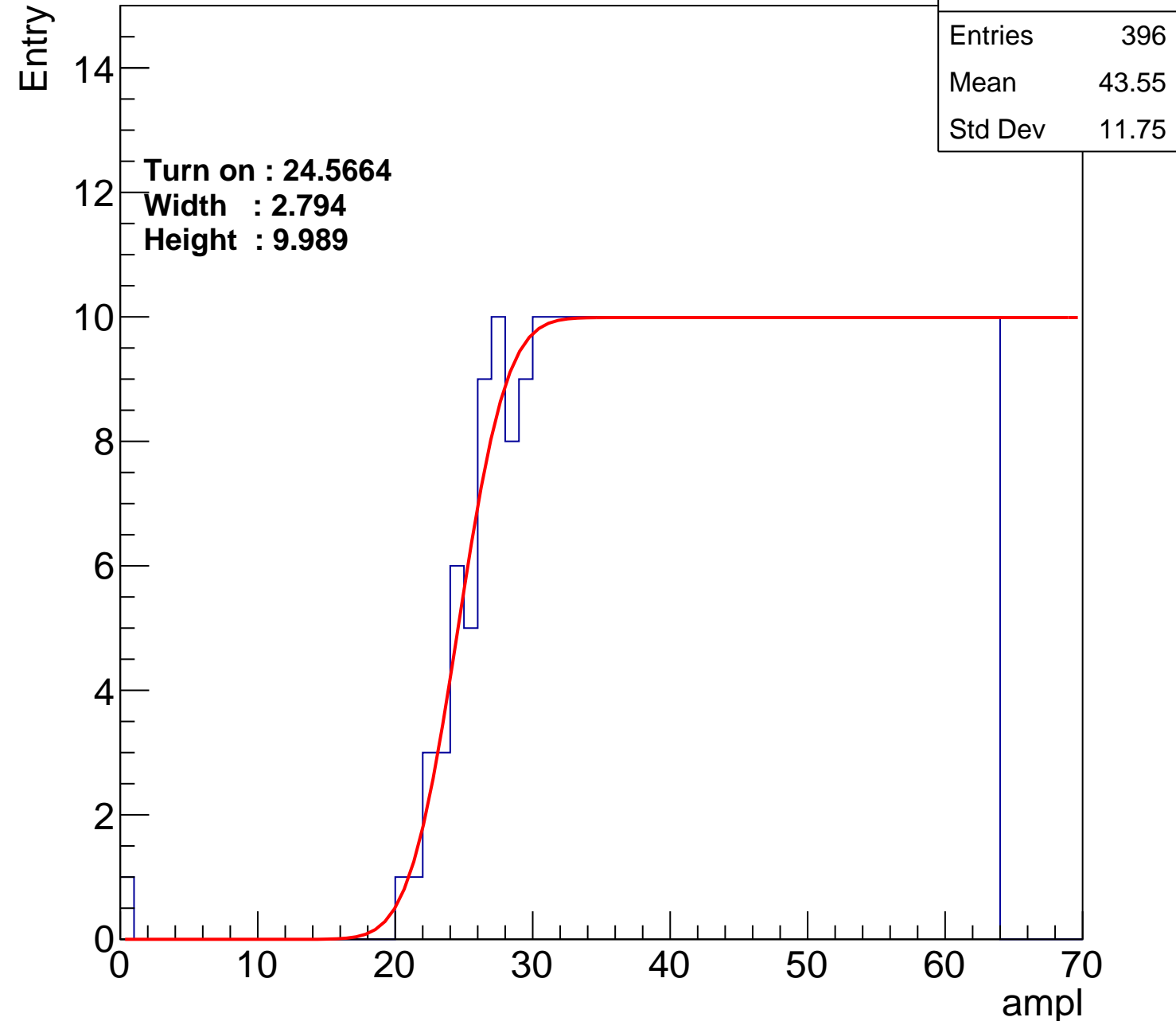
Width : 2.794

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch95

calib_packv5_042523_0143.root, FC#3, port B3

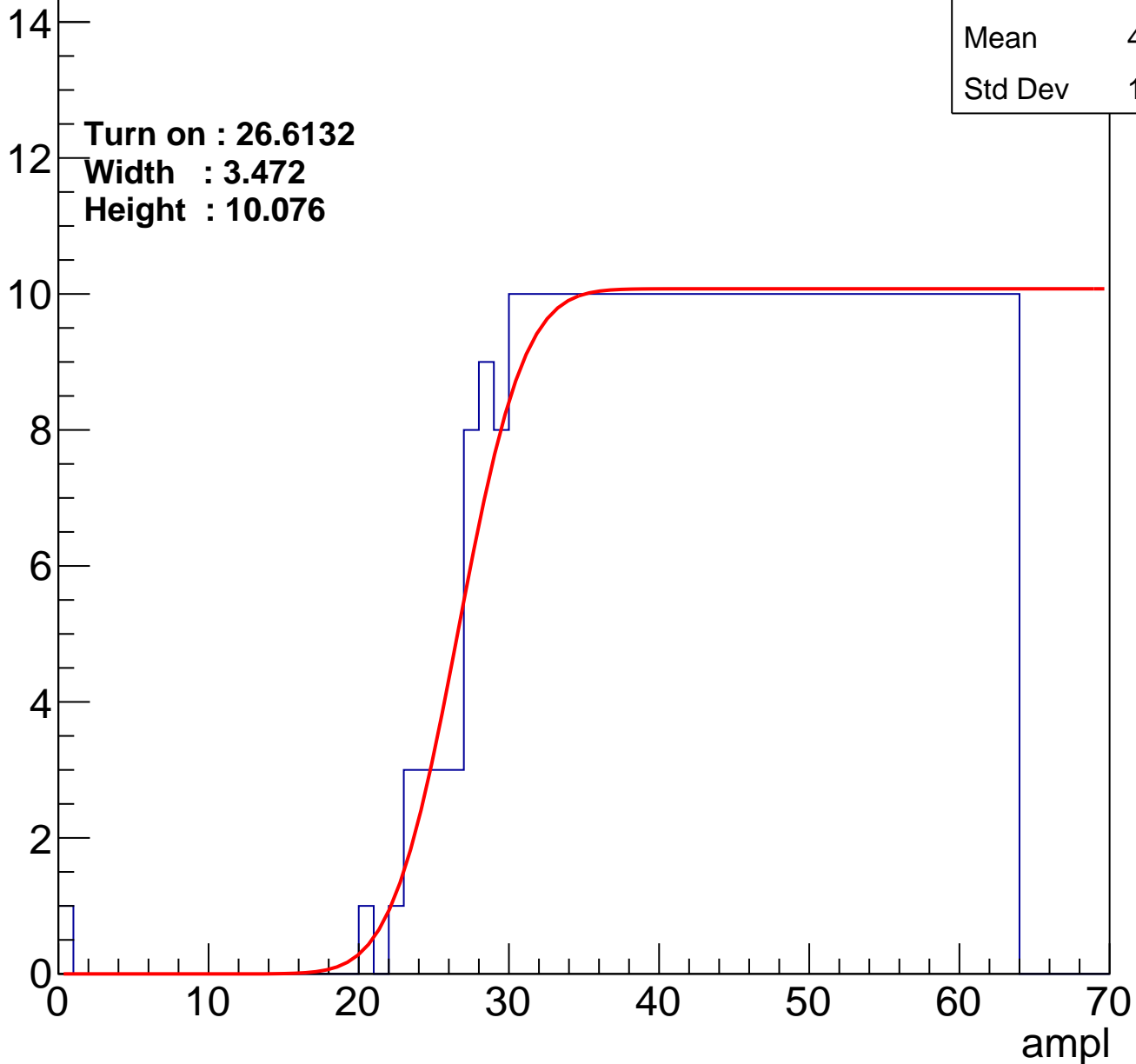
Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.6132

Width : 3.472

Height : 10.076

Entry



B1L000S, U15-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.83
Std Dev	11

Turn on : 27.3441

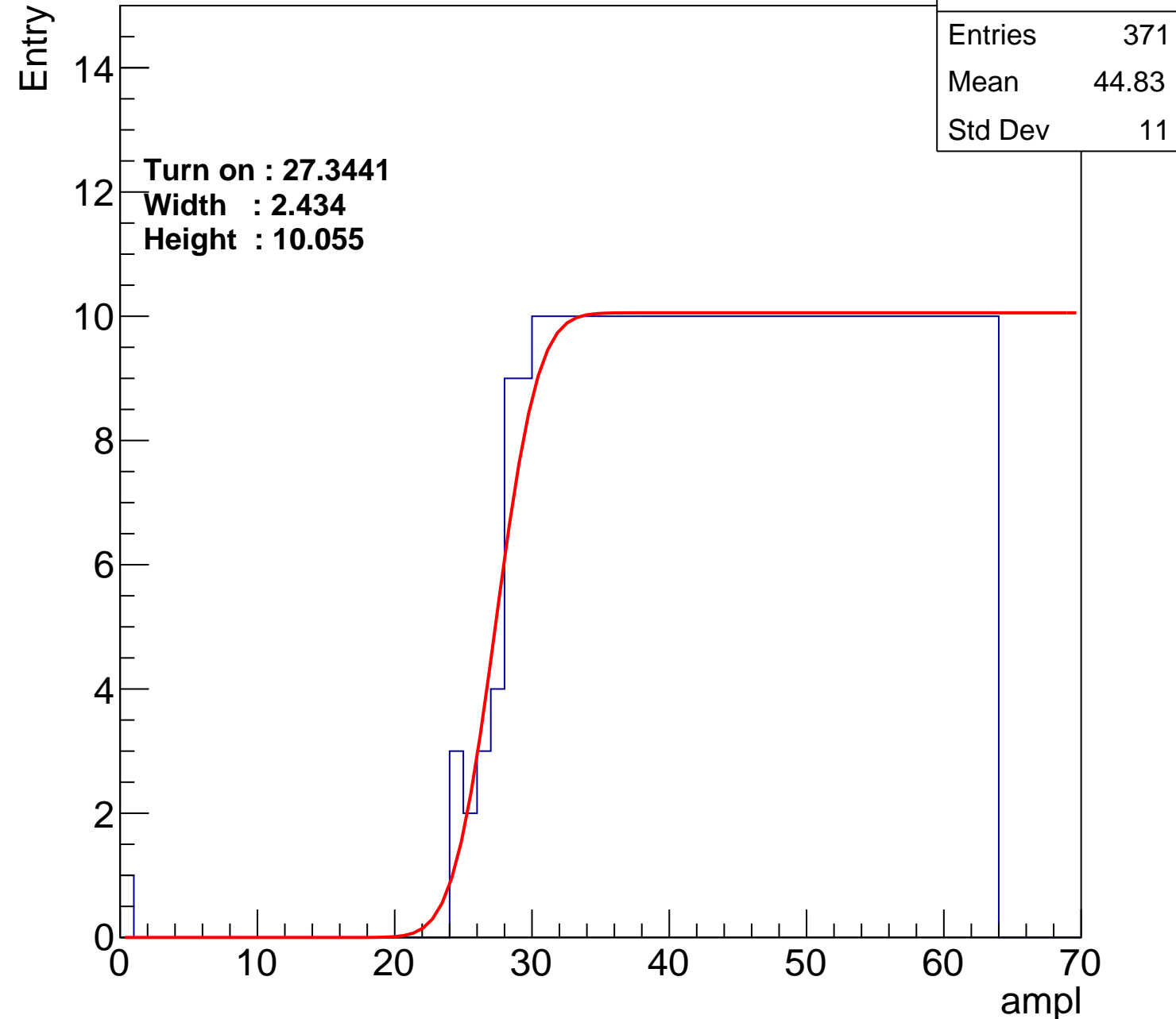
Width : 2.434

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch97

calib_packv5_042523_0143.root, FC#3, port B3

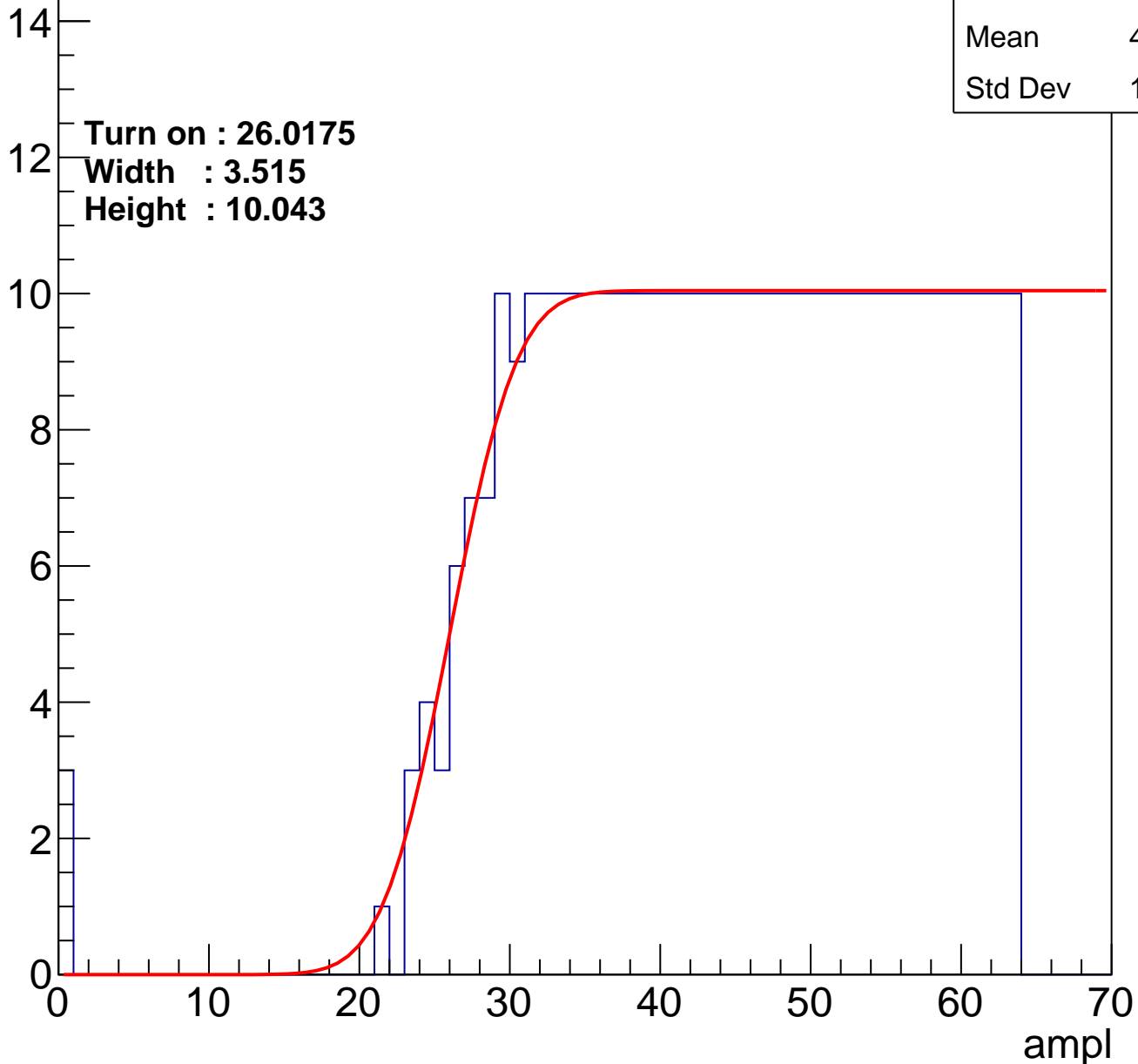
Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.0175

Width : 3.515

Height : 10.043

Entry



B1L000S, U15-ch98

calib_packv5_042523_0143.root, FC#3, port B3

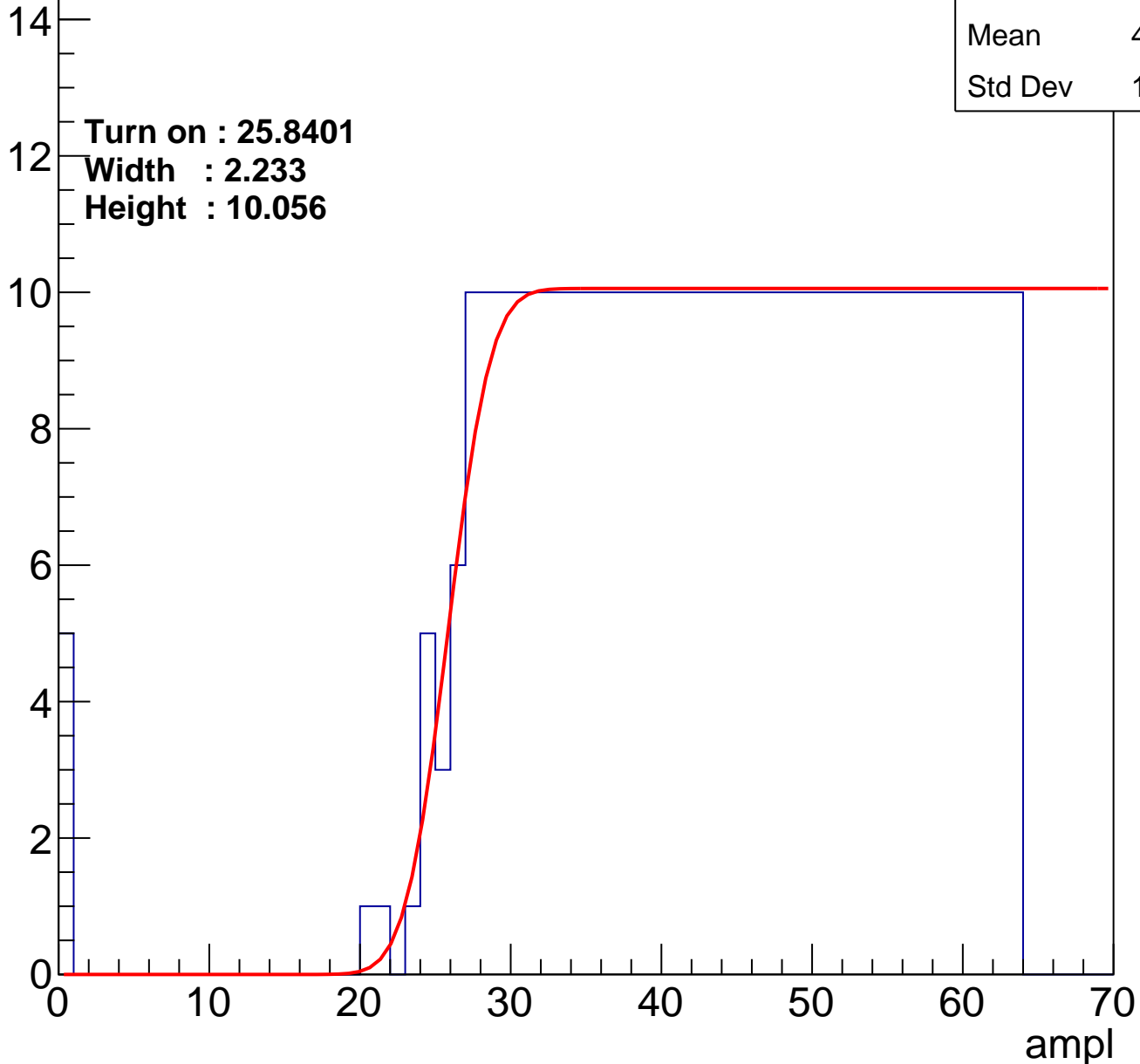
Entries	392
Mean	43.53
Std Dev	12.24

Turn on : 25.8401

Width : 2.233

Height : 10.056

Entry



B1L000S, U15-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 25.8268

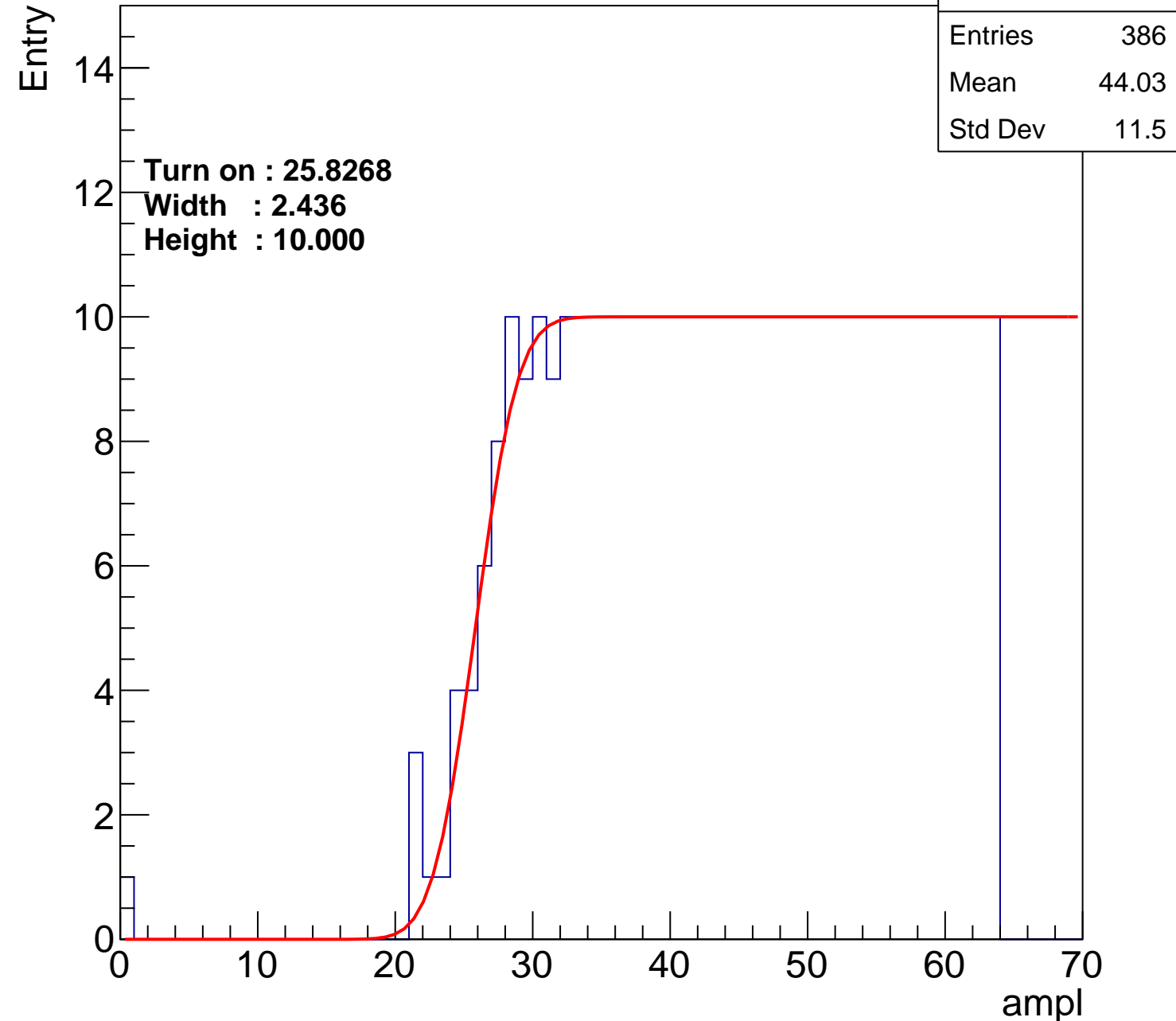
Width : 2.436

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.7
Std Dev	11.48

Turn on : 28.4209

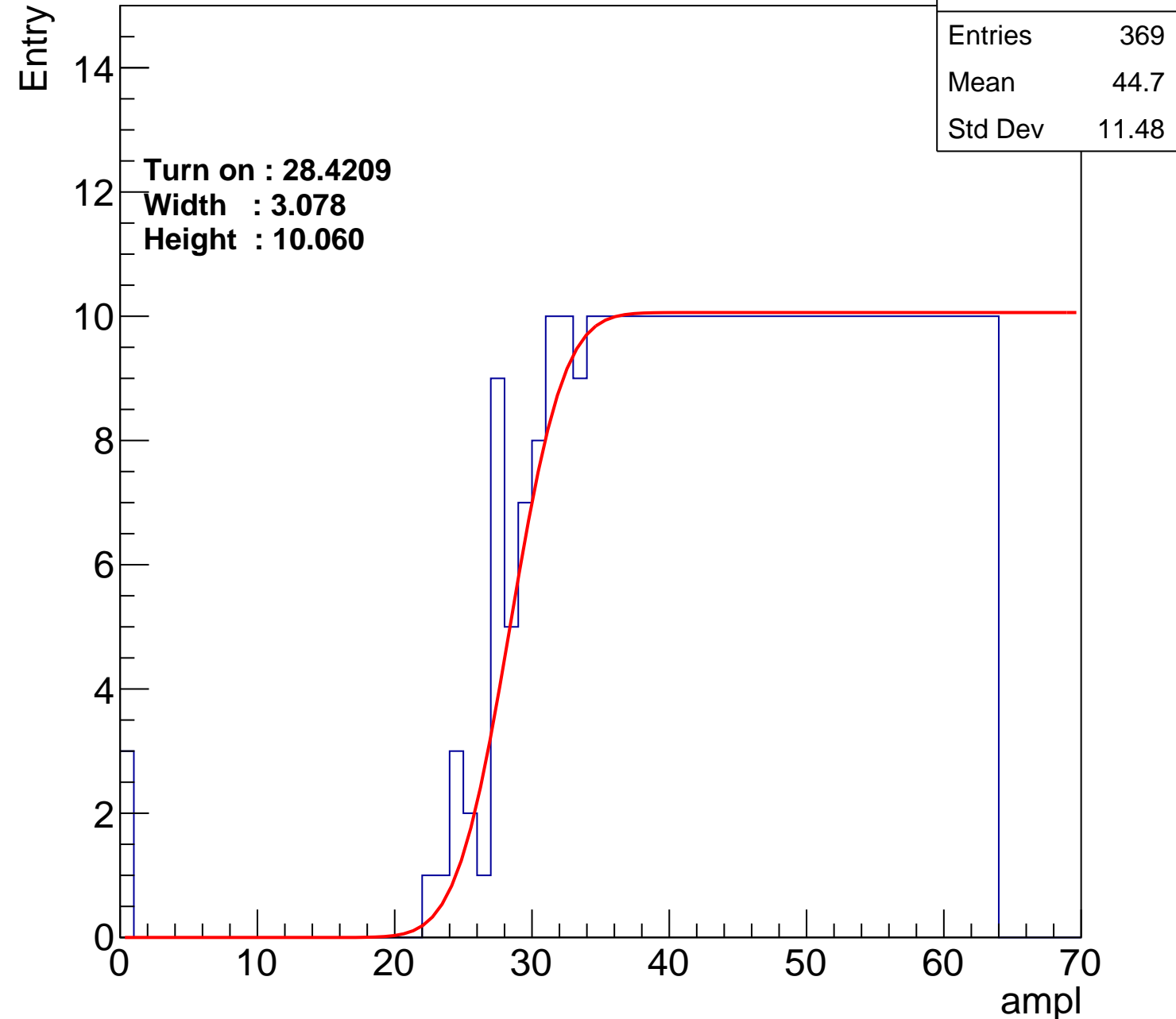
Width : 3.078

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.67
Std Dev	11.95

Turn on : 25.4274

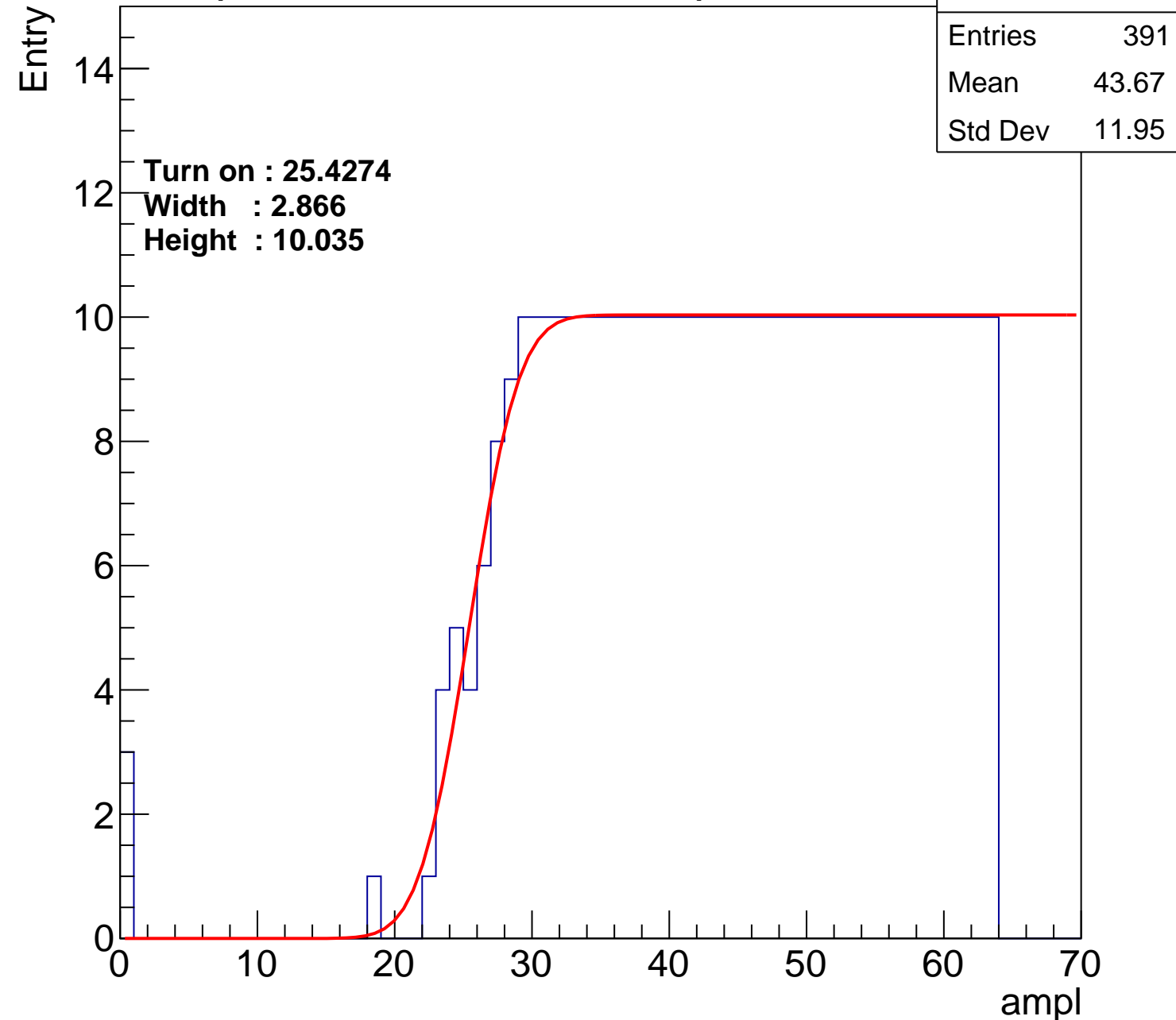
Width : 2.866

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.42
Std Dev	12.03

Turn on : 24.5710

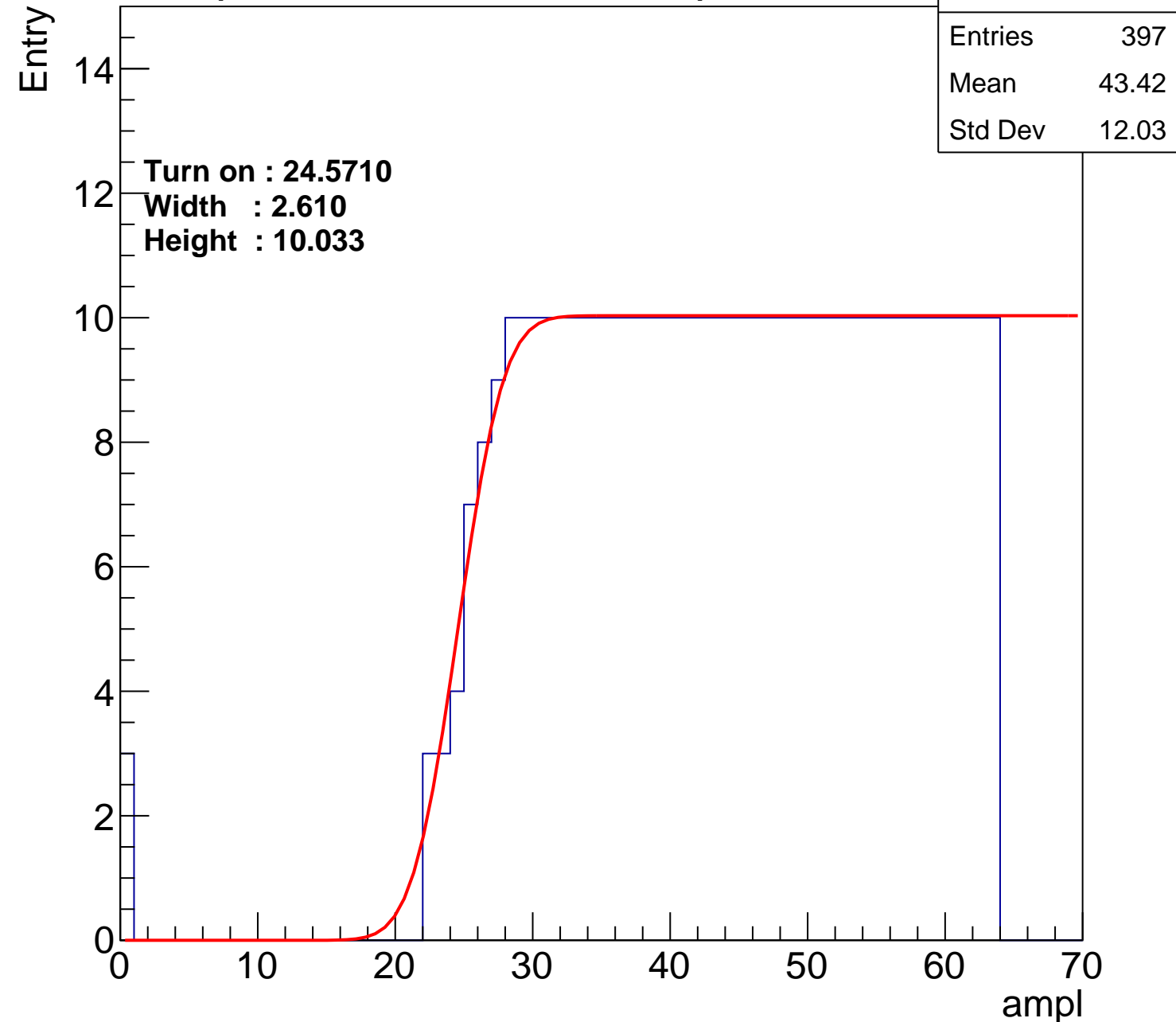
Width : 2.610

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch103

calib_packv5_042523_0143.root, FC#3, port B3

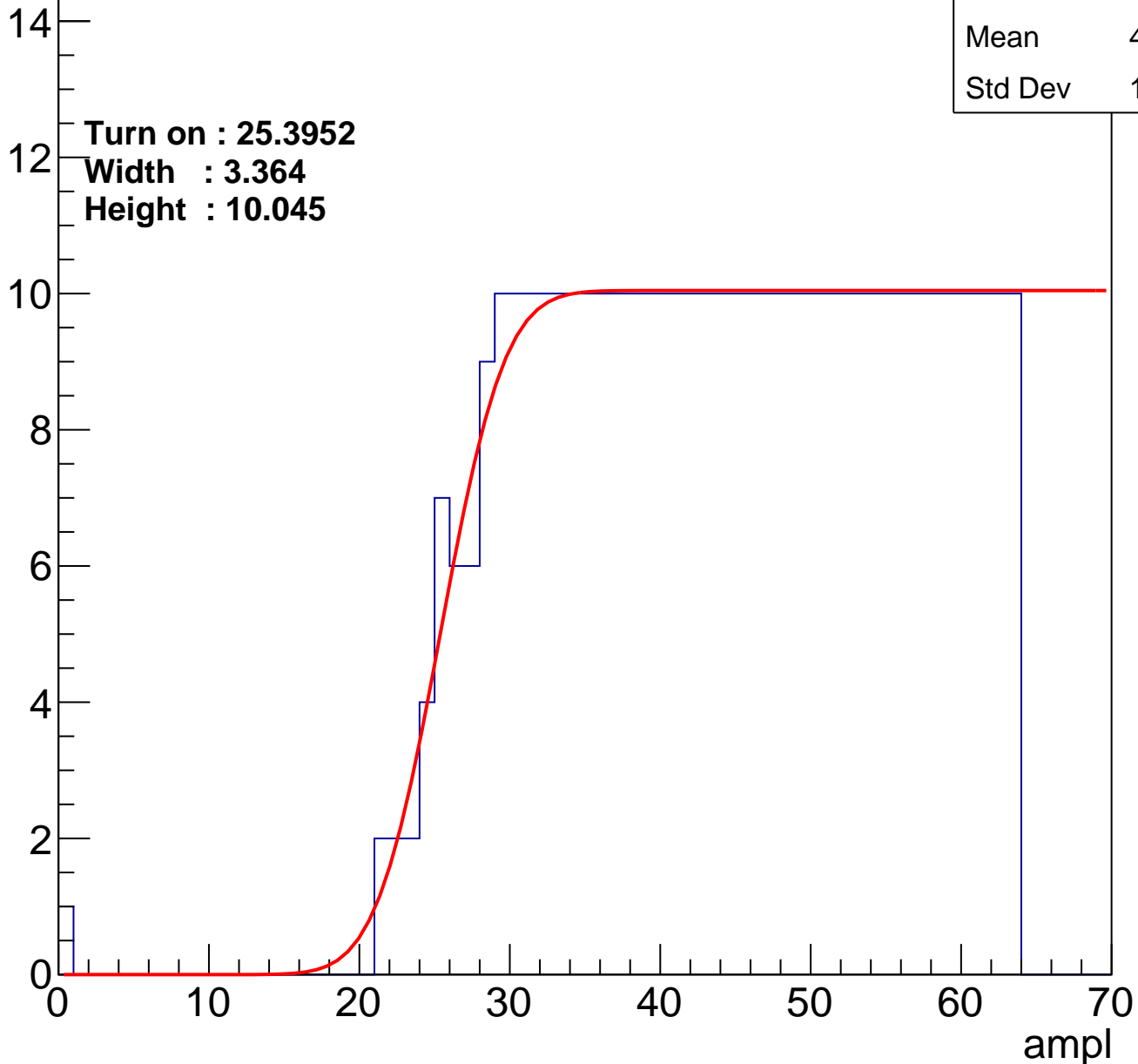
Entries	389
Mean	43.89
Std Dev	11.57

Turn on : 25.3952

Width : 3.364

Height : 10.045

Entry



B1L000S, U15-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.77
Std Dev	12.18

Turn on : 26.0911

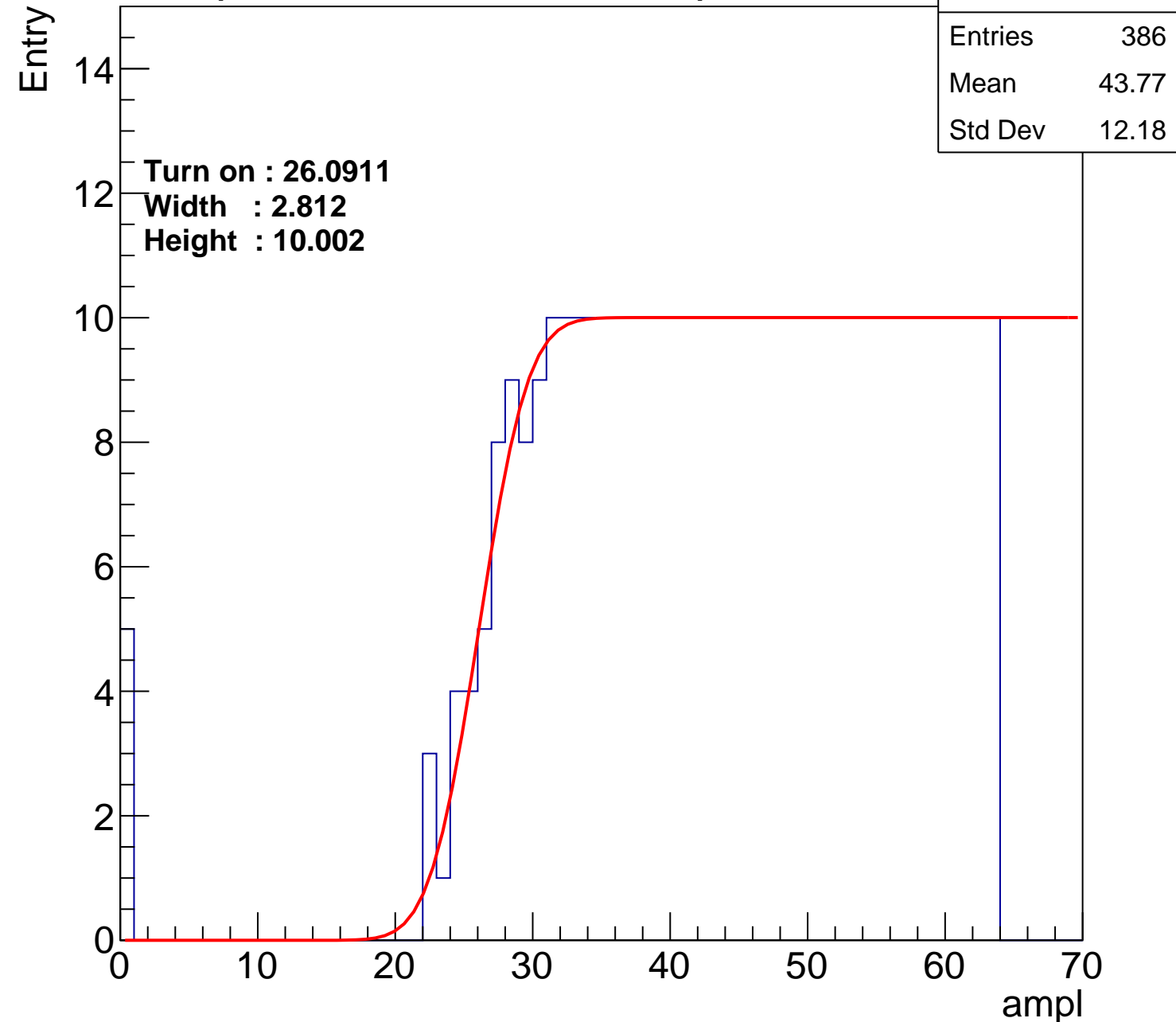
Width : 2.812

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch105

calib_packv5_042523_0143.root, FC#3, port B3

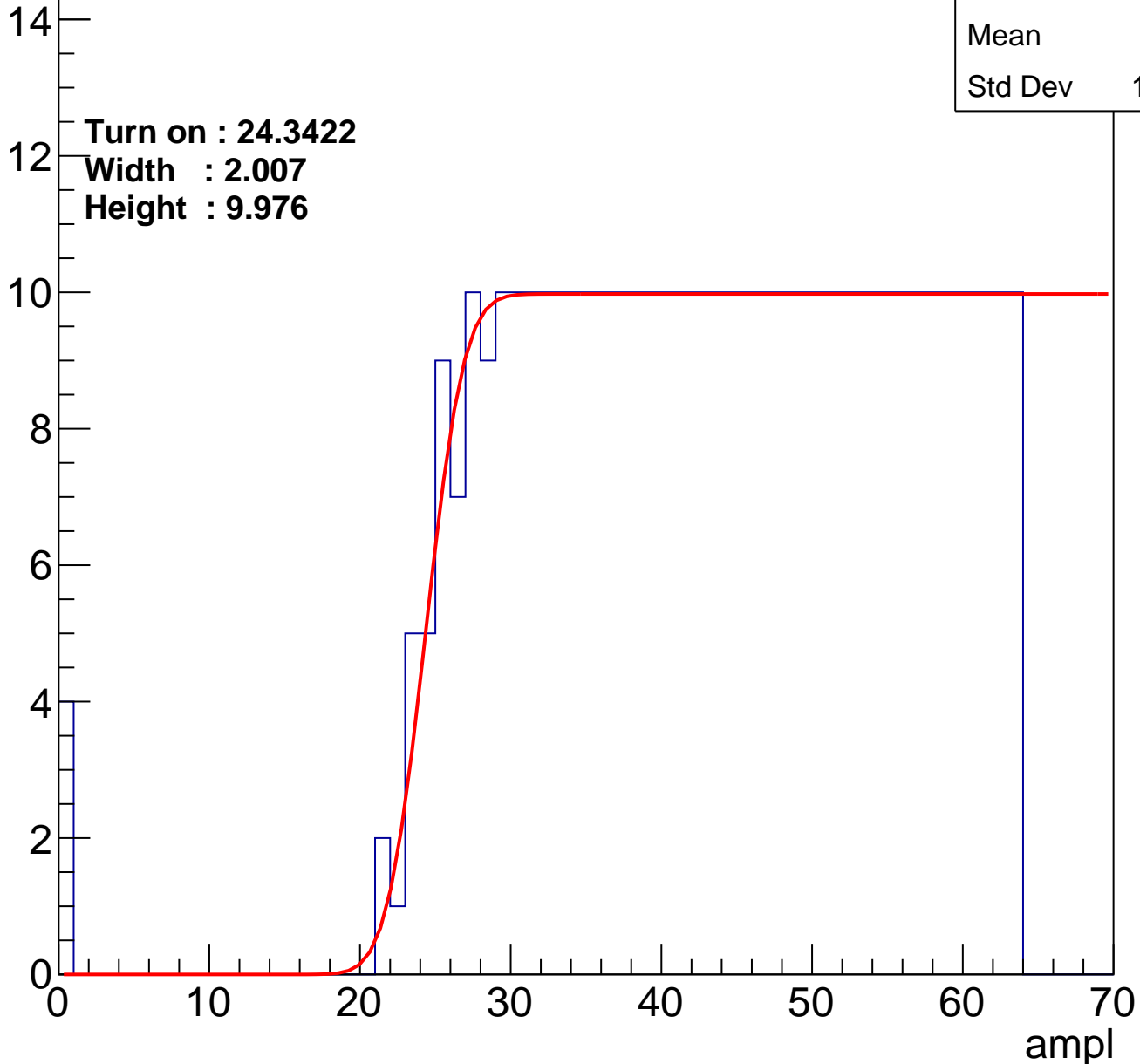
Entries	402
Mean	43.1
Std Dev	12.32

Turn on : 24.3422

Width : 2.007

Height : 9.976

Entry



B1L000S, U15-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 27.1260

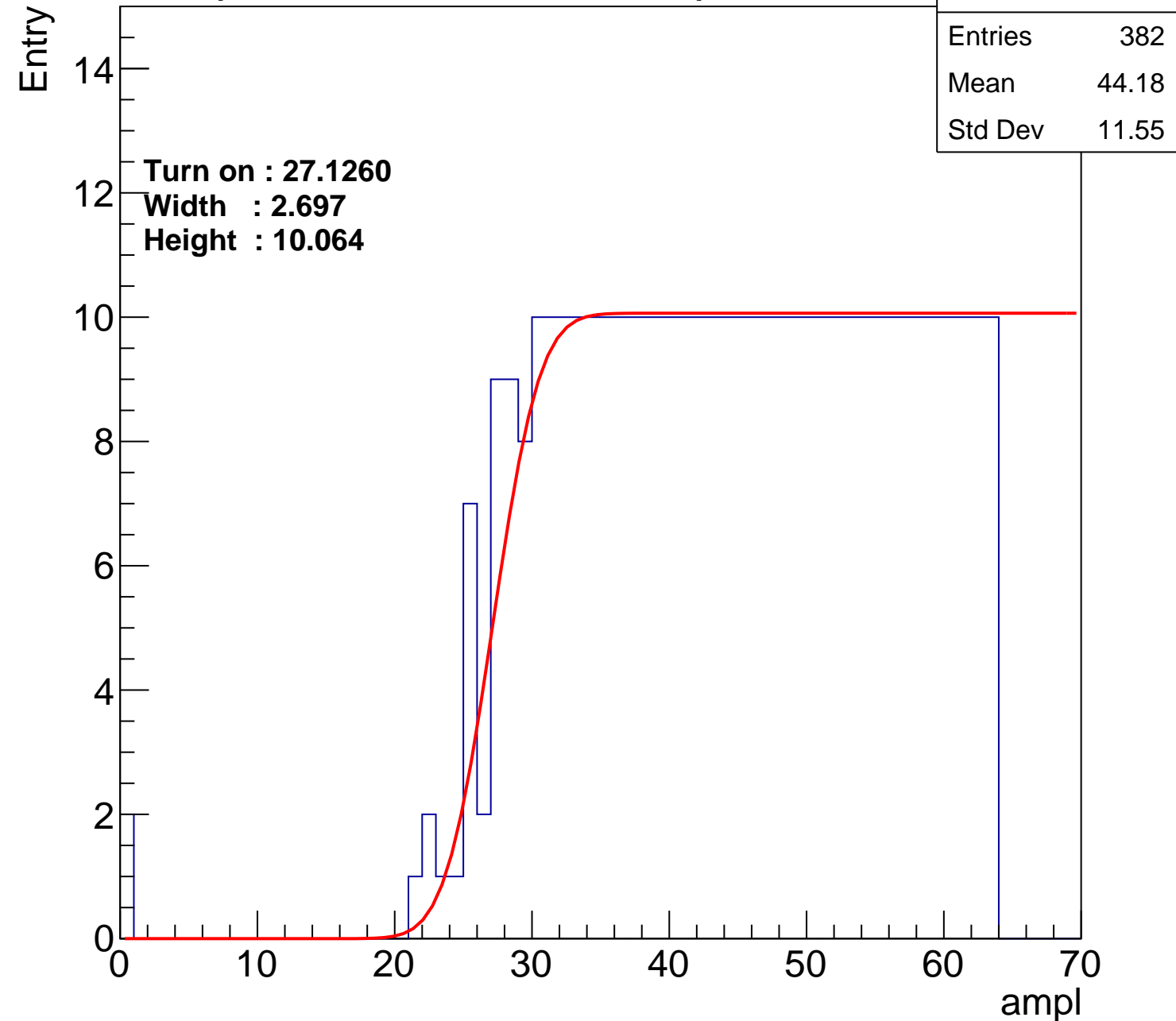
Width : 2.697

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.96
Std Dev	11.87

Turn on : 26.2248

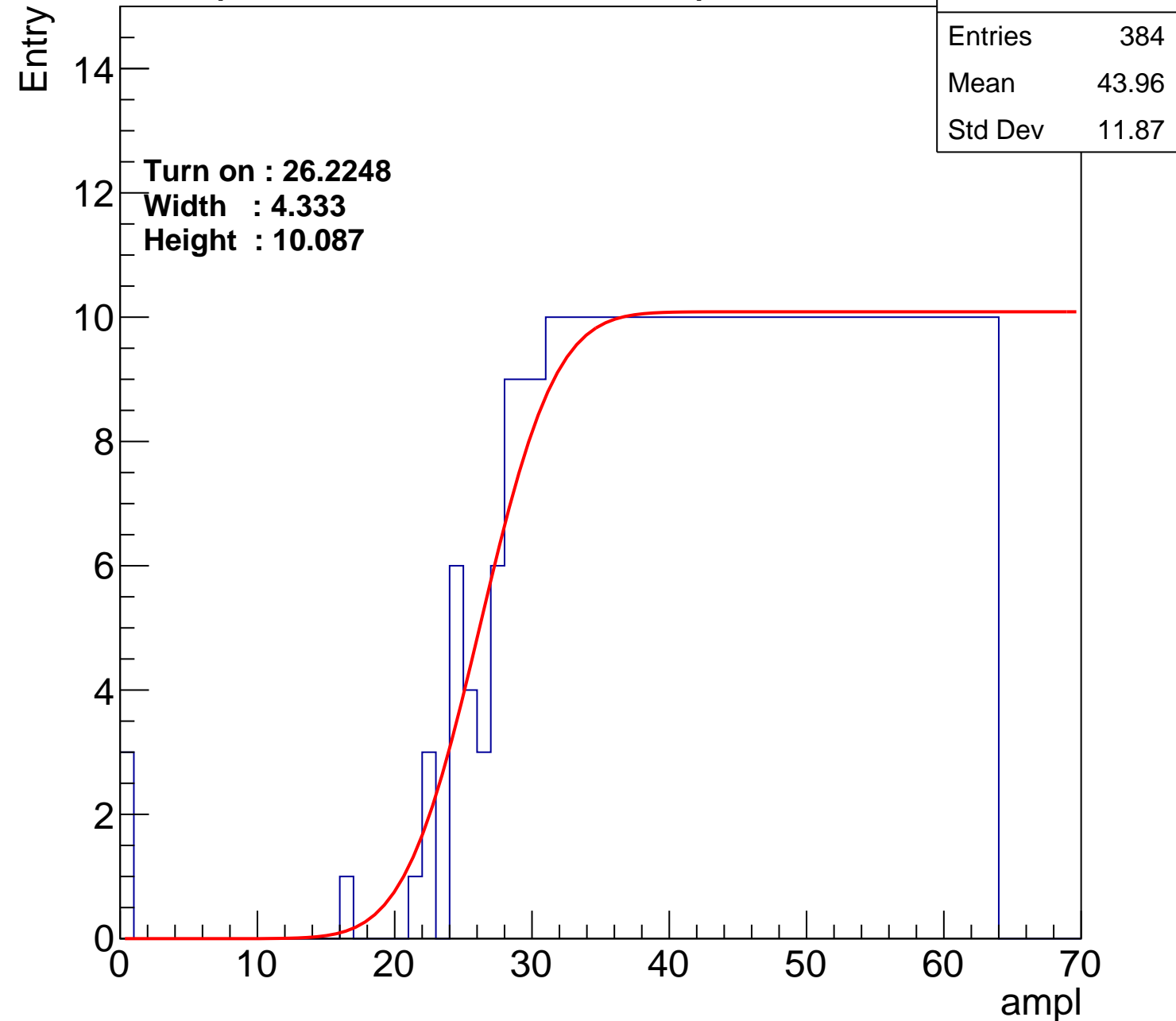
Width : 4.333

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
---------	-----

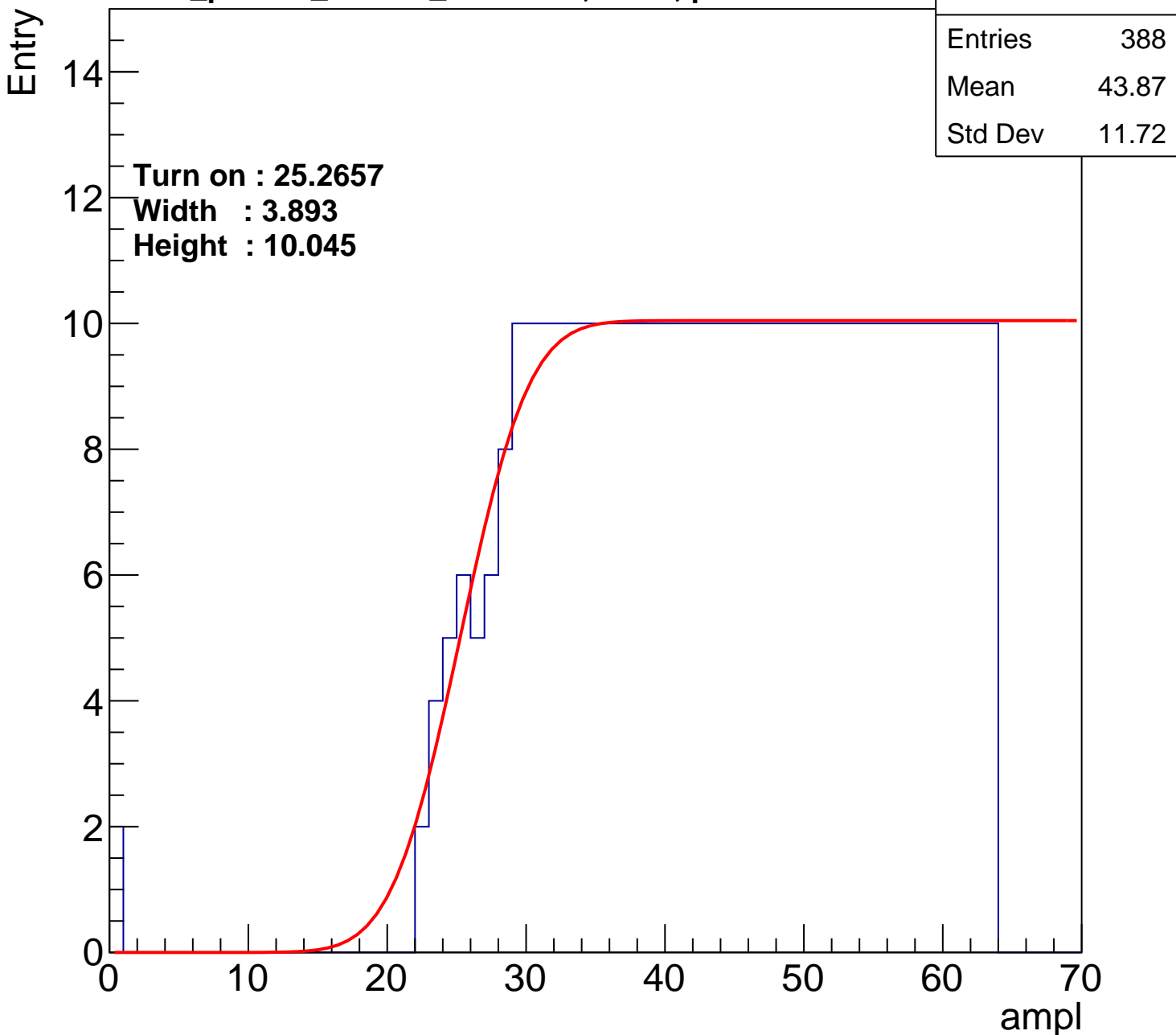
Mean	43.87
------	-------

Std Dev	11.72
---------	-------

Turn on : 25.2657

Width : 3.893

Height : 10.045



B1L000S, U15-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.1587

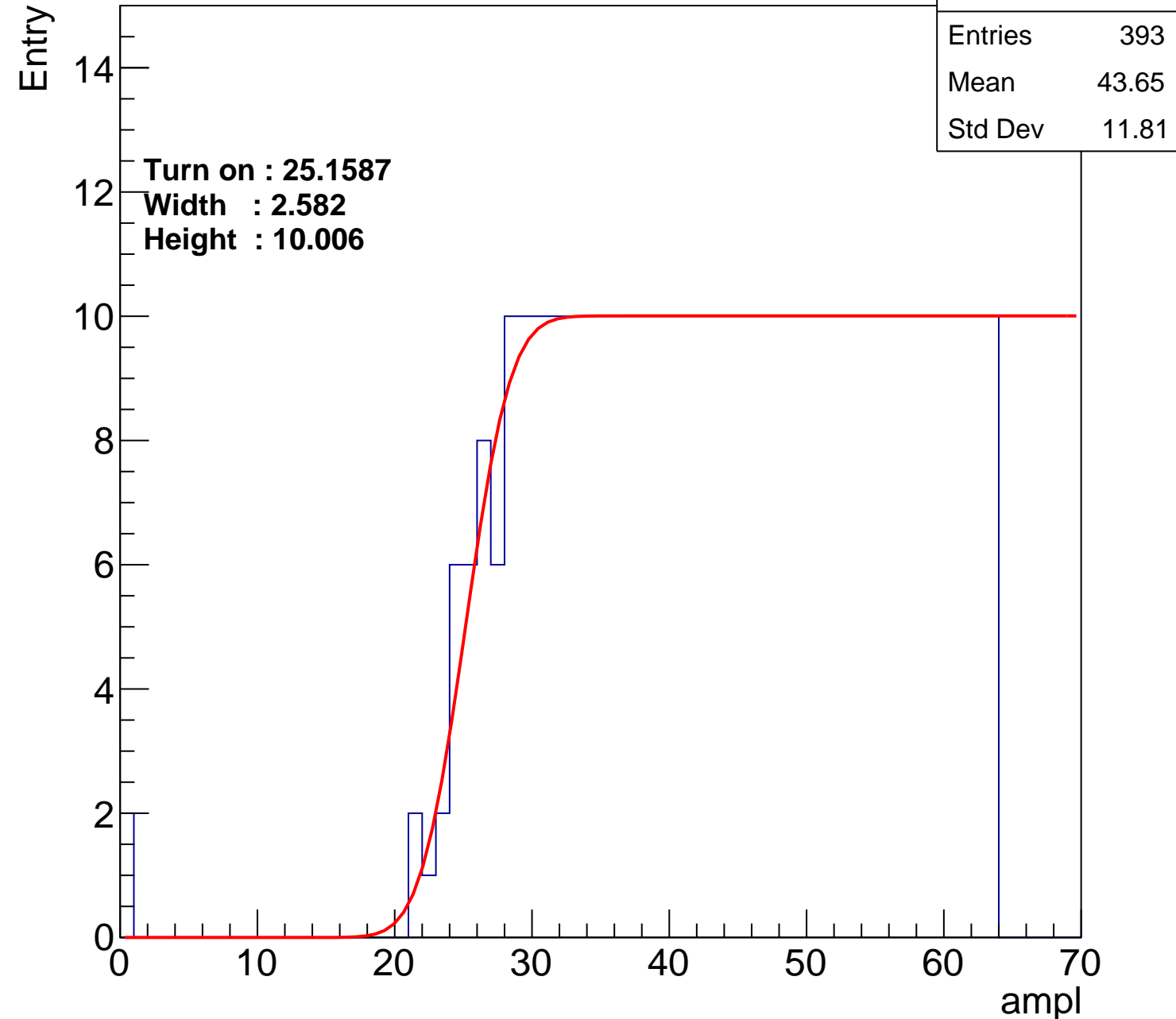
Width : 2.582

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch110

calib_packv5_042523_0143.root, FC#3, port B3

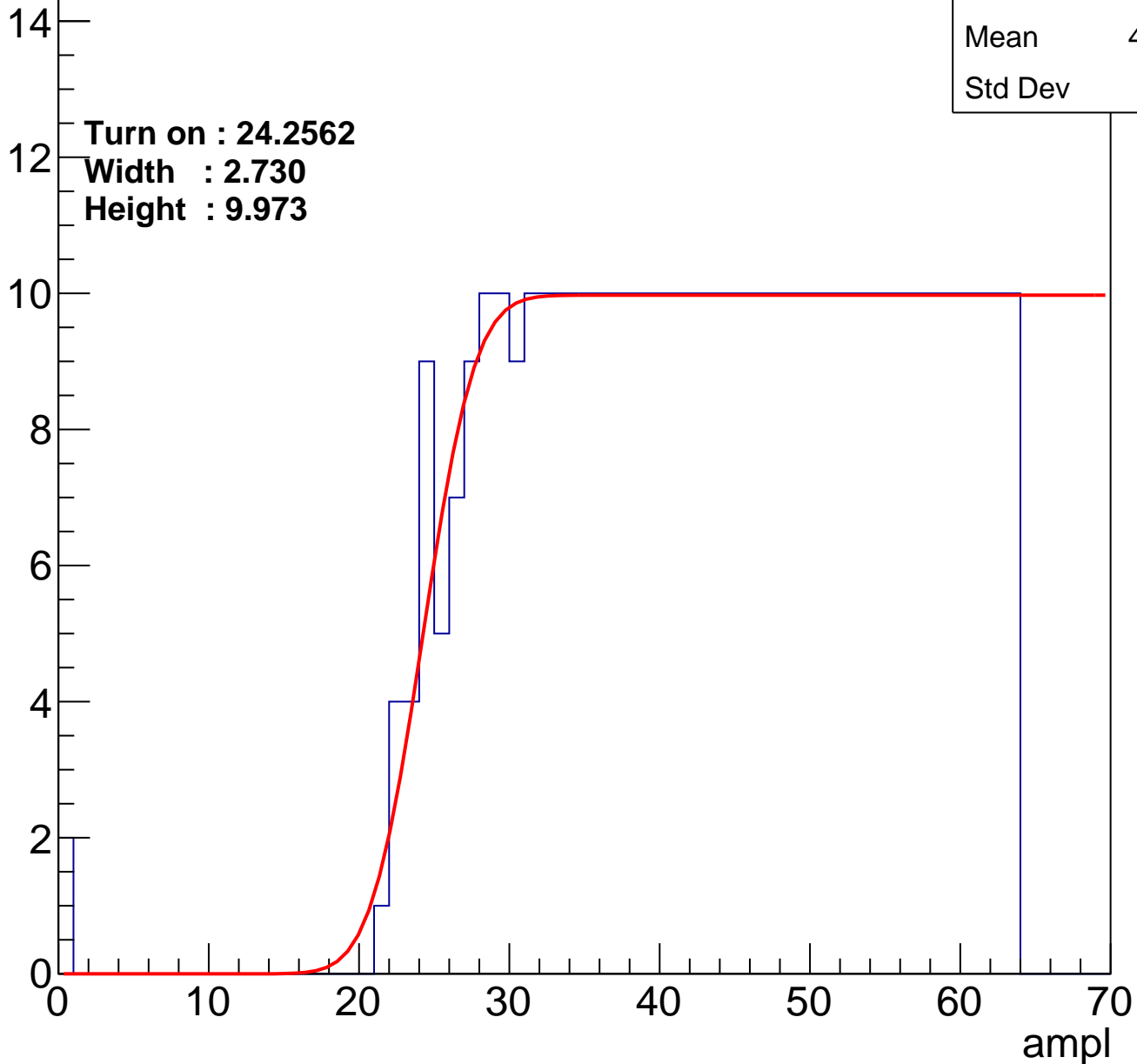
Entries	400
Mean	43.29
Std Dev	12

Turn on : 24.2562

Width : 2.730

Height : 9.973

Entry



B1L000S, U15-ch111

calib_packv5_042523_0143.root, FC#3, port B3

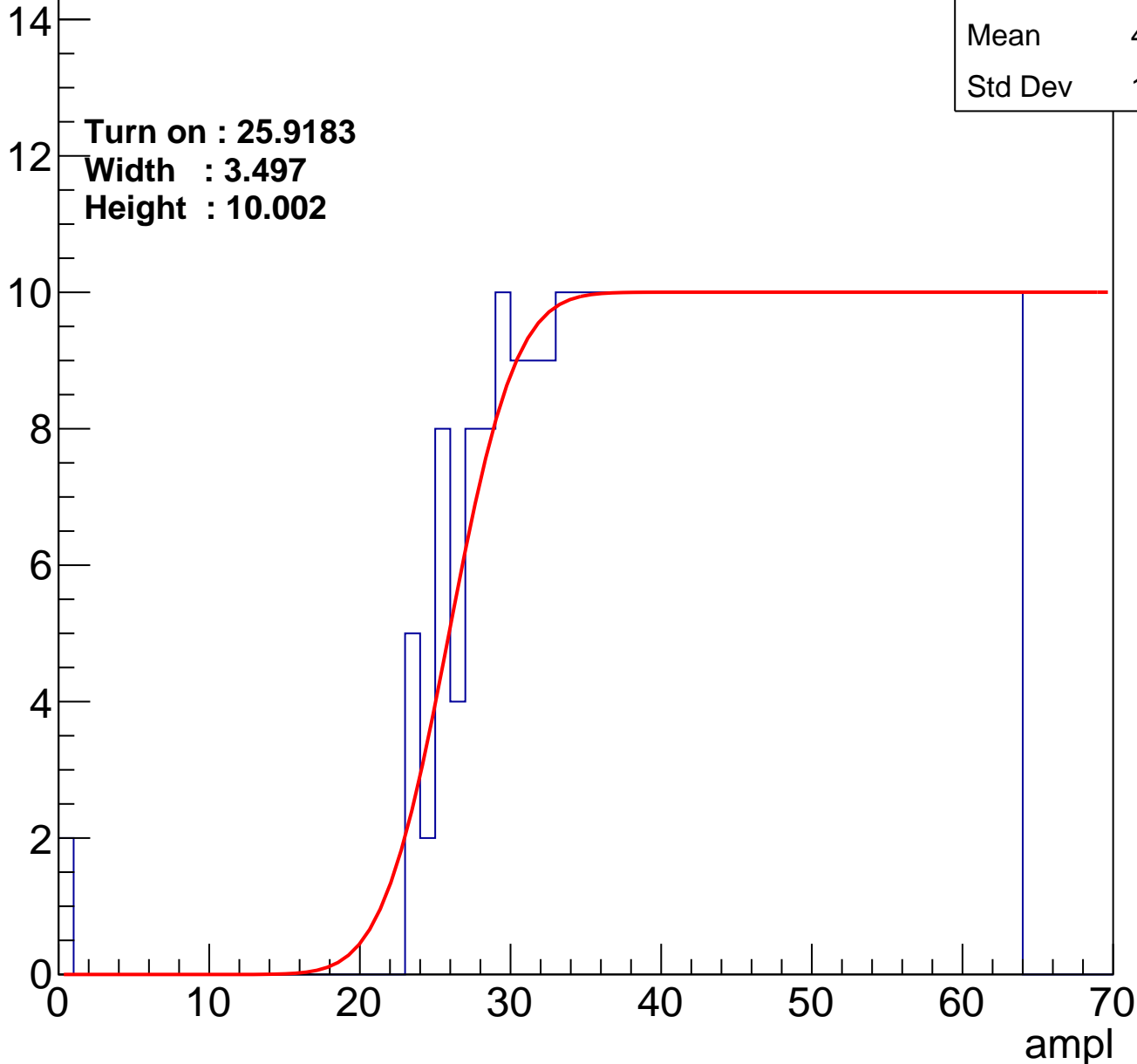
Entries	384
Mean	44.05
Std Dev	11.64

Turn on : 25.9183

Width : 3.497

Height : 10.002

Entry



B1L000S, U15-ch112

calib_packv5_042523_0143.root, FC#3, port B3

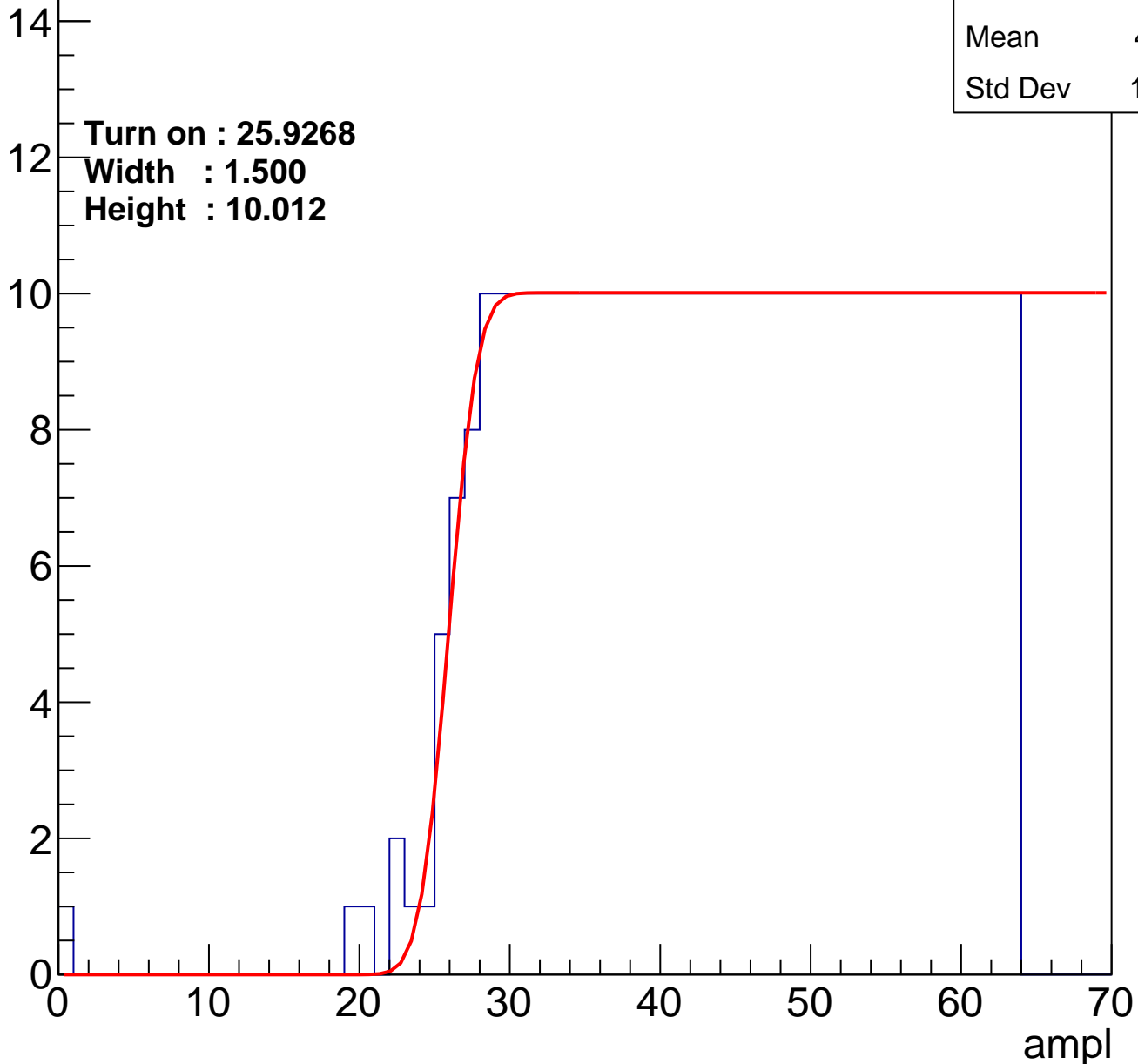
Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.9268

Width : 1.500

Height : 10.012

Entry



B1L000S, U15-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 26.9382

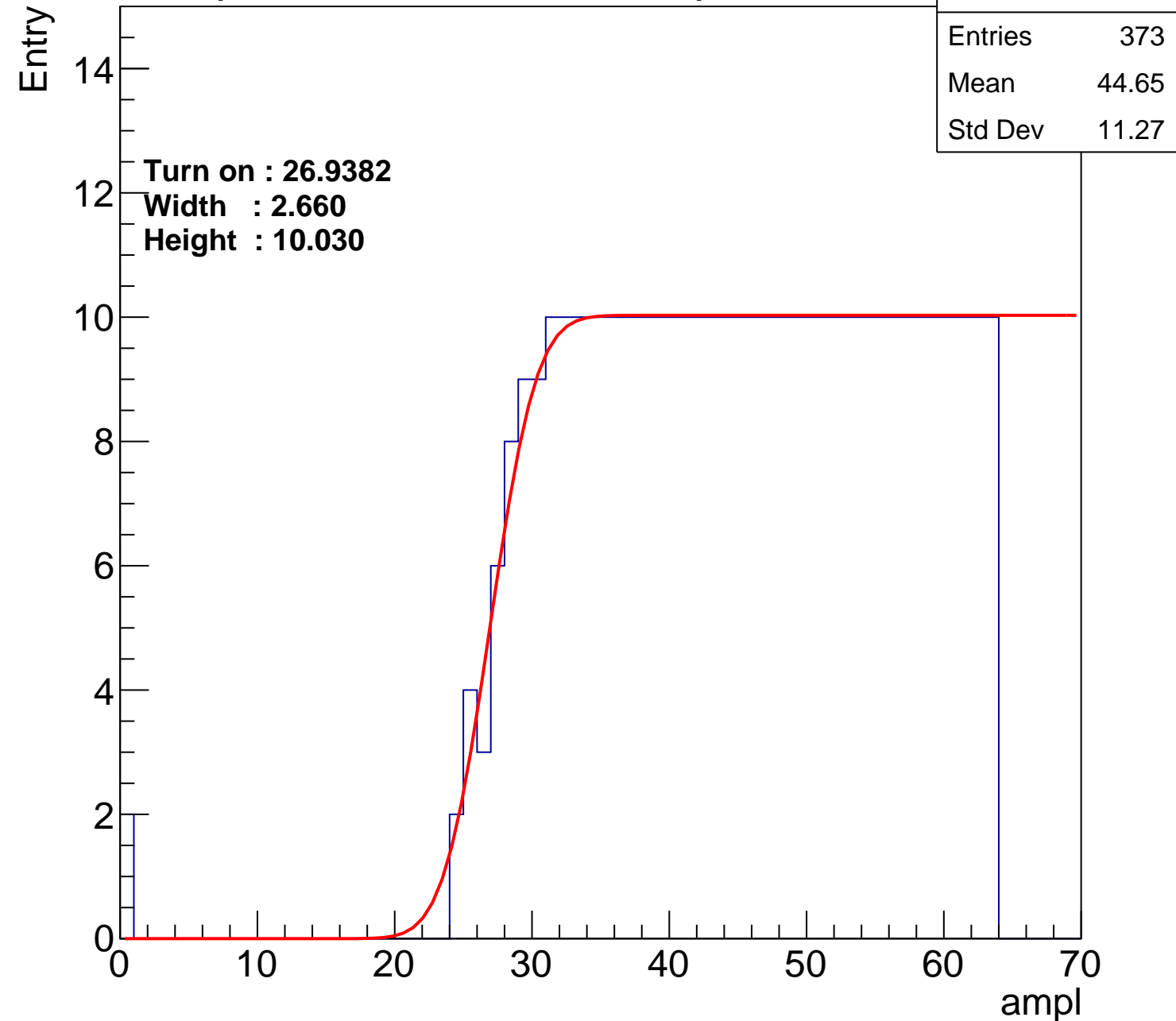
Width : 2.660

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.4953

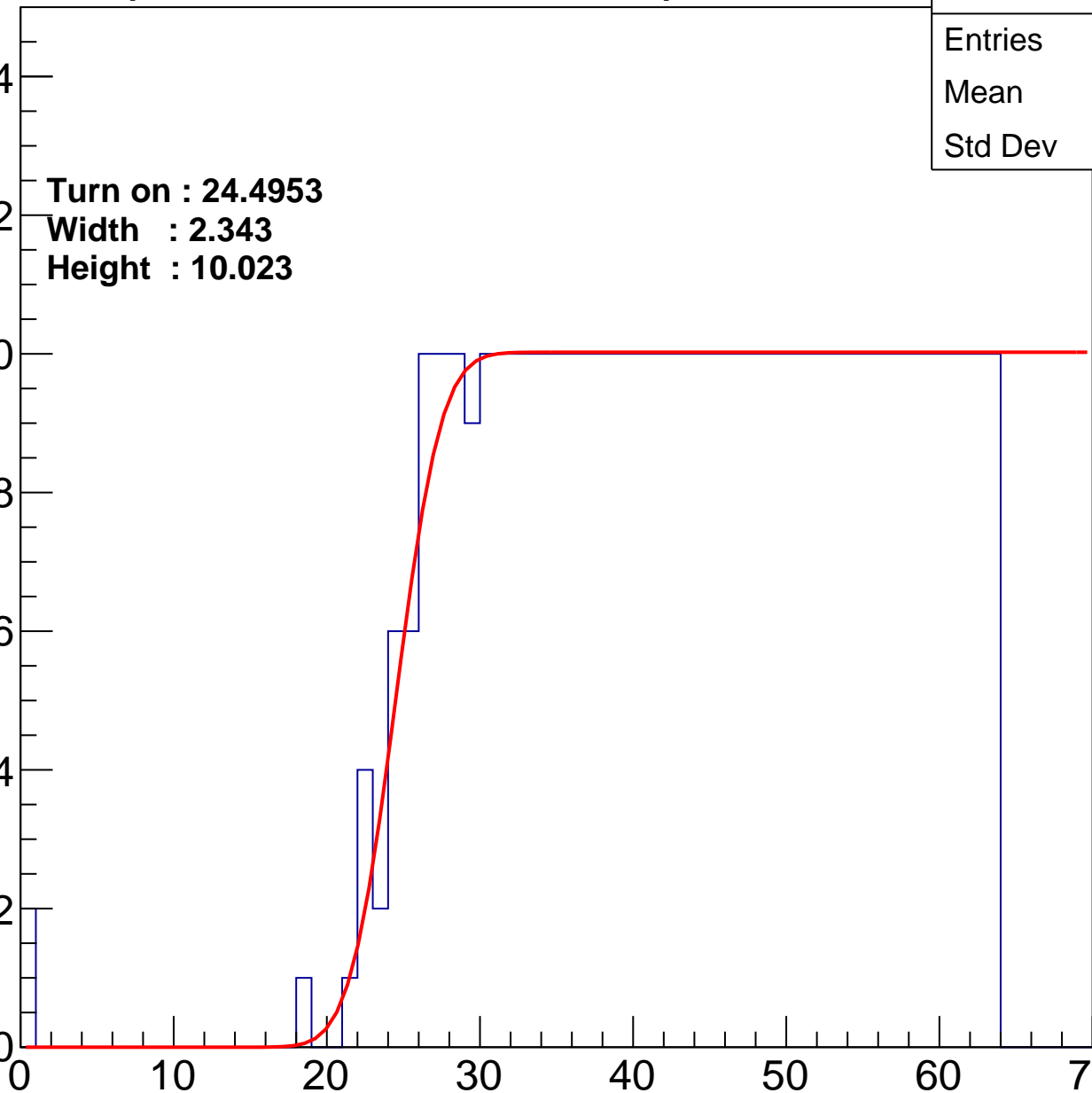
Width : 2.343

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	42.96
Std Dev	12.48

Turn on : 24.0793

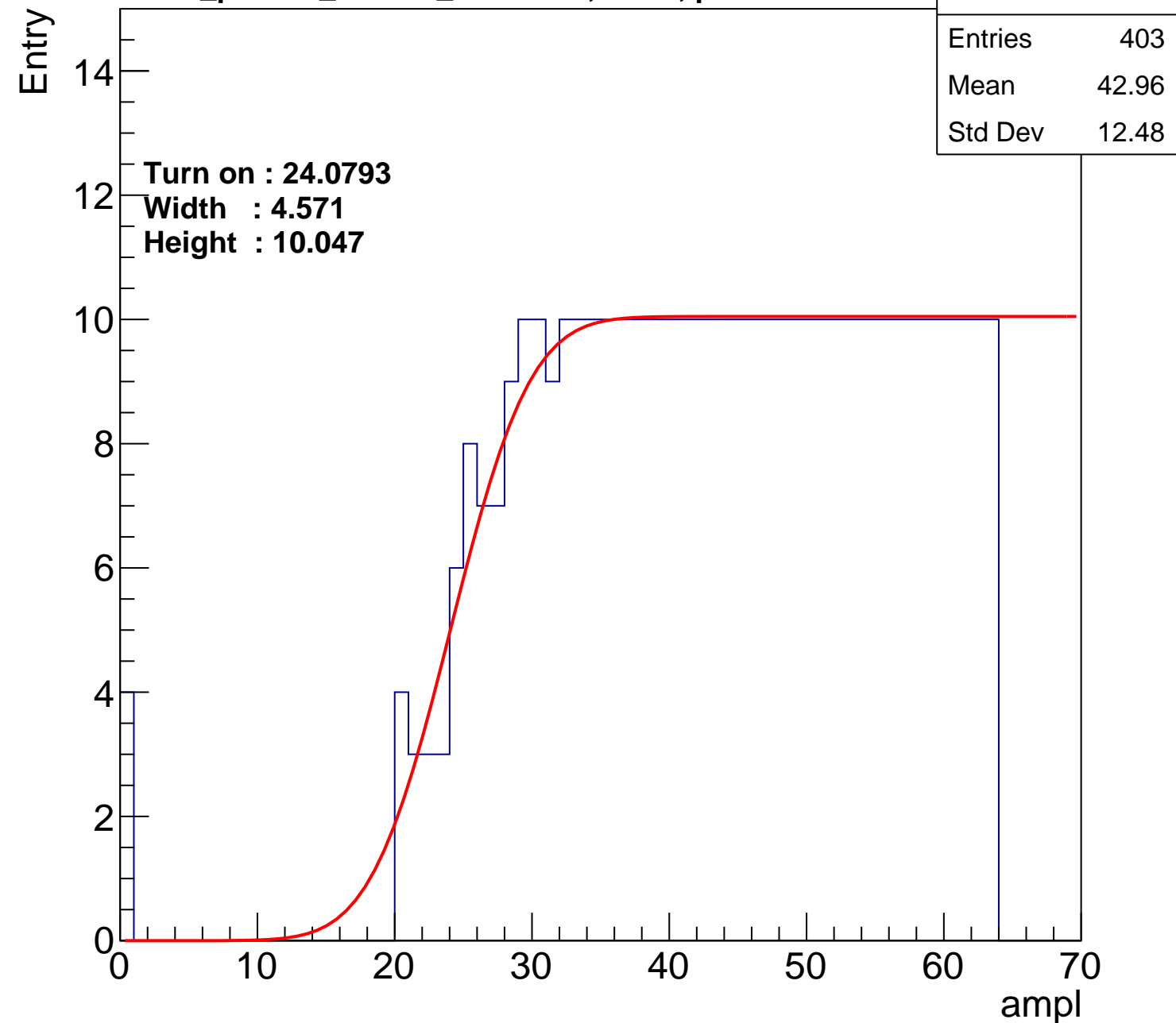
Width : 4.571

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.5512

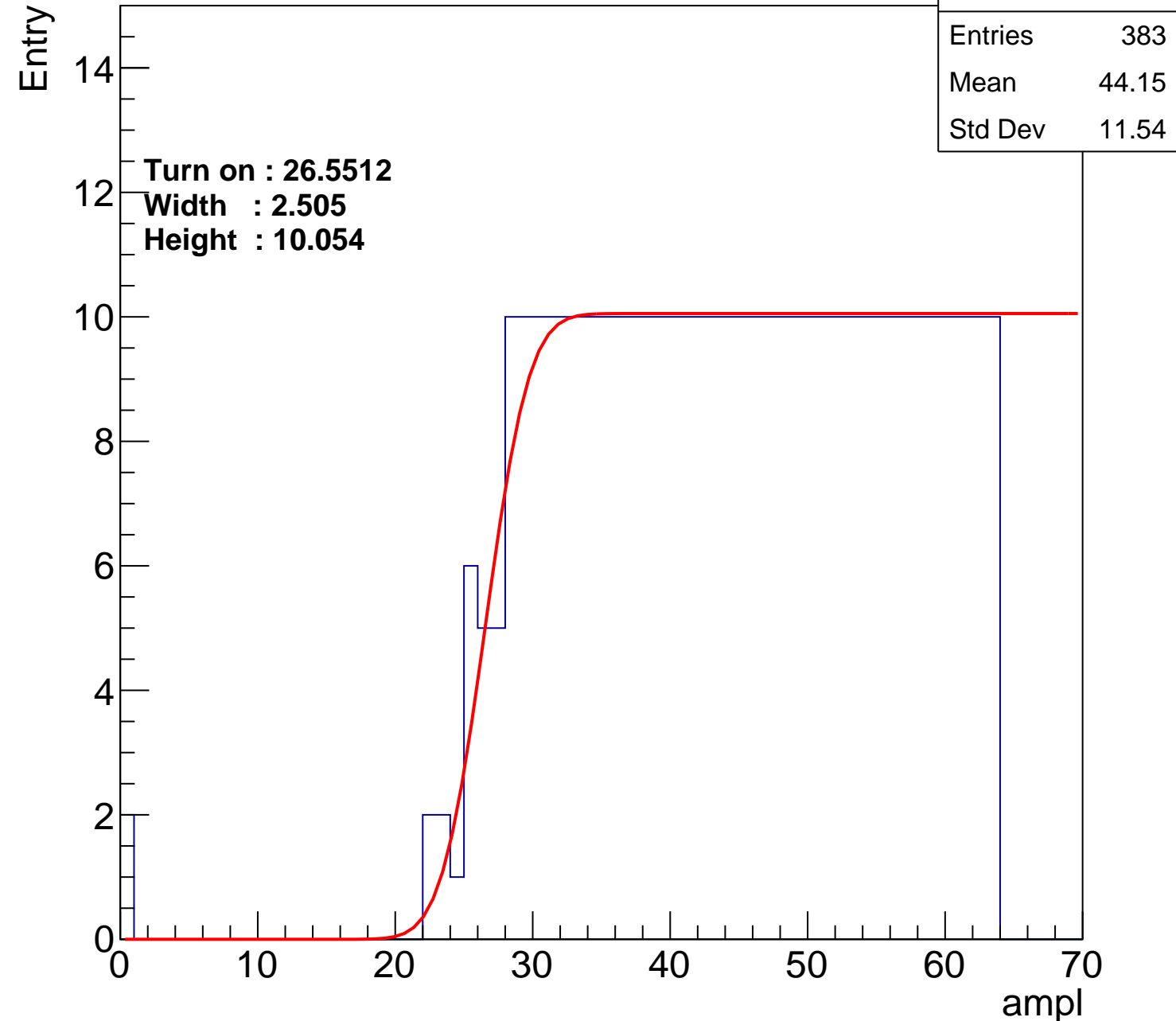
Width : 2.505

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch117

calib_packv5_042523_0143.root, FC#3, port B3

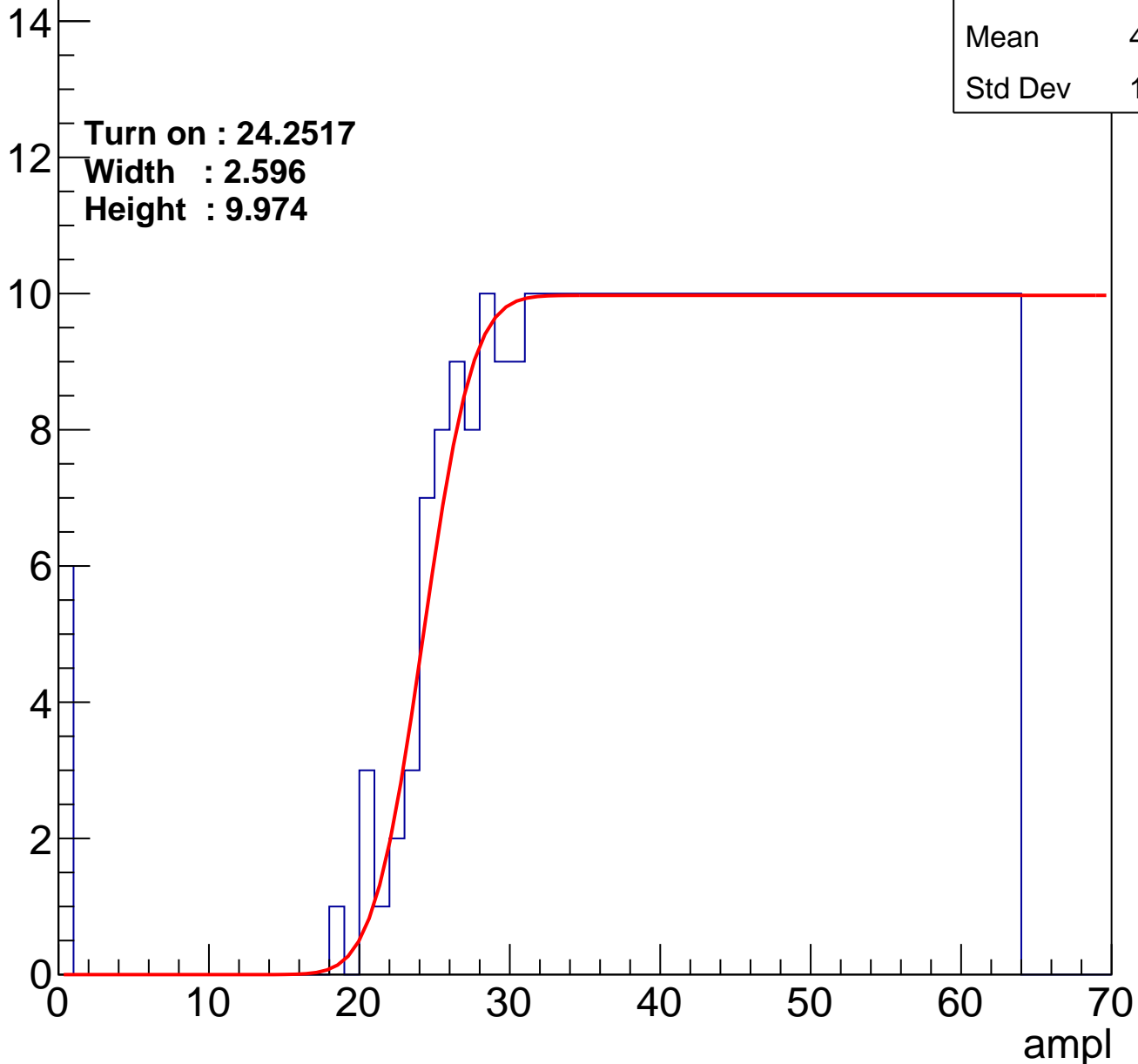
Entries	406
Mean	42.74
Std Dev	12.78

Turn on : 24.2517

Width : 2.596

Height : 9.974

Entry



B1L000S, U15-ch118

calib_packv5_042523_0143.root, FC#3, port B3

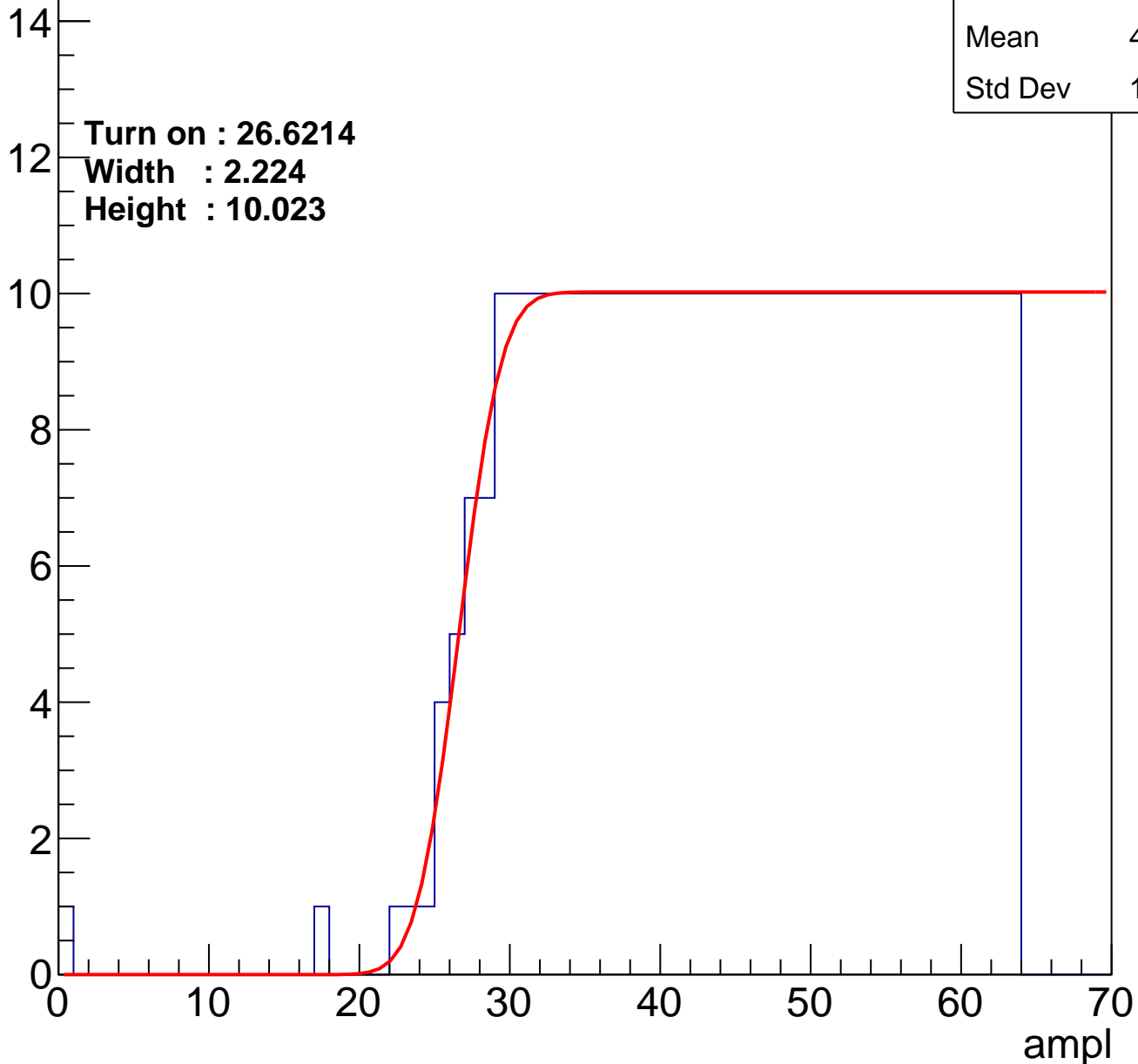
Entries	378
Mean	44.45
Std Dev	11.26

Turn on : 26.6214

Width : 2.224

Height : 10.023

Entry



B1L000S, U15-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.6986

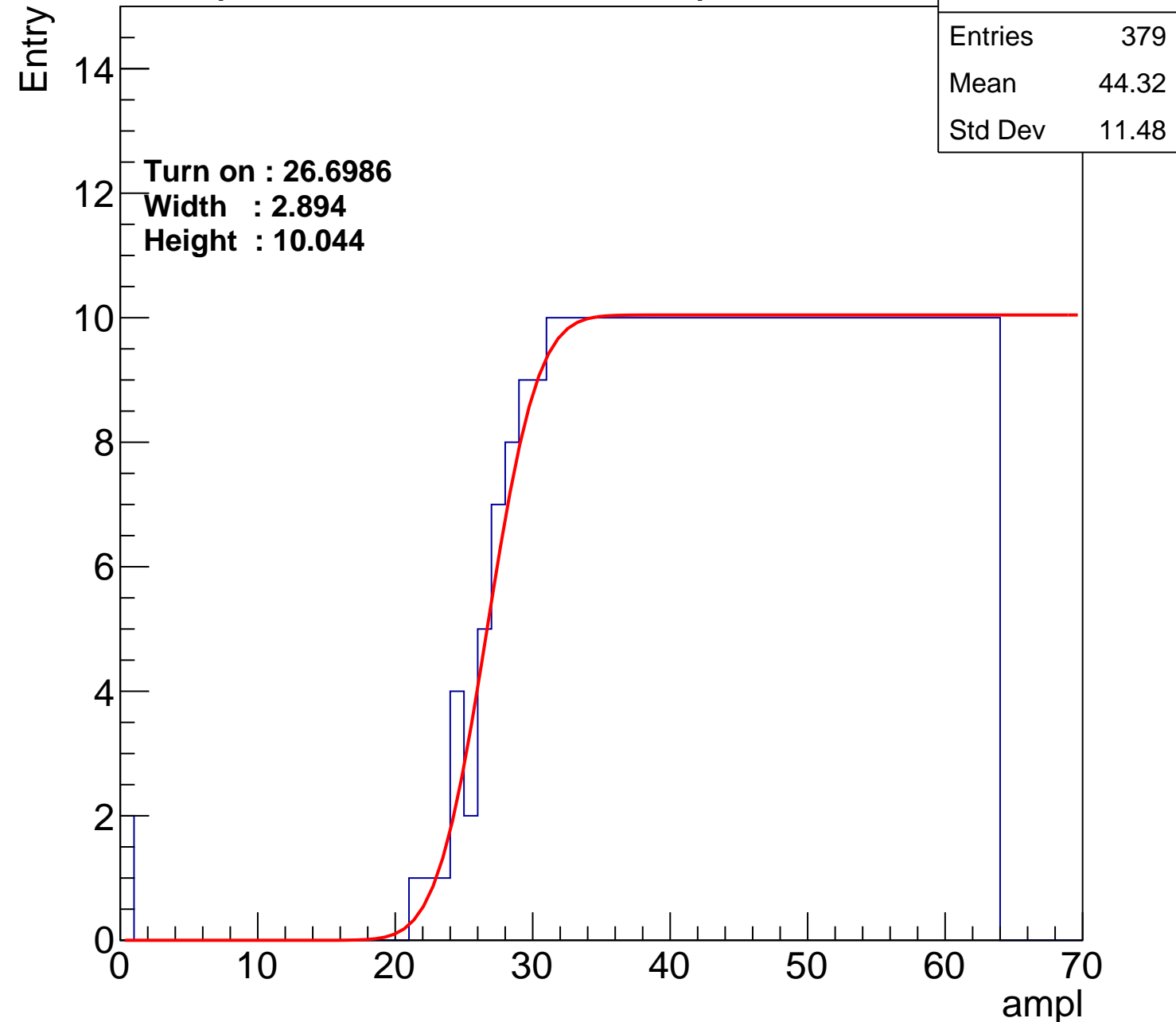
Width : 2.894

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.77
Std Dev	11.78

Turn on : 25.2691

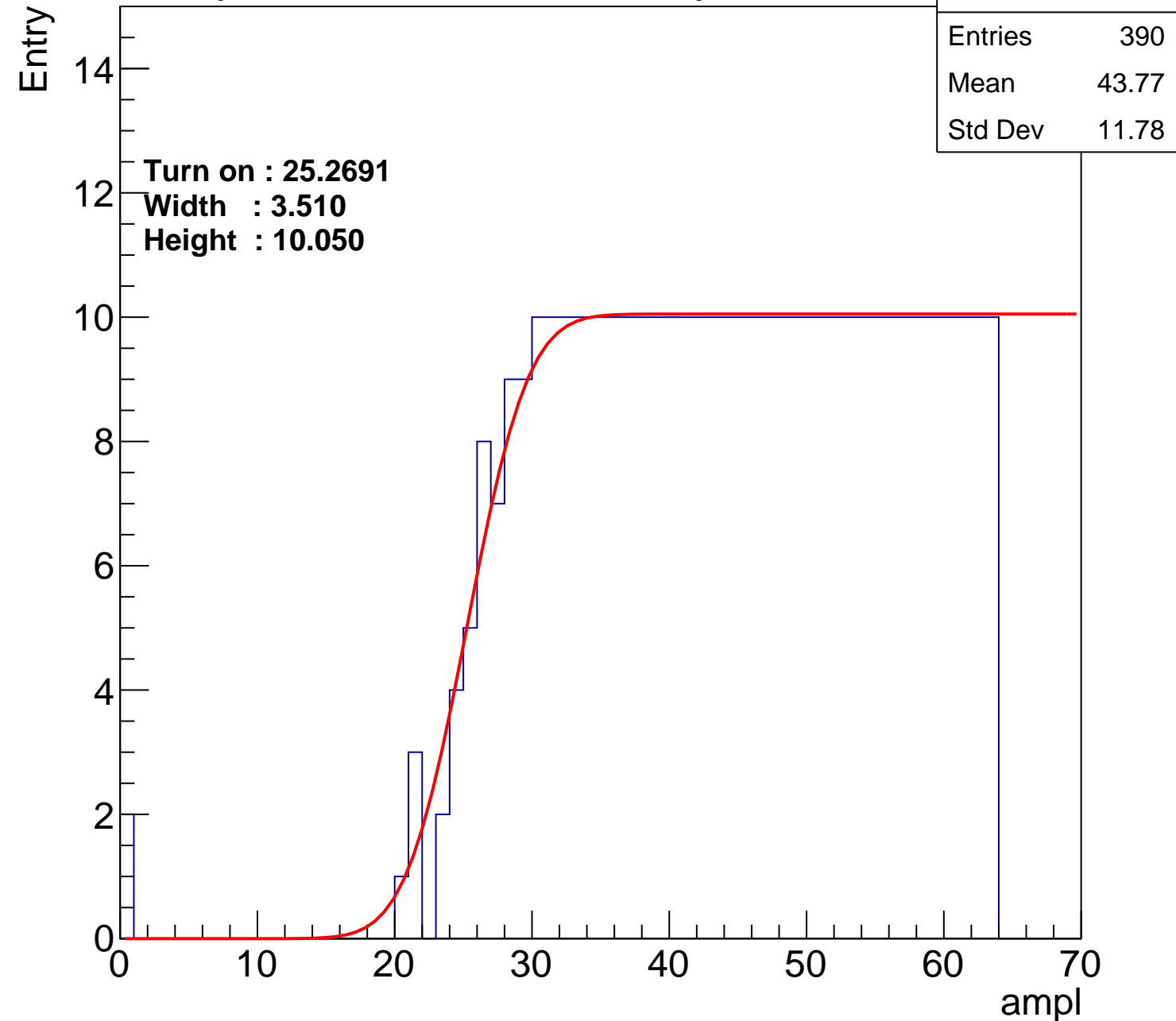
Width : 3.510

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch121

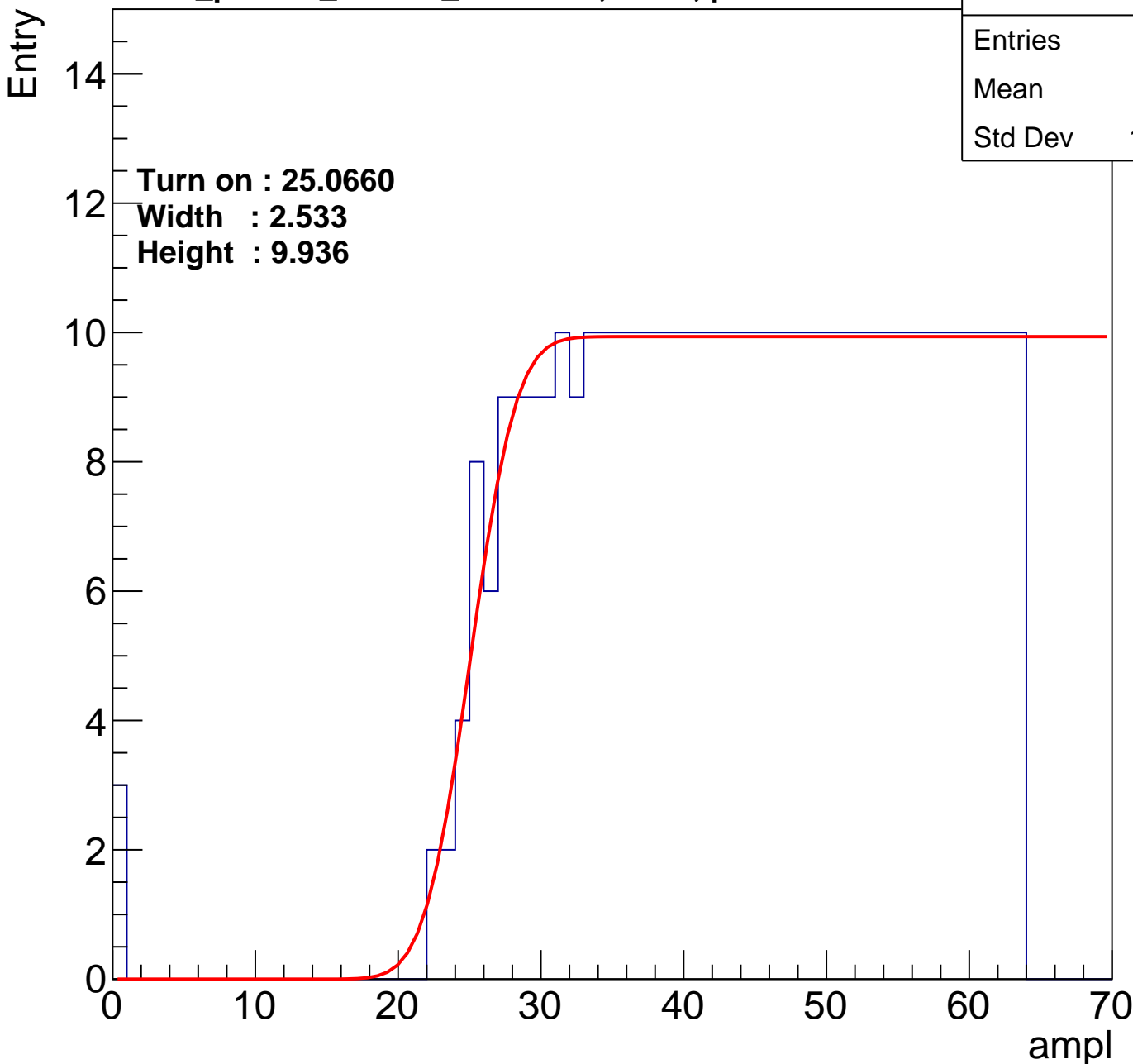
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.0660

Width : 2.533

Height : 9.936

Entries	390
Mean	43.71
Std Dev	11.93



B1L000S, U15-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.2
Std Dev	12.44

Turn on : 24.6980

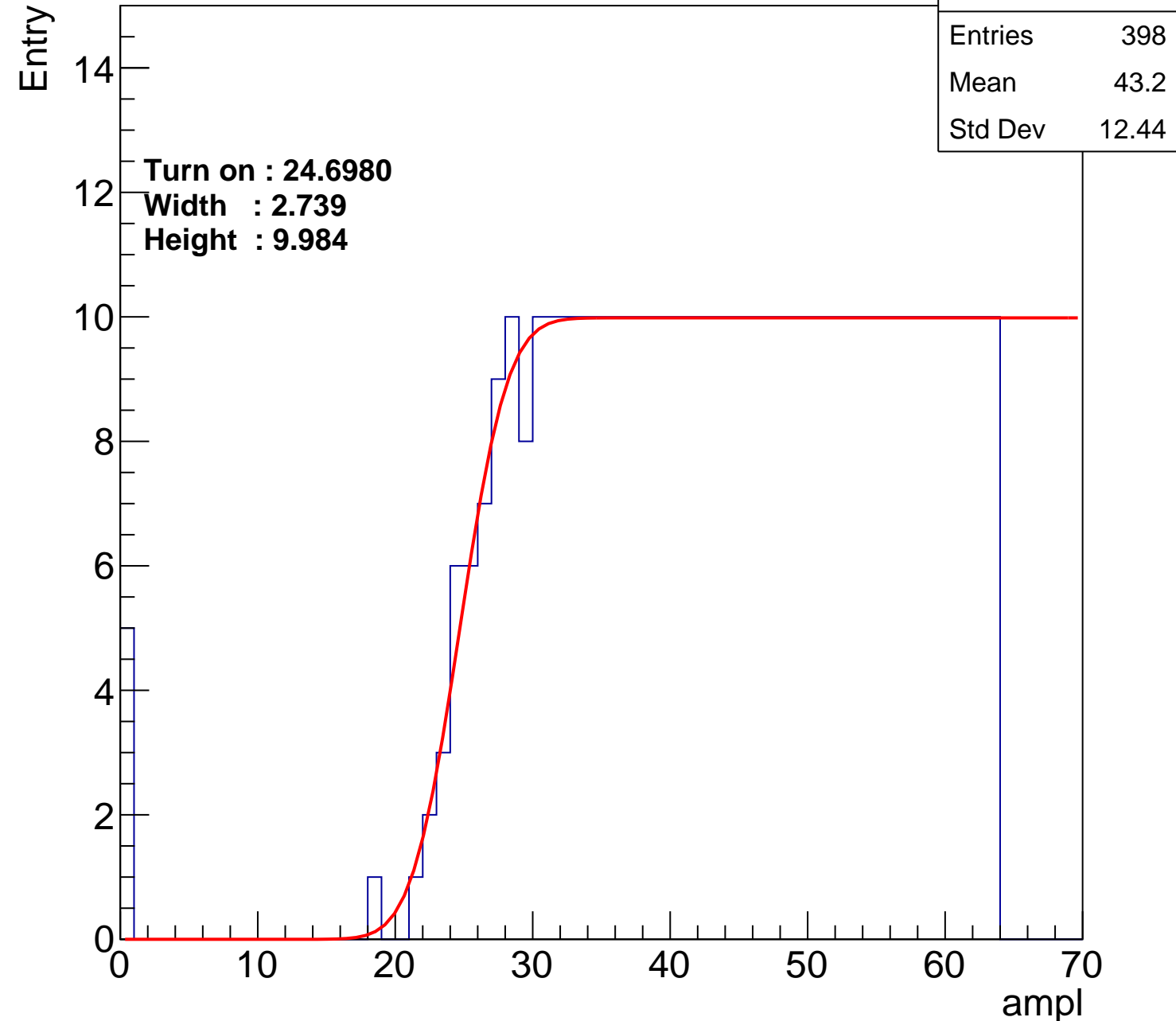
Width : 2.739

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.27
Std Dev	12.26

Turn on : 24.7950

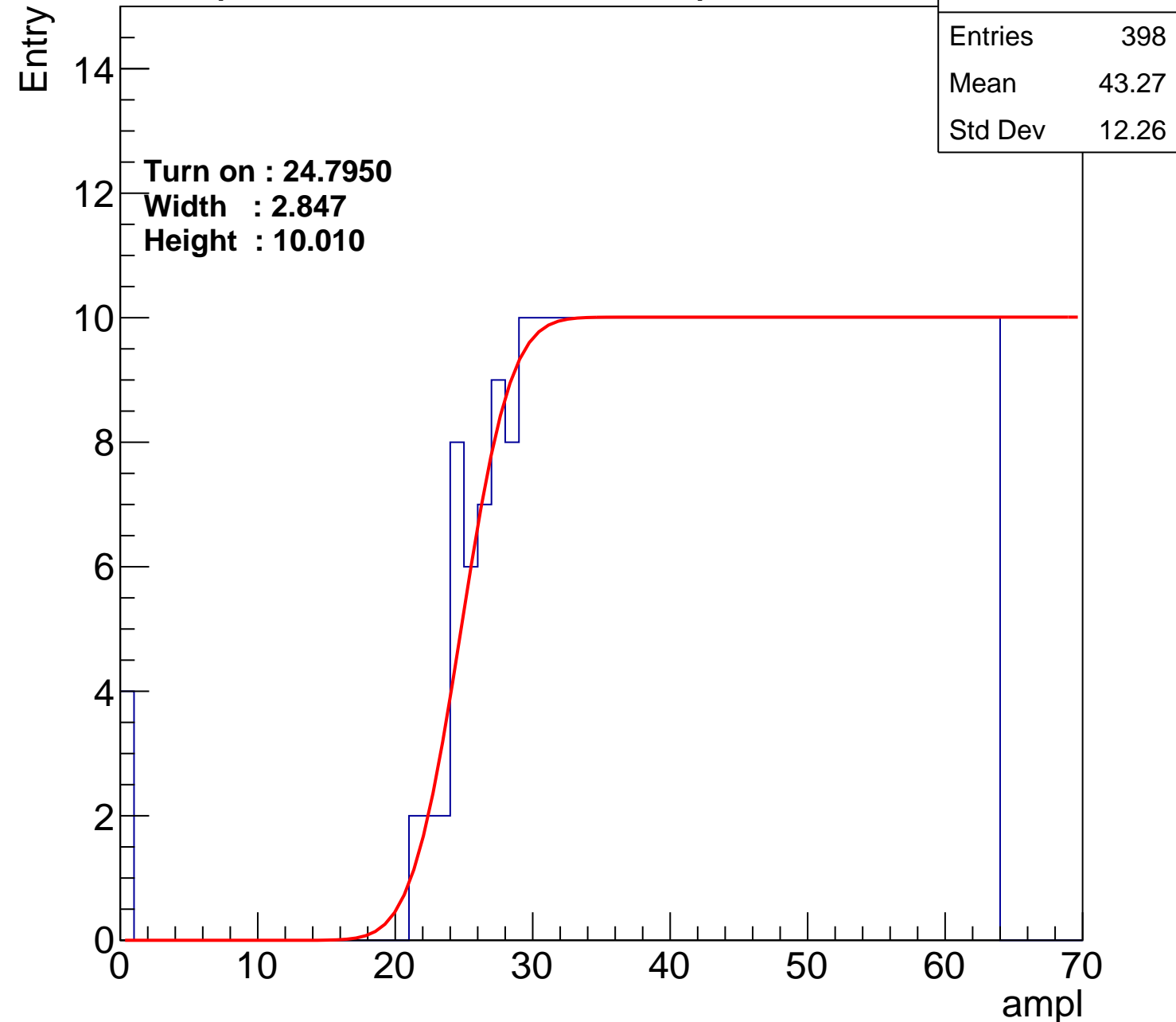
Width : 2.847

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.17
Std Dev	12.15

Turn on : 24.1513

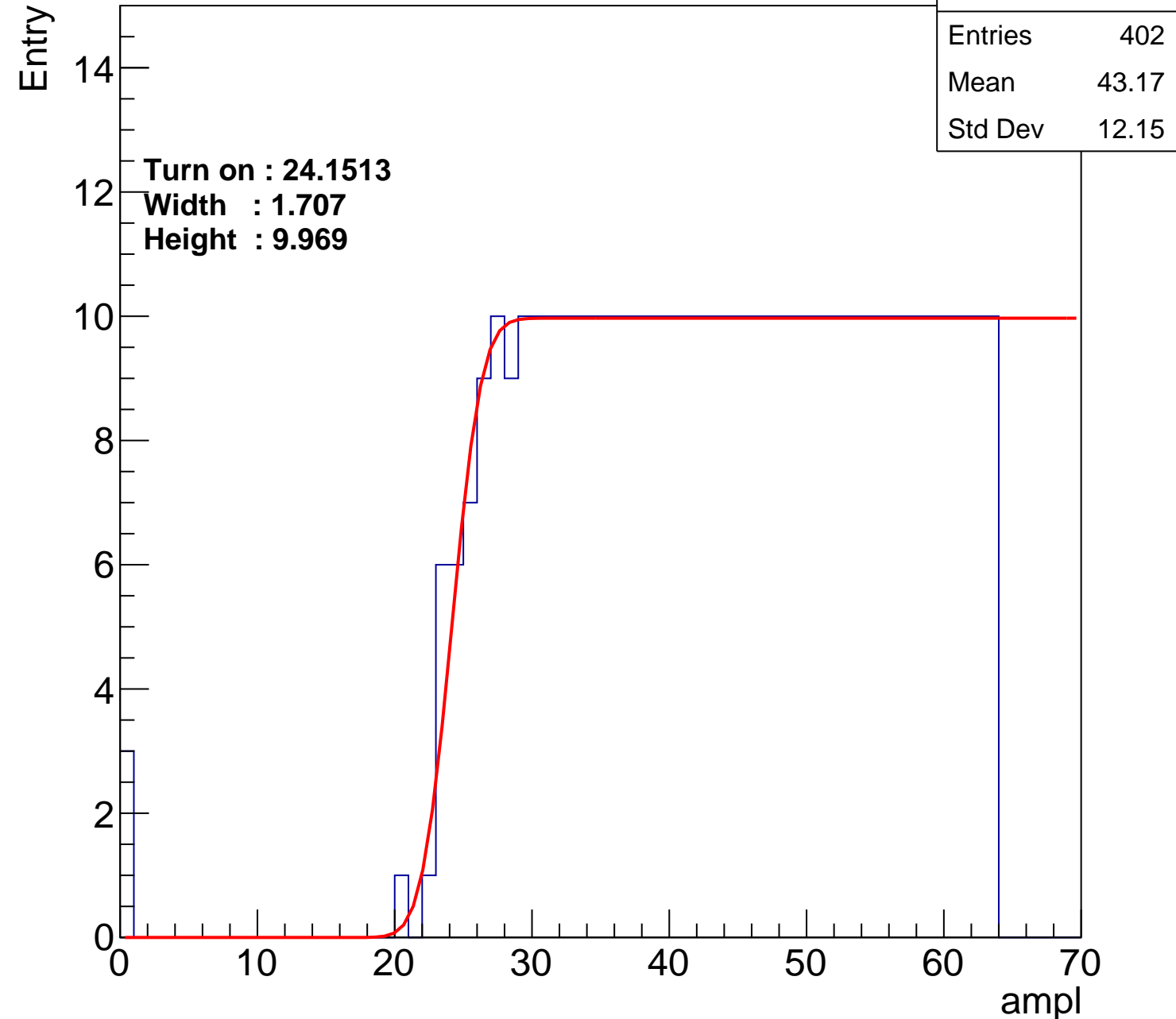
Width : 1.707

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 25.8040

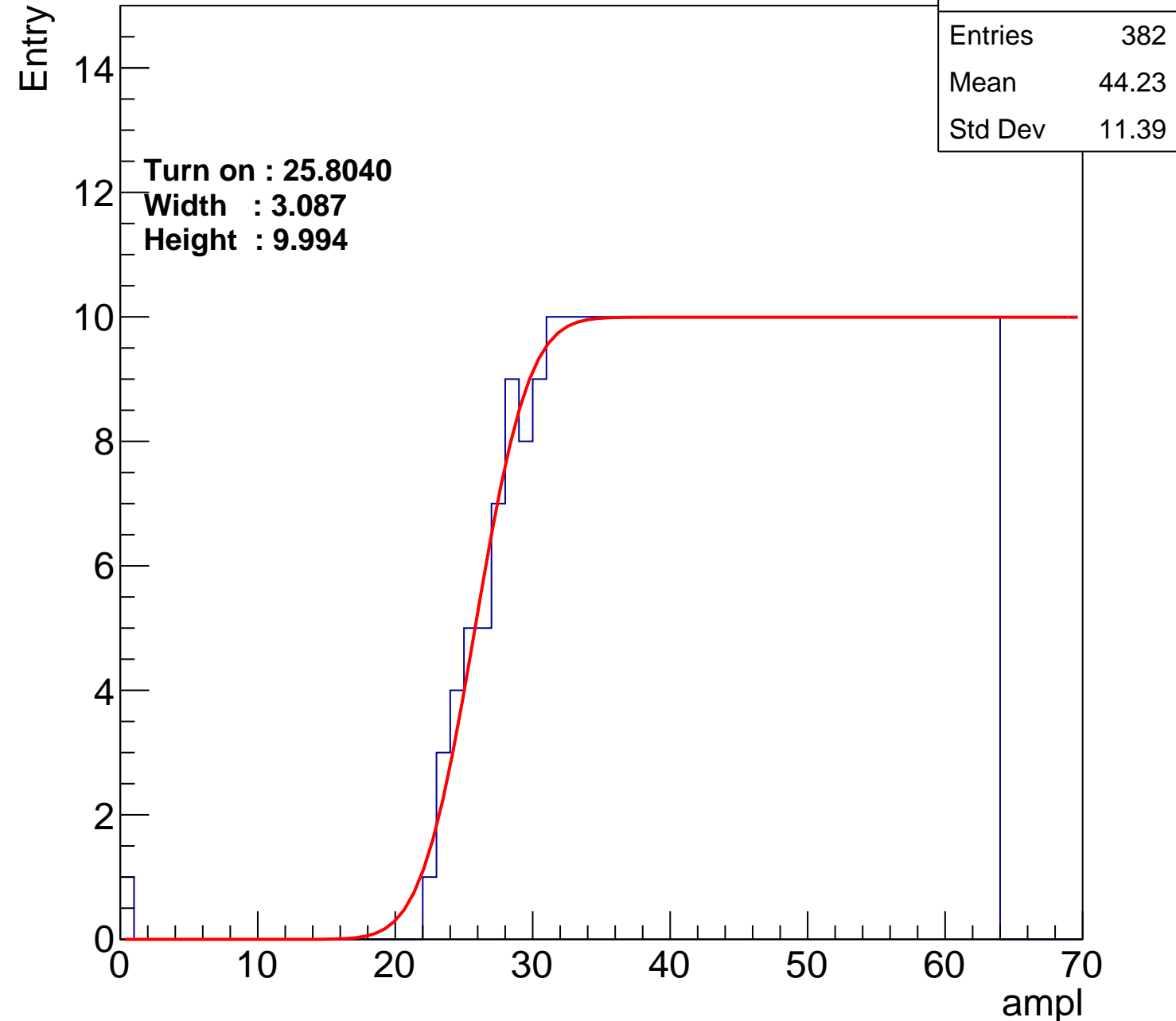
Width : 3.087

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.36
Std Dev	11.61

Turn on : 26.8325

Width : 1.198

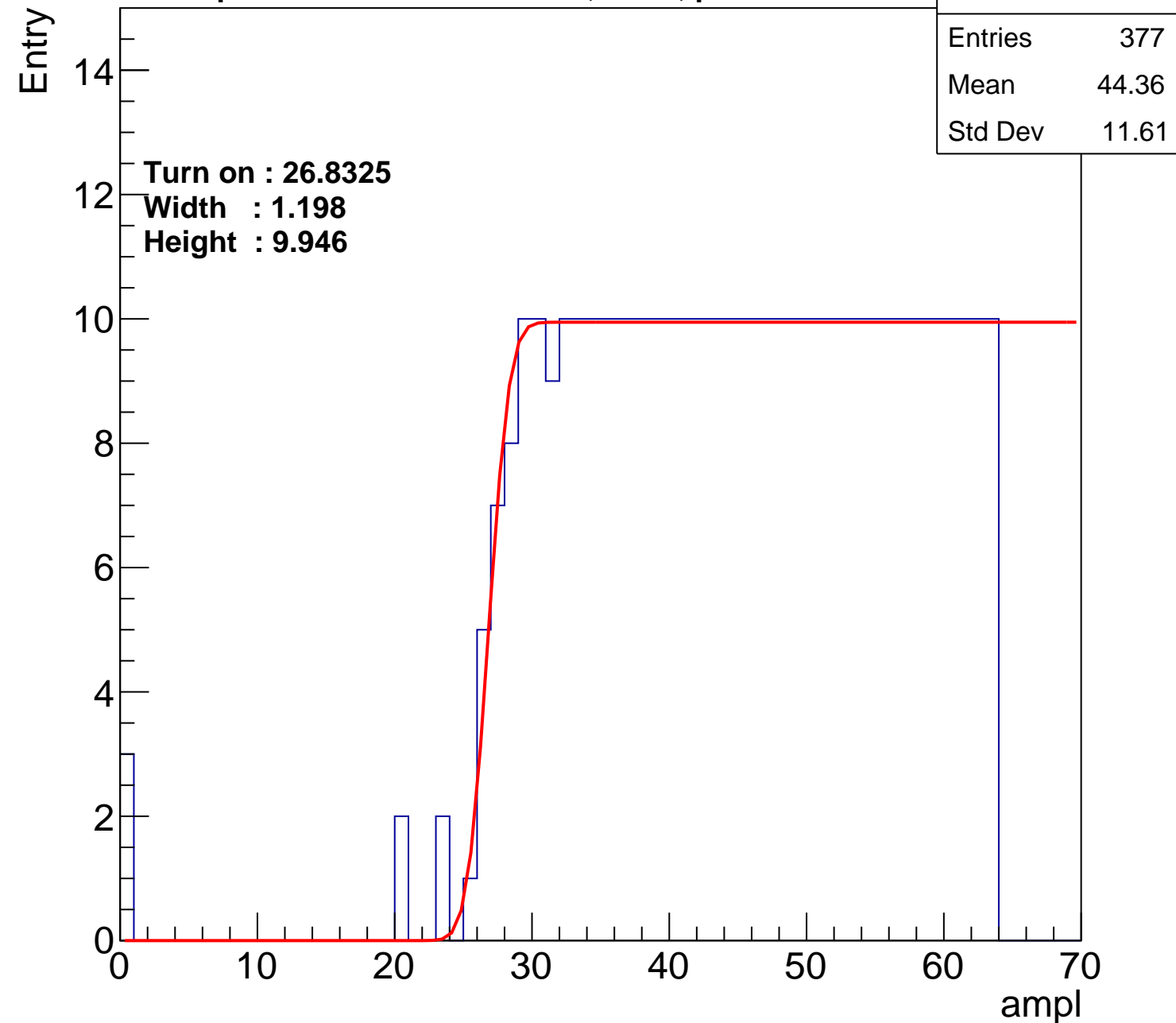
Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U15-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.08
Std Dev	11.89

Turn on : 26.2671

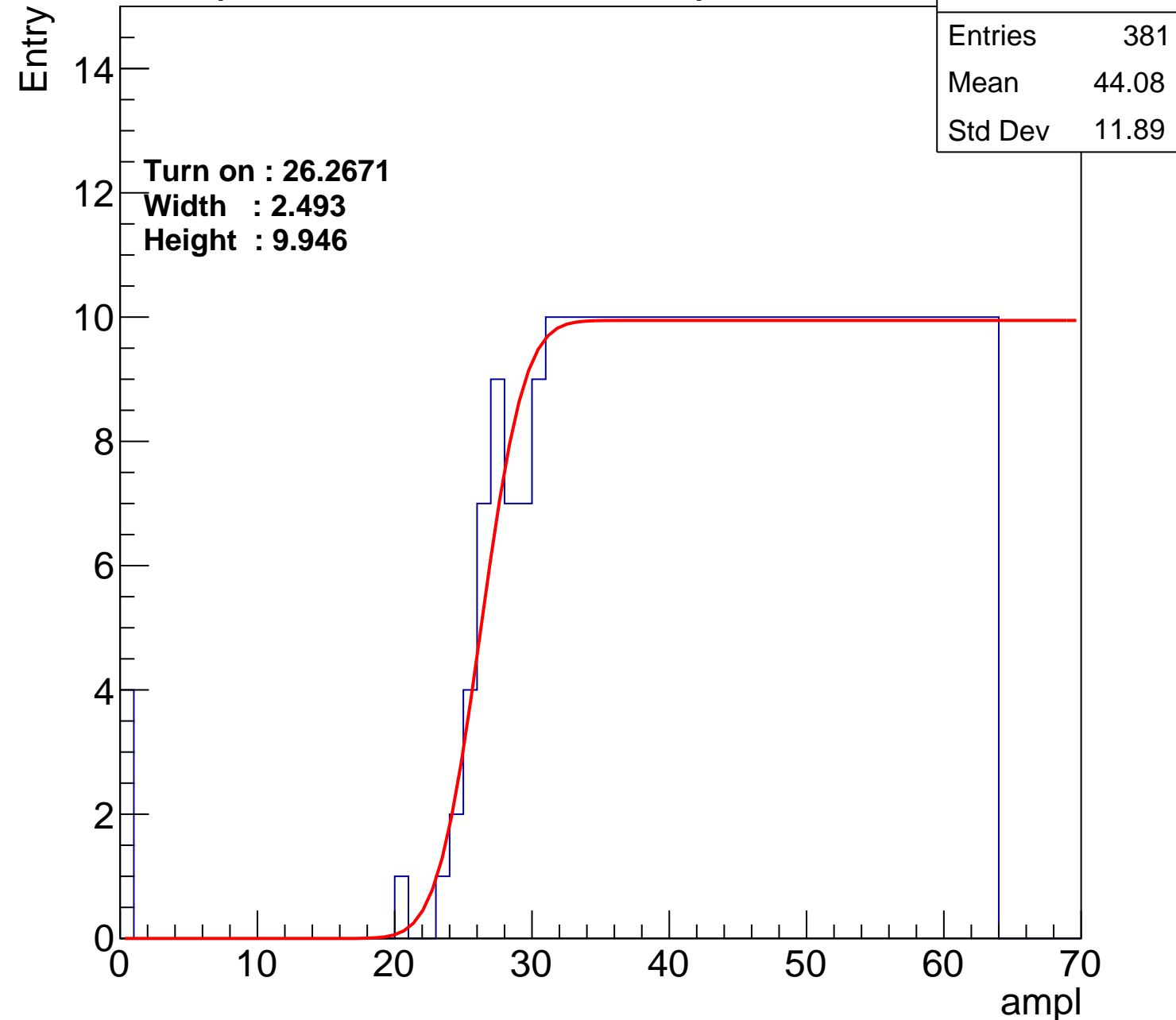
Width : 2.493

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U15-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.08
Std Dev	11.89

Turn on : 26.2671

Width : 2.493

Height : 9.946

Entry

