

B1L101S, U26-ch0, adc0

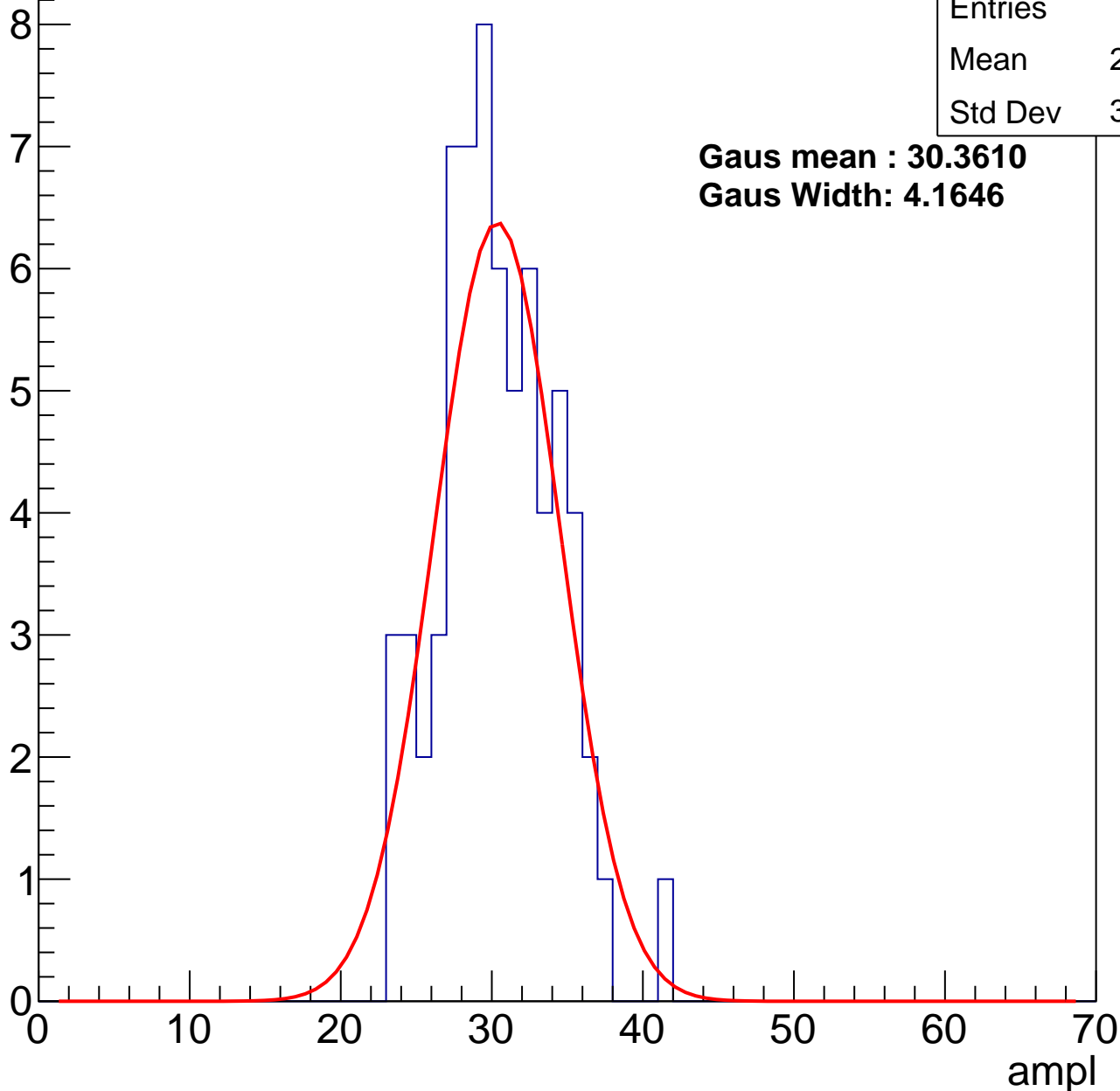
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.93
Std Dev	3.743

Gaus mean : 30.3610

Gaus Width: 4.1646



B1L101S, U26-ch0, adc1

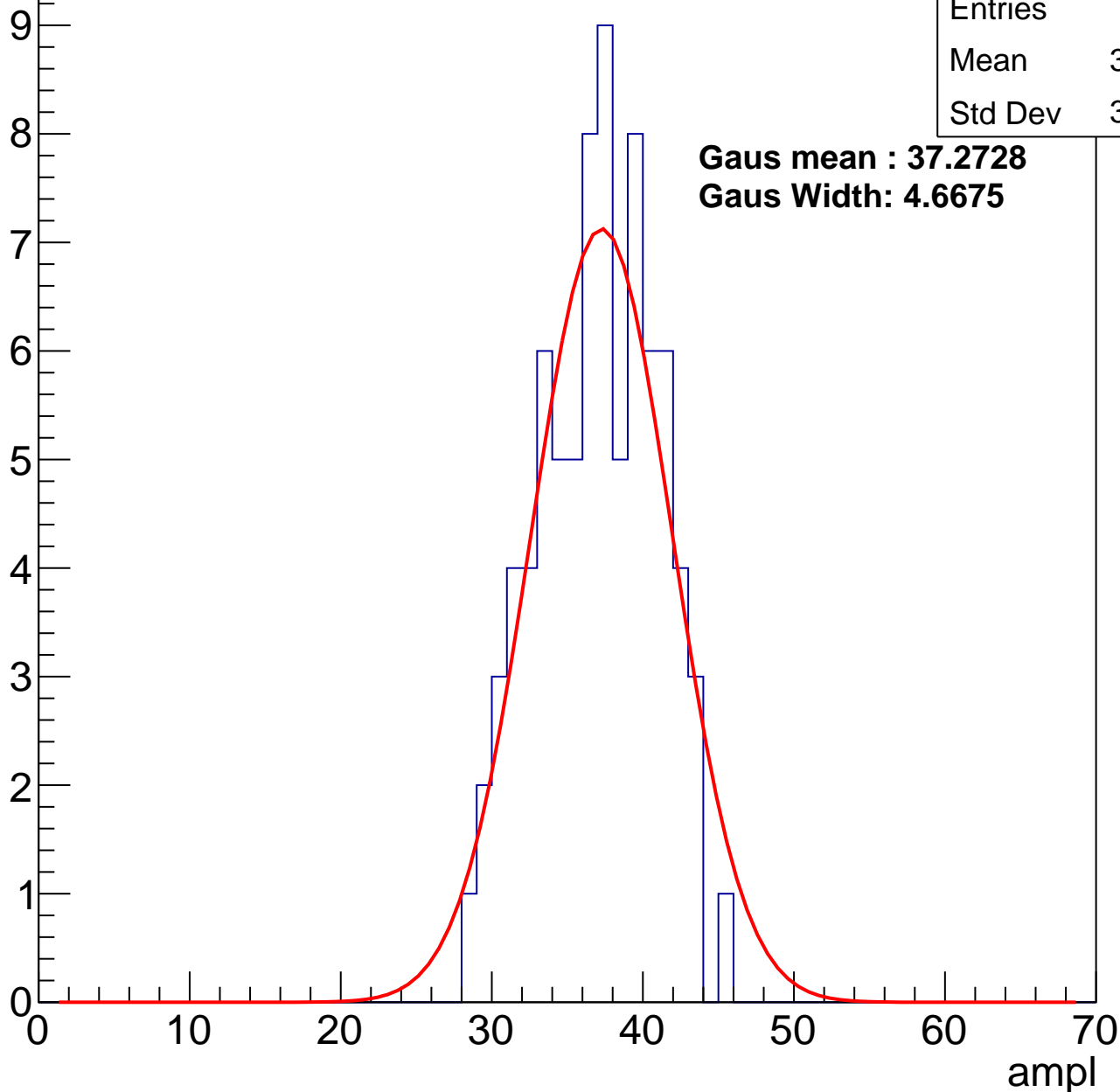
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.52
Std Dev	3.889

Gaus mean : 37.2728

Gaus Width: 4.6675



B1L101S, U26-ch0, adc2

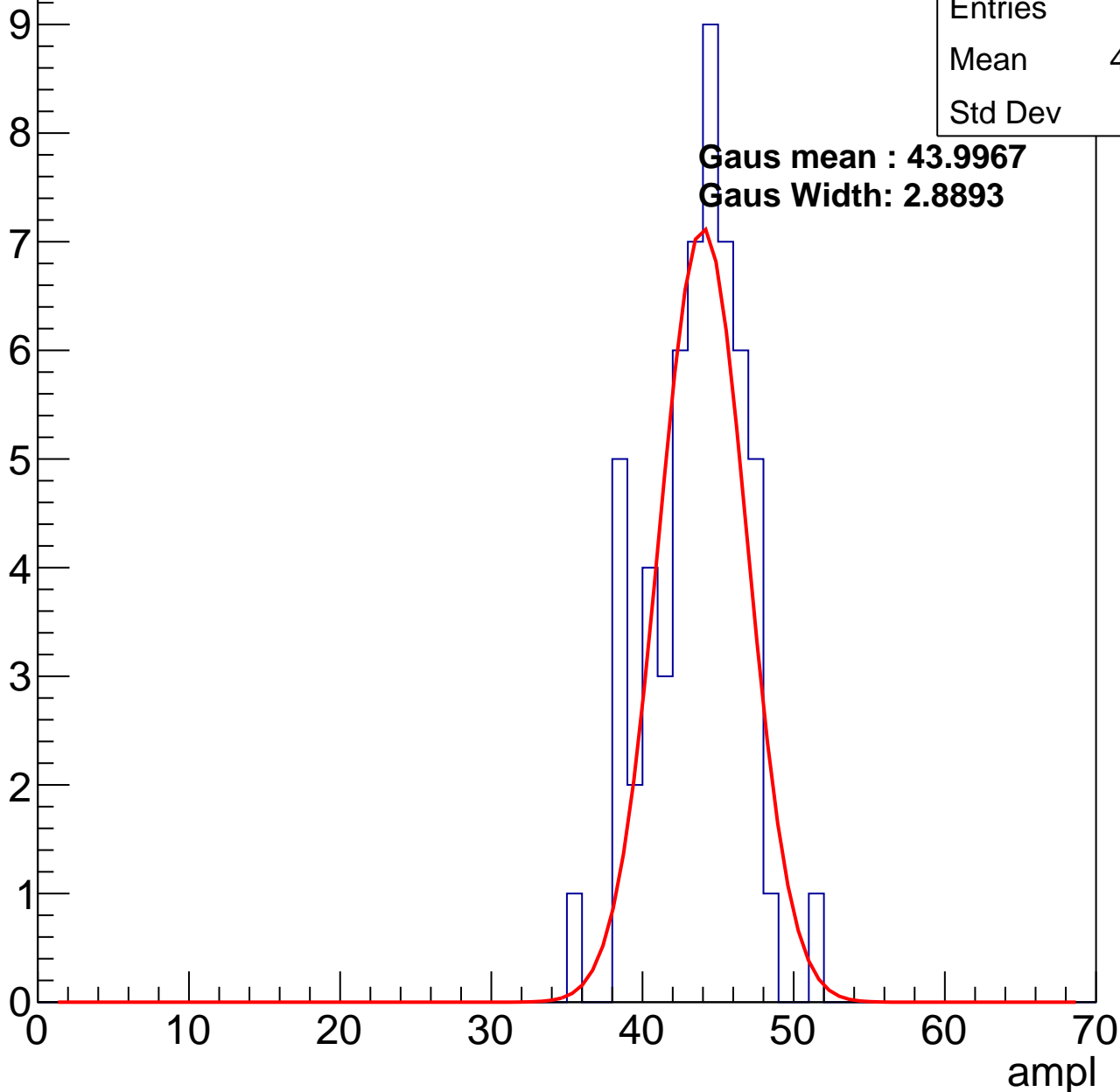
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.16
Std Dev	3.06

Gaus mean : 43.9967

Gaus Width: 2.8893

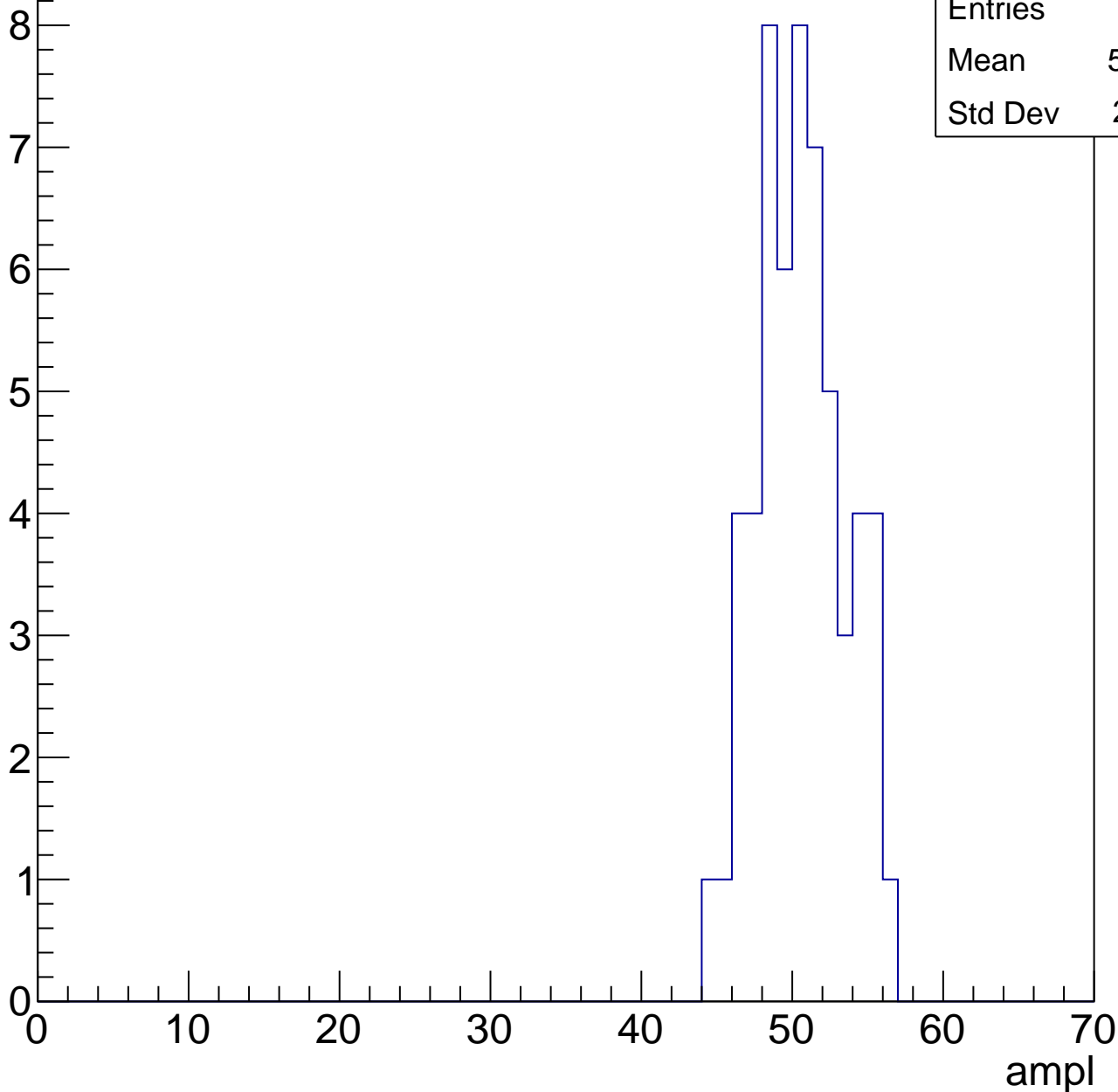


B1L101S, U26-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.12
Std Dev	2.841

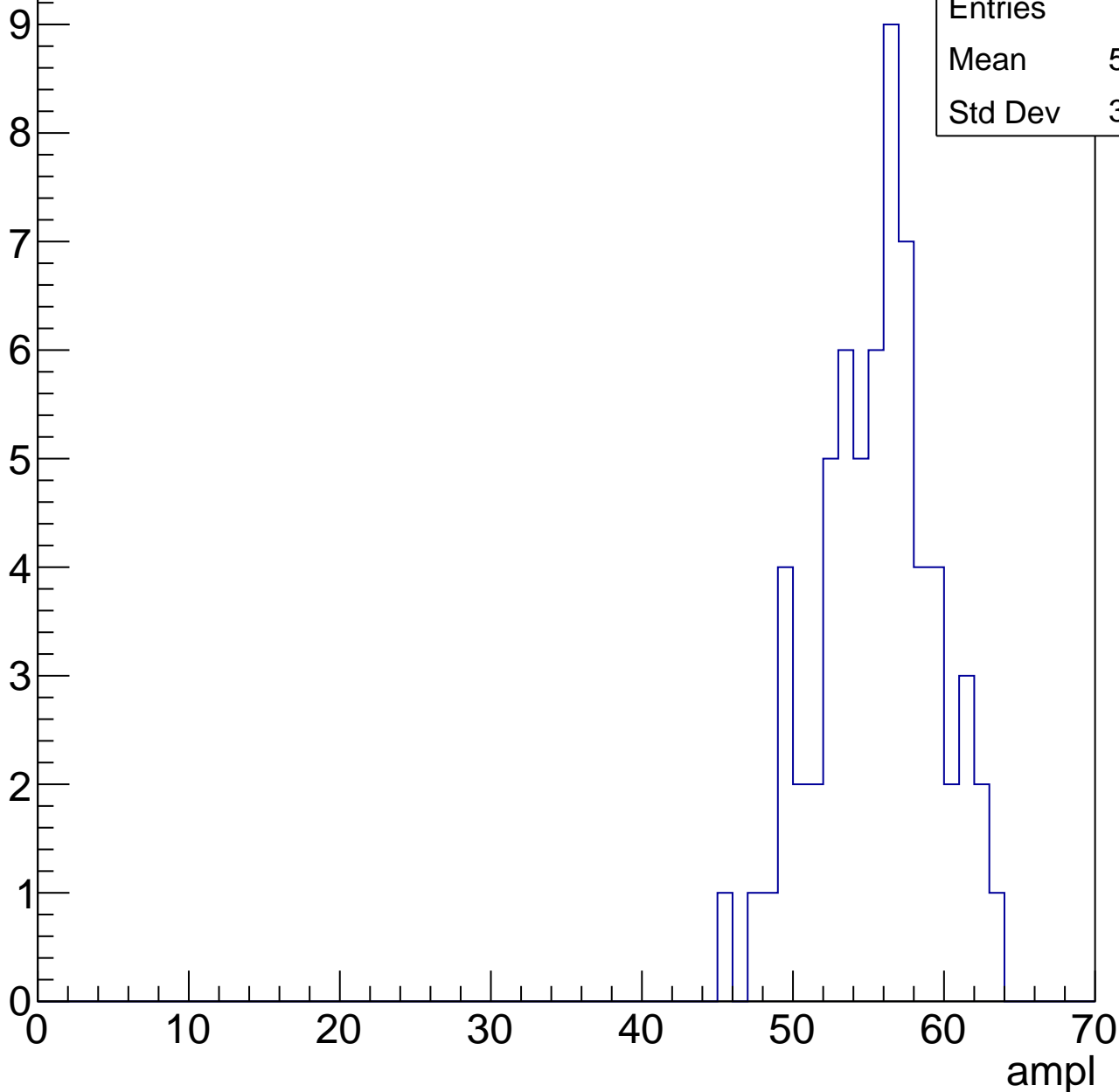


B1L101S, U26-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.03
Std Dev	3.863

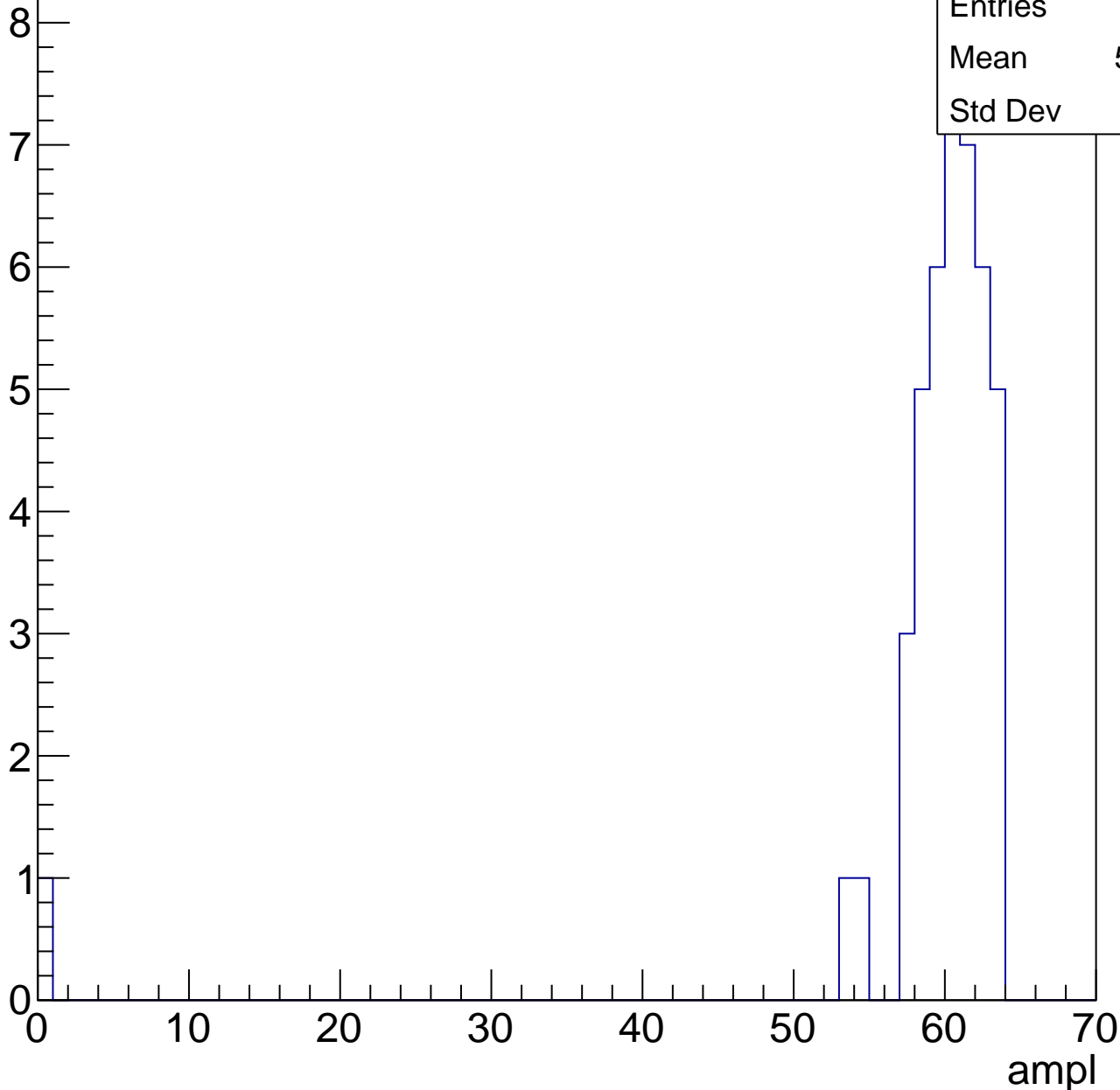


B1L101S, U26-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

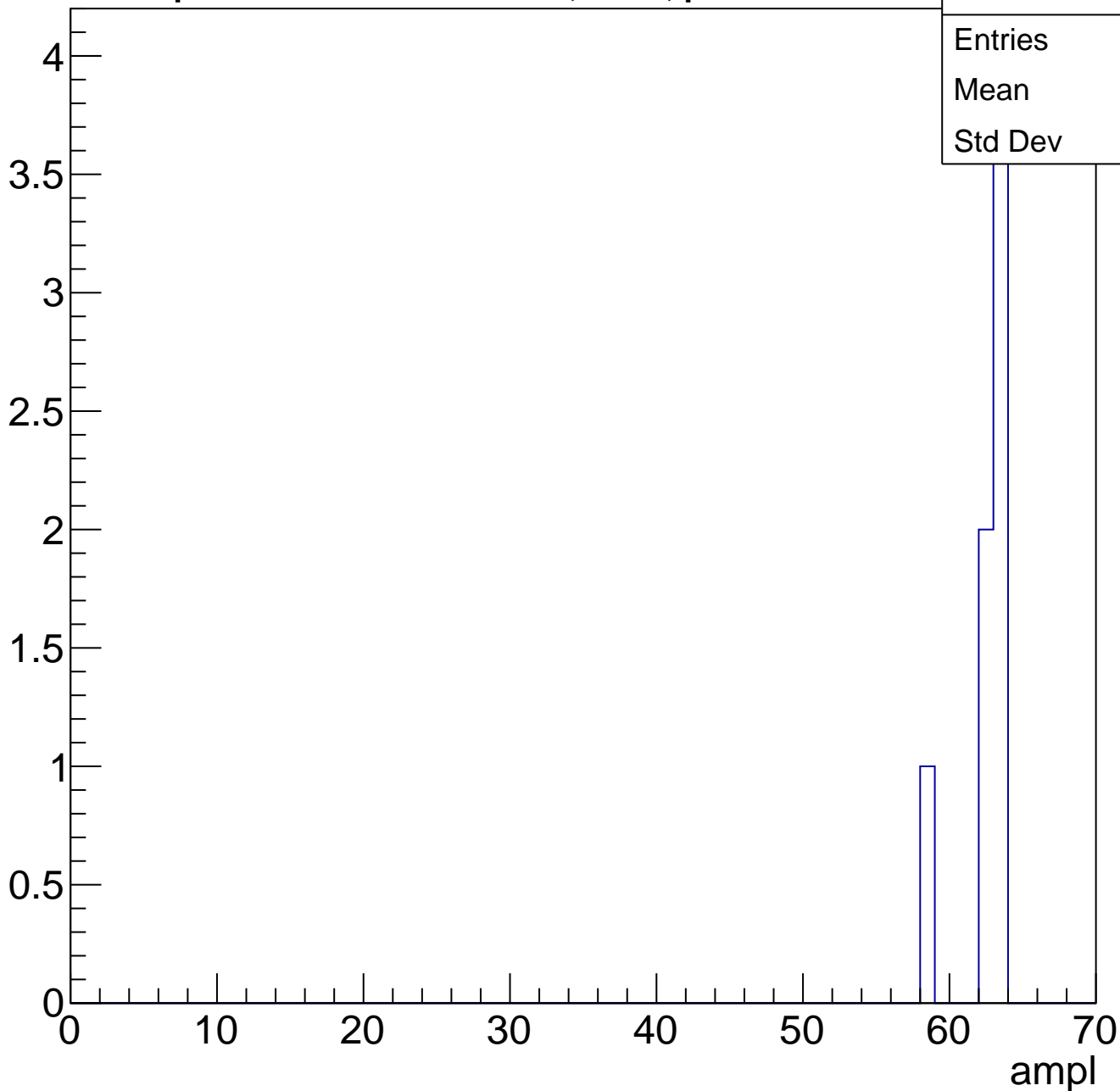
Entries	43
Mean	58.51
Std Dev	9.3



B1L101S, U26-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch1, adc0

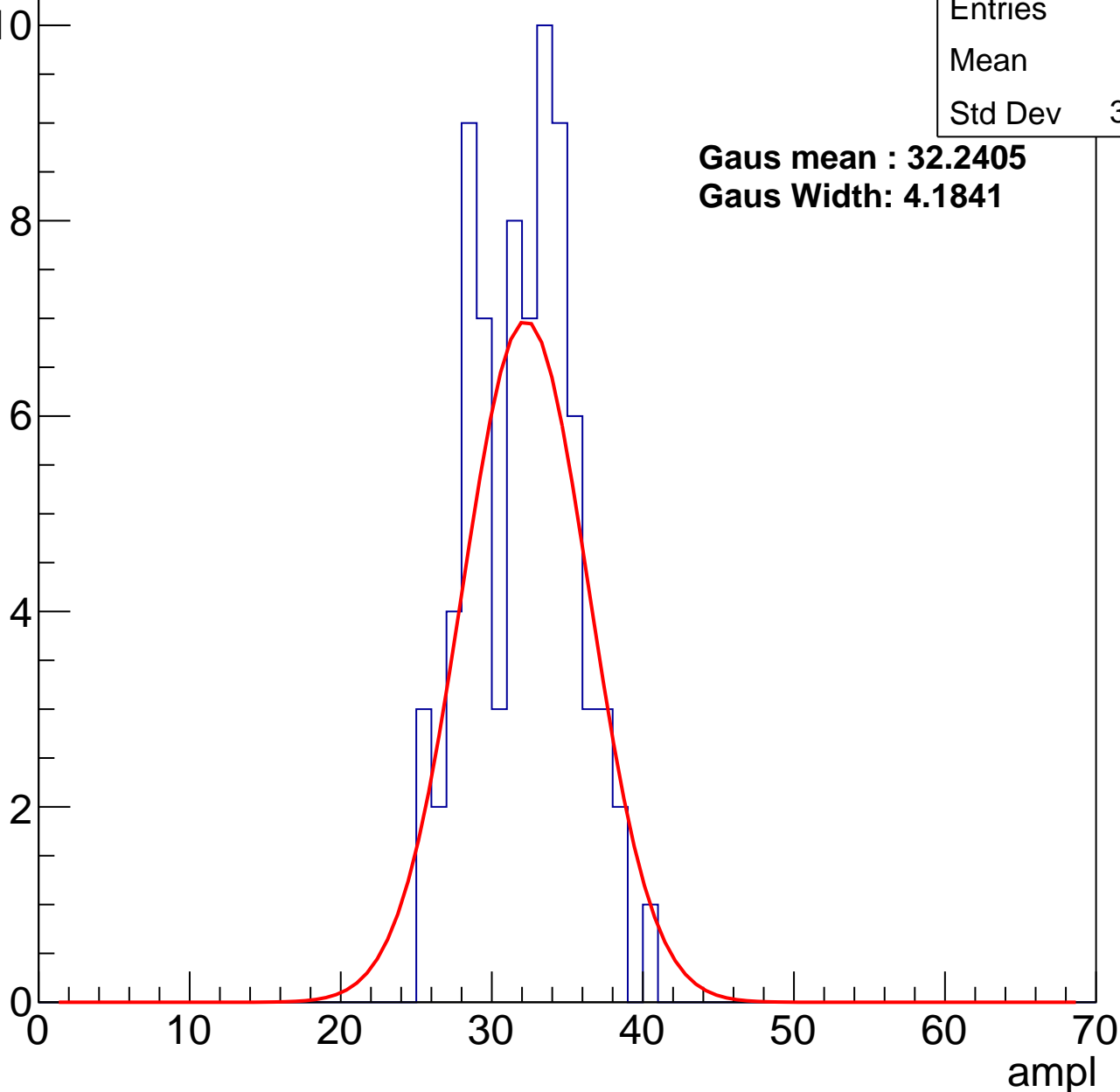
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	31.6
Std Dev	3.405

Gaus mean : 32.2405

Gaus Width: 4.1841



B1L101S, U26-ch1, adc1

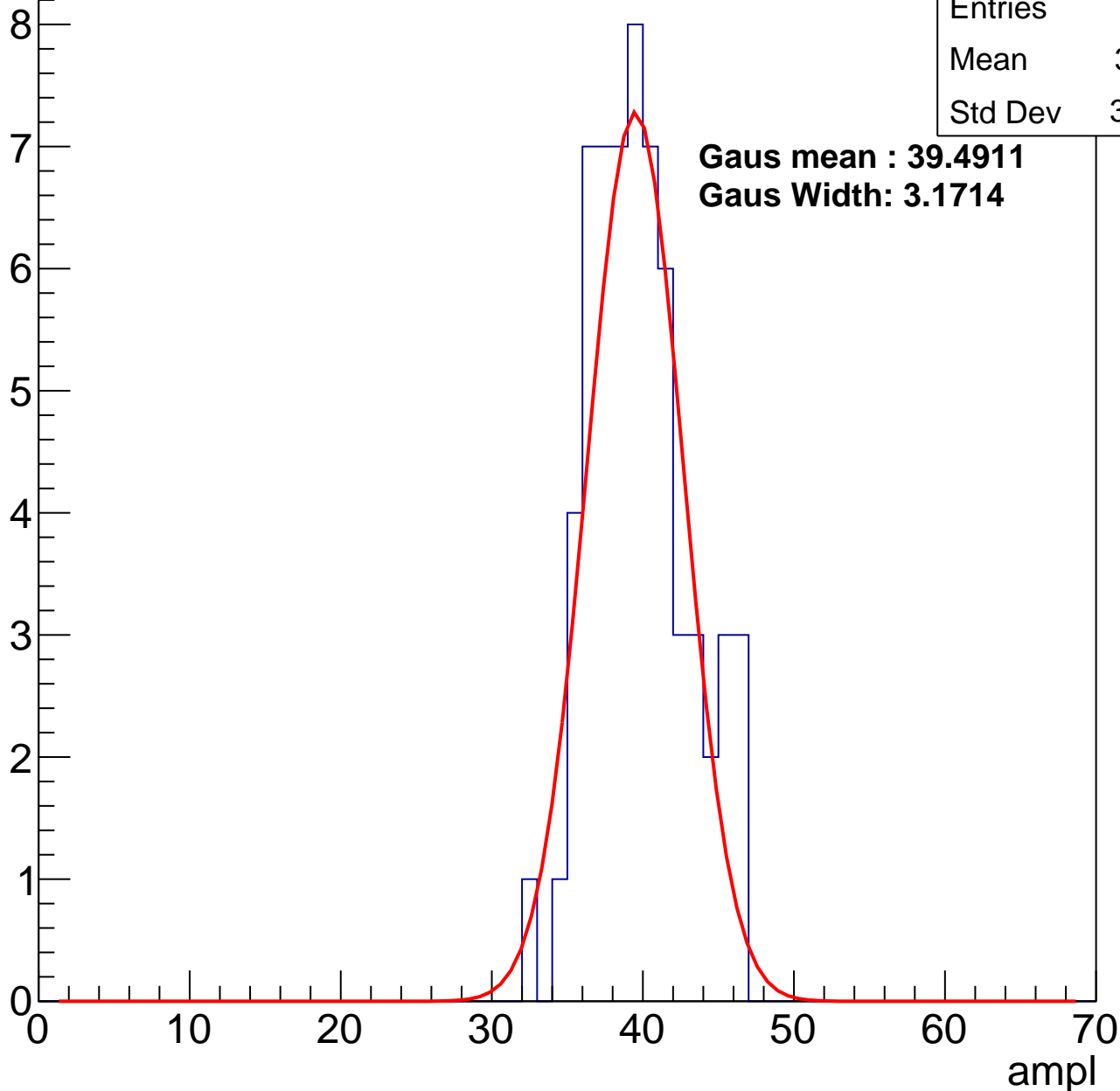
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	39.31
Std Dev	3.216

Gaus mean : 39.4911

Gaus Width: 3.1714



B1L101S, U26-ch1, adc2

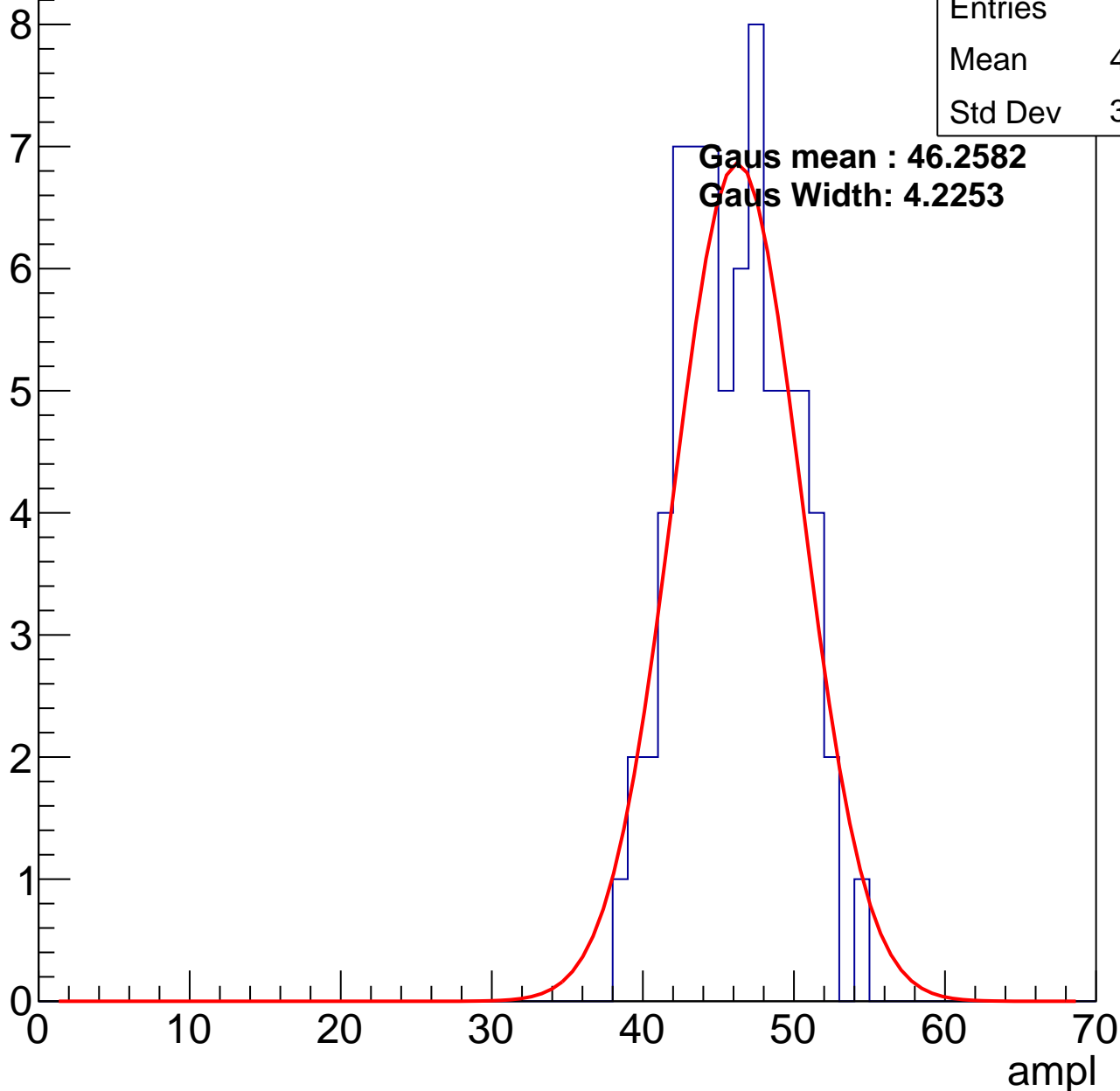
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	45.59
Std Dev	3.598

Gaus mean : 46.2582

Gaus Width: 4.2253

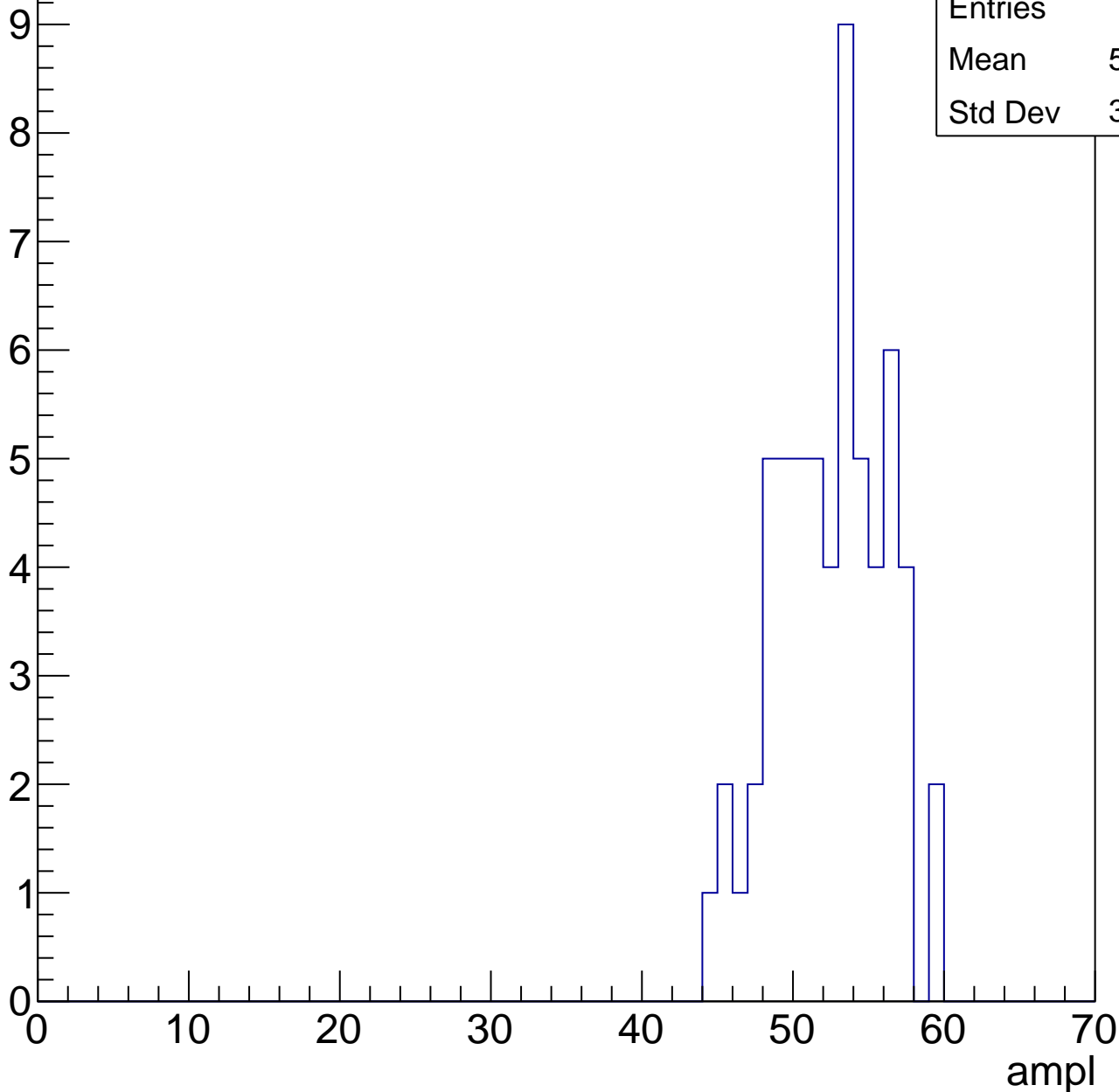


B1L101S, U26-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	52.02
Std Dev	3.552

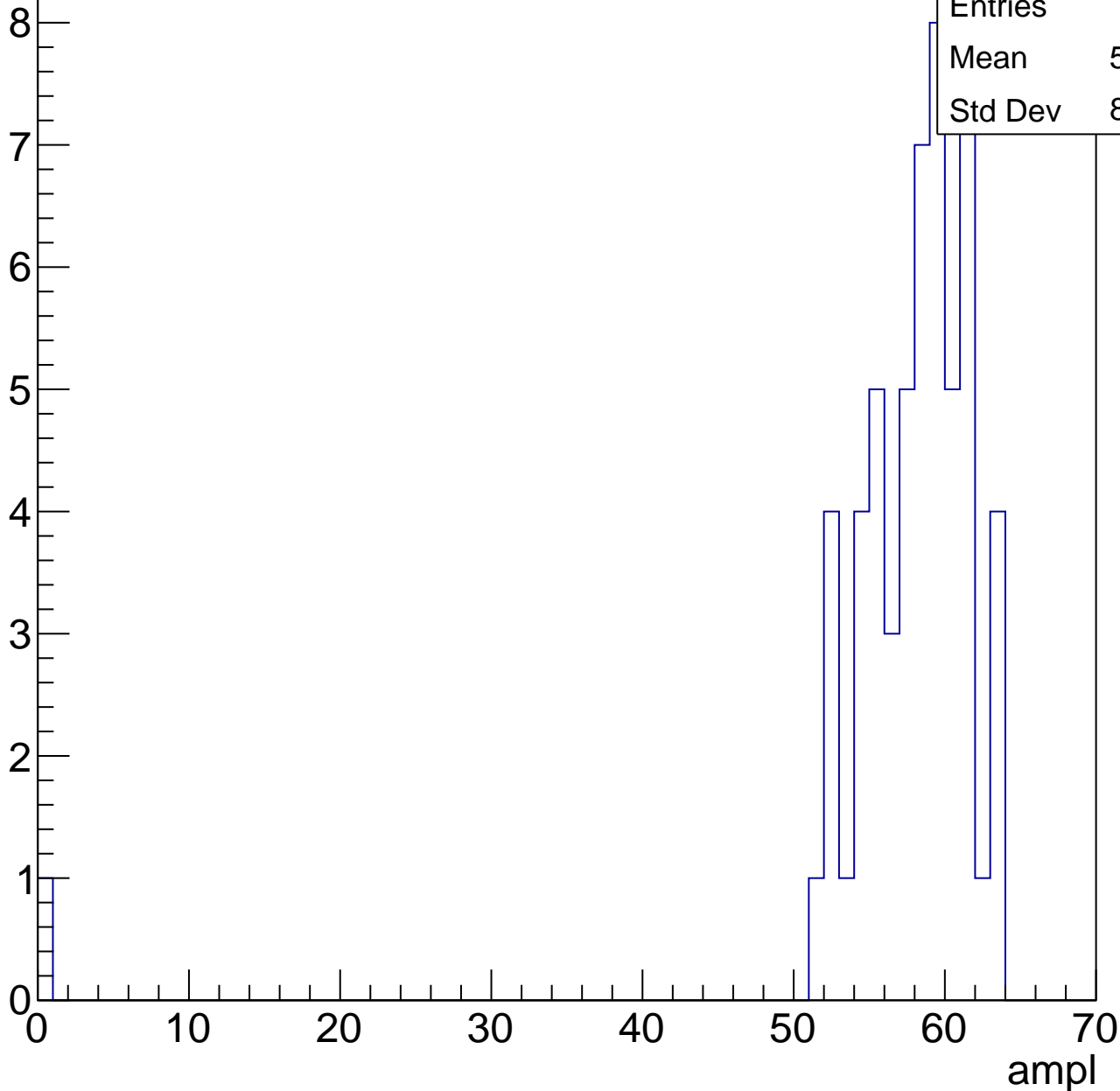


B1L101S, U26-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.77
Std Dev	8.206

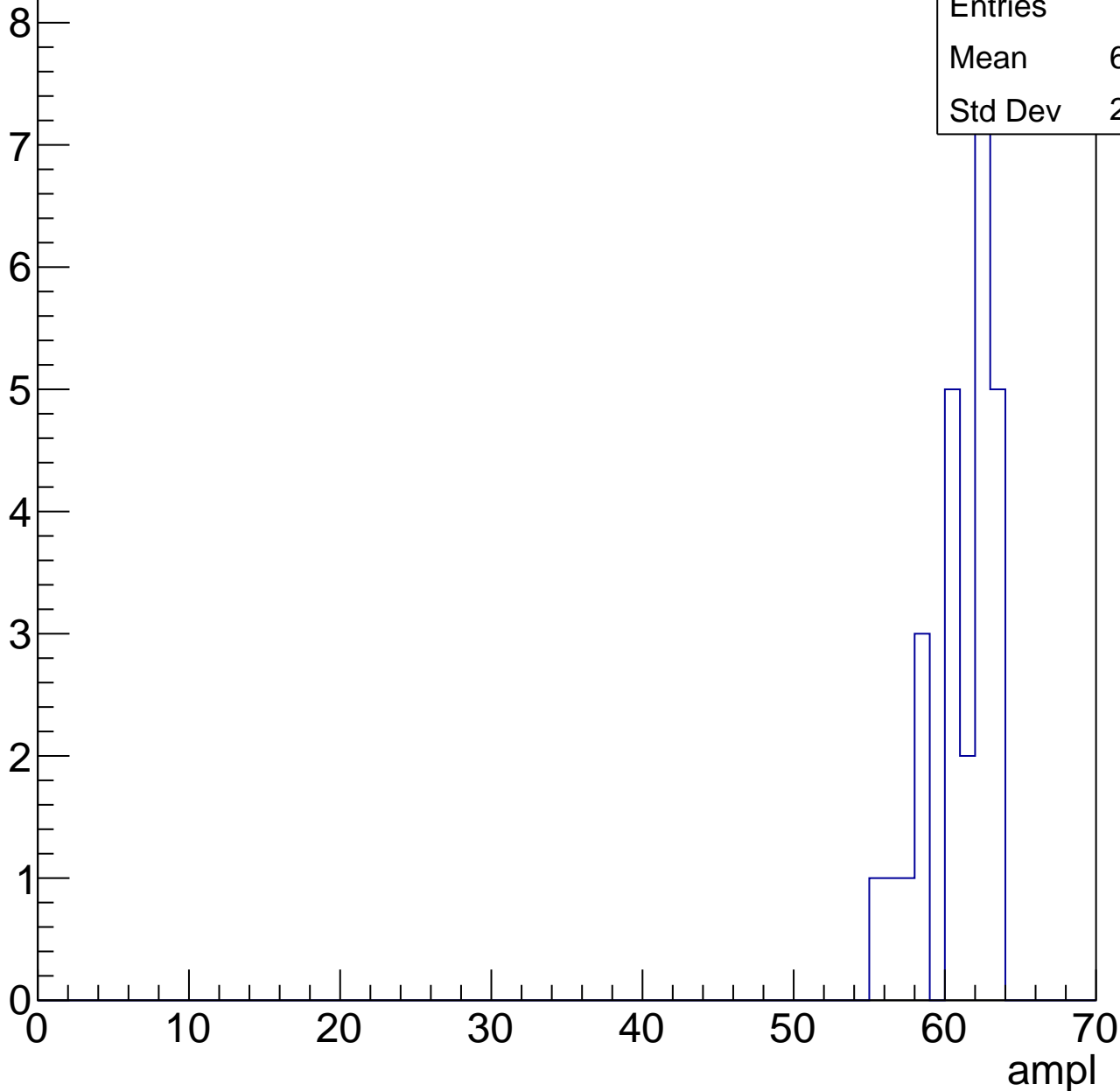


B1L101S, U26-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	60.58
Std Dev	2.256



B1L101S, U26-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch2, adc0

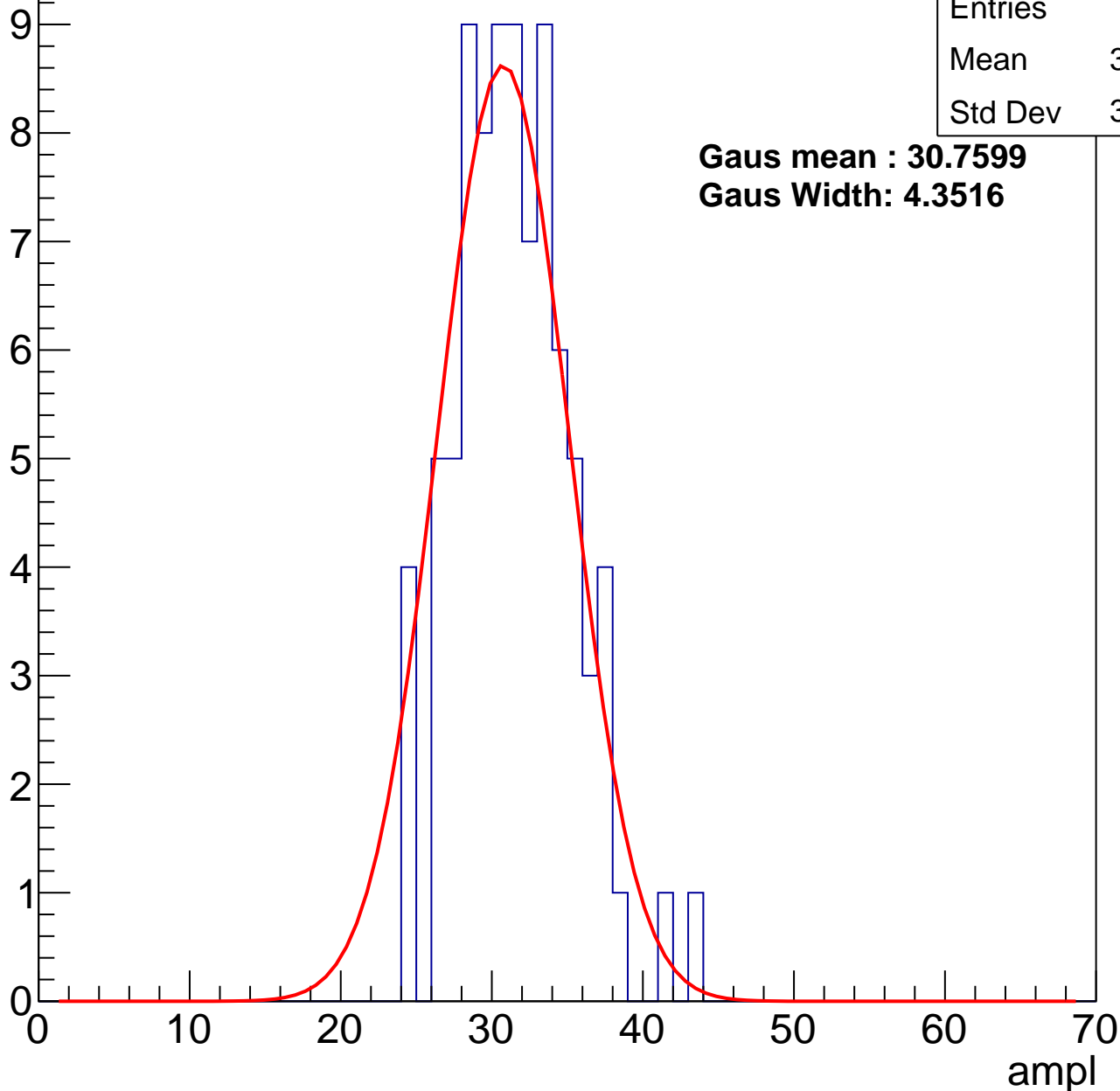
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	31.07
Std Dev	3.753

Gaus mean : 30.7599

Gaus Width: 4.3516



B1L101S, U26-ch2, adc1

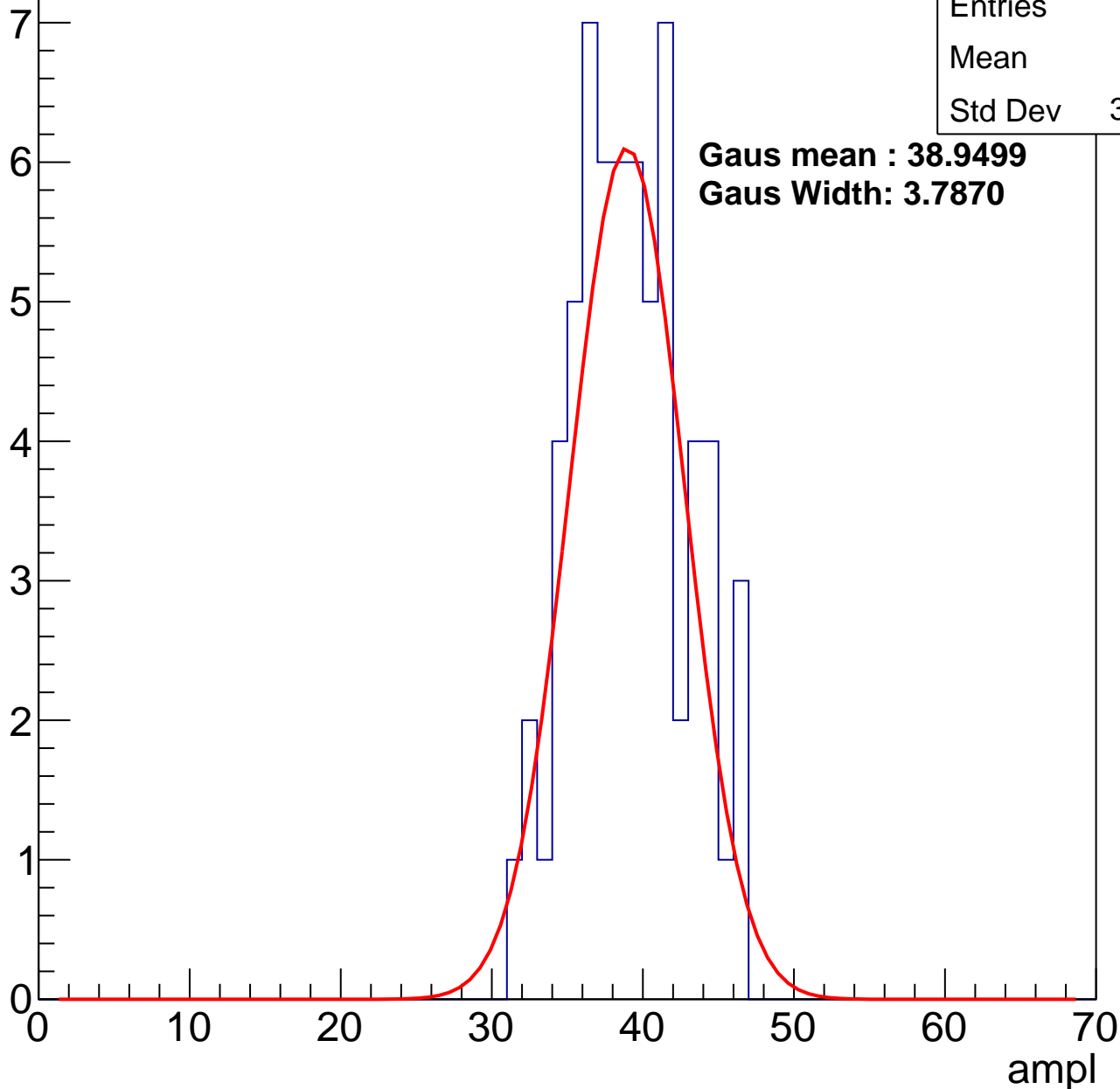
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	38.7
Std Dev	3.673

Gaus mean : 38.9499

Gaus Width: 3.7870



B1L101S, U26-ch2, adc2

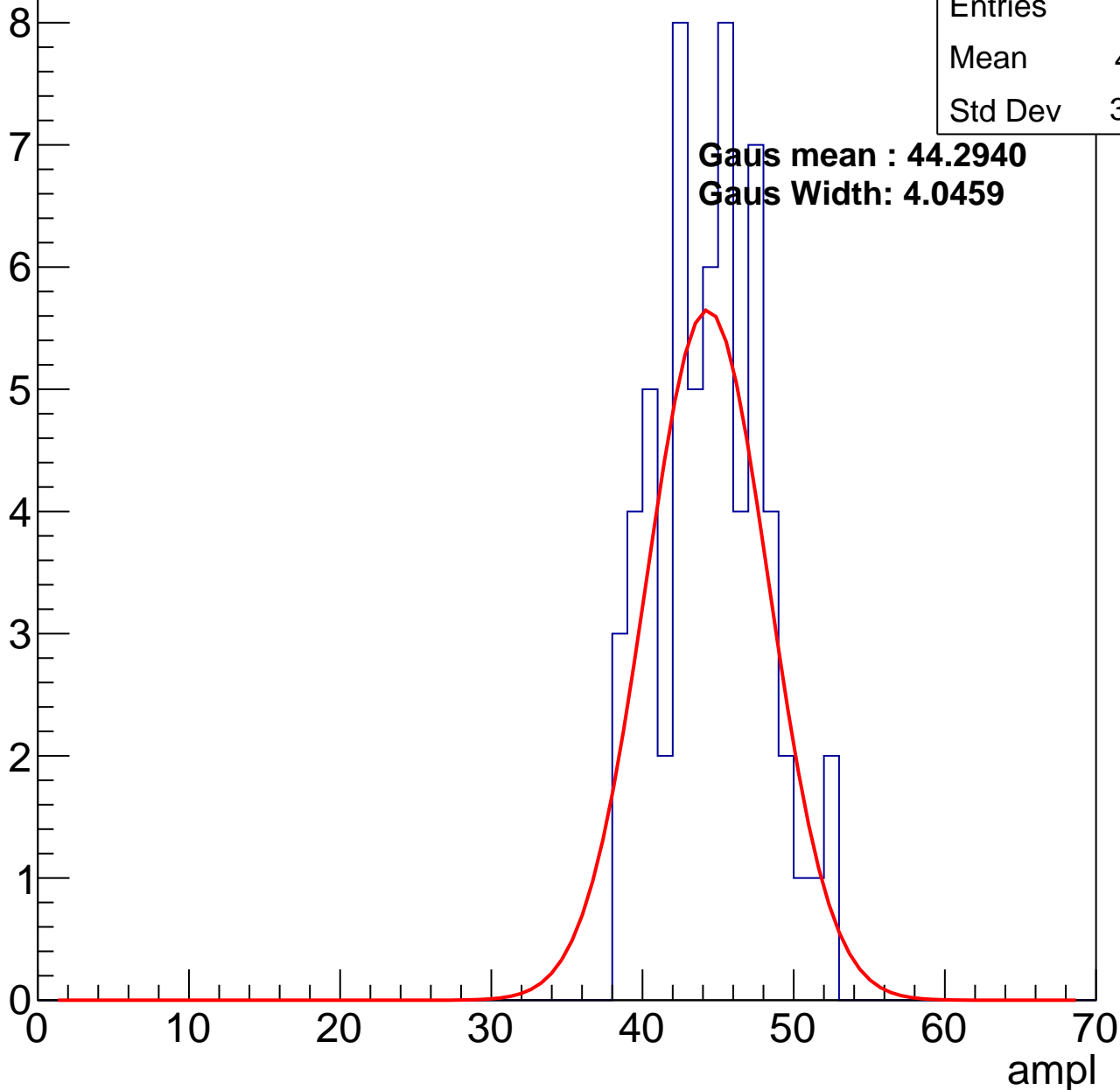
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.11
Std Dev	3.492

Gaus mean : 44.2940

Gaus Width: 4.0459

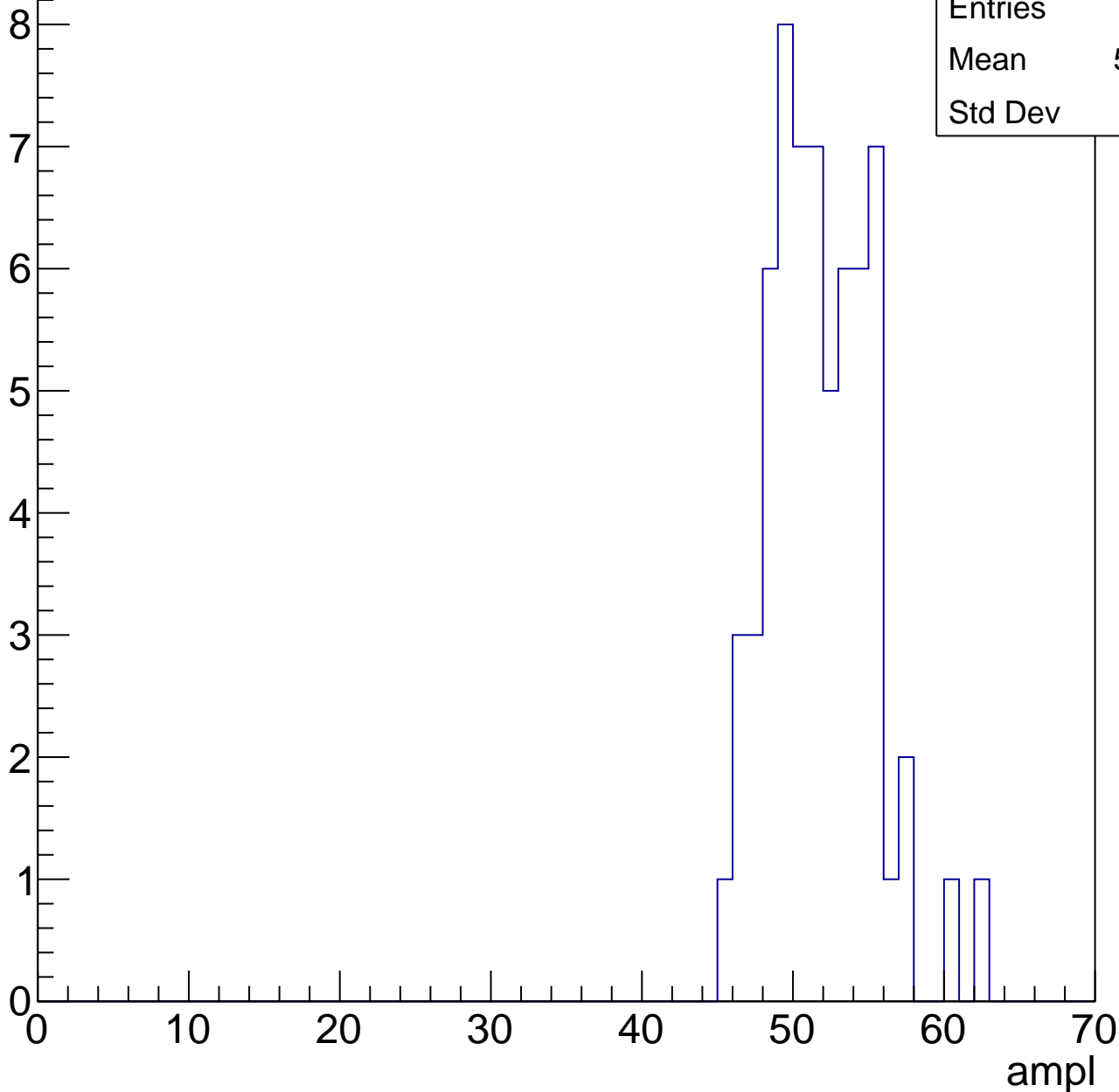


B1L101S, U26-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

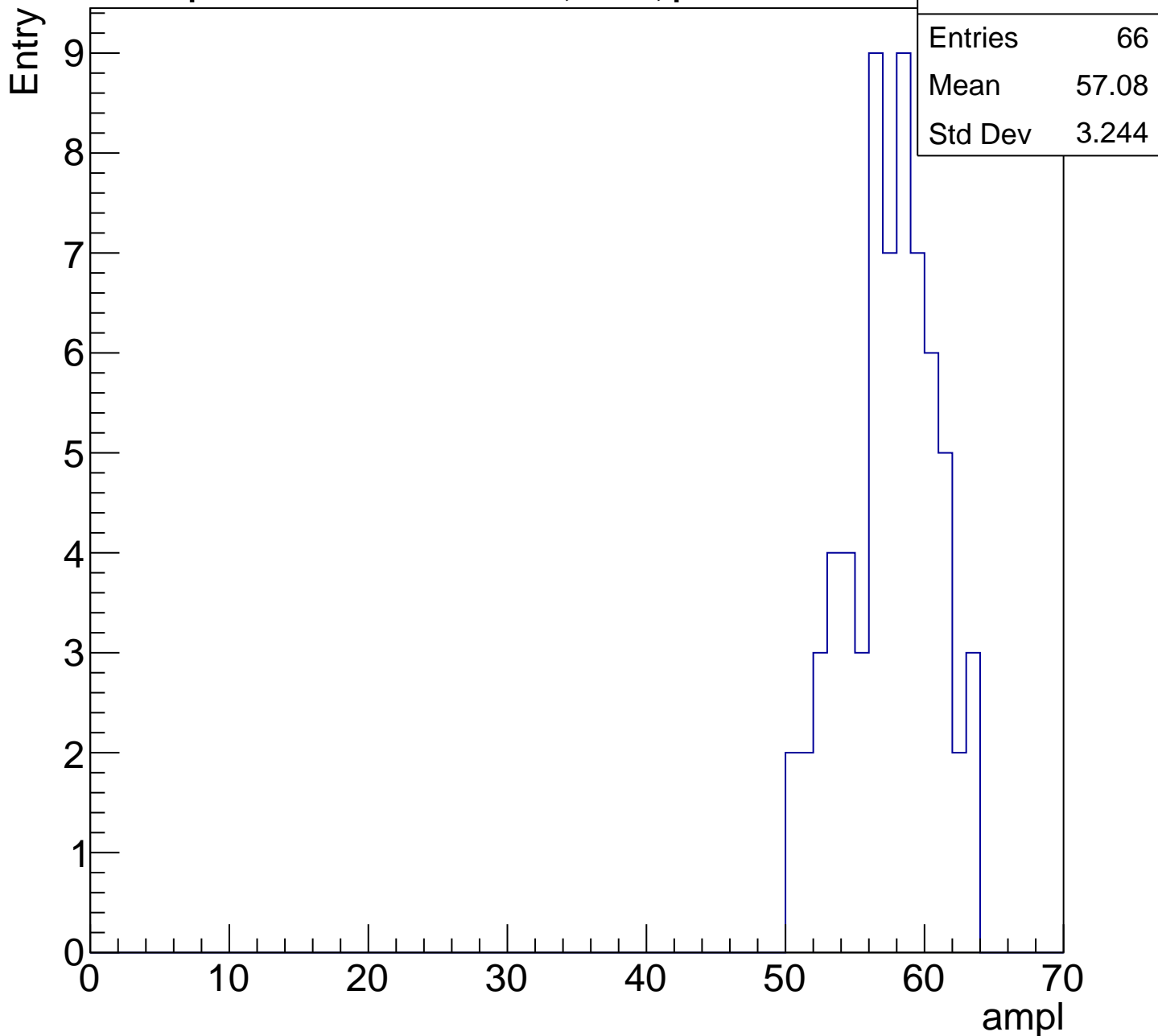
Entry

Entries	64
Mean	51.41
Std Dev	3.39



B1L101S, U26-ch2, adc4

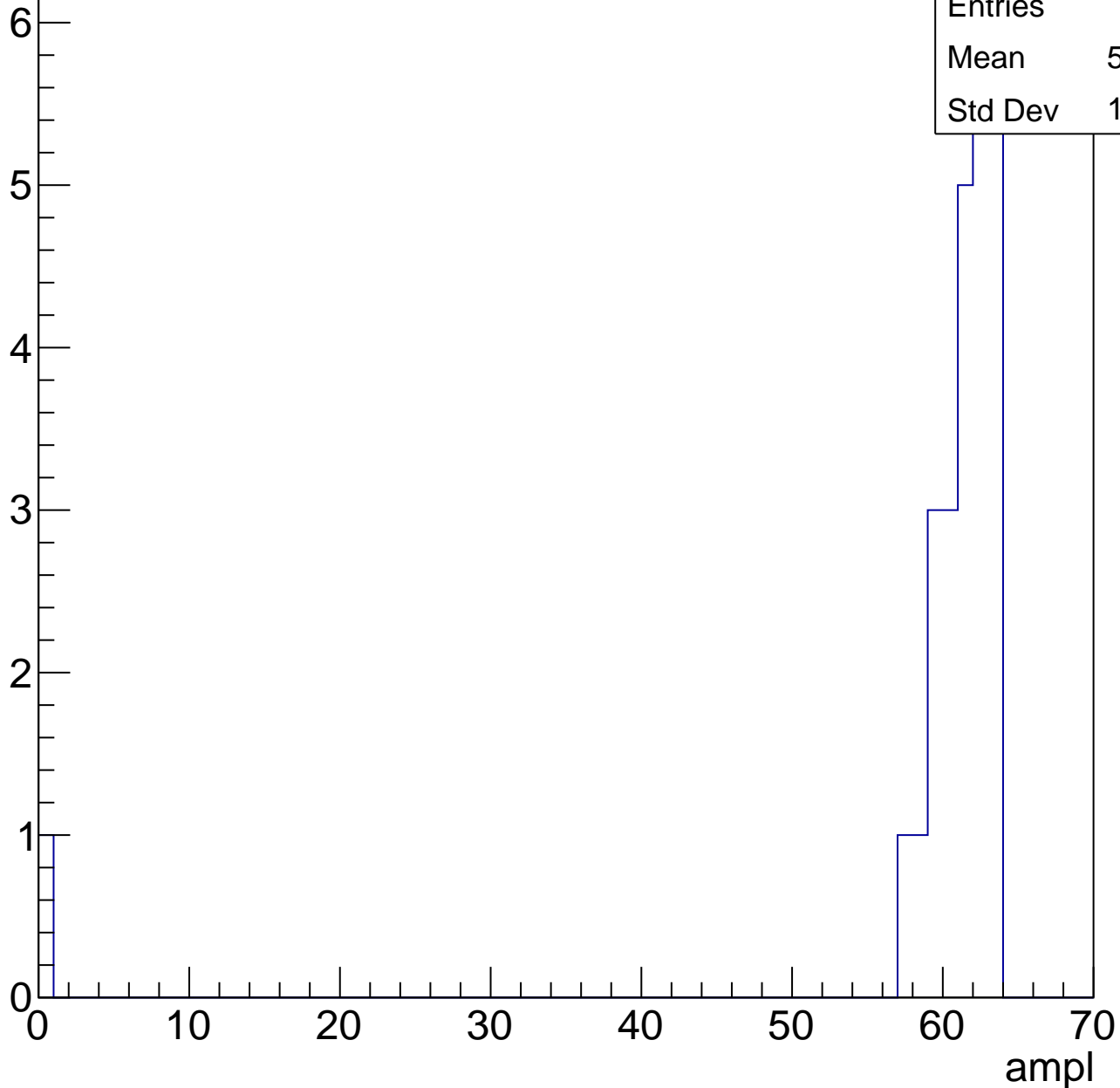
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L101S, U26-ch3, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	32.45
Std Dev	5.161

Gaus mean : 33.4641

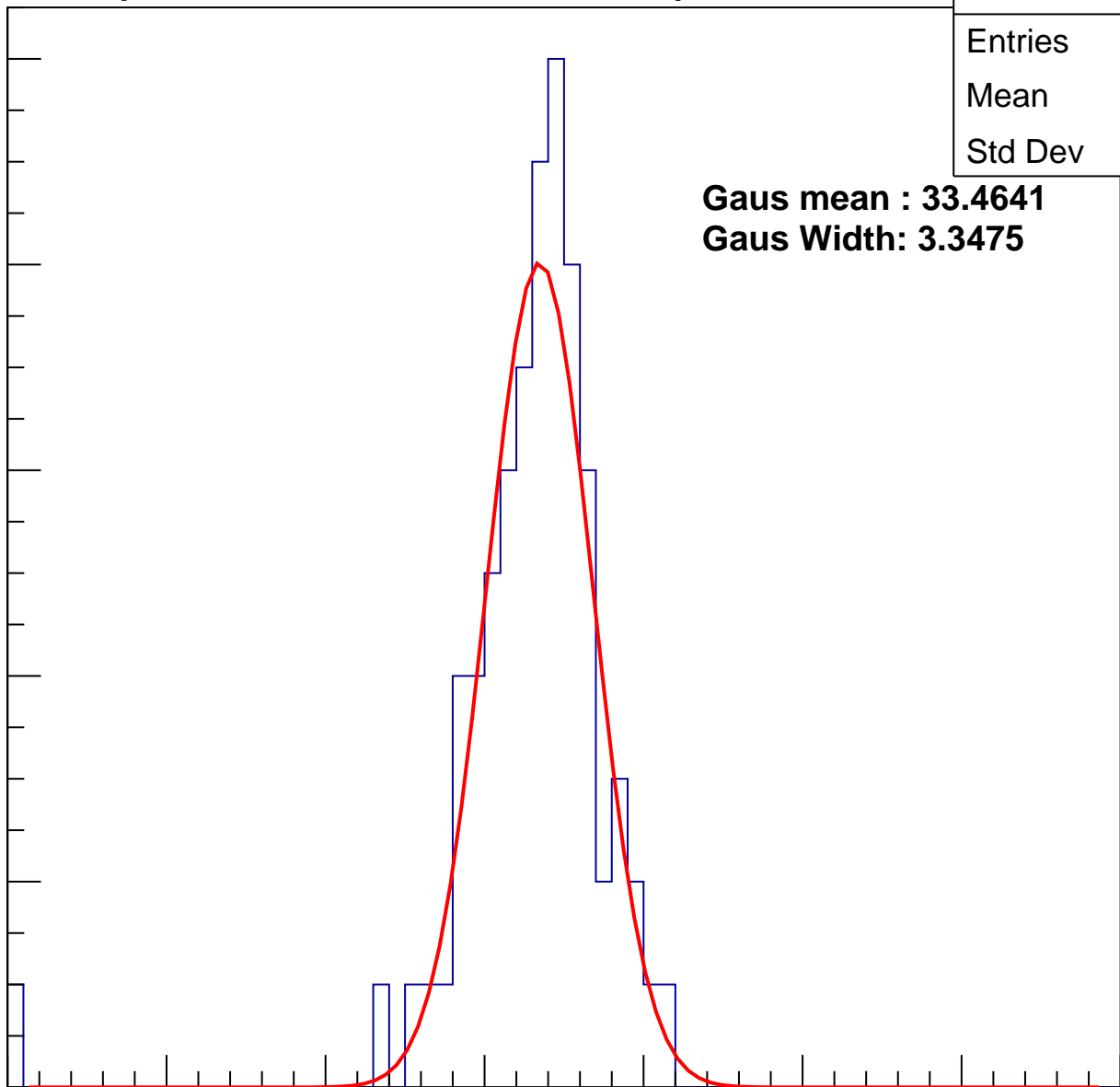
Gaus Width: 3.3475

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch3, adc1

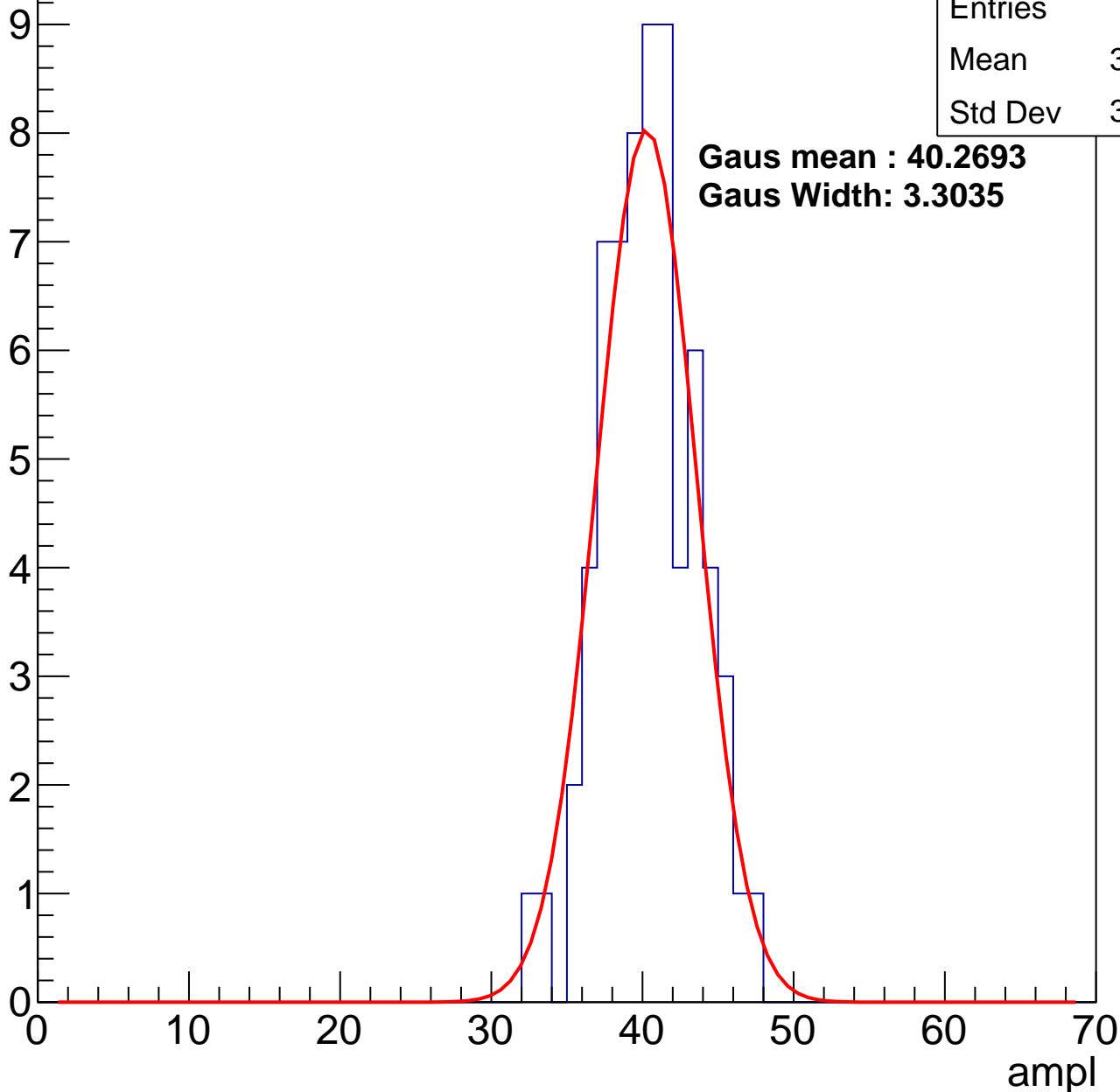
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	39.93
Std Dev	3.063

Gaus mean : 40.2693

Gaus Width: 3.3035



B1L101S, U26-ch3, adc2

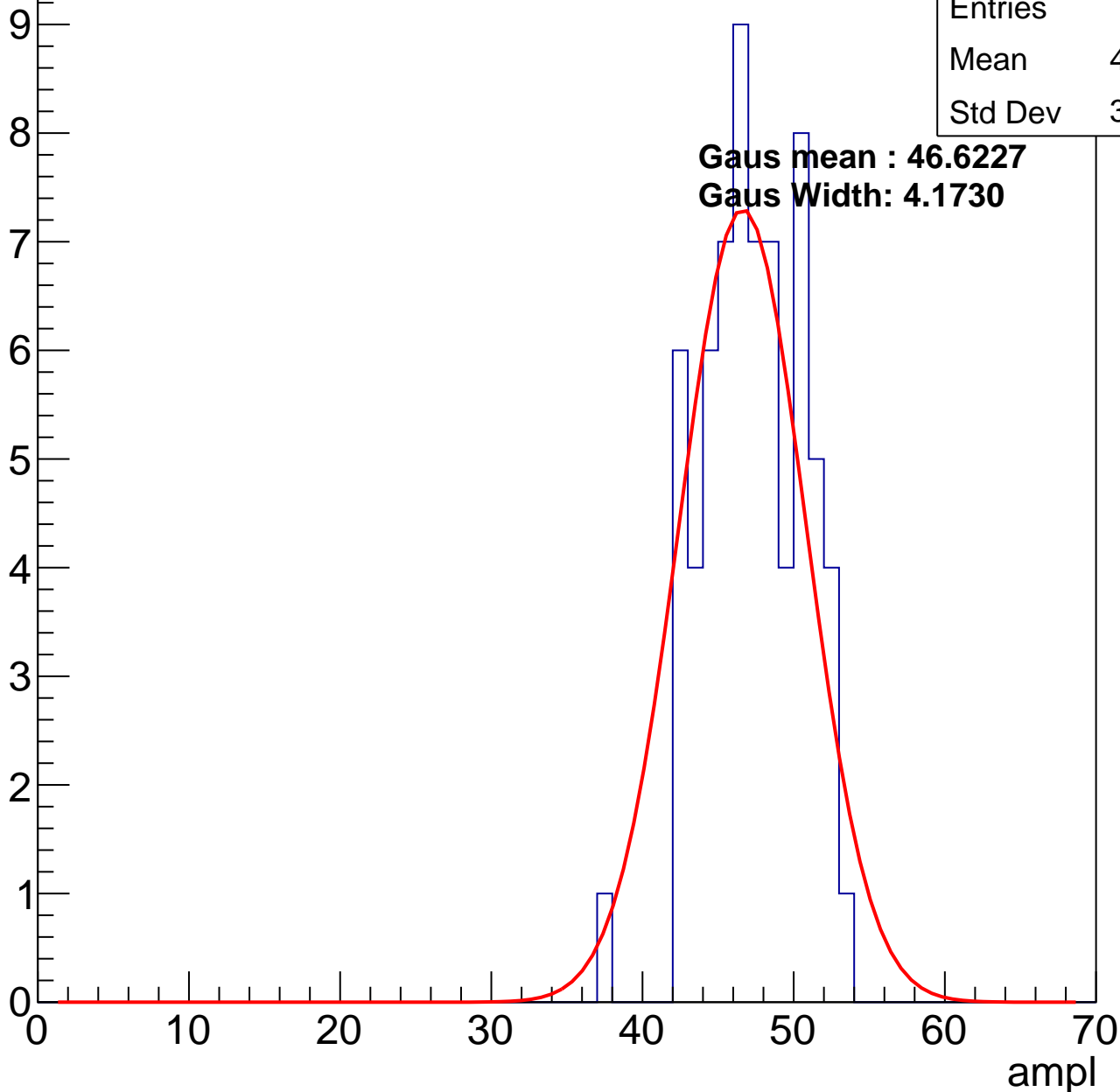
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	46.83
Std Dev	3.217

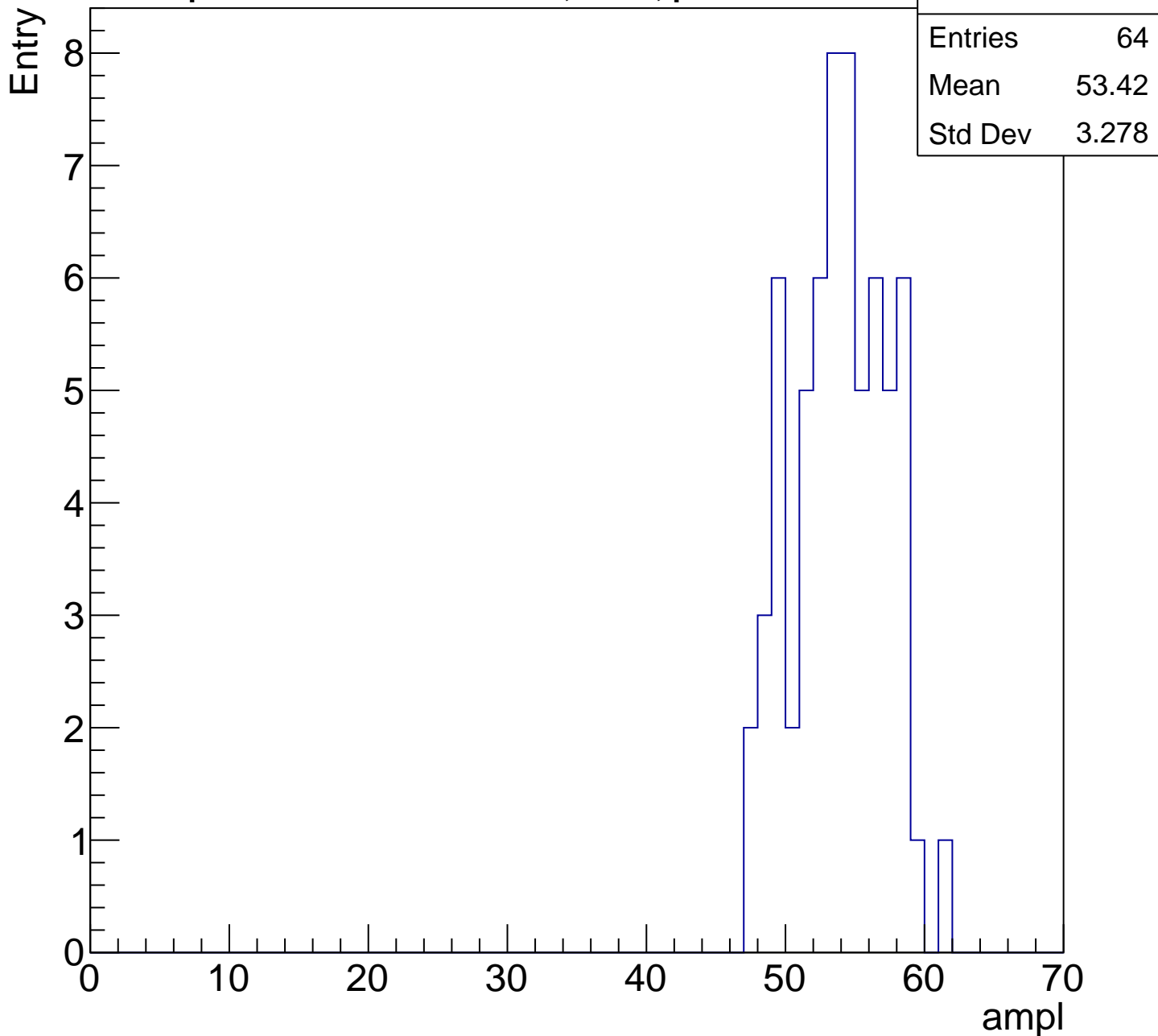
Gaus mean : 46.6227

Gaus Width: 4.1730



B1L101S, U26-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch3, adc4

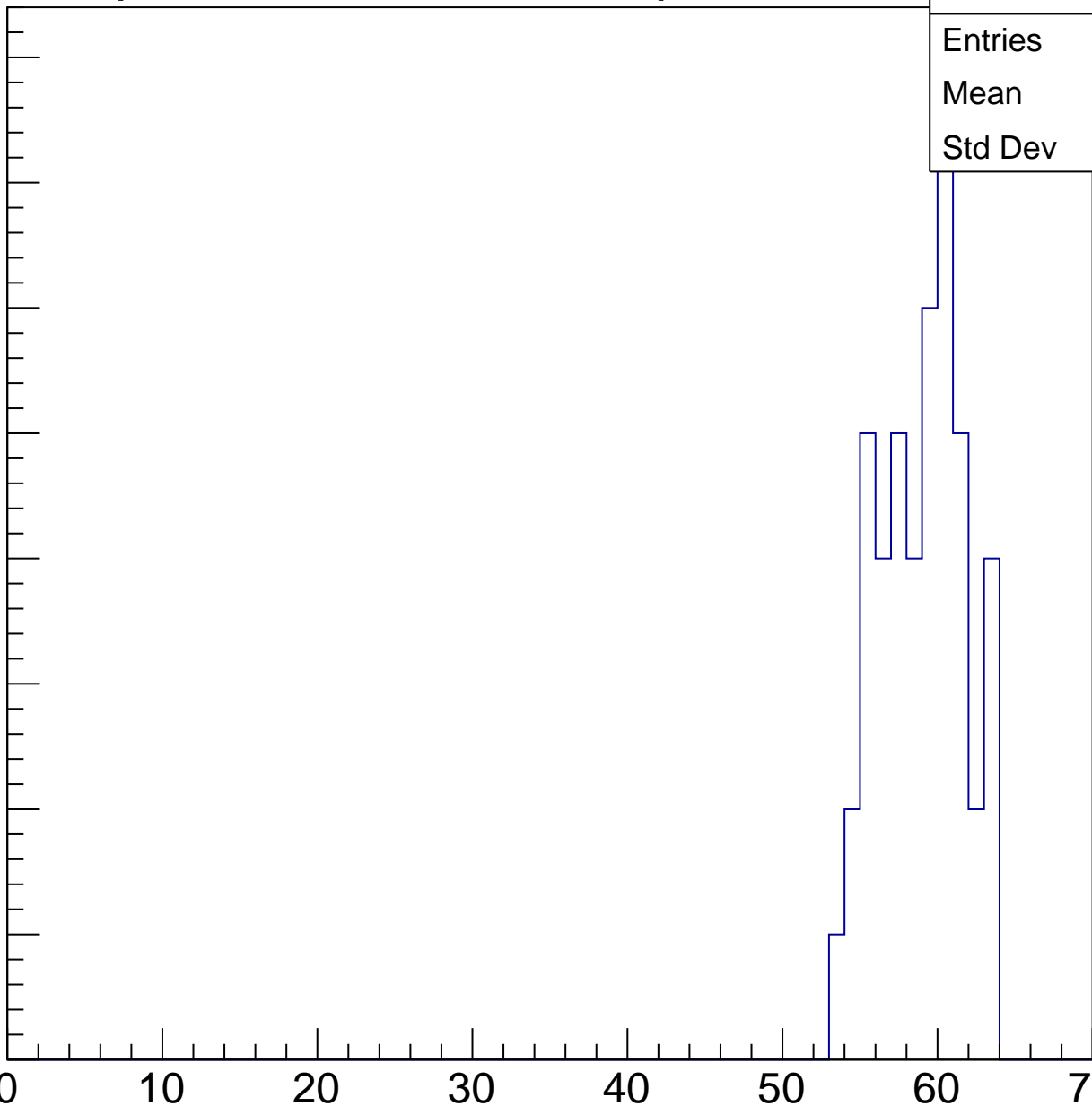
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.52
Std Dev	2.66

ampl

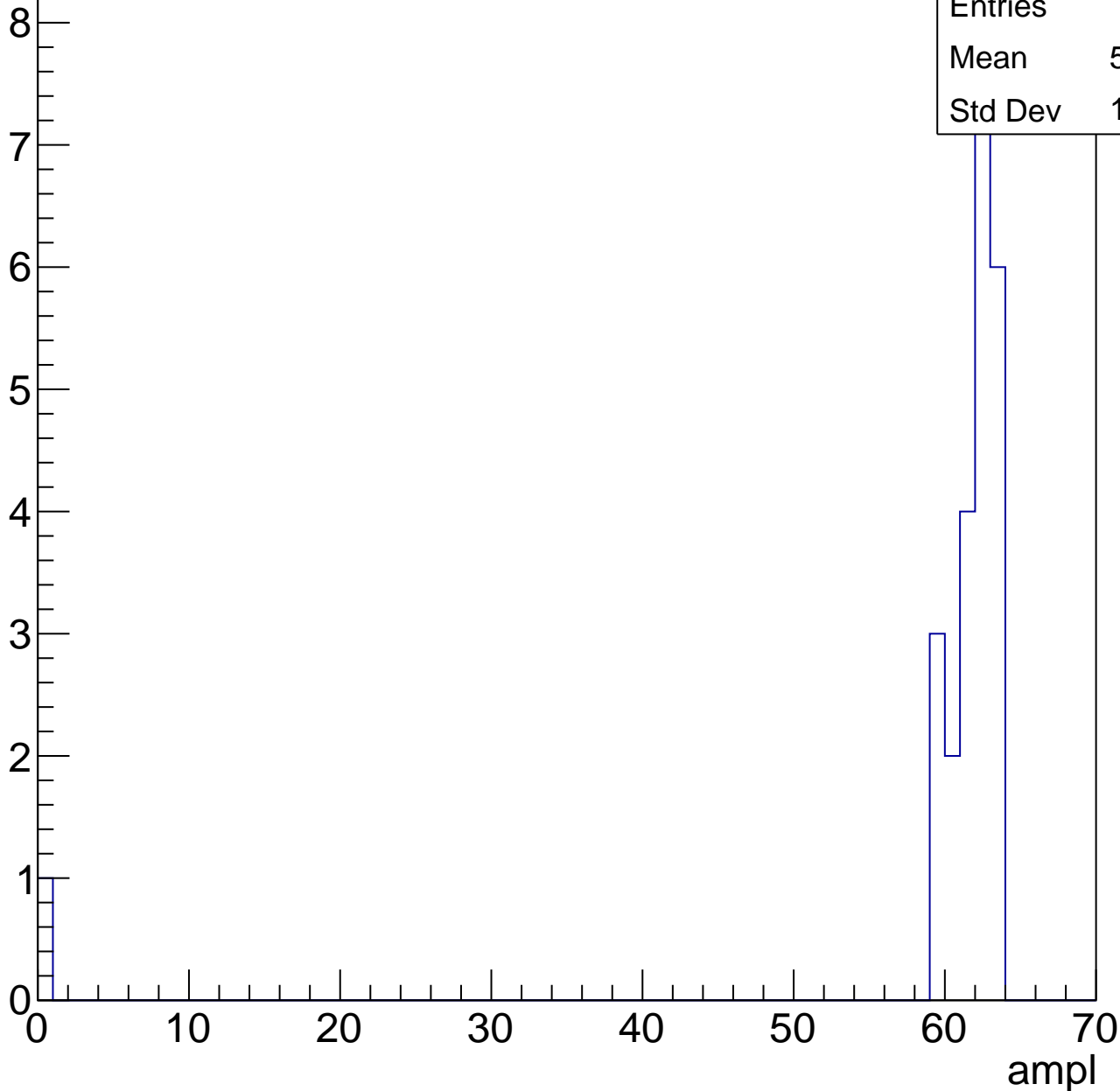


B1L101S, U26-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	58.96
Std Dev	12.36



B1L101S, U26-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch4, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.18
Std Dev	5.962

Gaus mean : 30.4803

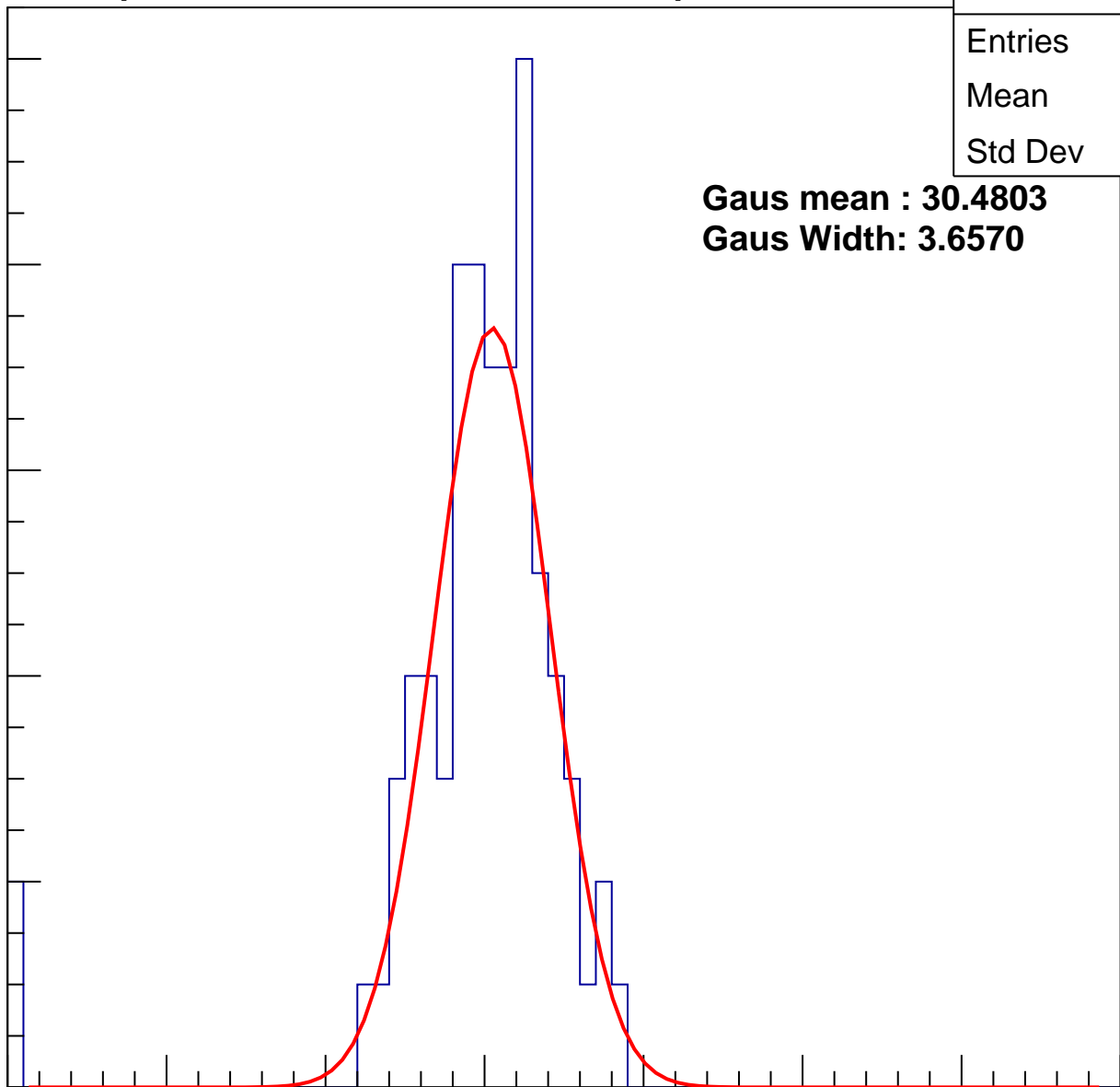
Gaus Width: 3.6570

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch4, adc1

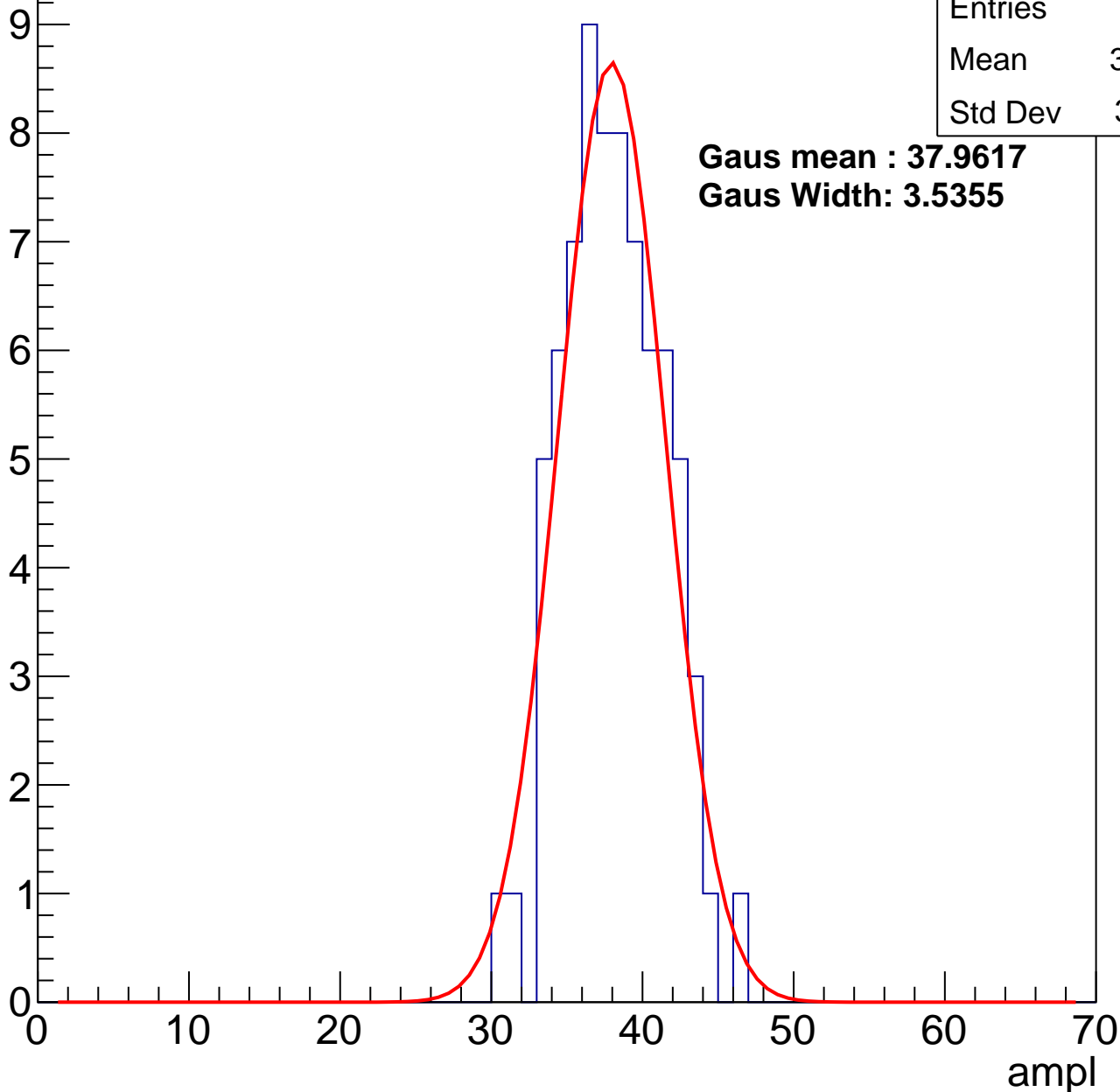
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.66
Std Dev	3.231

Gaus mean : 37.9617

Gaus Width: 3.5355



B1L101S, U26-ch4, adc2

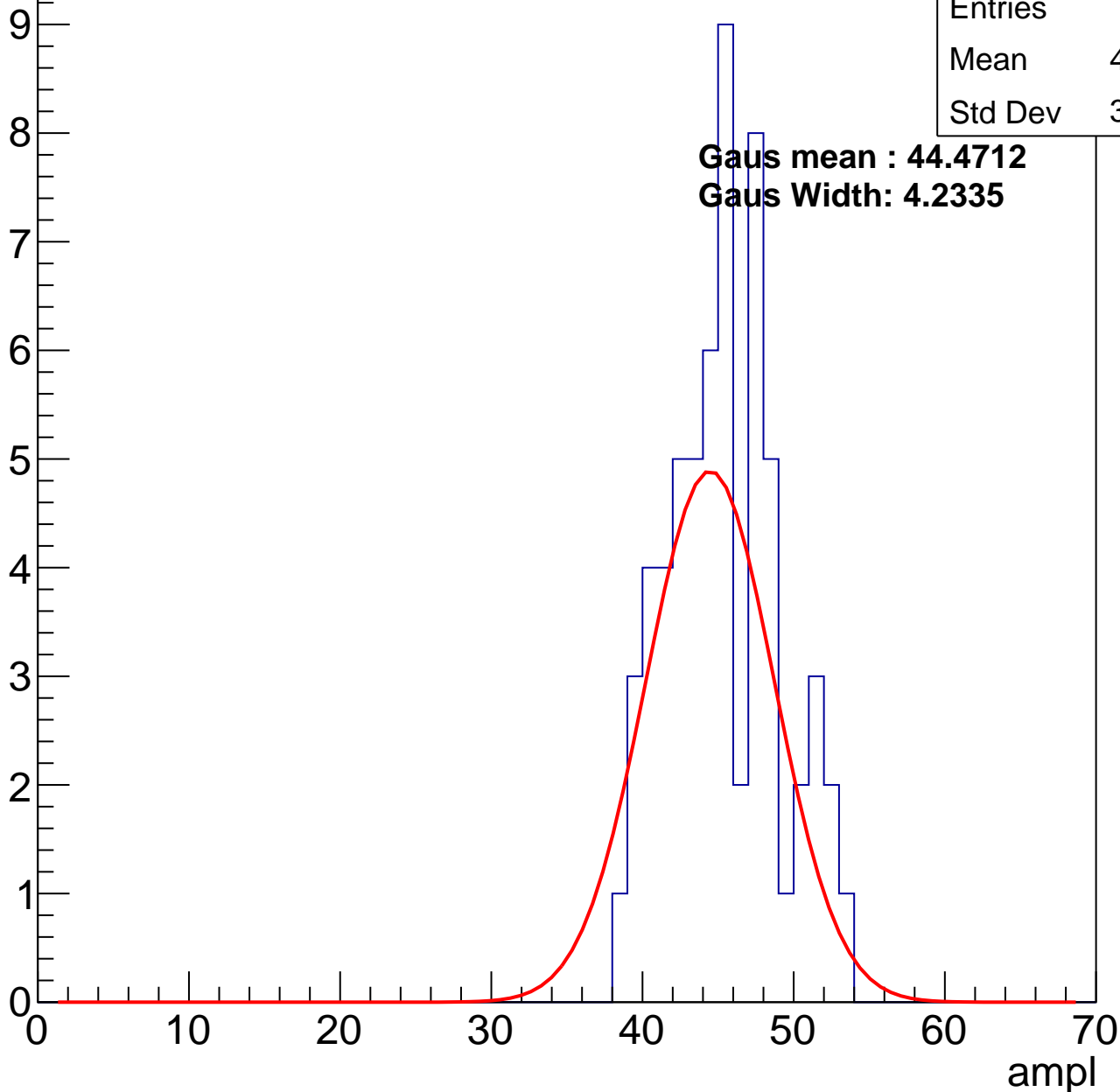
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	44.92
Std Dev	3.636

Gaus mean : 44.4712

Gaus Width: 4.2335

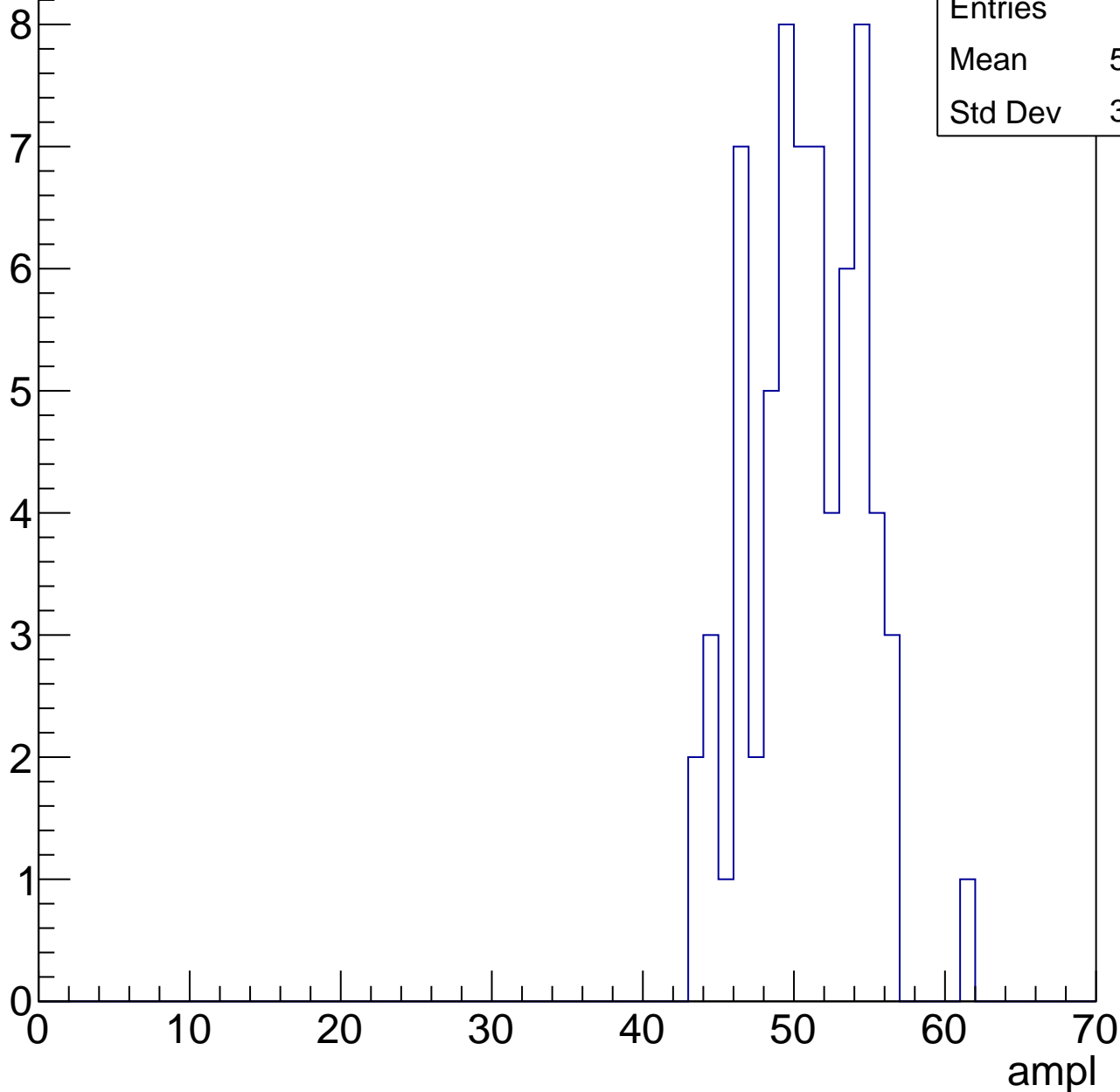


B1L101S, U26-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

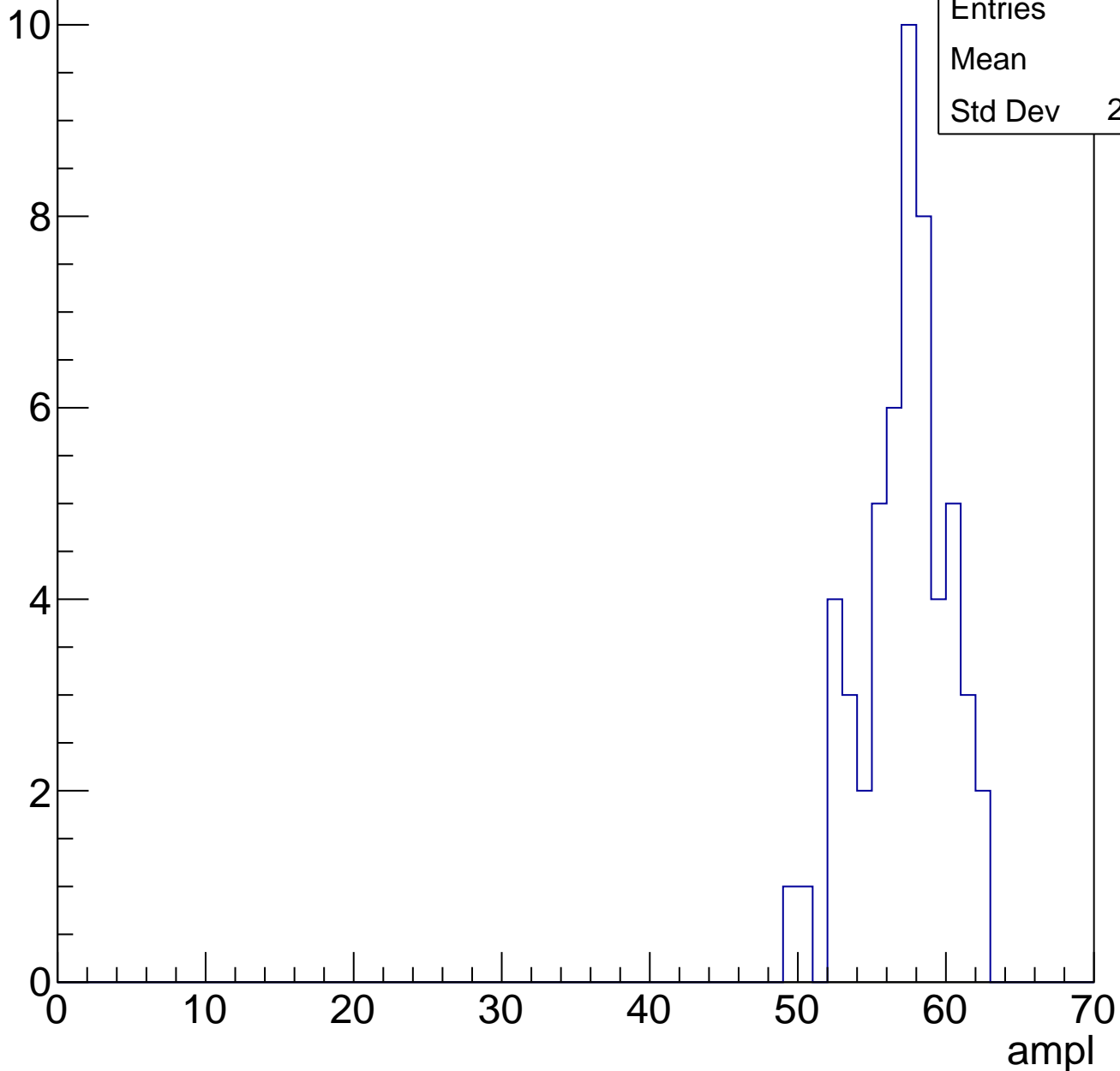
Entries	68
Mean	50.37
Std Dev	3.666



B1L101S, U26-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

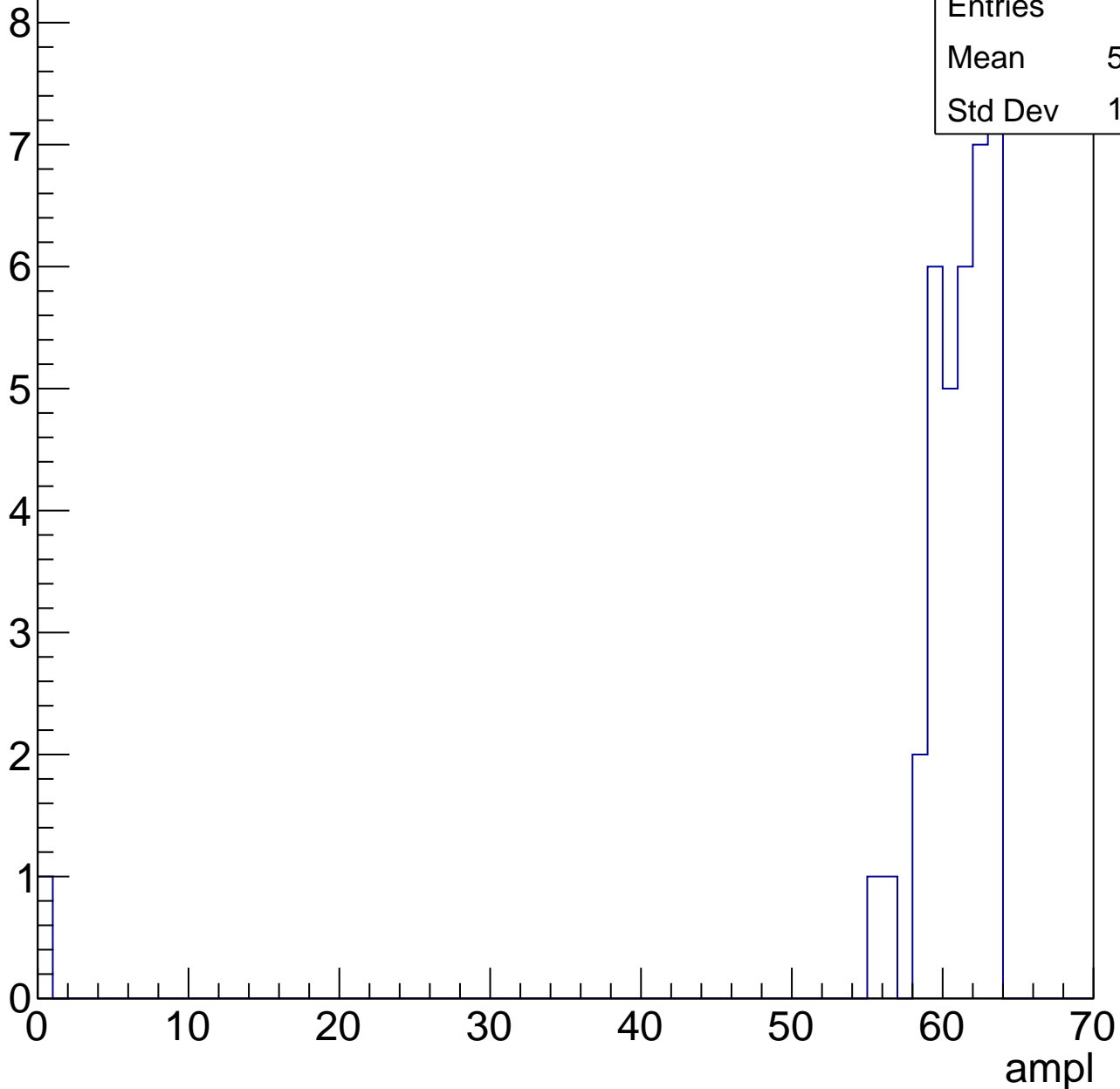


B1L101S, U26-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	59.05
Std Dev	10.04



B1L101S, U26-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch5, adc0

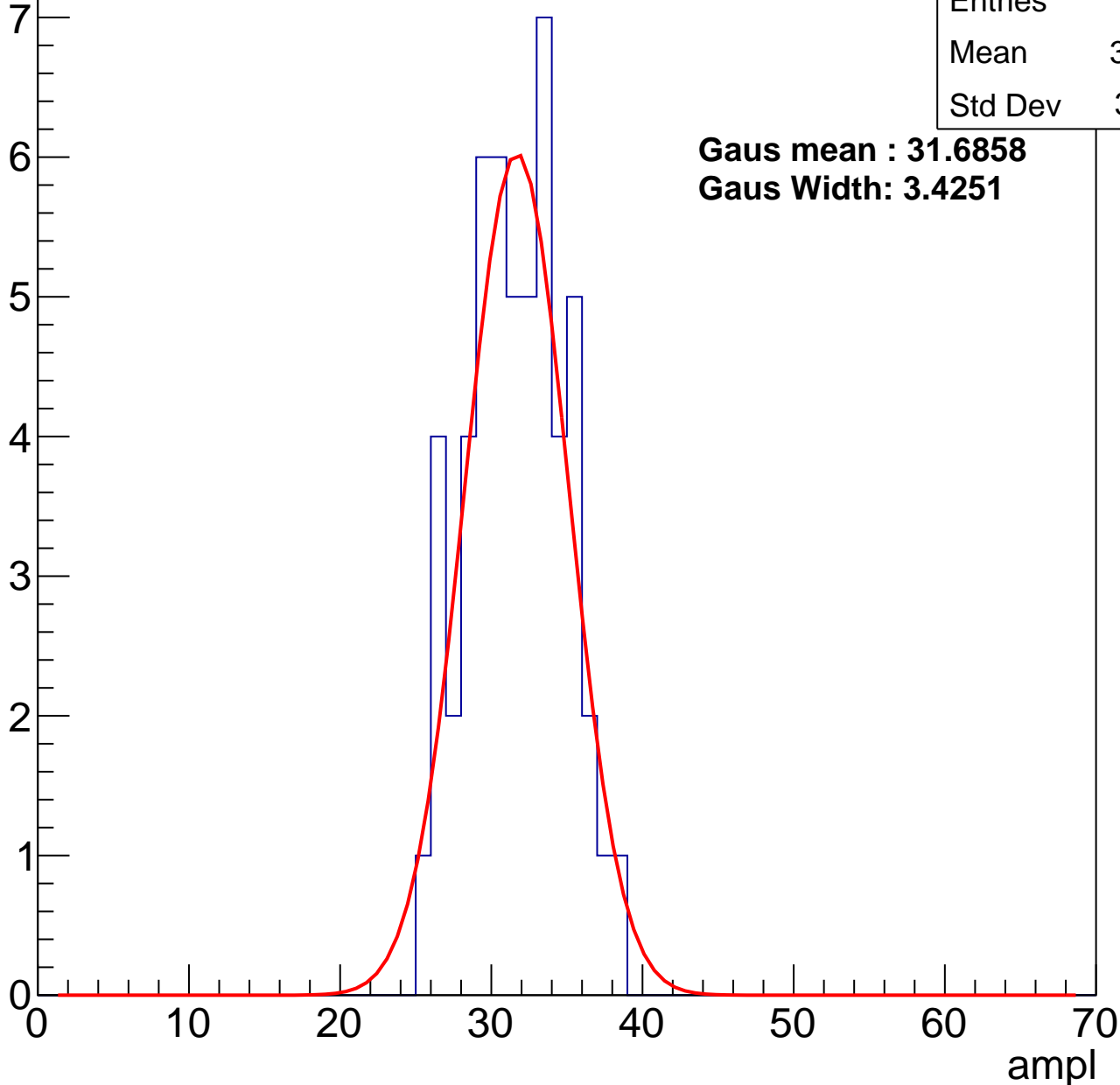
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	31.19
Std Dev	3.121

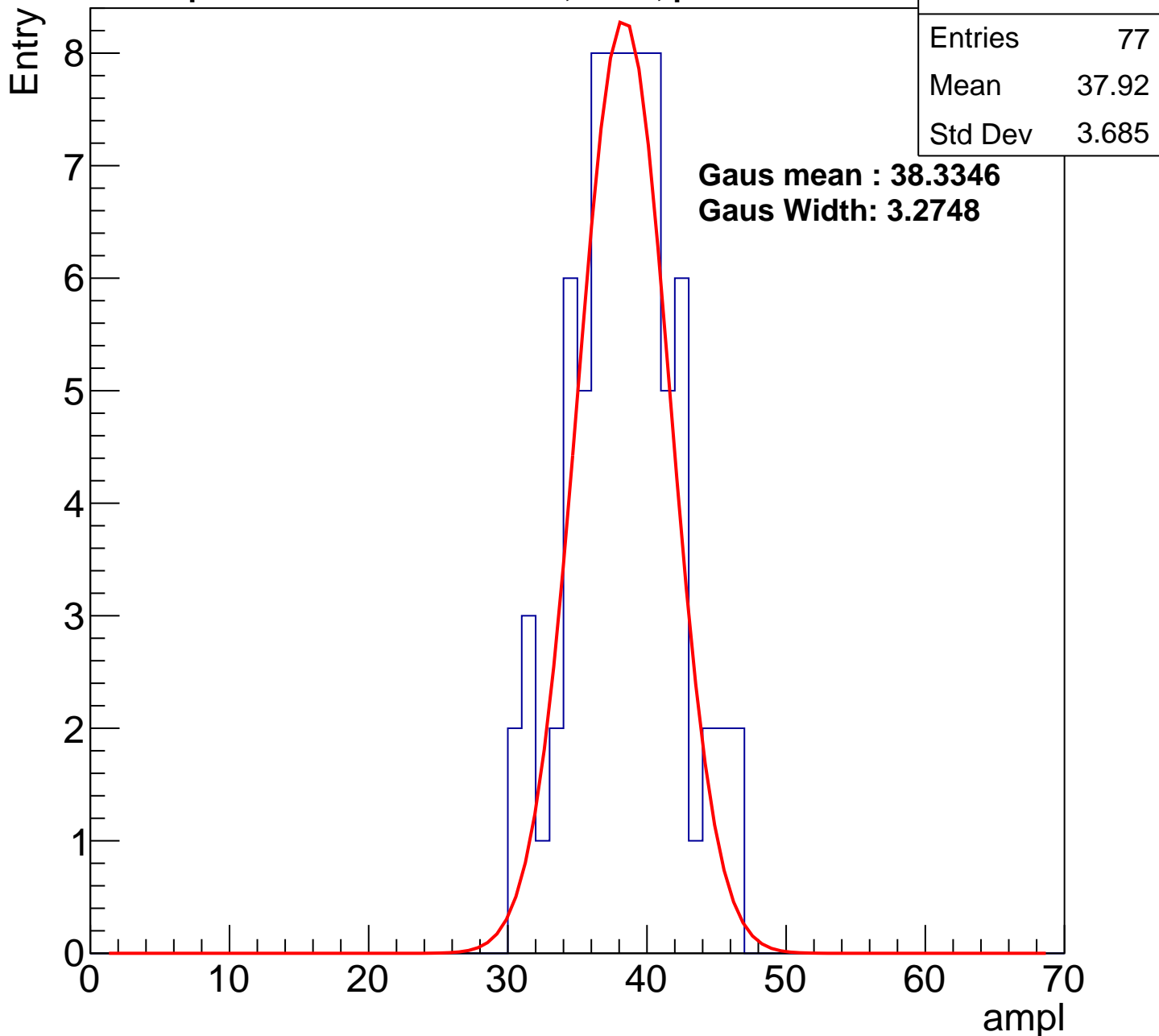
Gaus mean : 31.6858

Gaus Width: 3.4251



B1L101S, U26-ch5, adc1

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch5, adc2

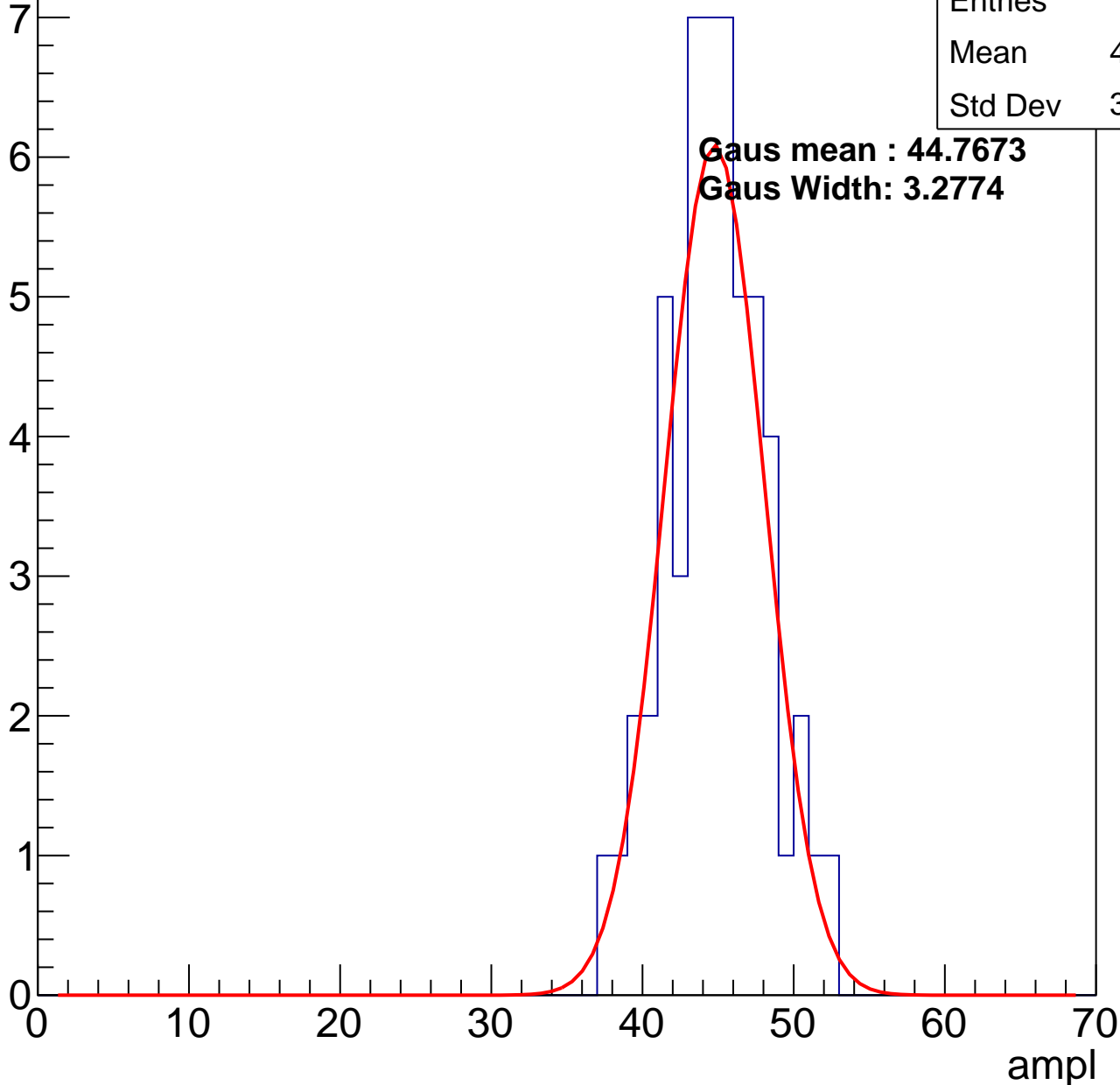
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.39
Std Dev	3.246

Gaus mean : 44.7673

Gaus Width: 3.2774

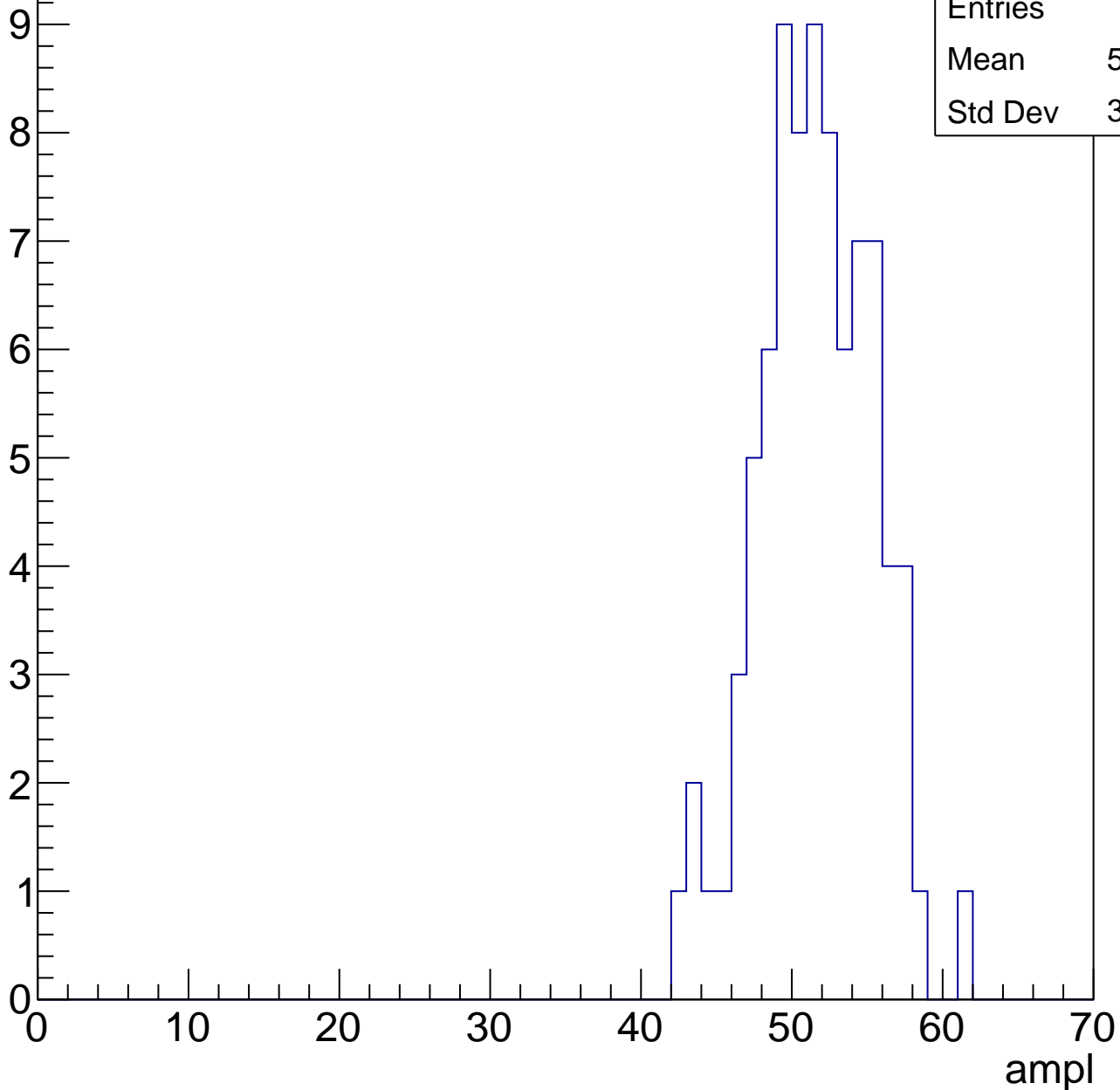


B1L101S, U26-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	51.16
Std Dev	3.714

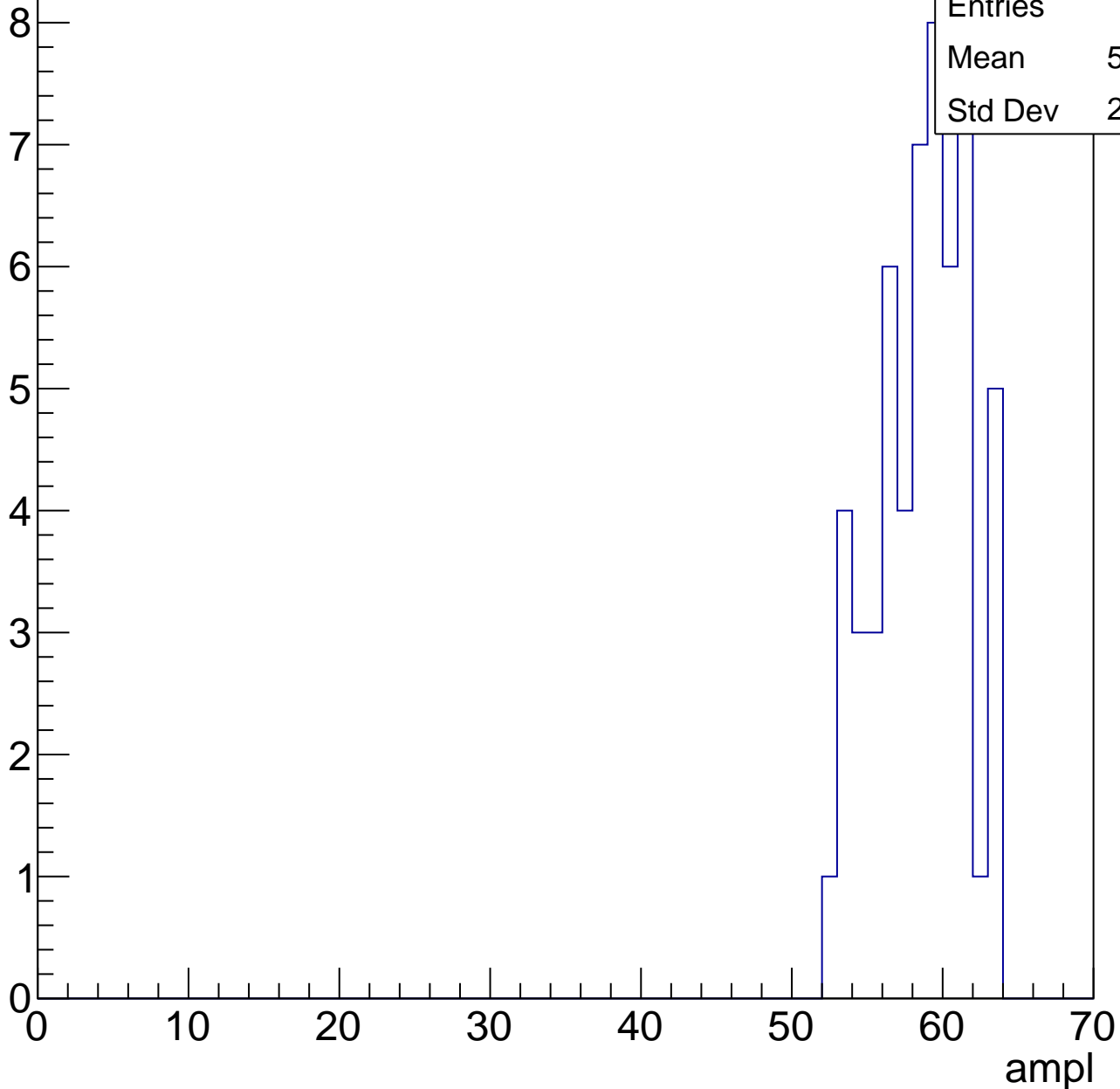


B1L101S, U26-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	58.18
Std Dev	2.934



B1L101S, U26-ch5, adc5

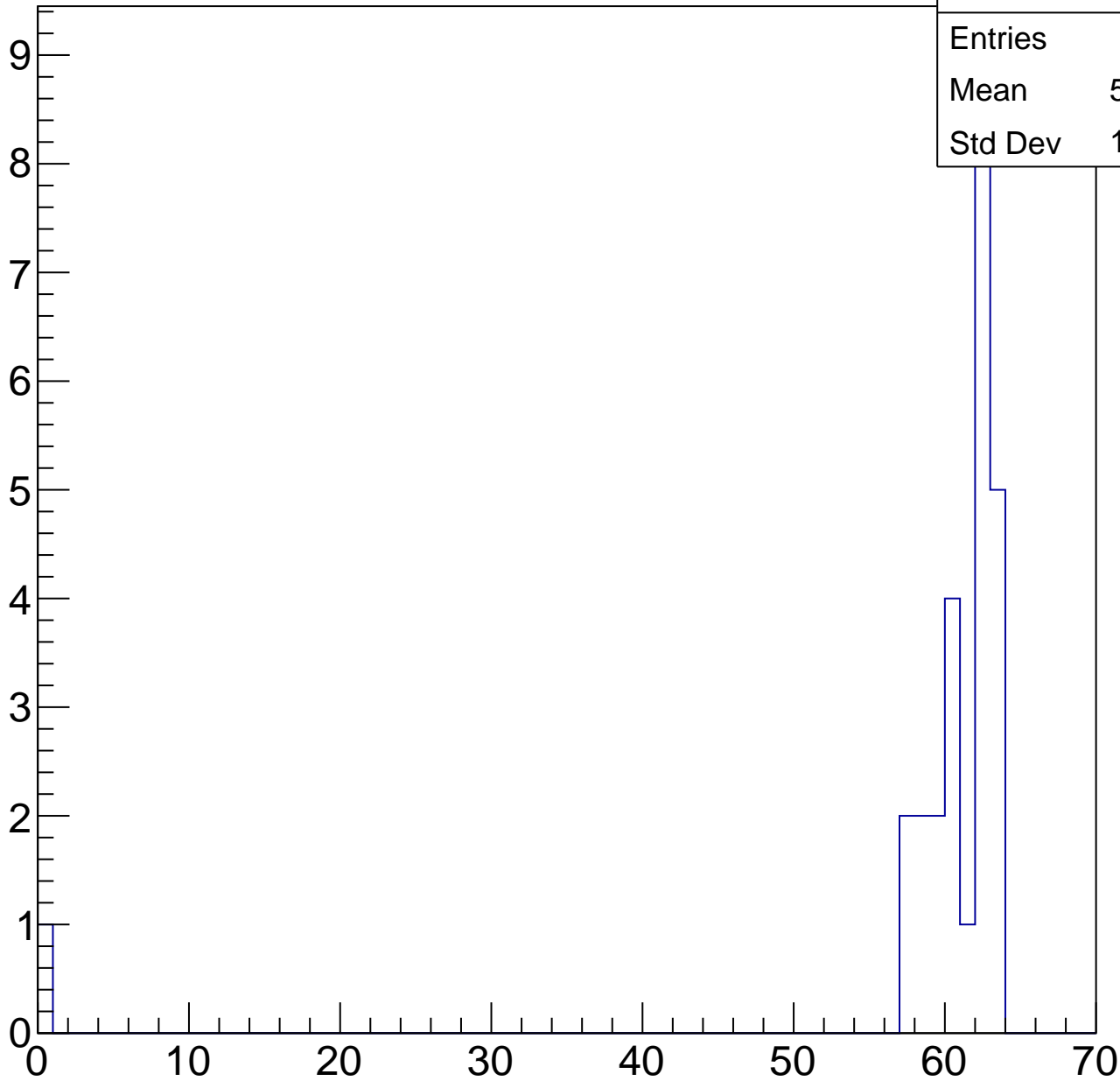
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	26
Mean	58.54
Std Dev	11.86

ampl



B1L101S, U26-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch6, adc0

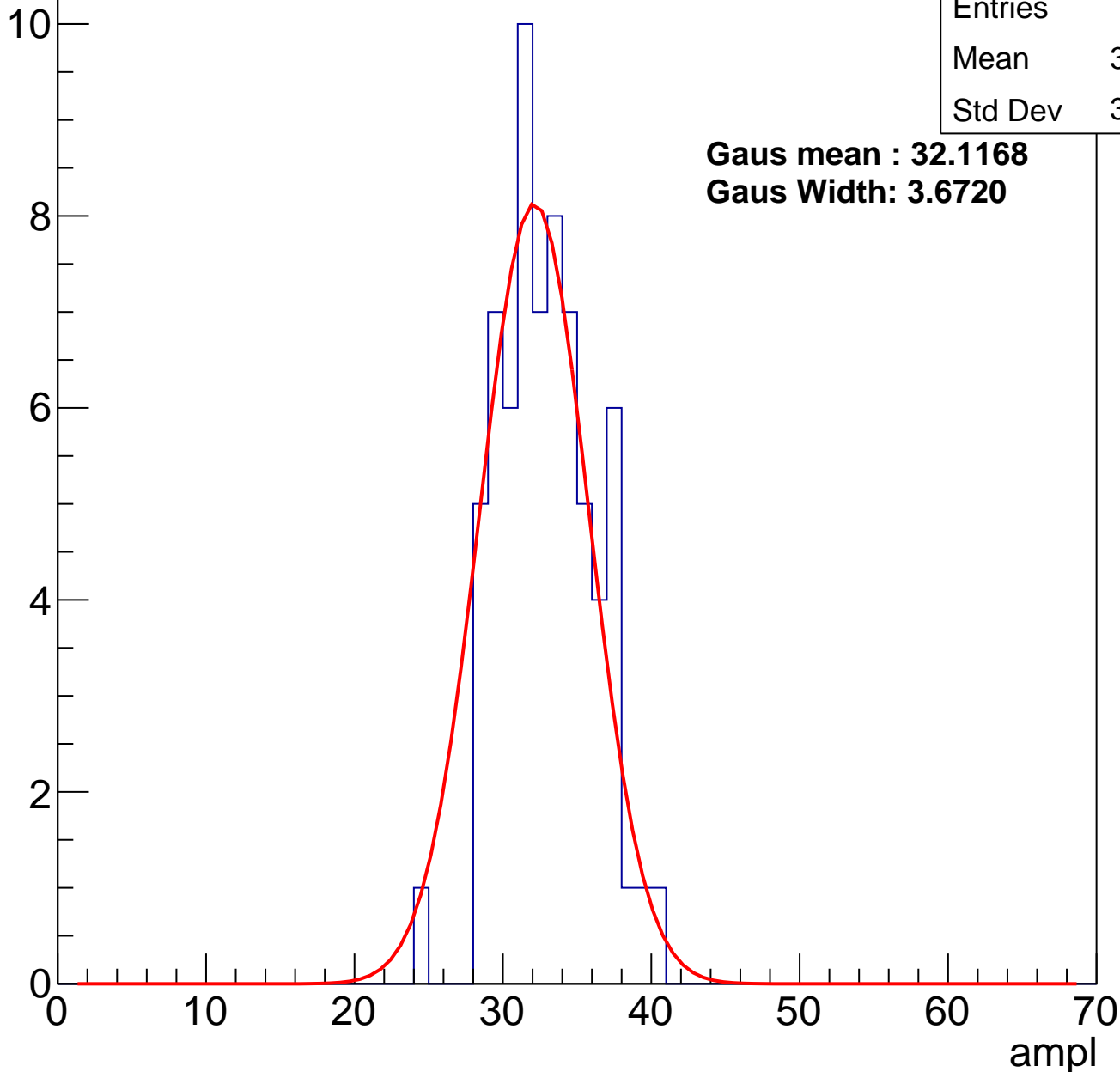
calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	32.48
Std Dev	3.114

Gaus mean : 32.1168

Gaus Width: 3.6720

Entry



B1L101S, U26-ch6, adc1

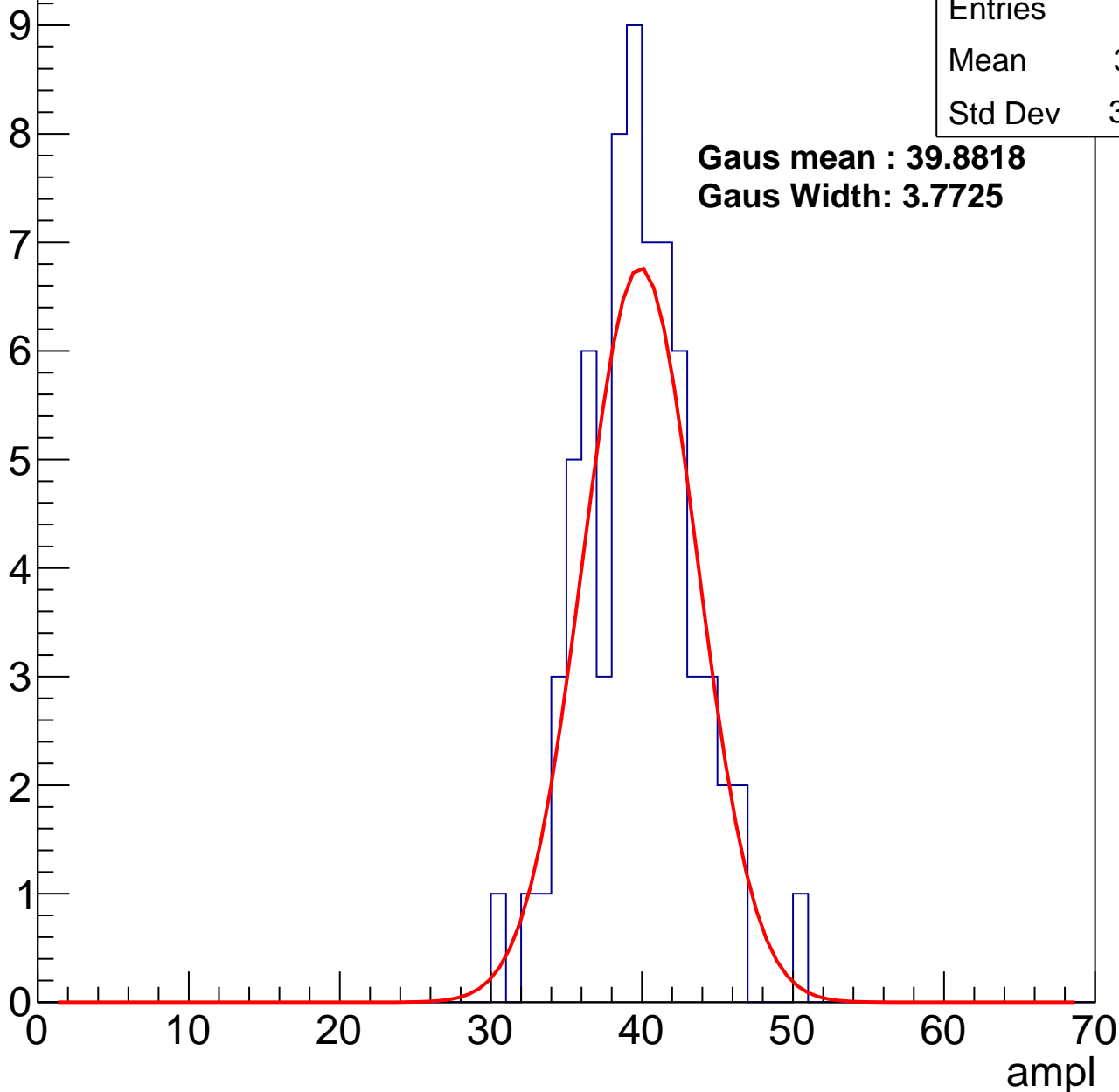
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	39.21
Std Dev	3.628

Gaus mean : 39.8818

Gaus Width: 3.7725



B1L101S, U26-ch6, adc2

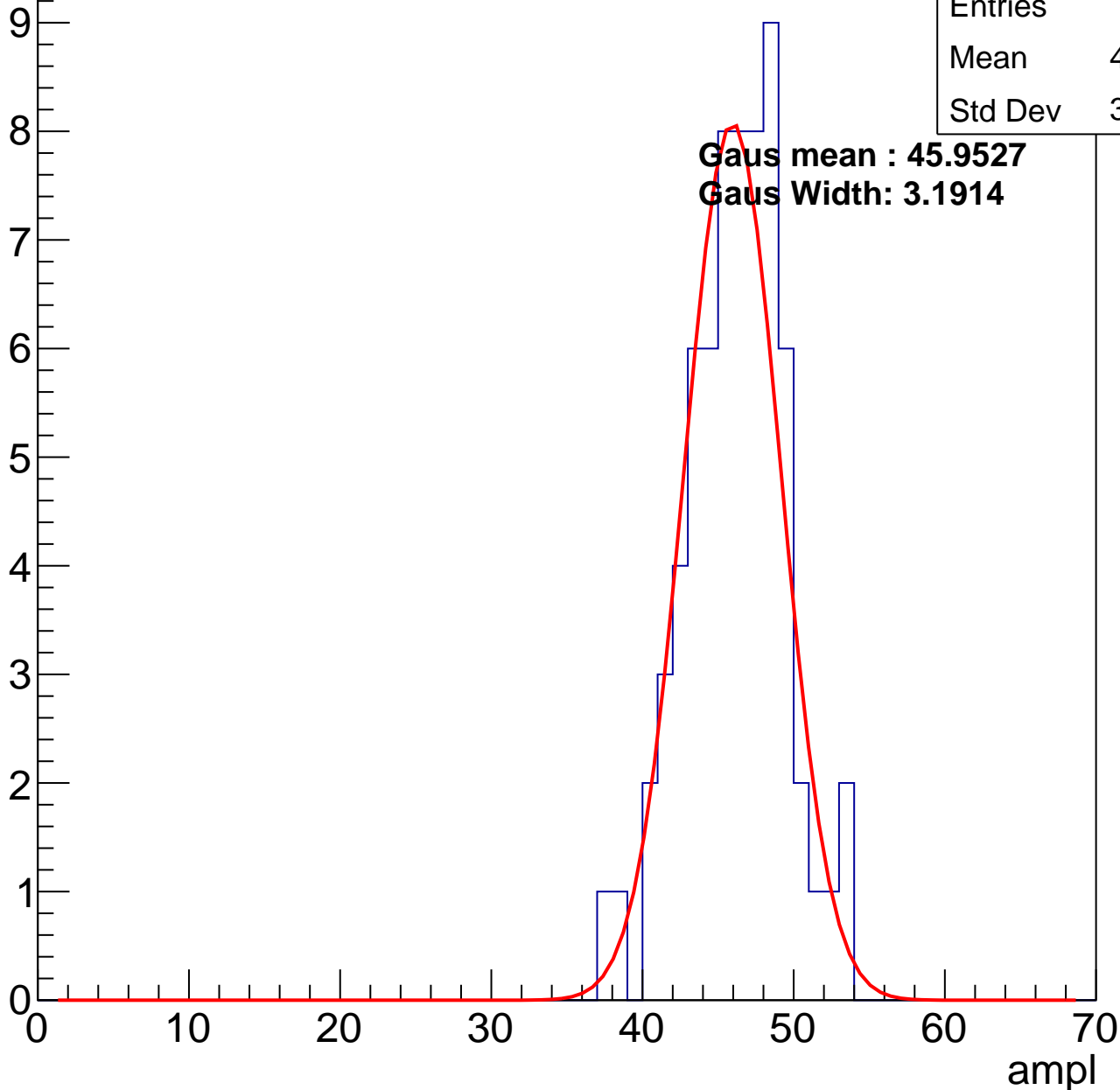
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	45.69
Std Dev	3.264

Gaus mean : 45.9527

Gaus Width: 3.1914

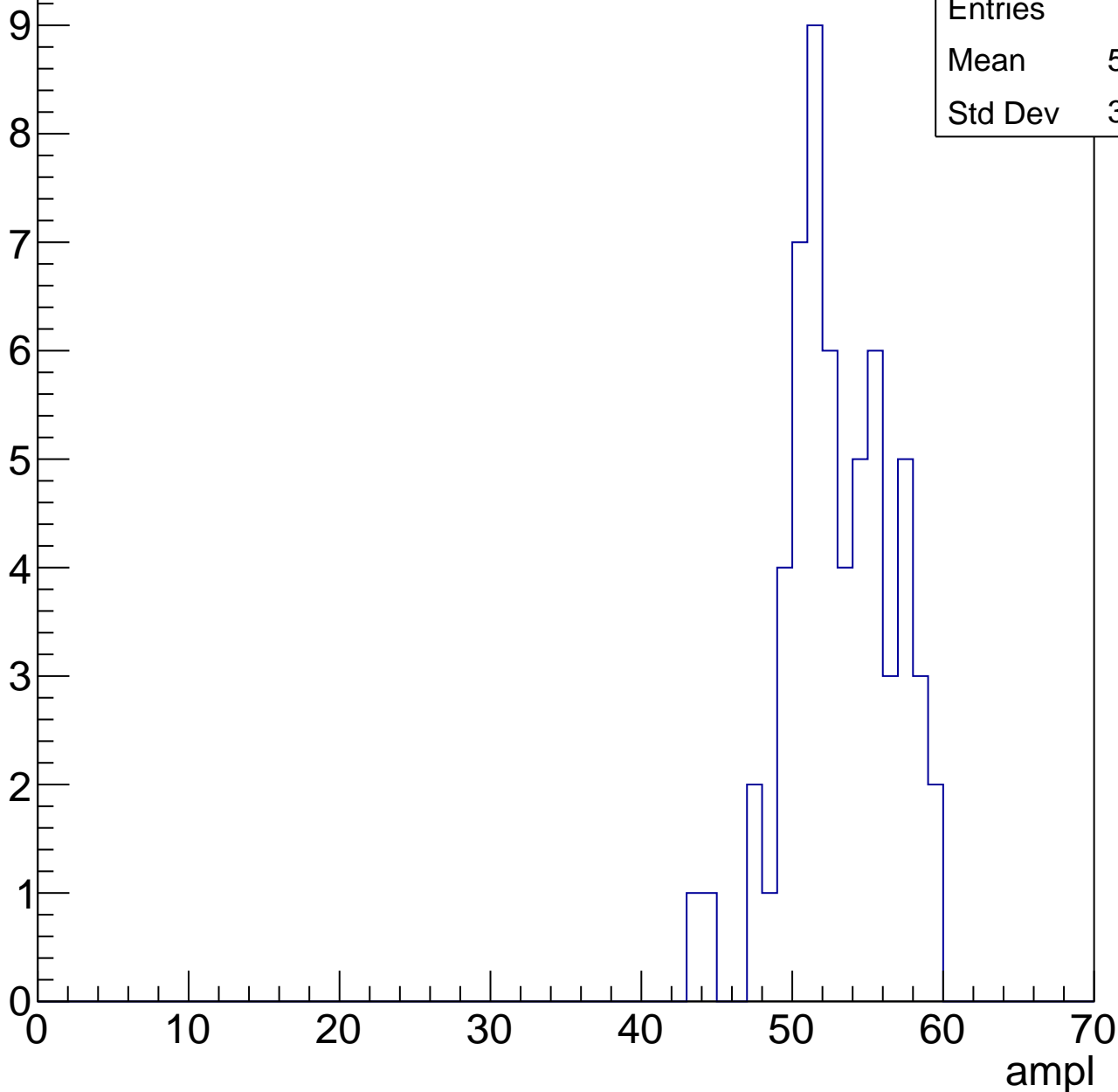


B1L101S, U26-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	52.59
Std Dev	3.494



B1L101S, U26-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	53
Mean	56.19
Std Dev	8.358

ampl

0

10

20

30

40

50

60

70

B1L101S, U26-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

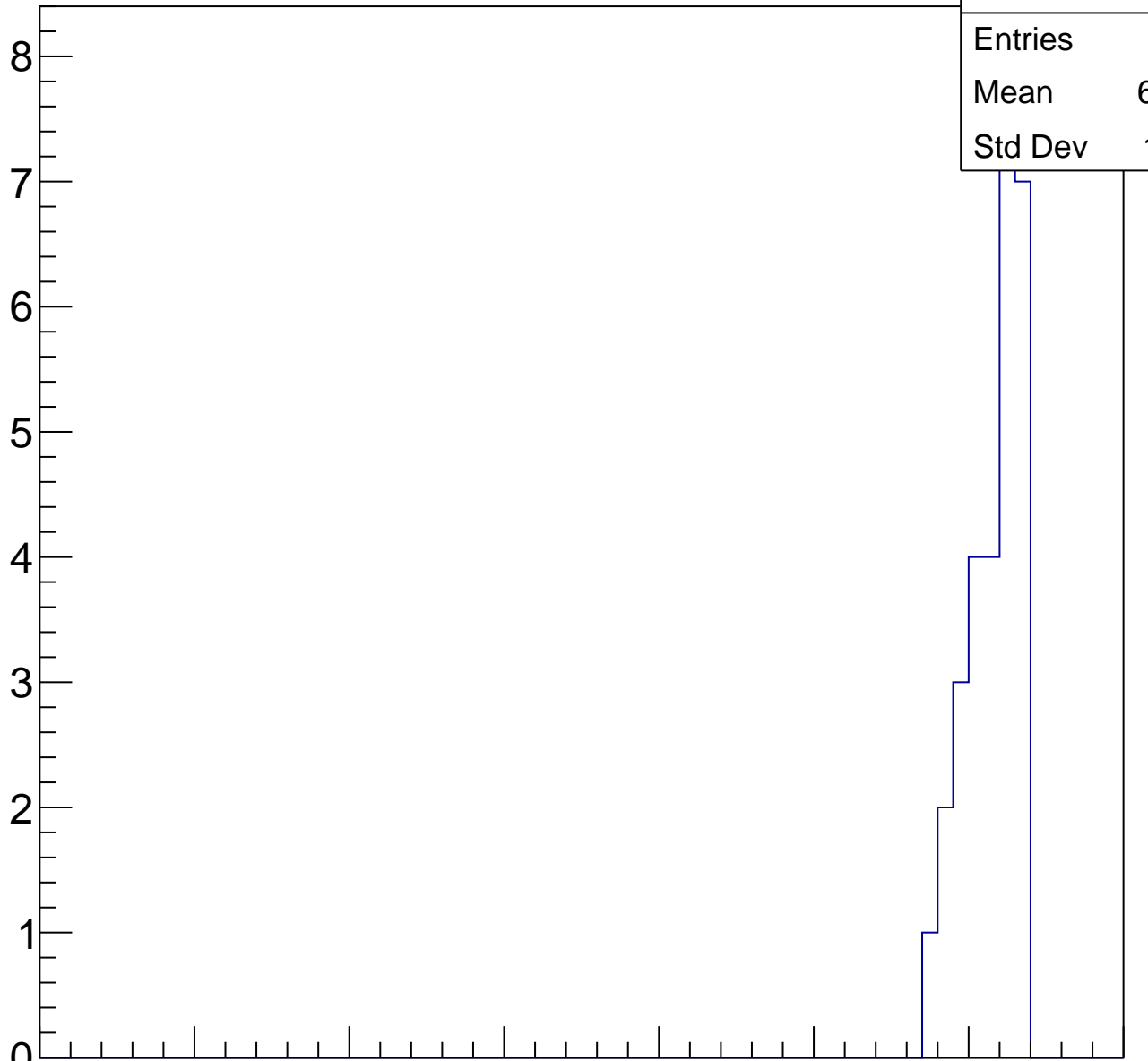
Entry

8
7
6
5
4
3
2
1
0

Entries	29
Mean	61.07
Std Dev	1.721

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch7, adc0

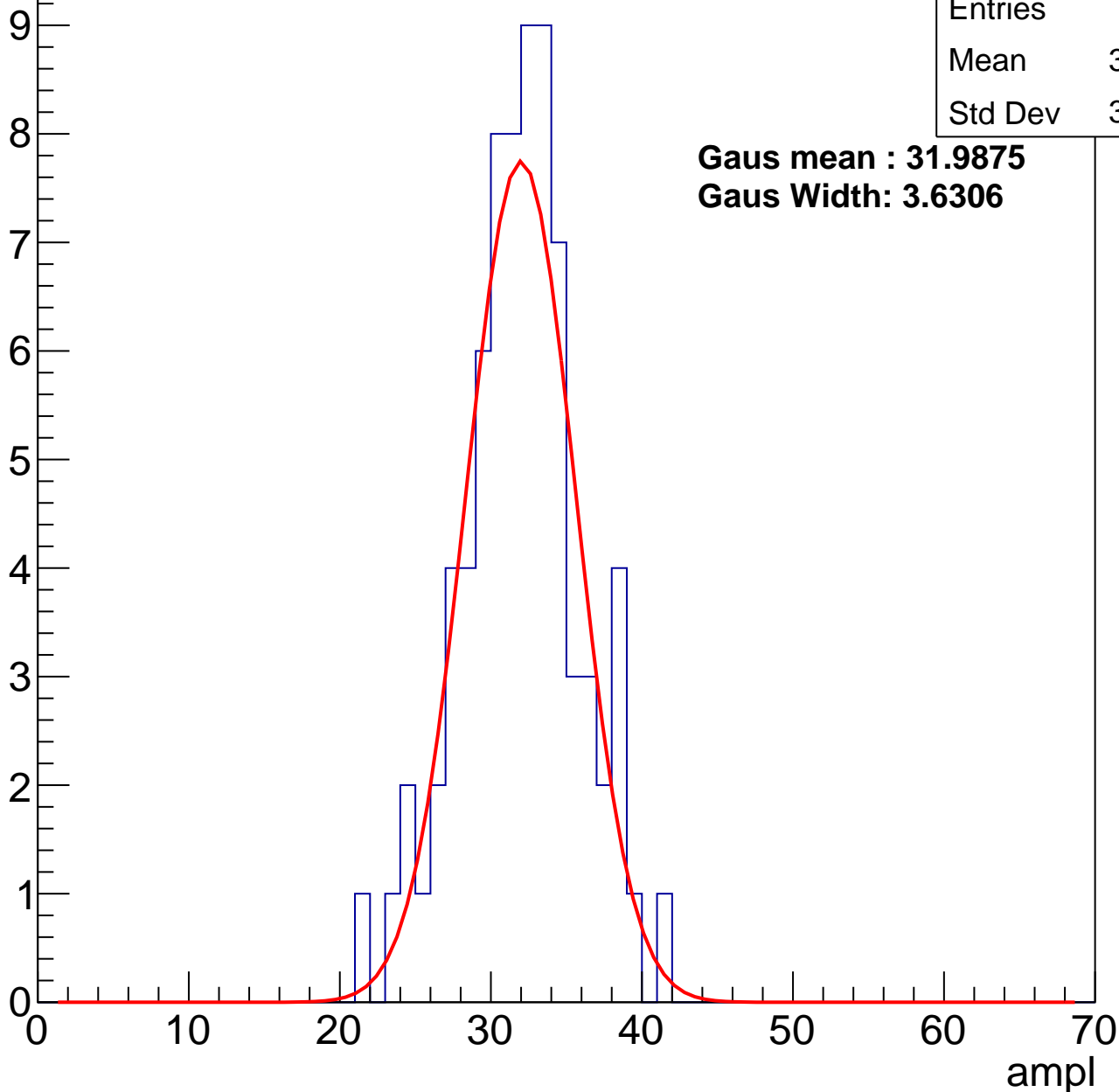
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	31.49
Std Dev	3.847

Gaus mean : 31.9875

Gaus Width: 3.6306



B1L101S, U26-ch7, adc1

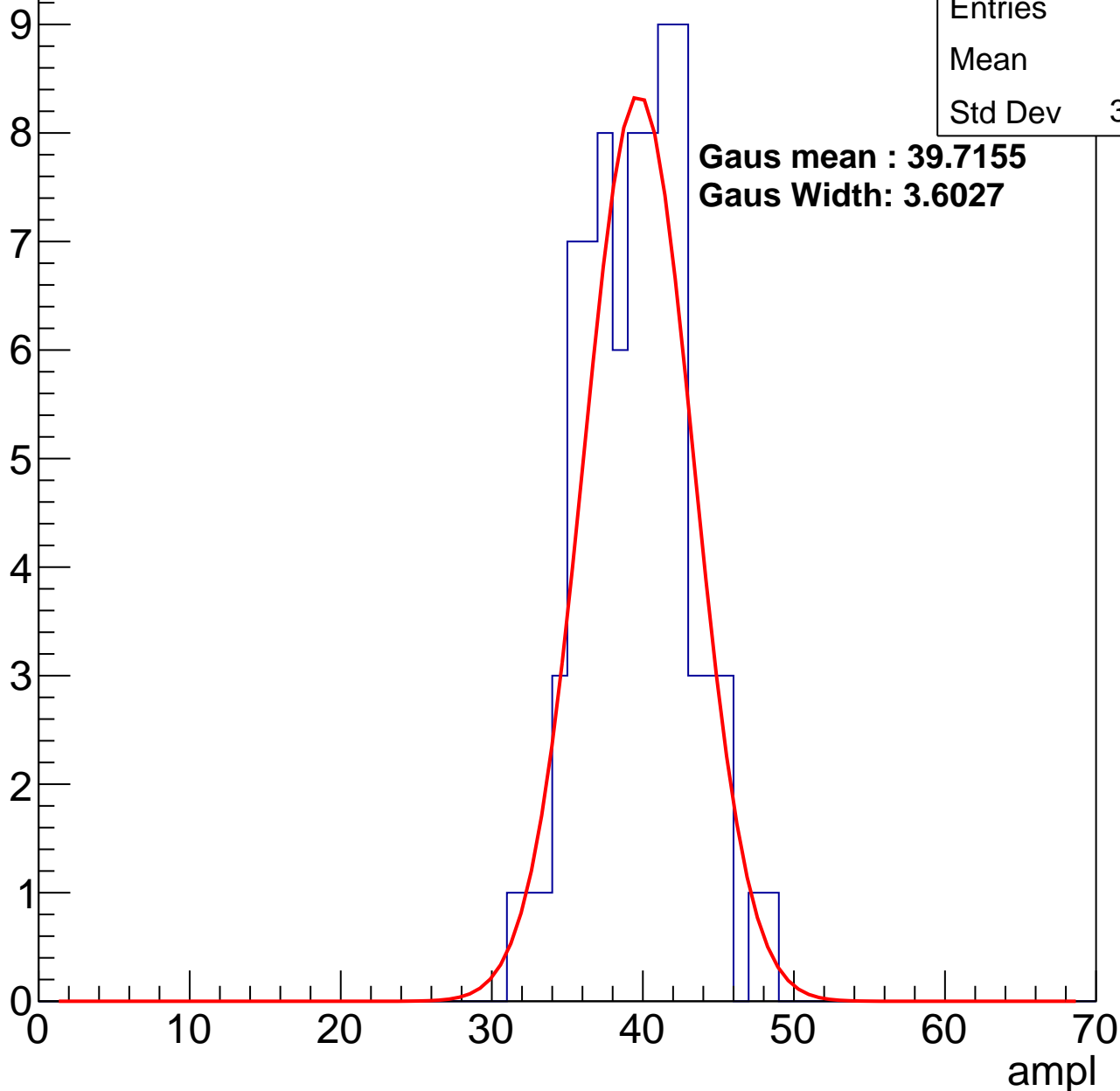
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	39.1
Std Dev	3.444

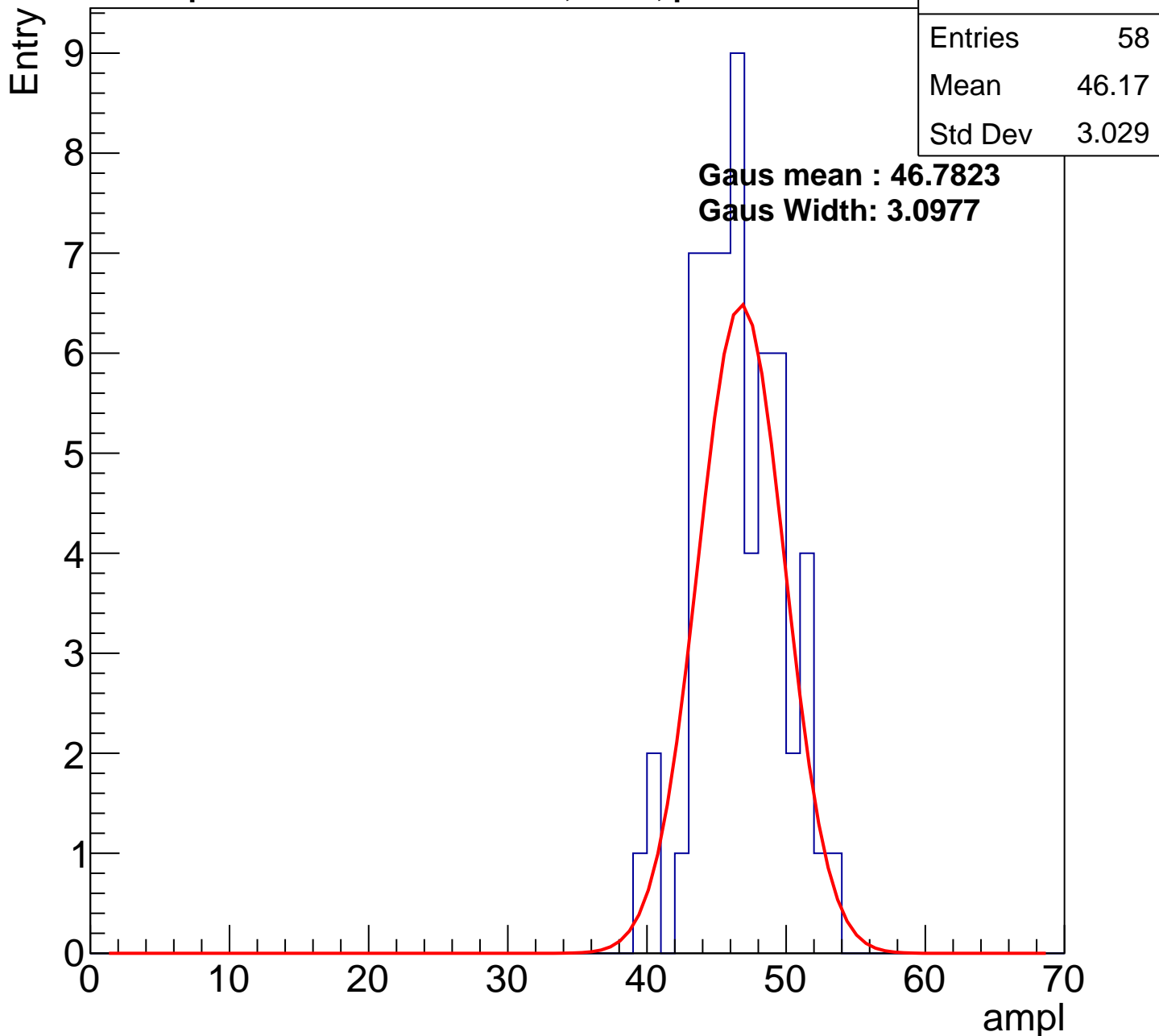
Gaus mean : 39.7155

Gaus Width: 3.6027



B1L101S, U26-ch7, adc2

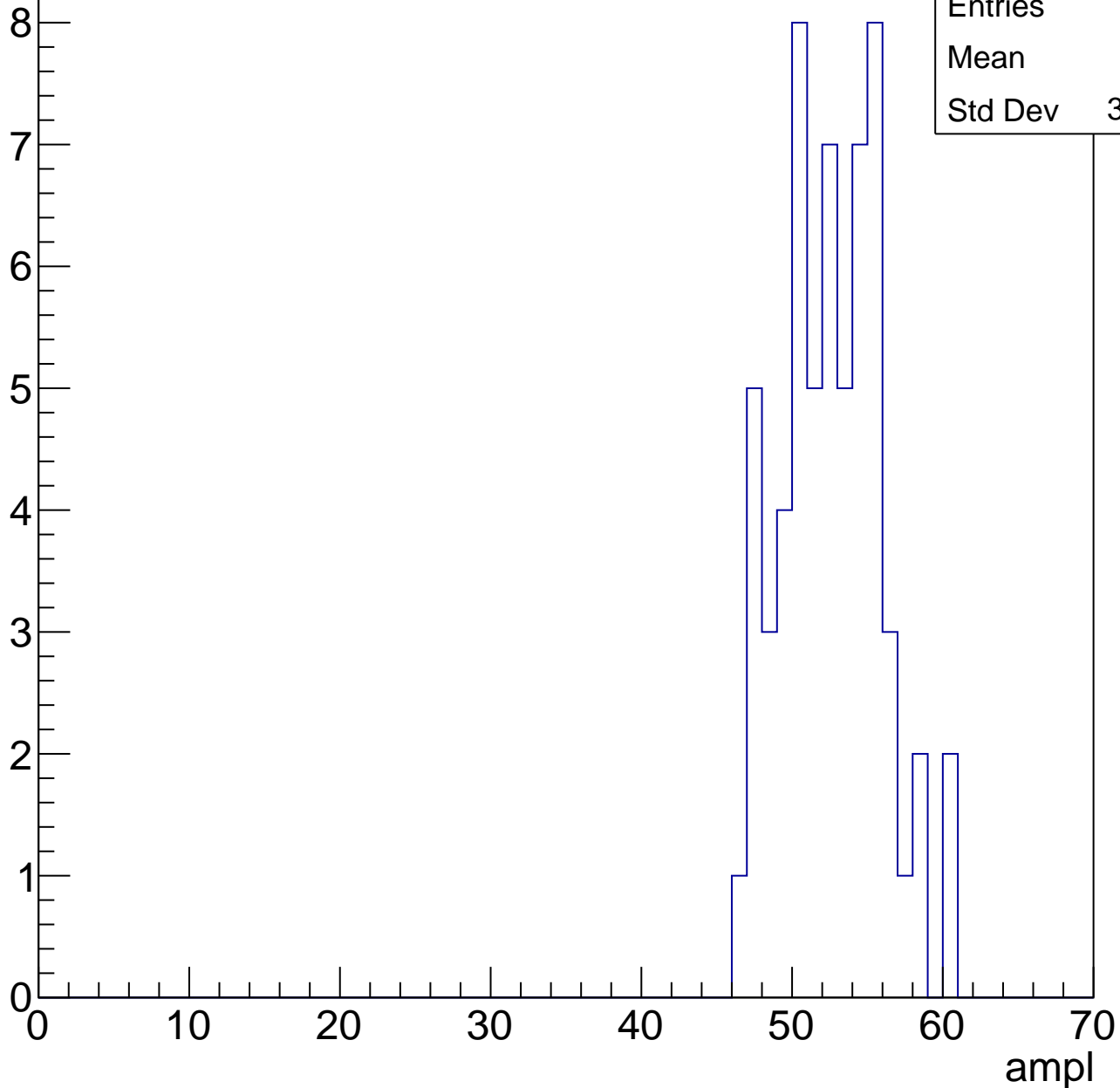
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

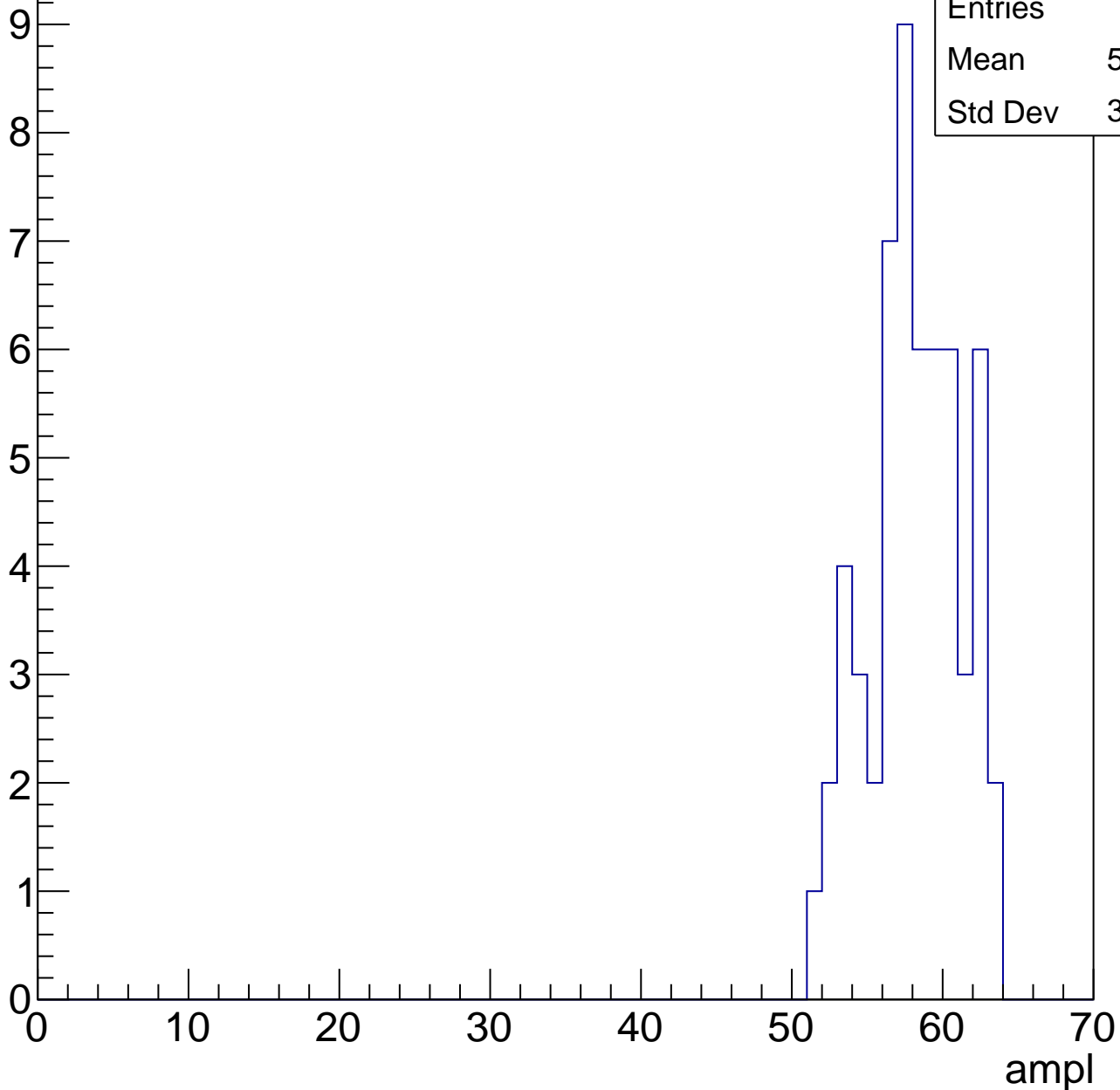


Entries	61
Mean	52.2
Std Dev	3.283

B1L101S, U26-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



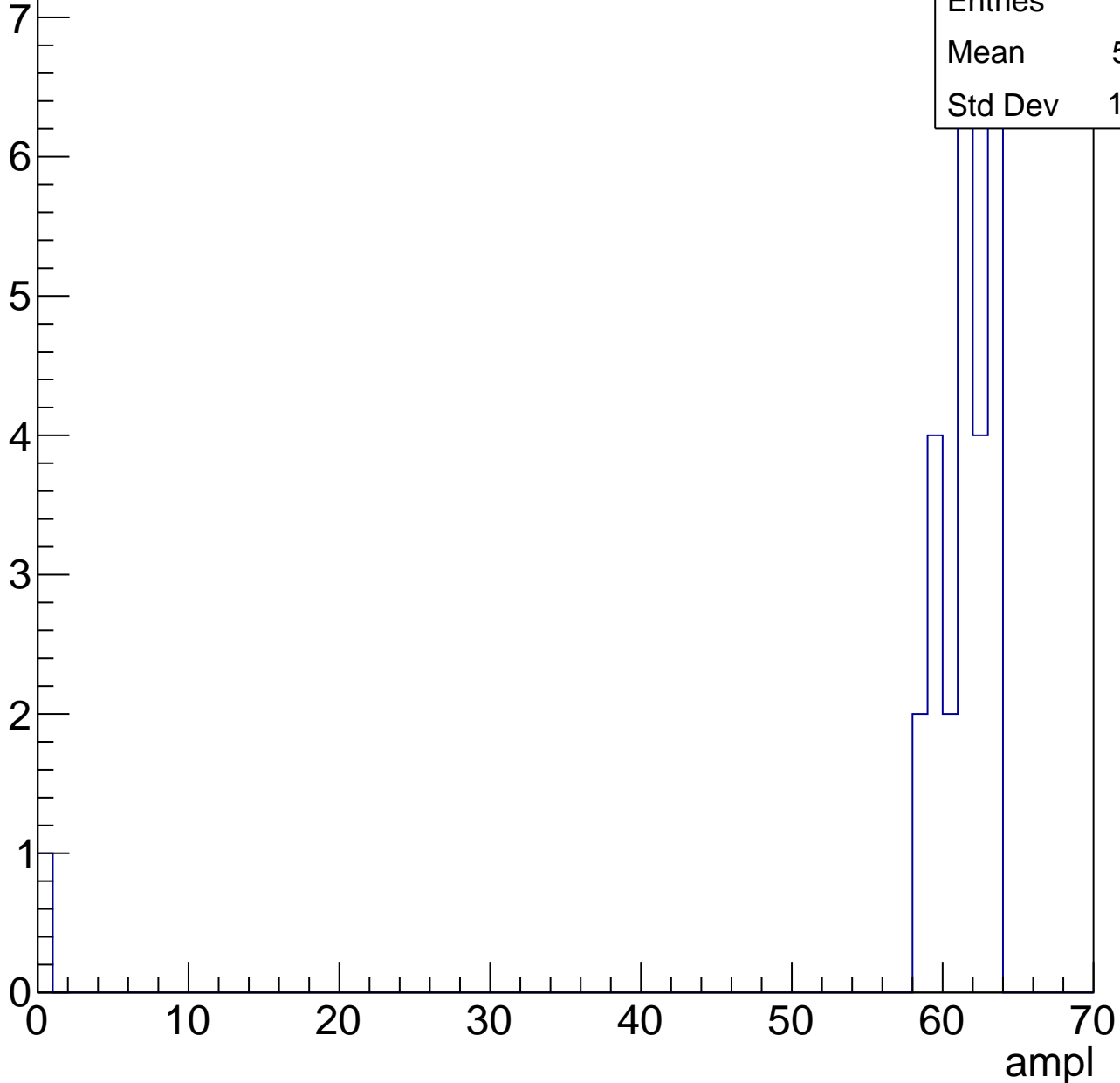
Entries	57
Mean	57.67
Std Dev	3.022

B1L101S, U26-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.81
Std Dev	11.64



B1L101S, U26-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch8, adc0

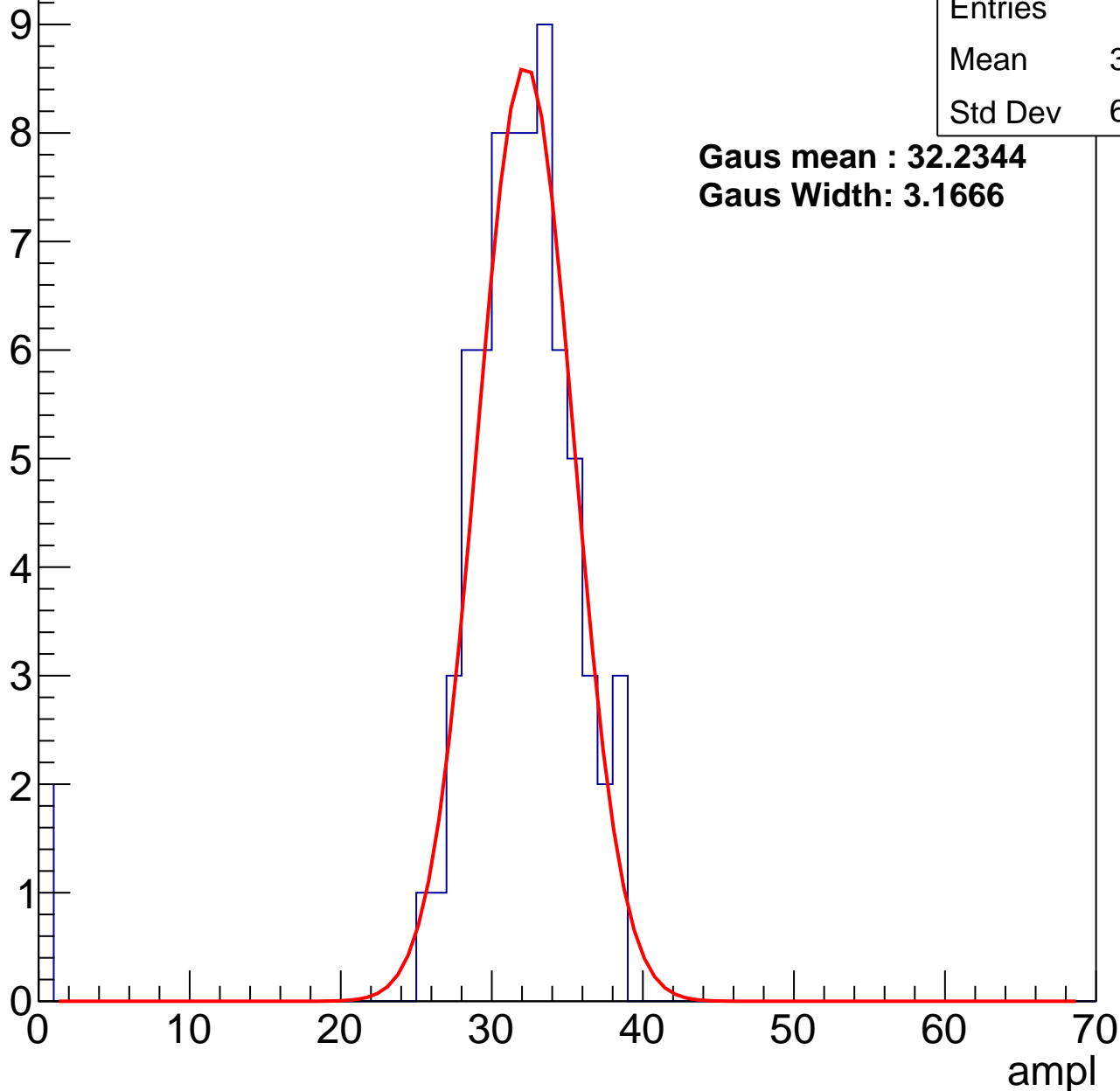
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.85
Std Dev	6.037

Gaus mean : 32.2344

Gaus Width: 3.1666



B1L101S, U26-ch8, adc1

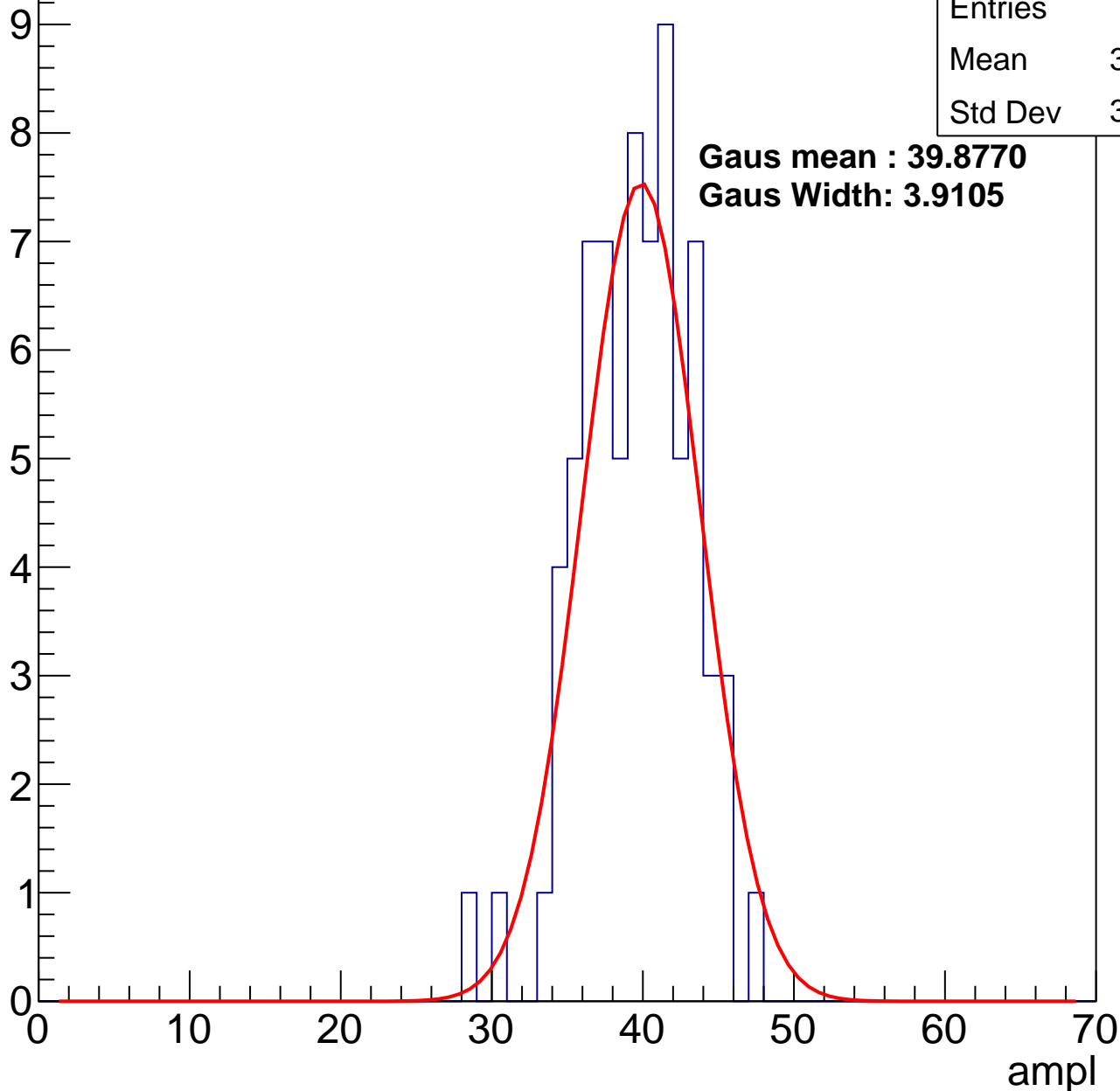
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	39.04
Std Dev	3.603

Gaus mean : 39.8770

Gaus Width: 3.9105



B1L101S, U26-ch8, adc2

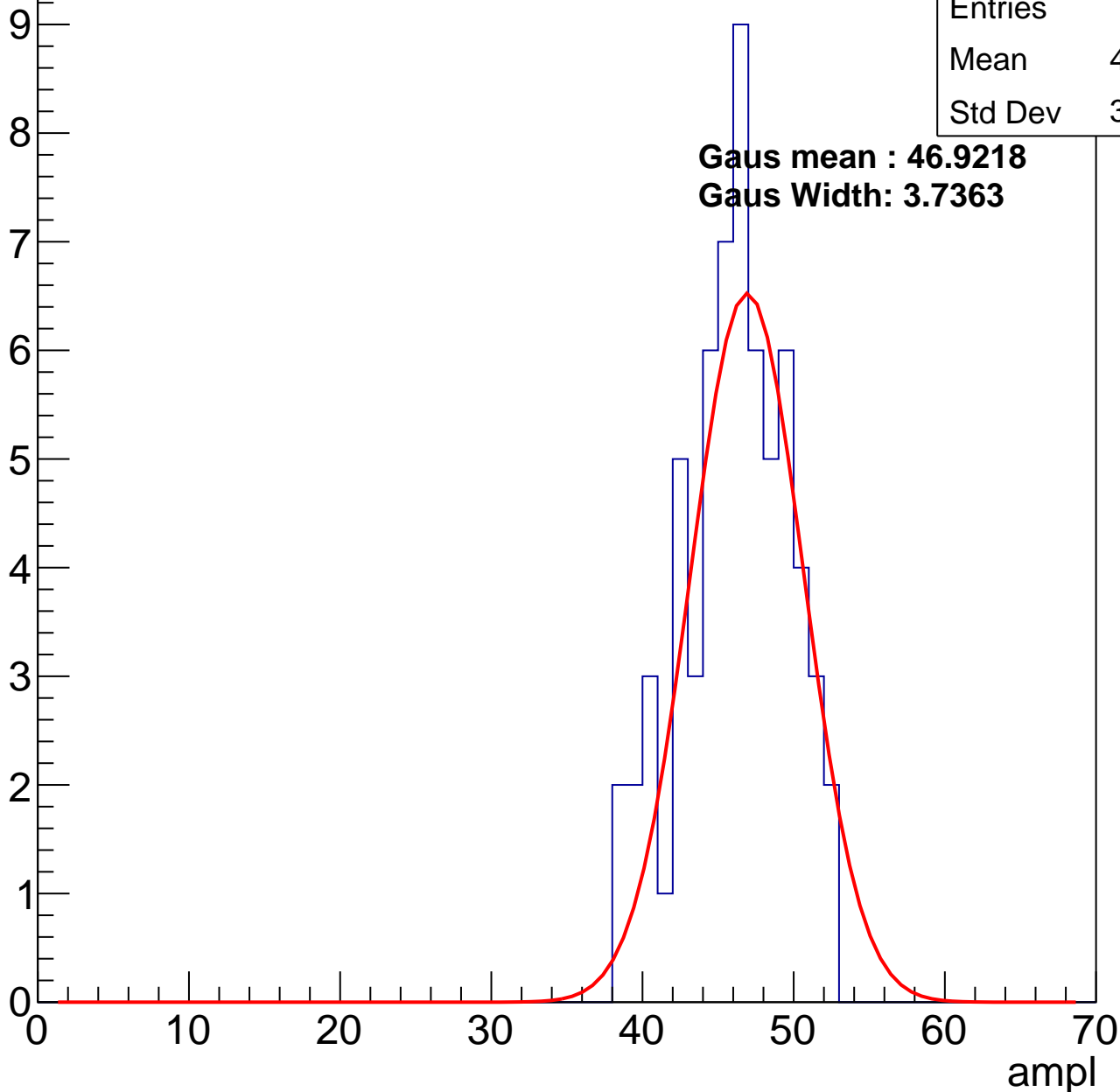
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.62
Std Dev	3.489

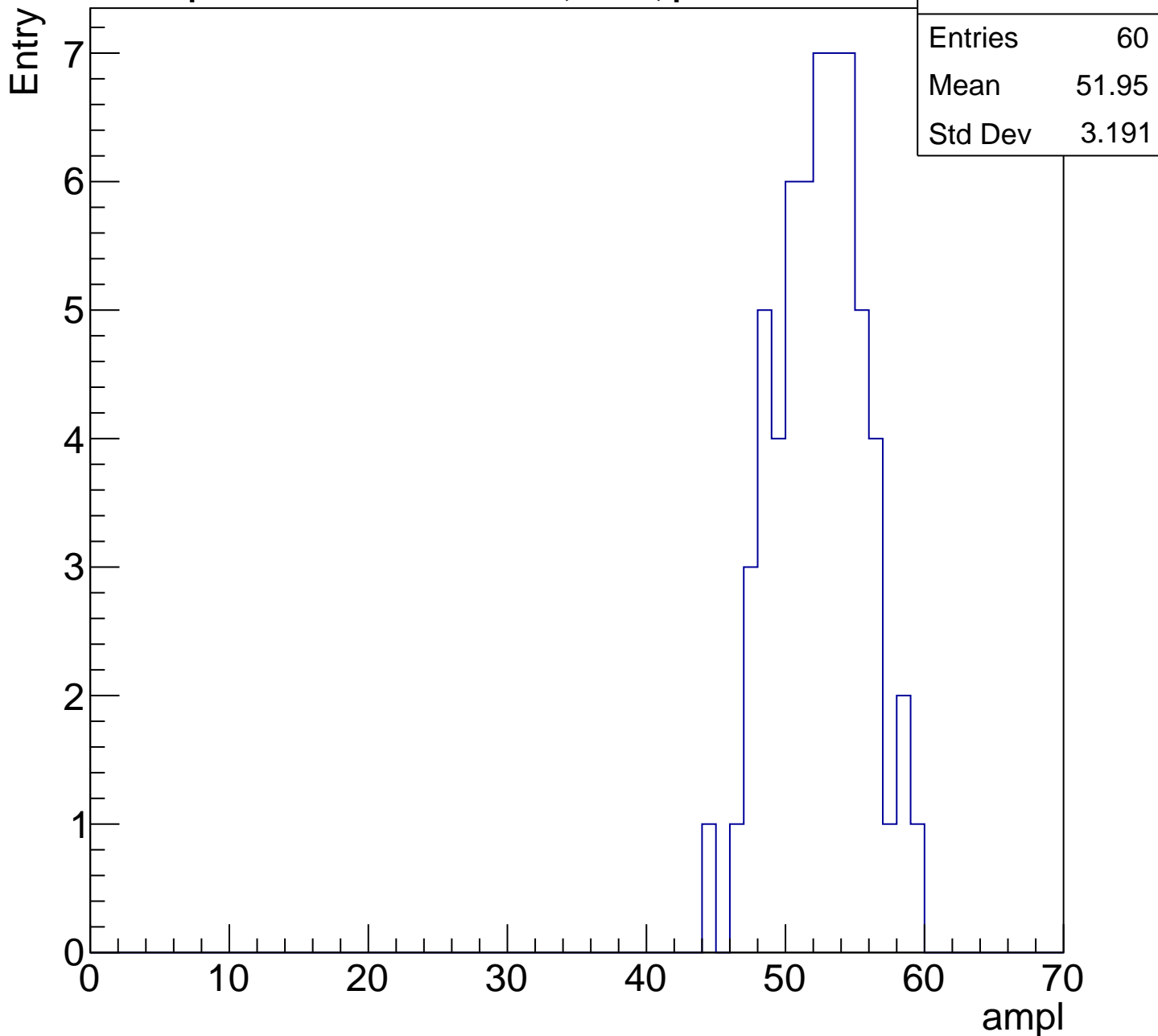
Gaus mean : 46.9218

Gaus Width: 3.7363



B1L101S, U26-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

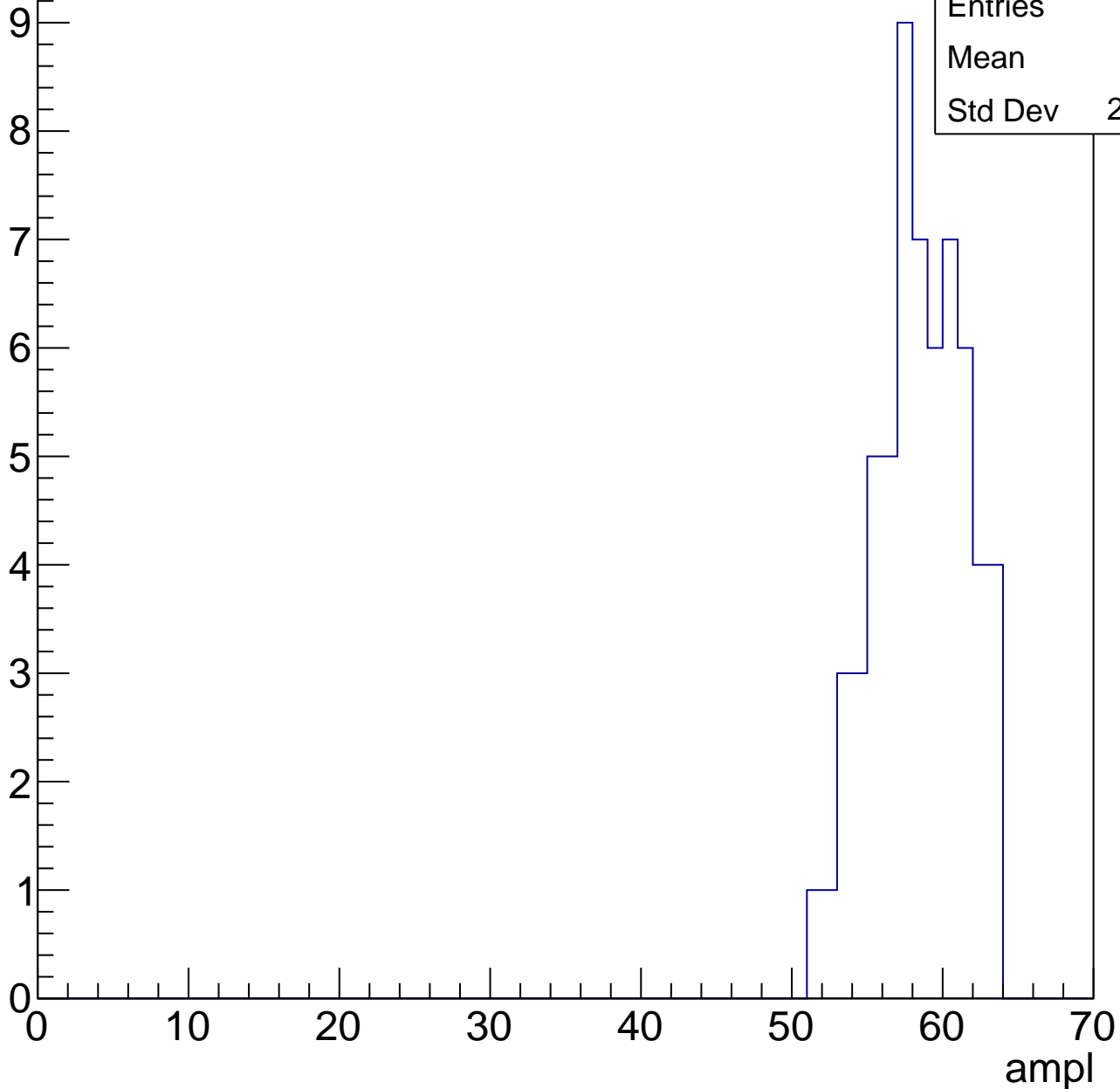


B1L101S, U26-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	58
Std Dev	2.959

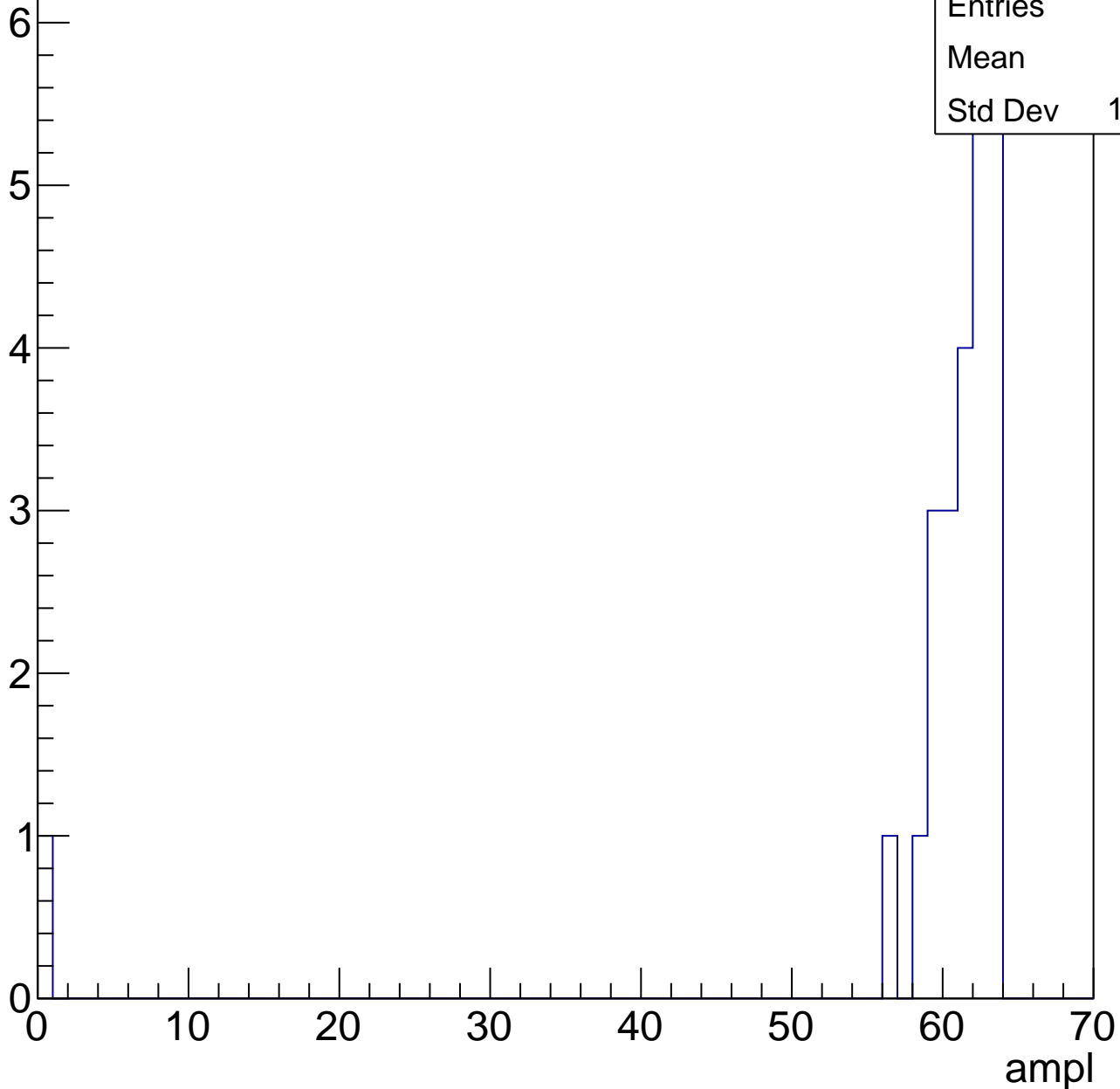


B1L101S, U26-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.6
Std Dev	12.09



B1L101S, U26-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch9, adc0

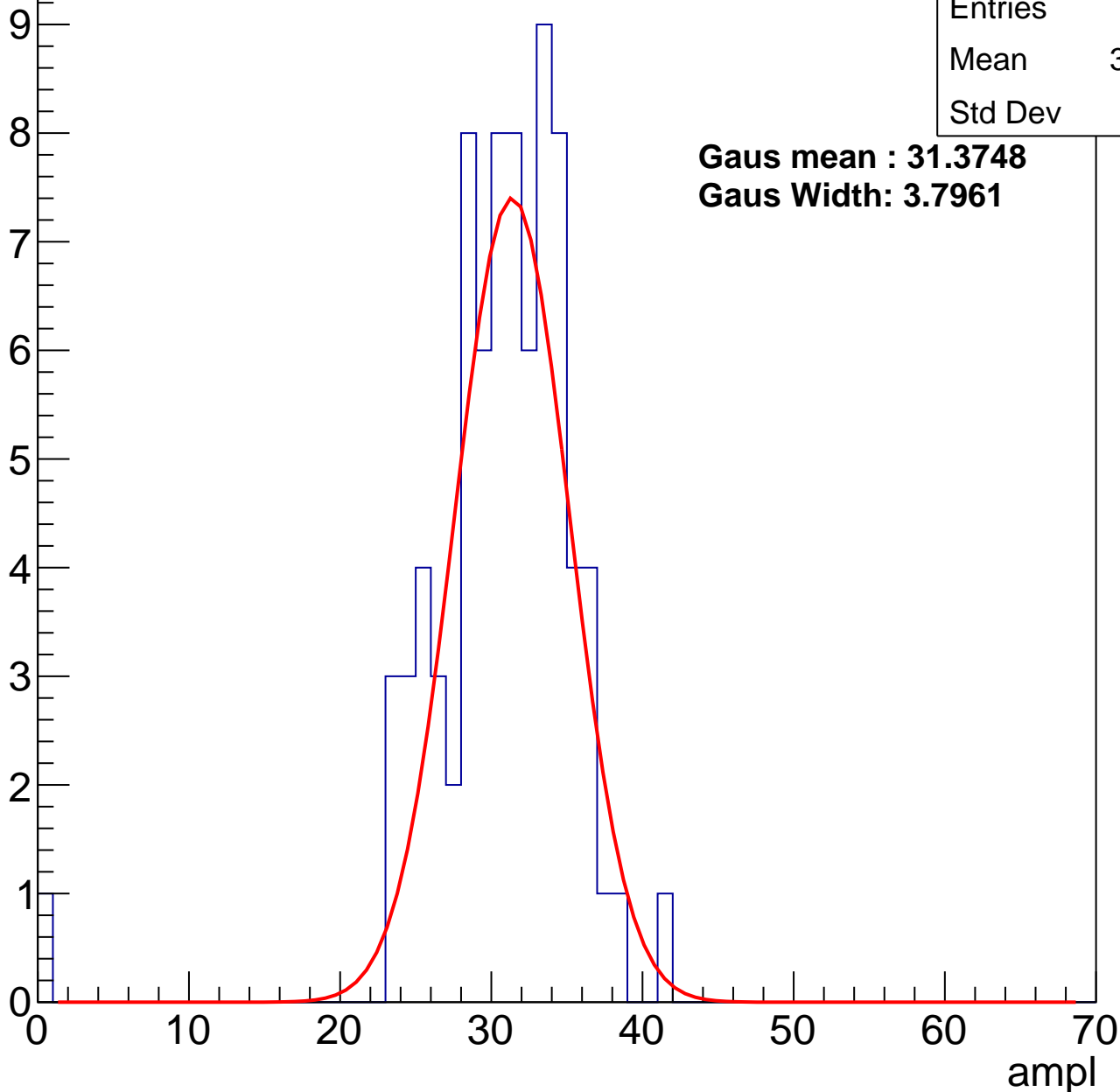
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.25
Std Dev	5.09

Gaus mean : 31.3748

Gaus Width: 3.7961



B1L101S, U26-ch9, adc1

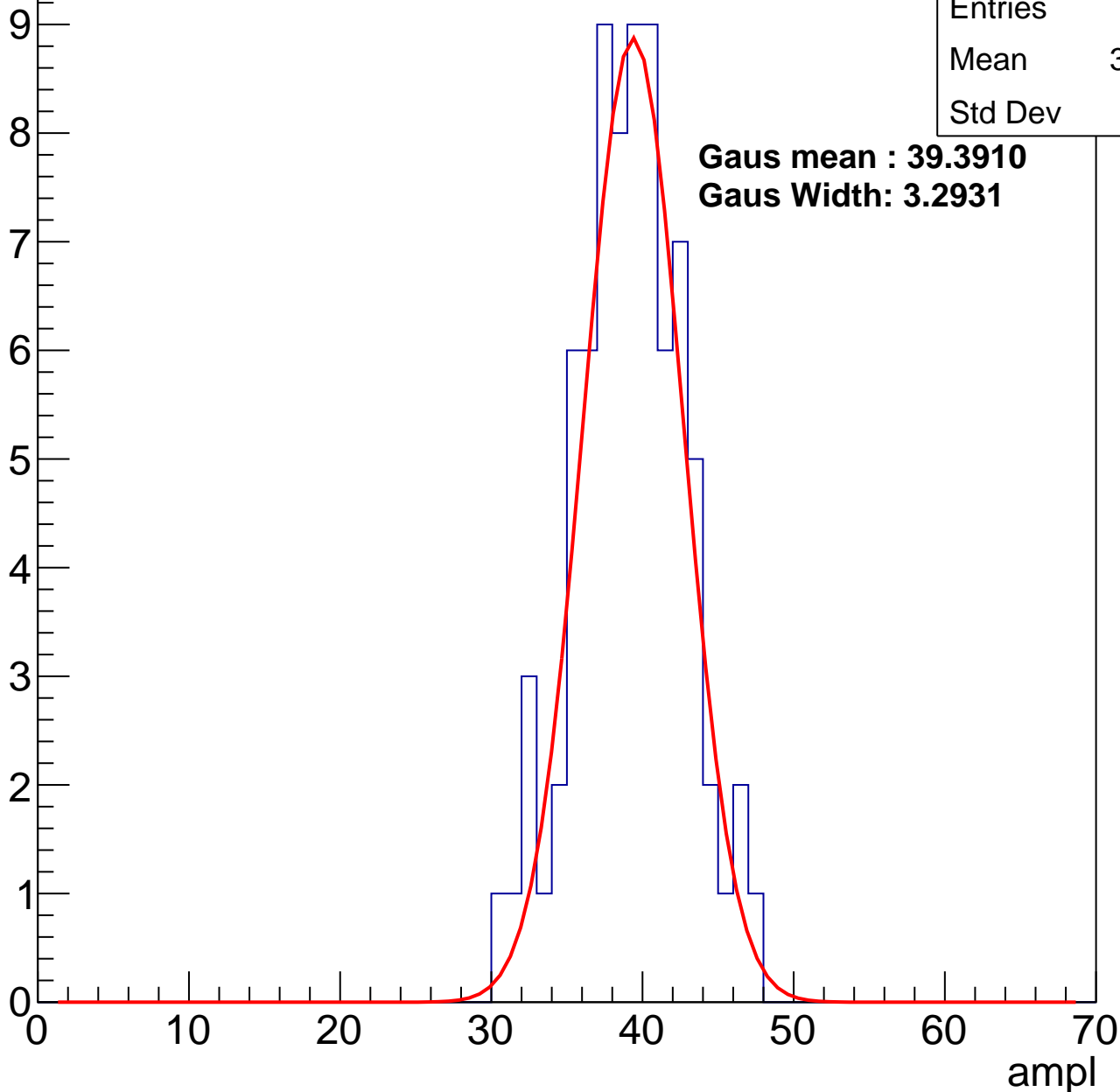
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	38.72
Std Dev	3.54

Gaus mean : 39.3910

Gaus Width: 3.2931



B1L101S, U26-ch9, adc2

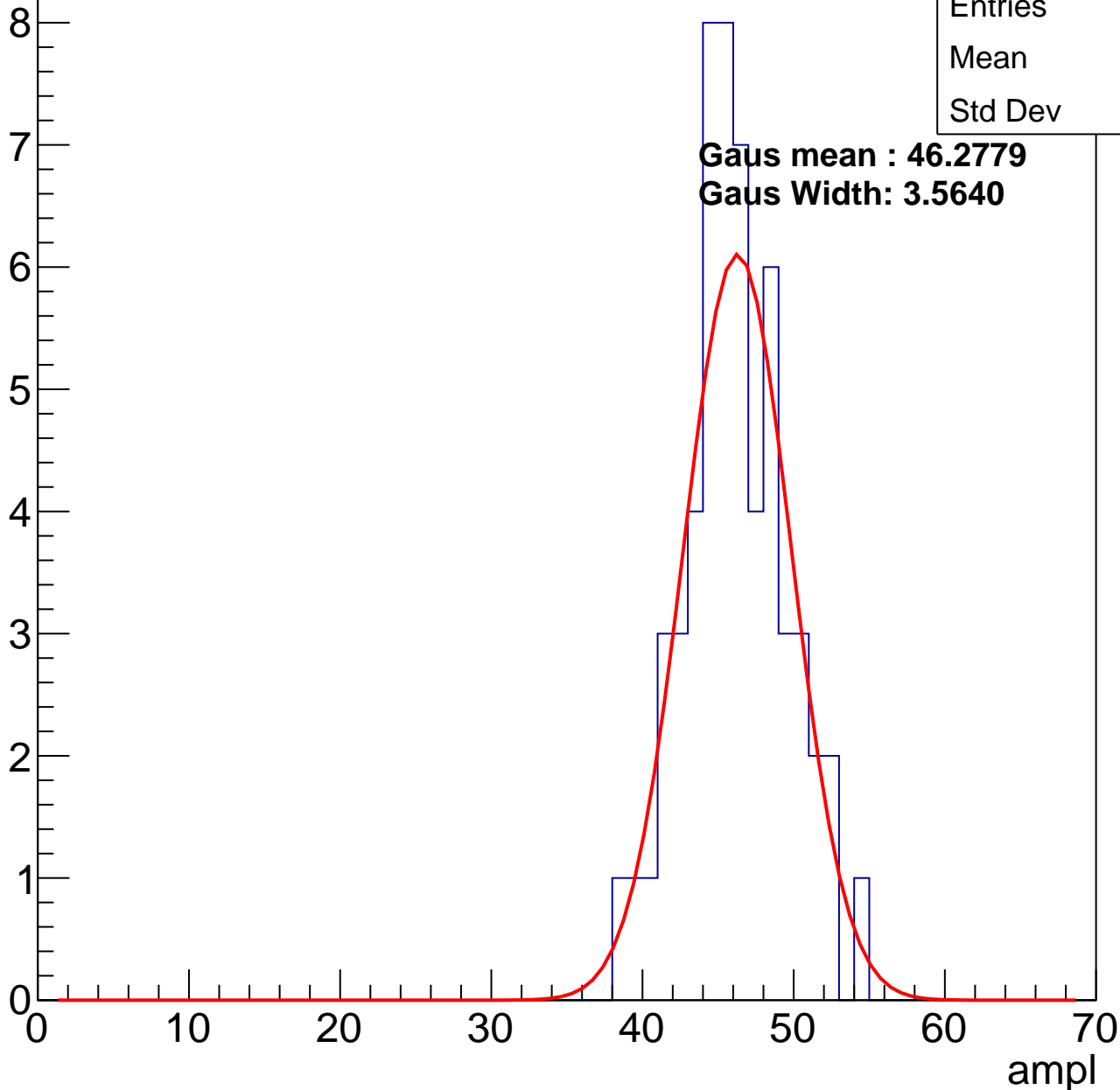
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	45.7
Std Dev	3.33

Gaus mean : 46.2779

Gaus Width: 3.5640

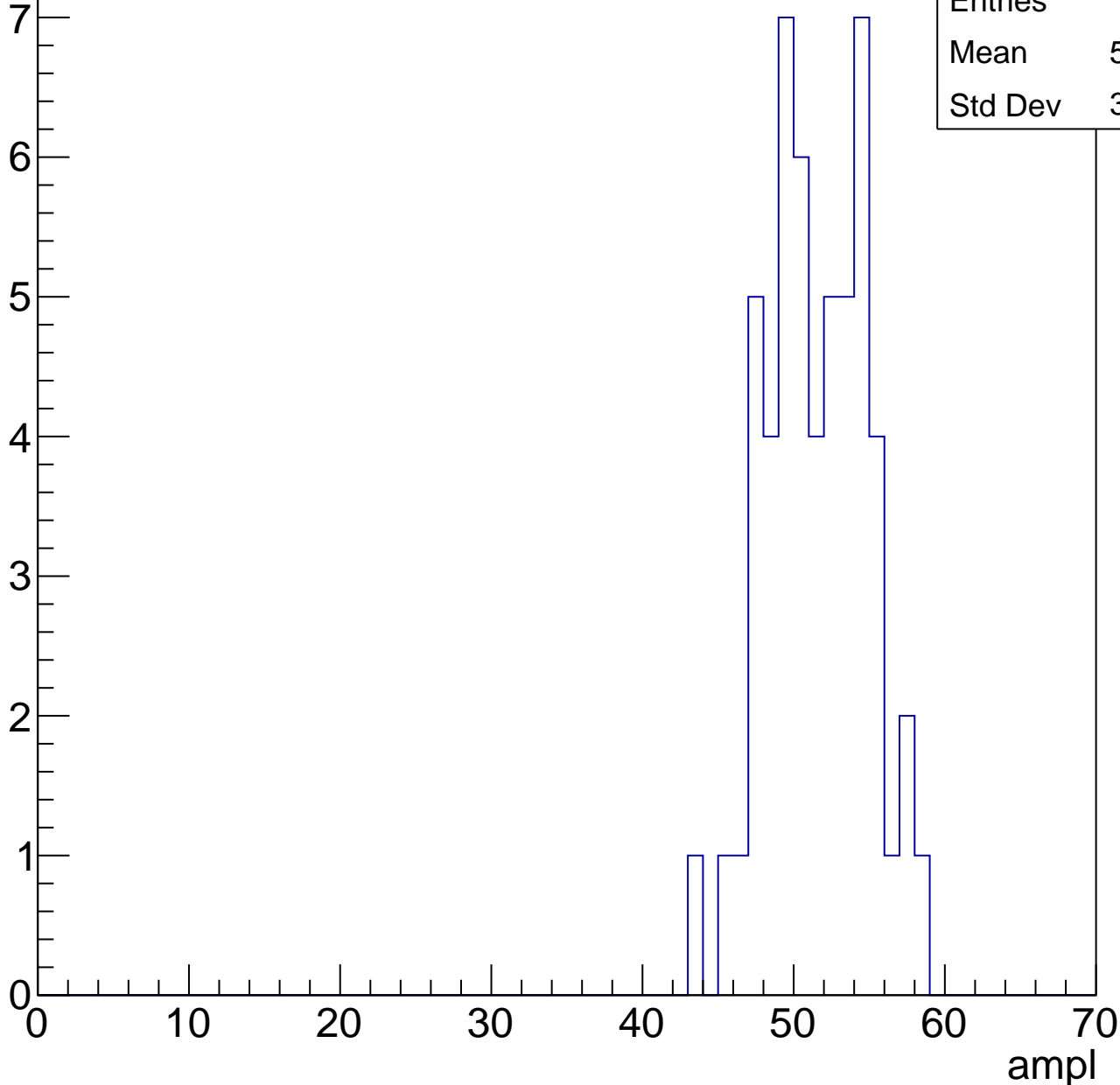


B1L101S, U26-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	51.09
Std Dev	3.256

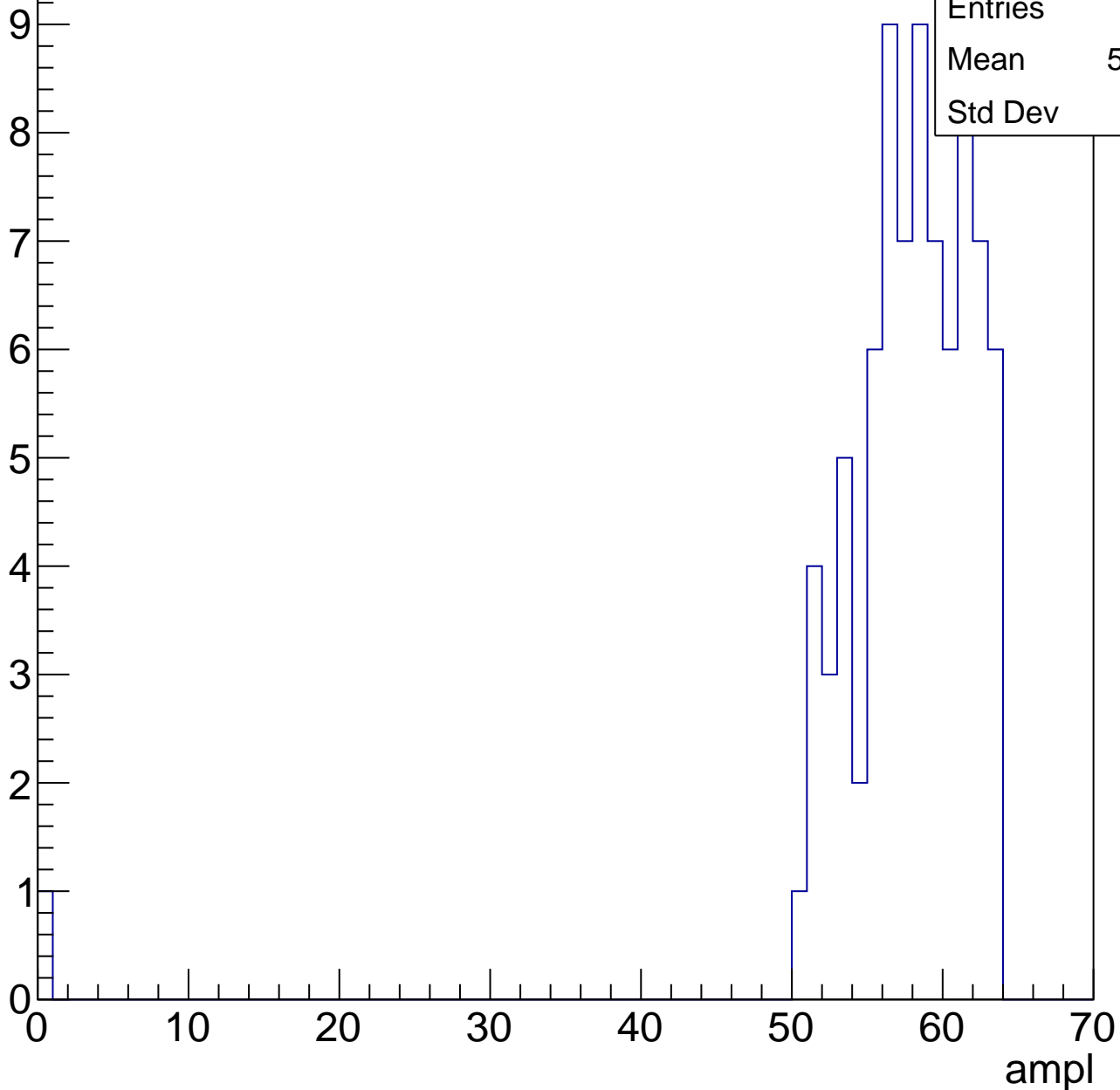


B1L101S, U26-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

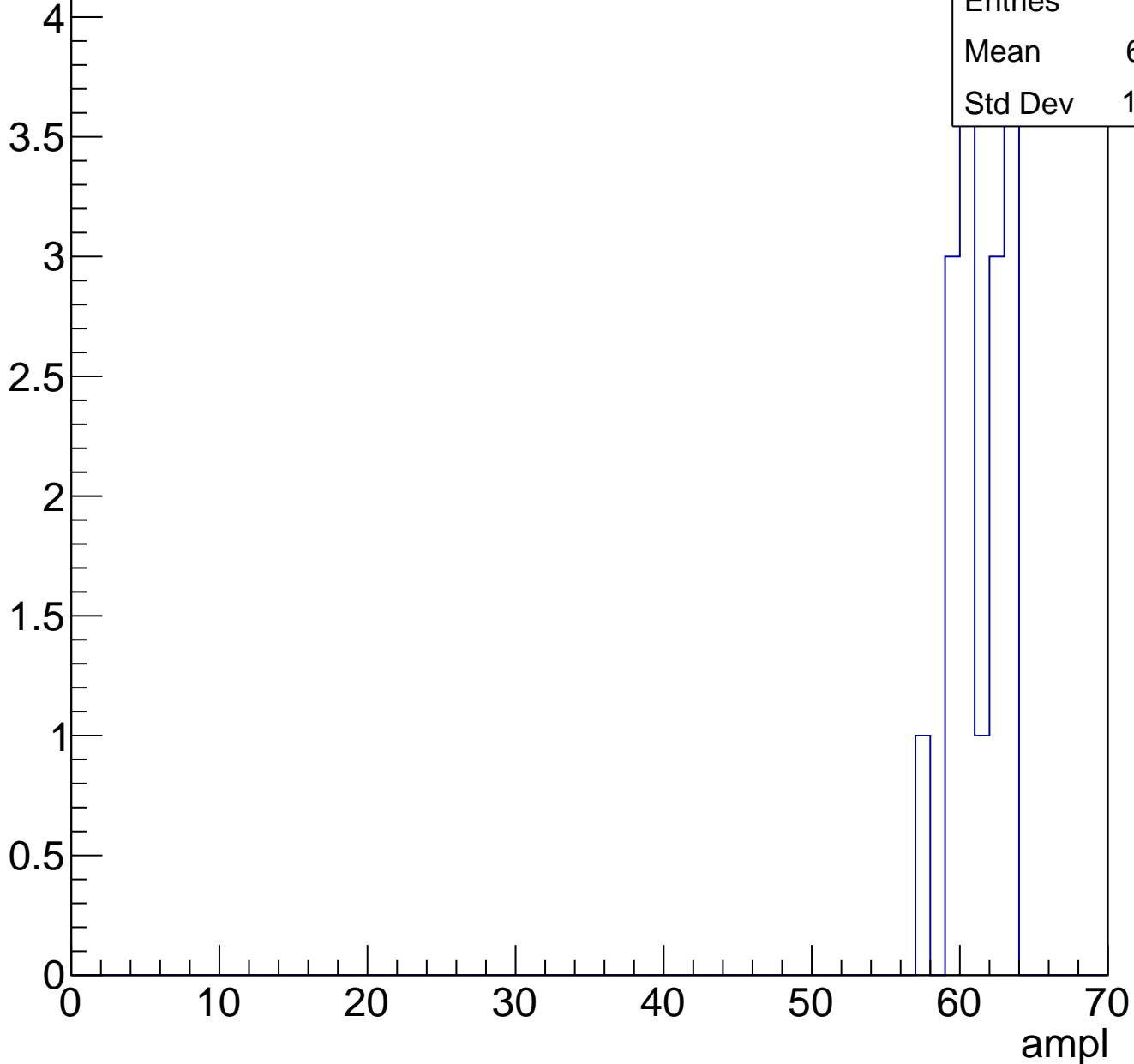
Entries	82
Mean	56.98
Std Dev	7.22



B1L101S, U26-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch10, adc0

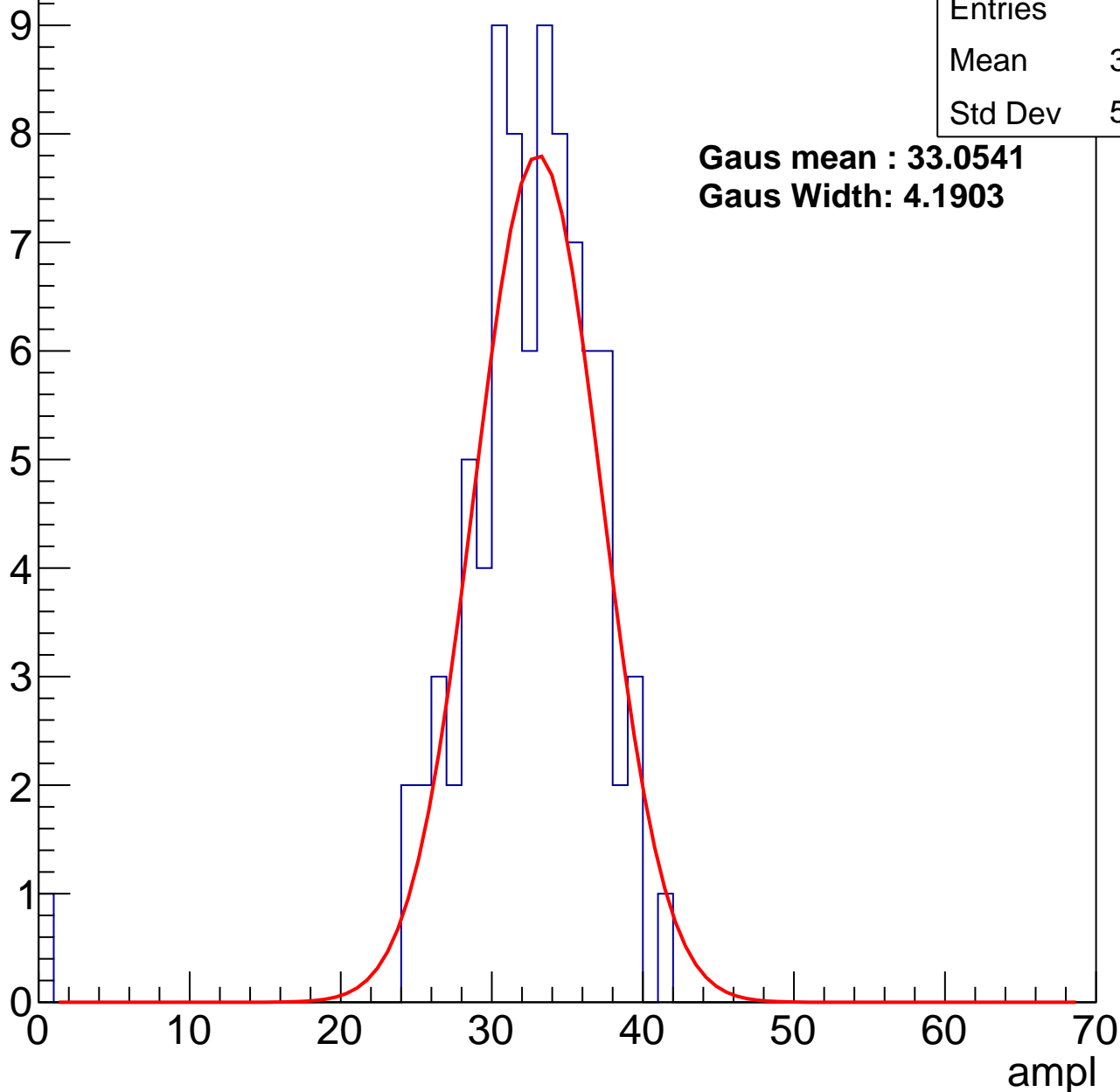
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	31.93
Std Dev	5.138

Gaus mean : 33.0541

Gaus Width: 4.1903



B1L101S, U26-ch10, adc1

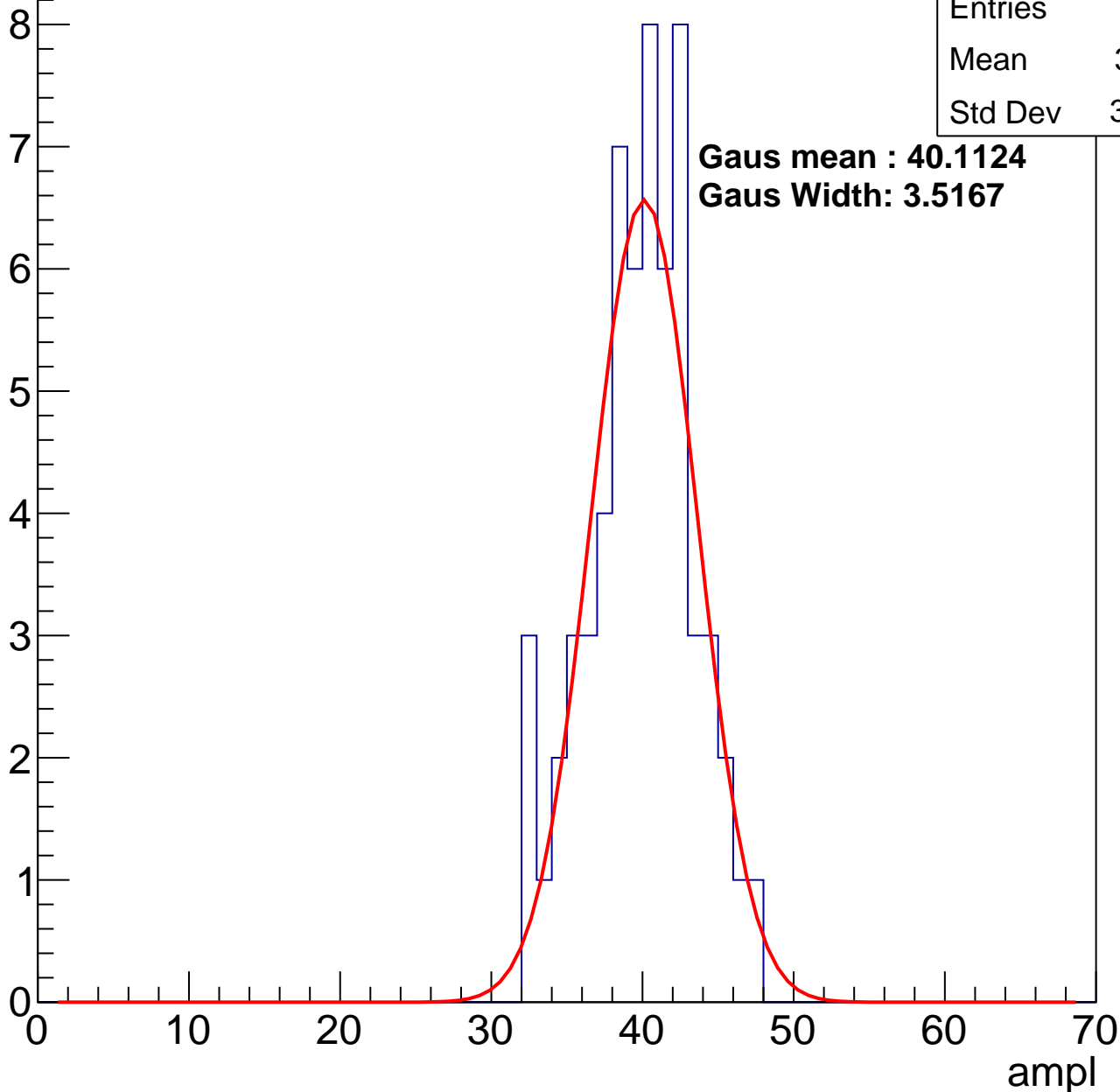
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	39.41
Std Dev	3.466

Gaus mean : 40.1124

Gaus Width: 3.5167



B1L101S, U26-ch10, adc2

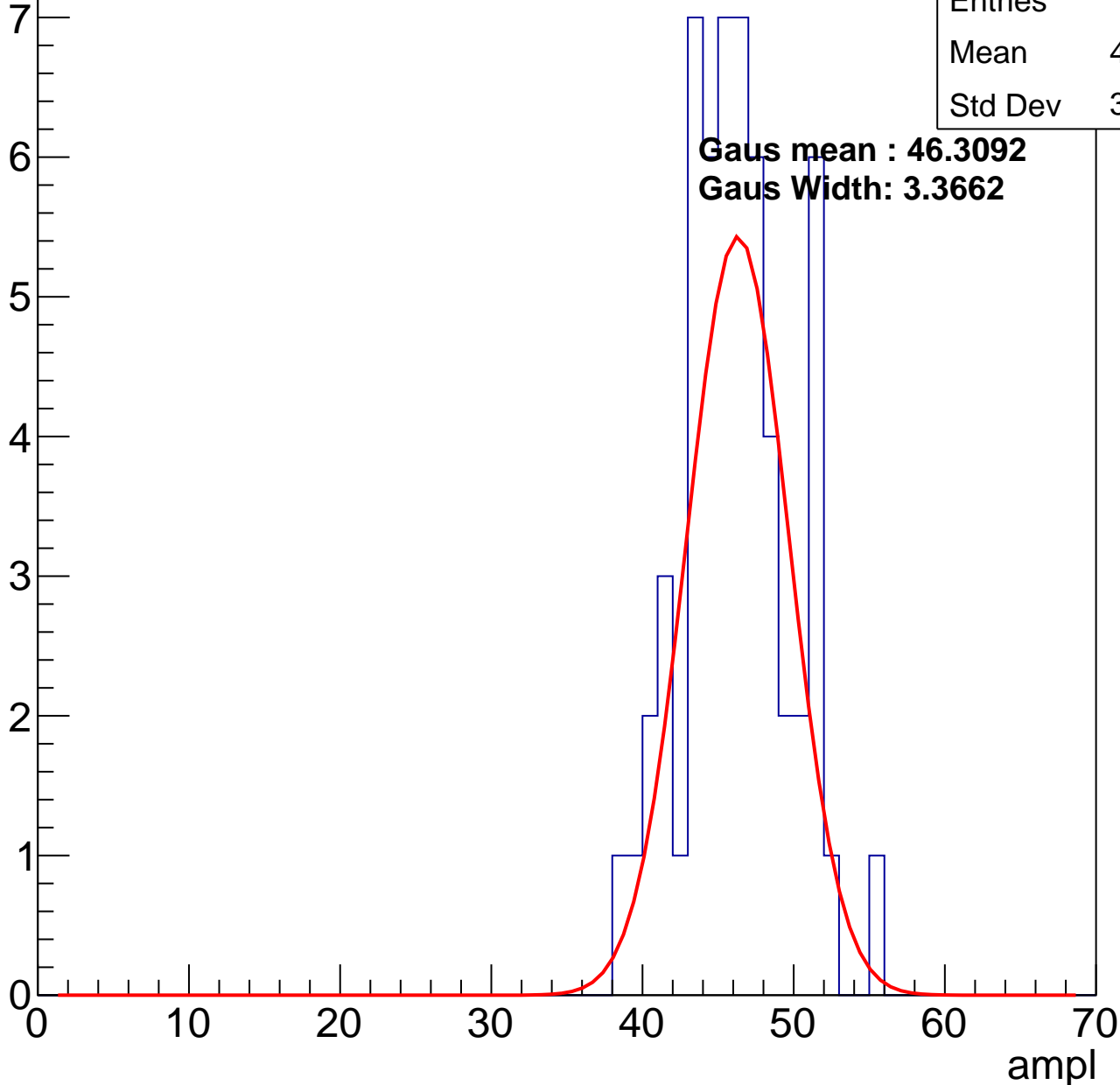
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	45.77
Std Dev	3.519

Gaus mean : 46.3092

Gaus Width: 3.3662

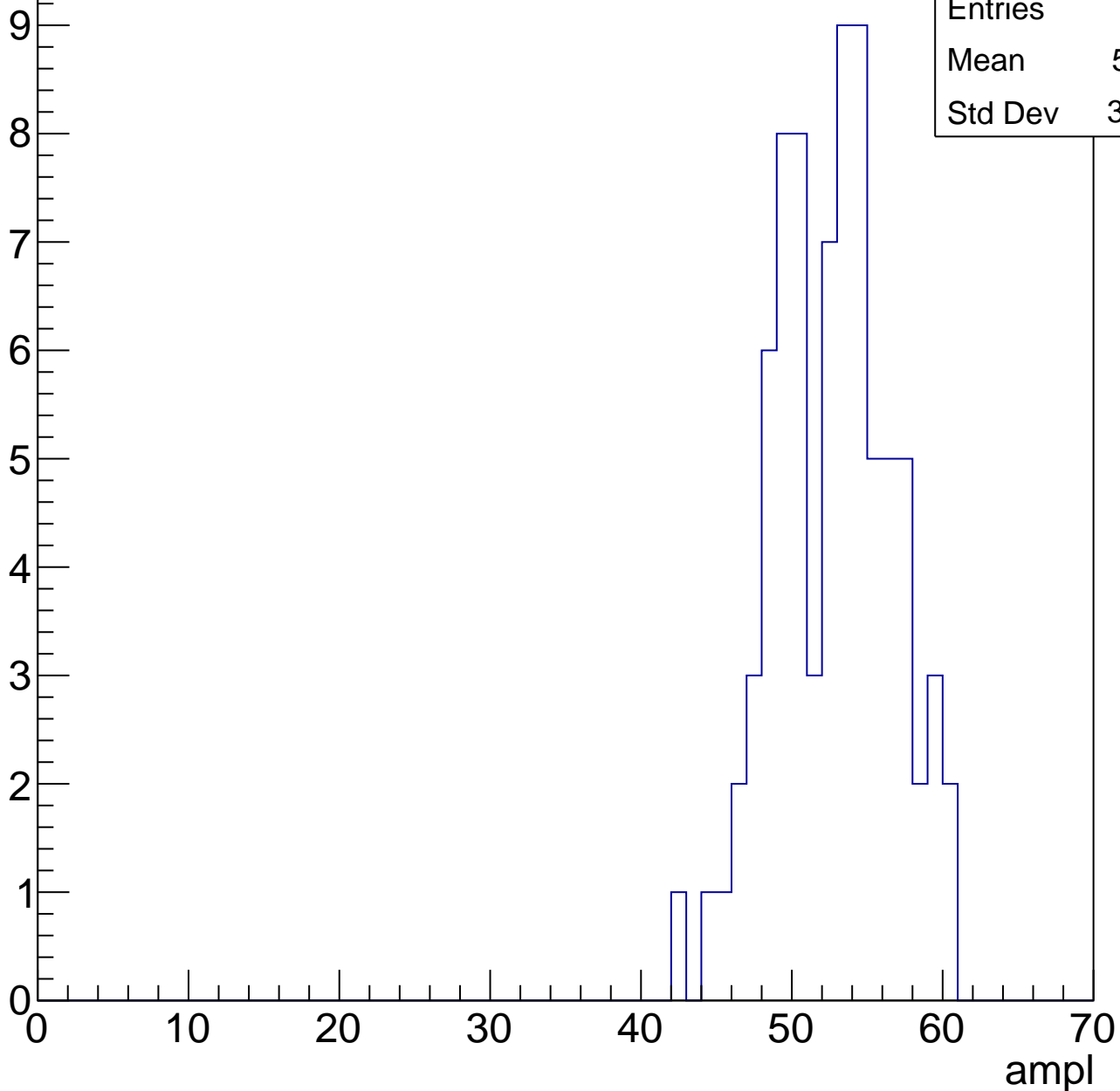


B1L101S, U26-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	52.21
Std Dev	3.875

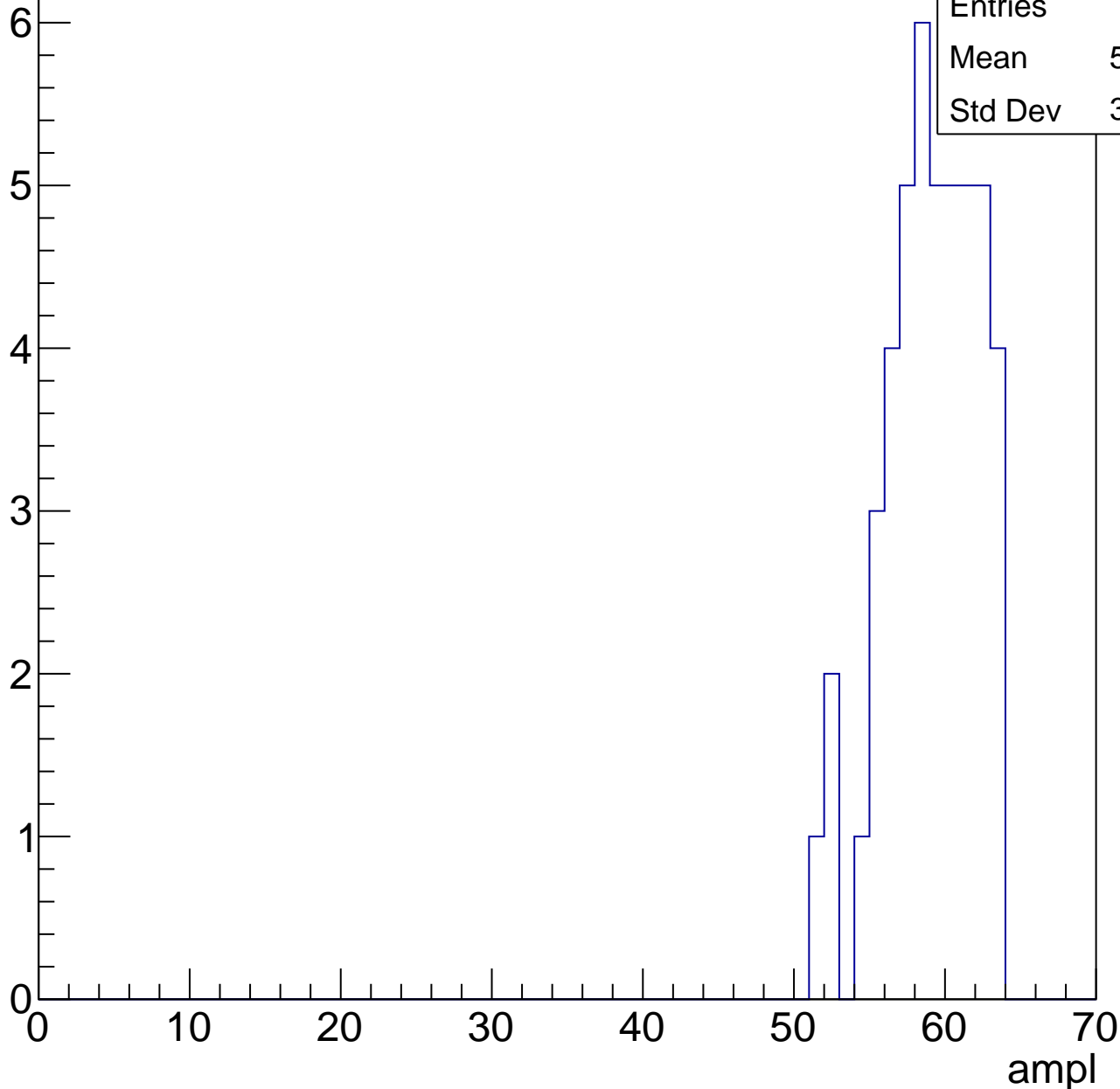


B1L101S, U26-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.54
Std Dev	3.027

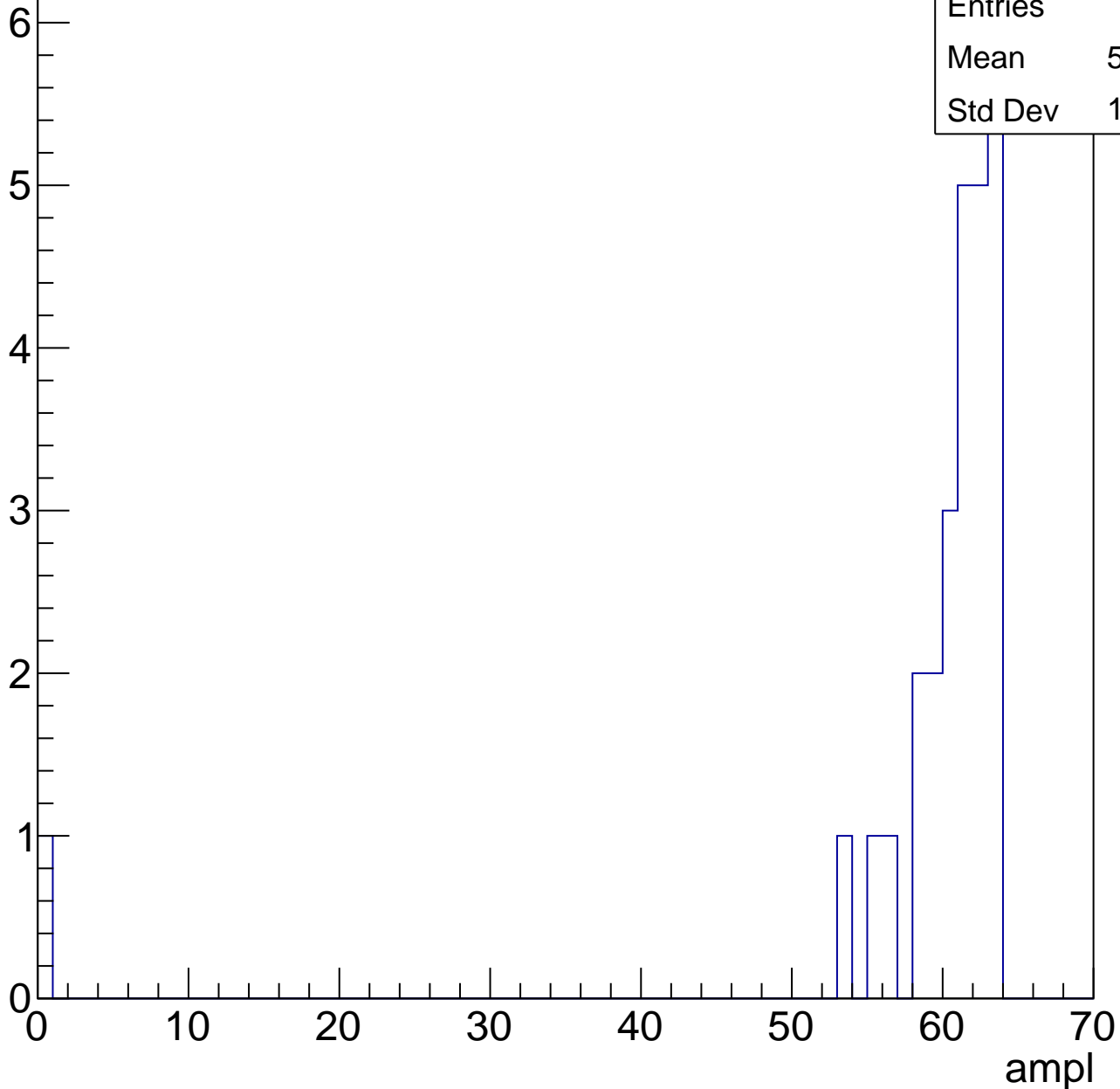


B1L101S, U26-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.19
Std Dev	11.69



B1L101S, U26-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch11, adc0

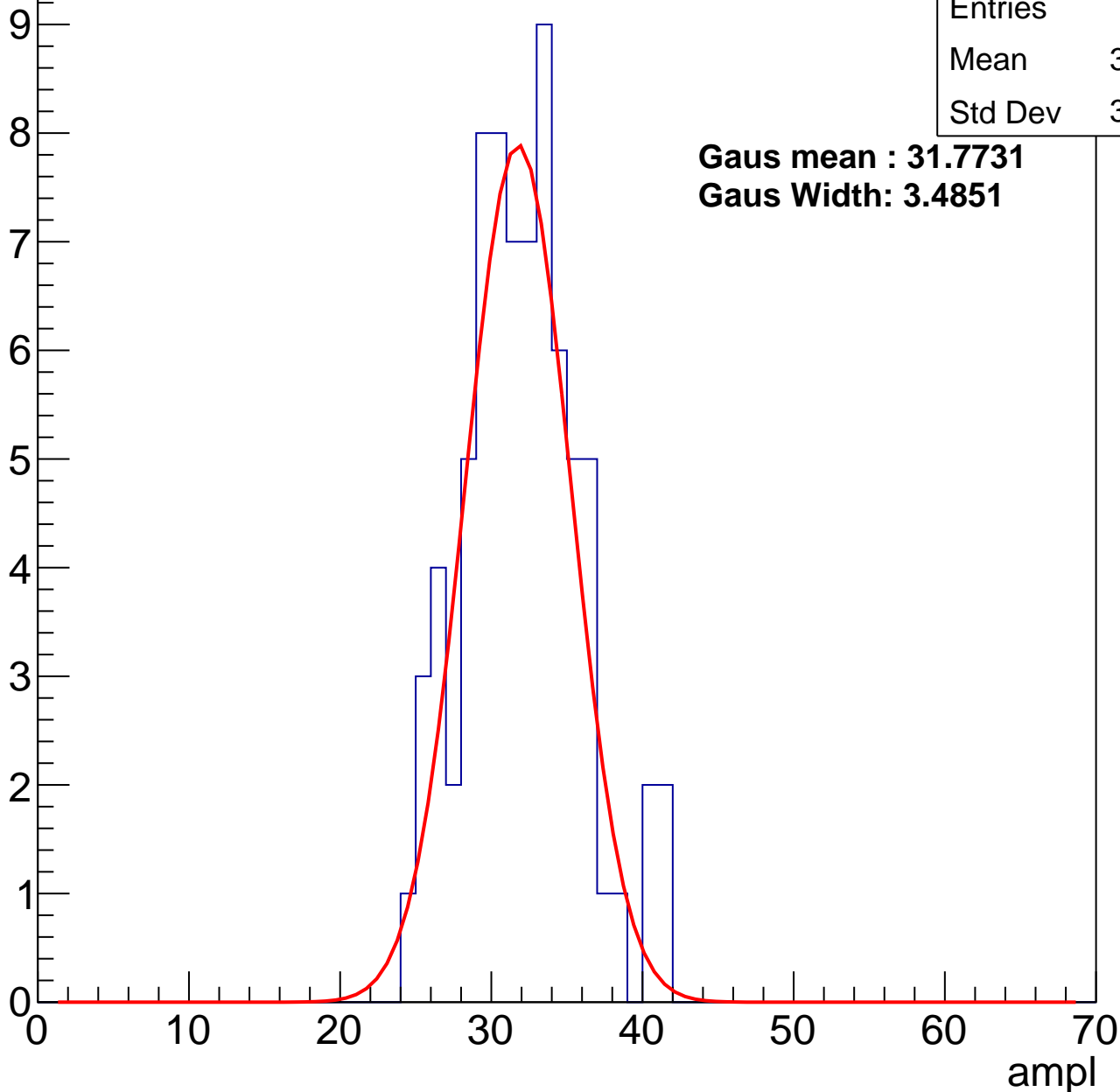
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	31.62
Std Dev	3.794

Gaus mean : 31.7731

Gaus Width: 3.4851



B1L101S, U26-ch11, adc1

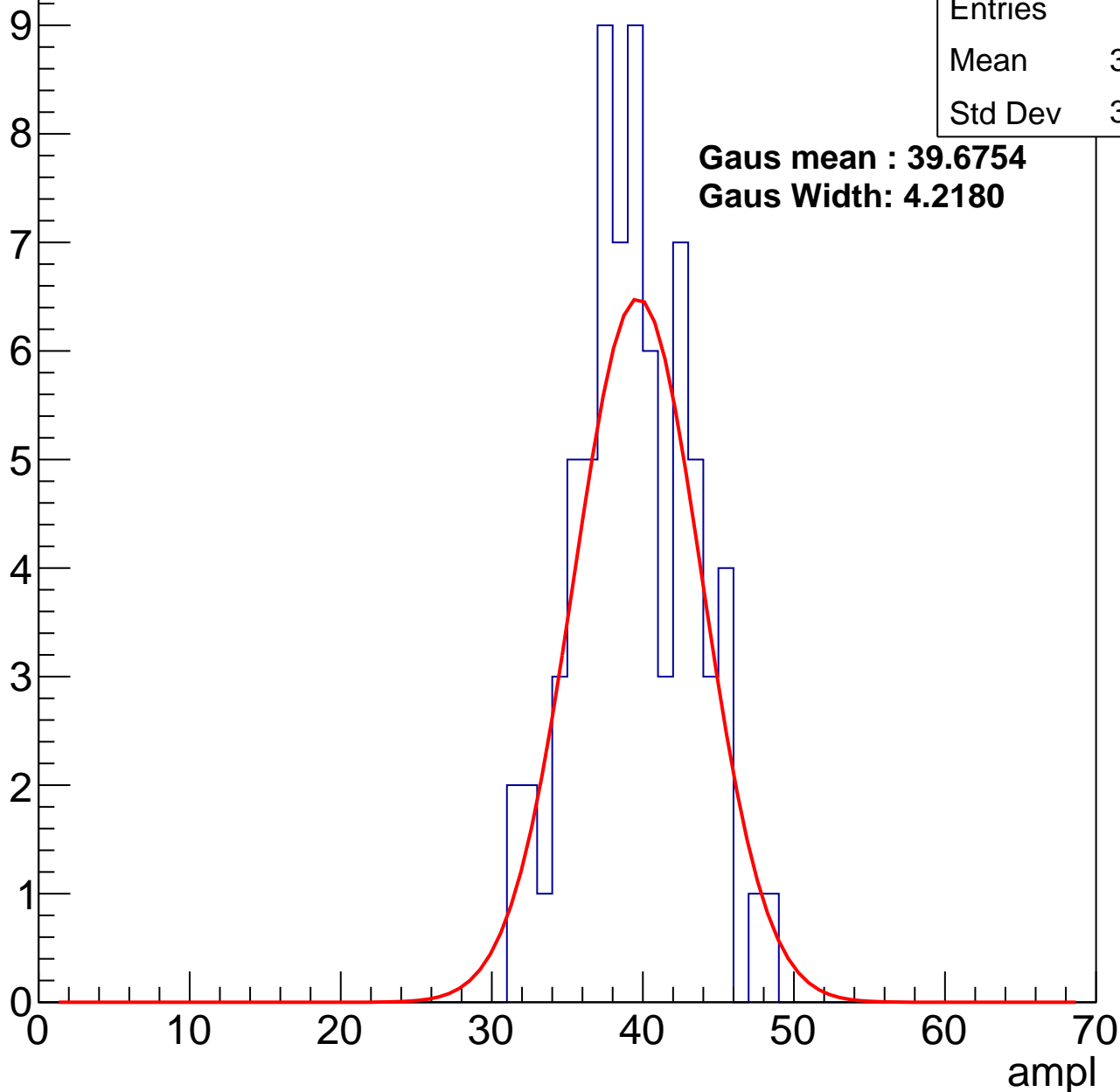
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	38.97
Std Dev	3.756

Gaus mean : 39.6754

Gaus Width: 4.2180



B1L101S, U26-ch11, adc2

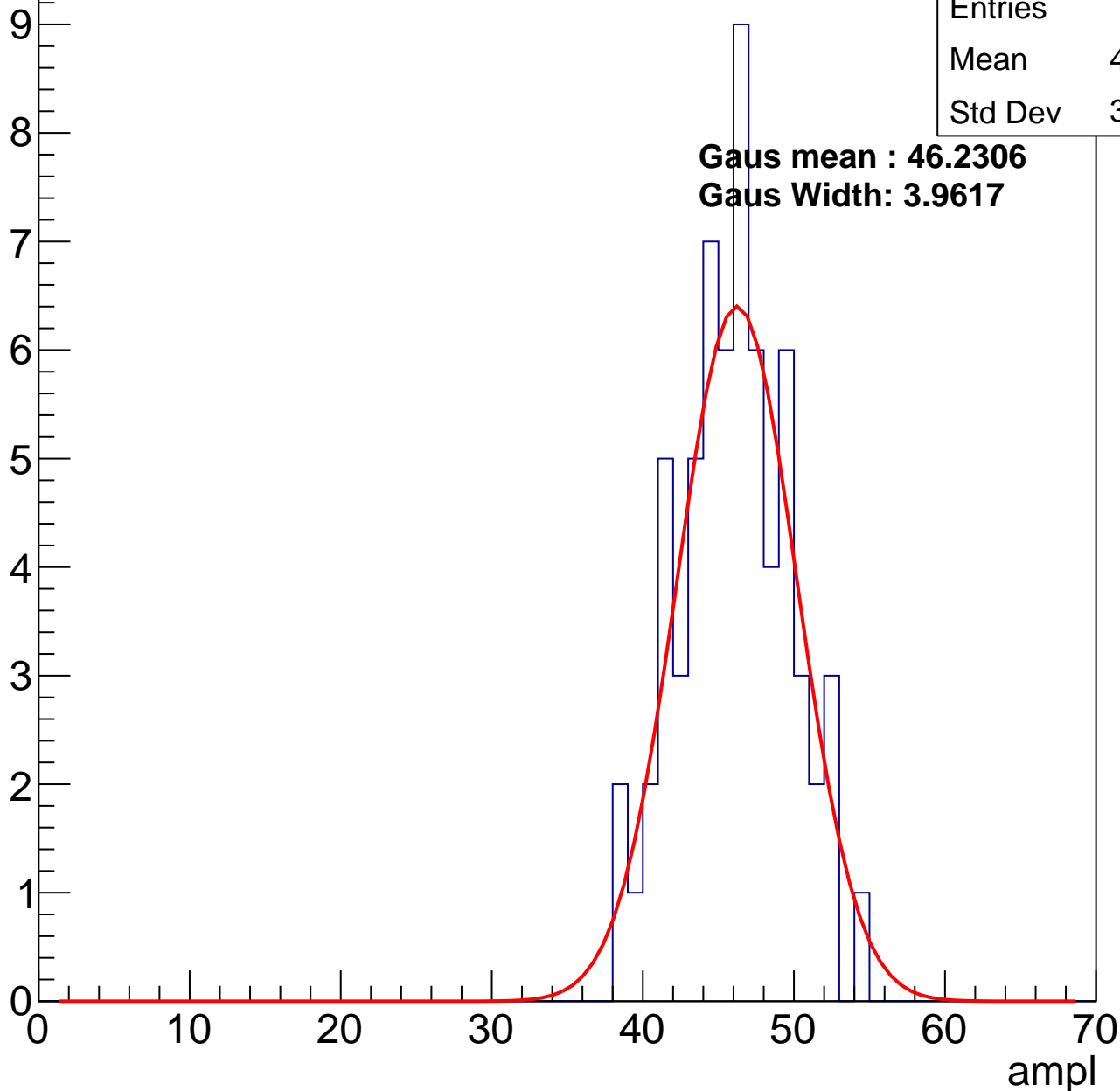
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.58
Std Dev	3.586

Gaus mean : 46.2306

Gaus Width: 3.9617

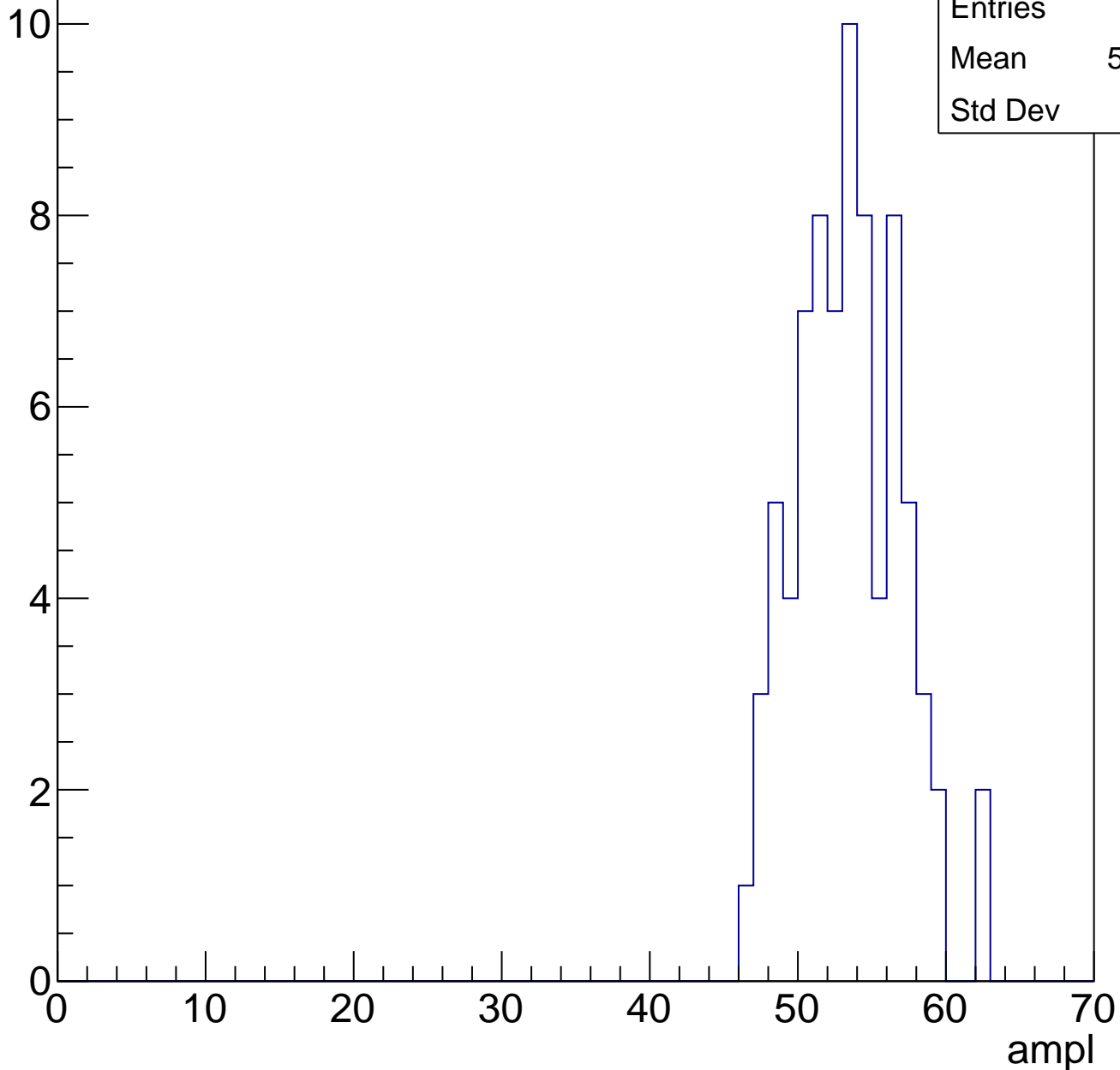


B1L101S, U26-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	52.94
Std Dev	3.48

Entry

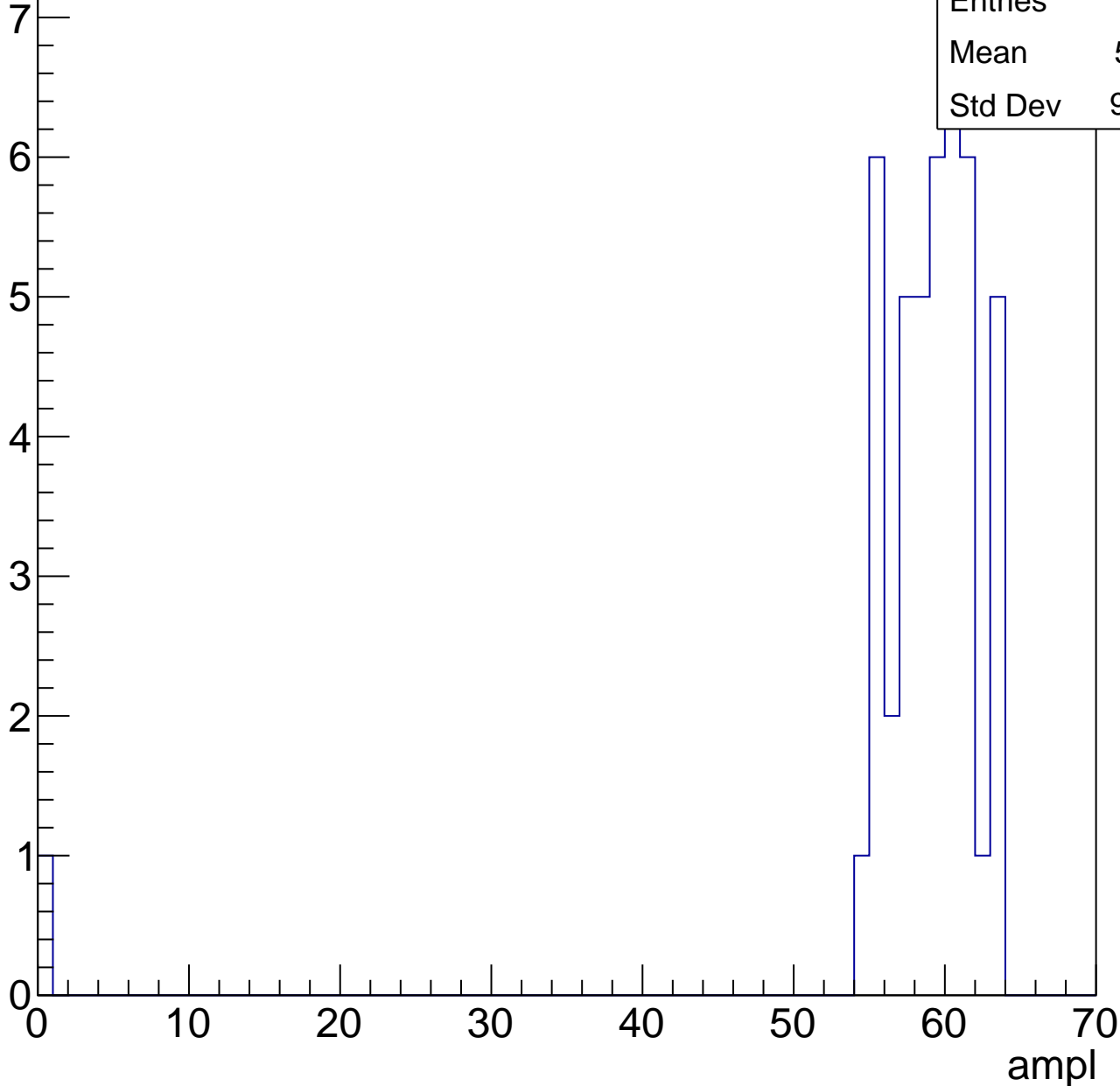


B1L101S, U26-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.51
Std Dev	9.025



B1L101S, U26-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries	21
Mean	61.05
Std Dev	1.558

ampl

10

20

30

40

50

60

70

B1L101S, U26-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch12, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	30.52
Std Dev	3.424

Gaus mean : 31.2755

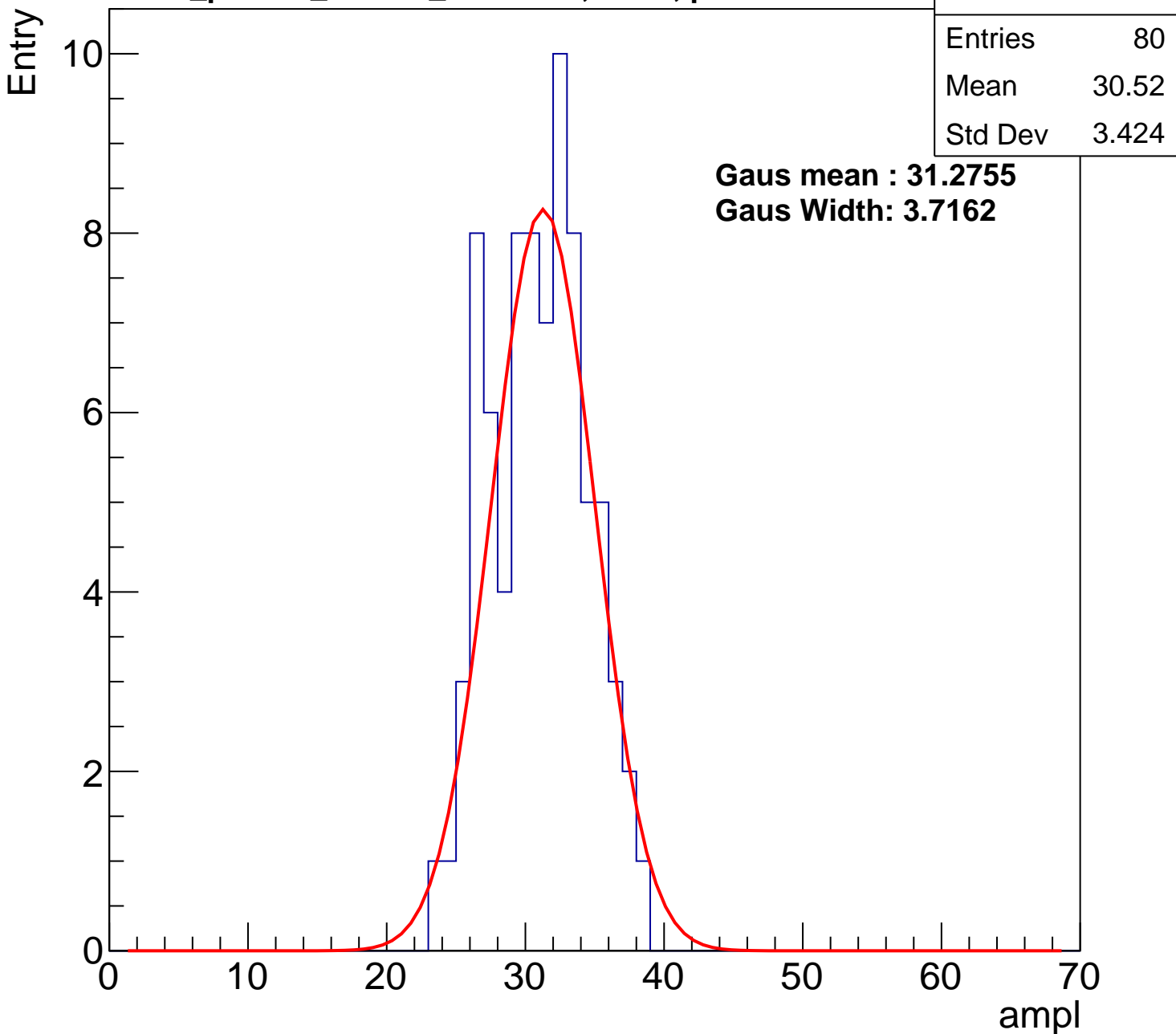
Gaus Width: 3.7162

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch12, adc1

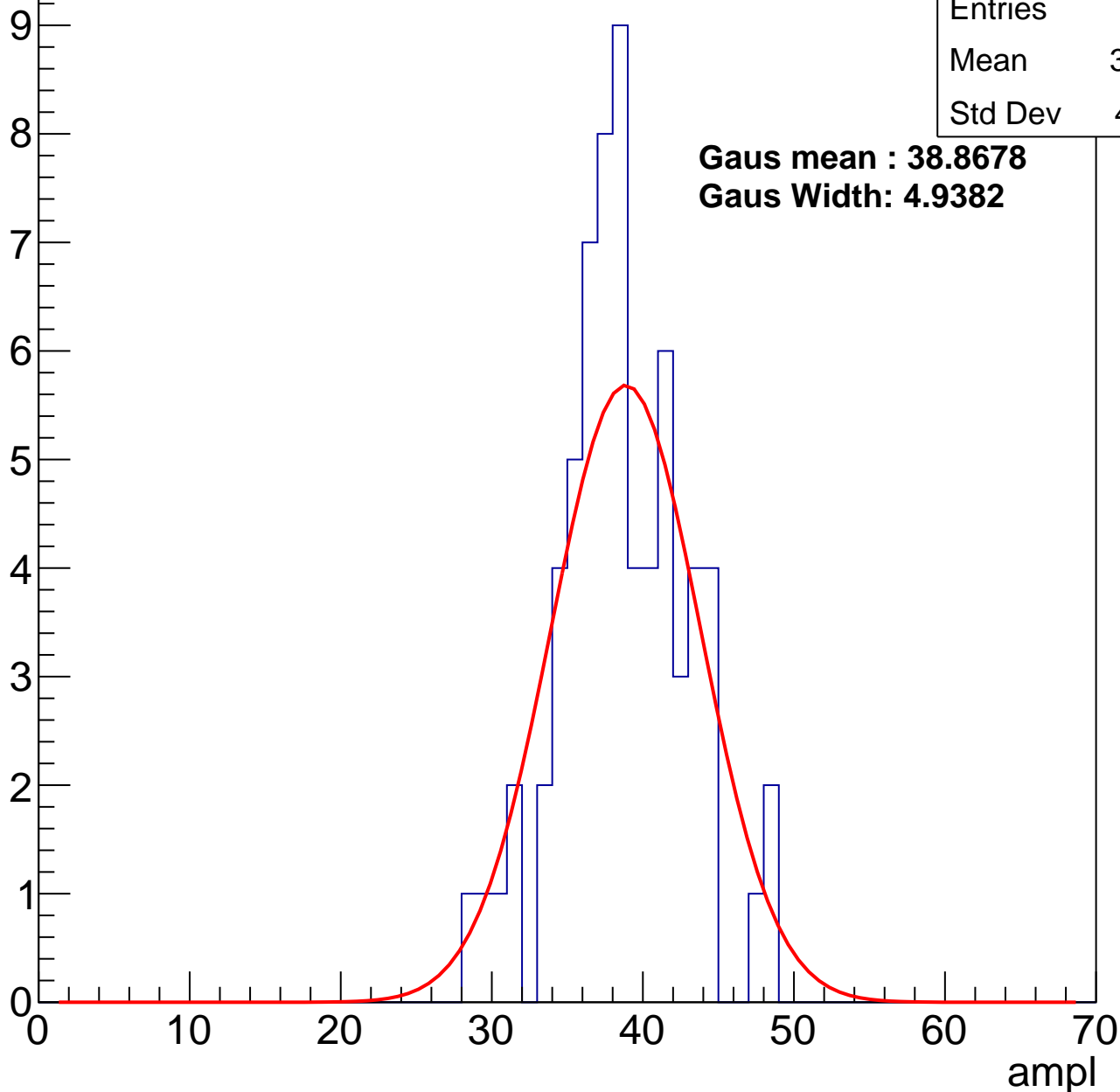
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	38.16
Std Dev	4.171

Gaus mean : 38.8678

Gaus Width: 4.9382



B1L101S, U26-ch12, adc2

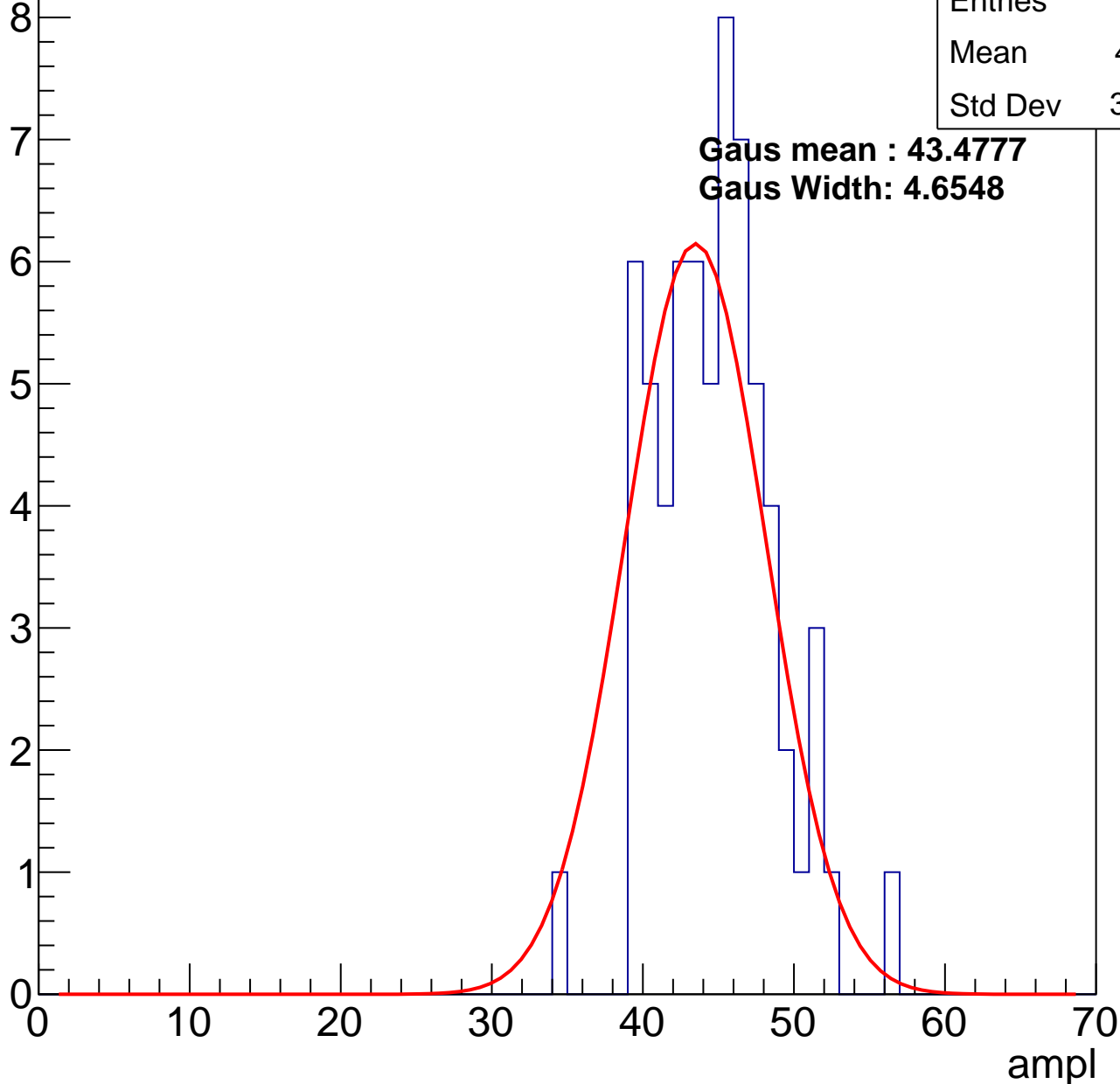
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.31
Std Dev	3.875

Gaus mean : 43.4777

Gaus Width: 4.6548

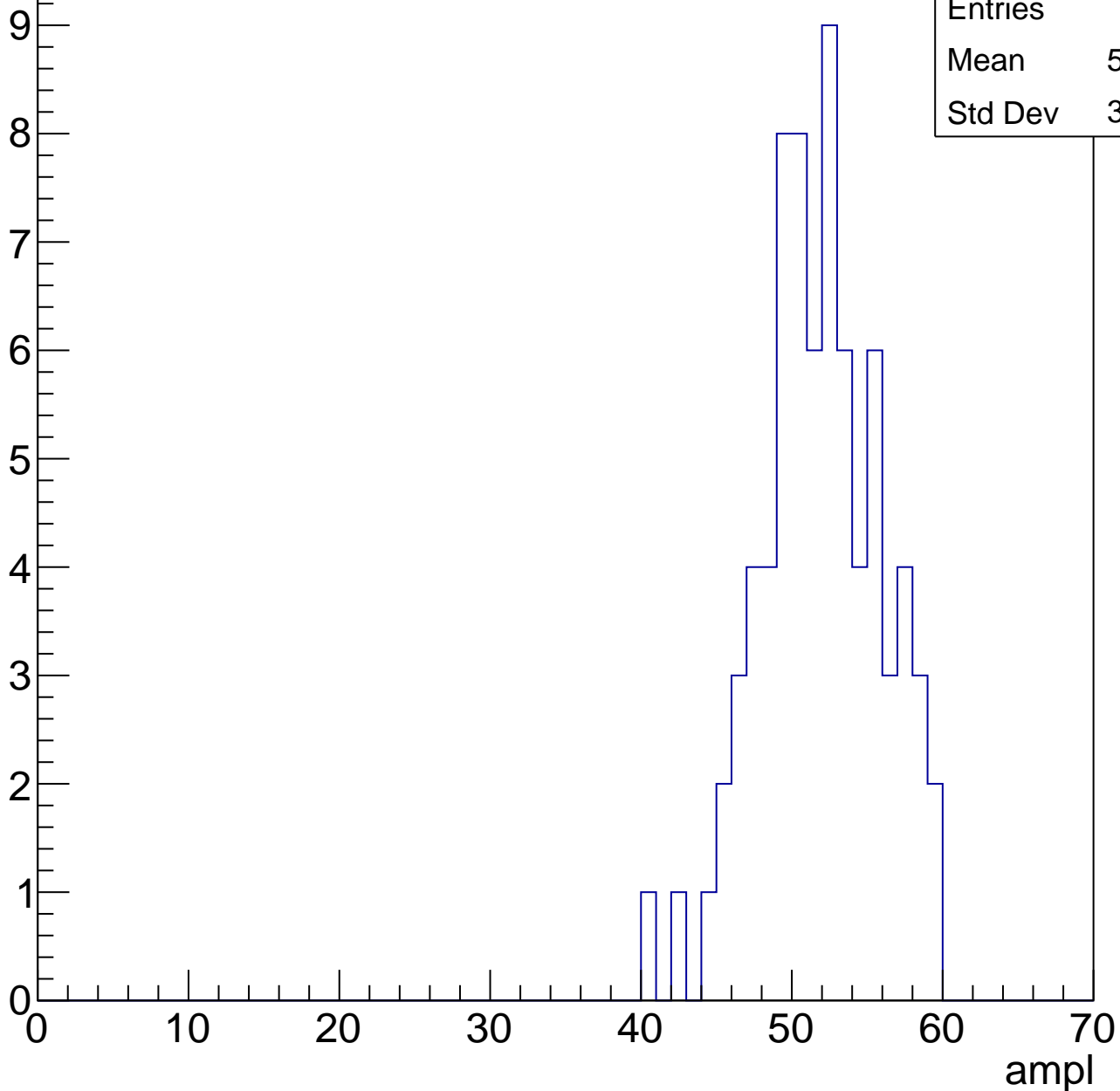


B1L101S, U26-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

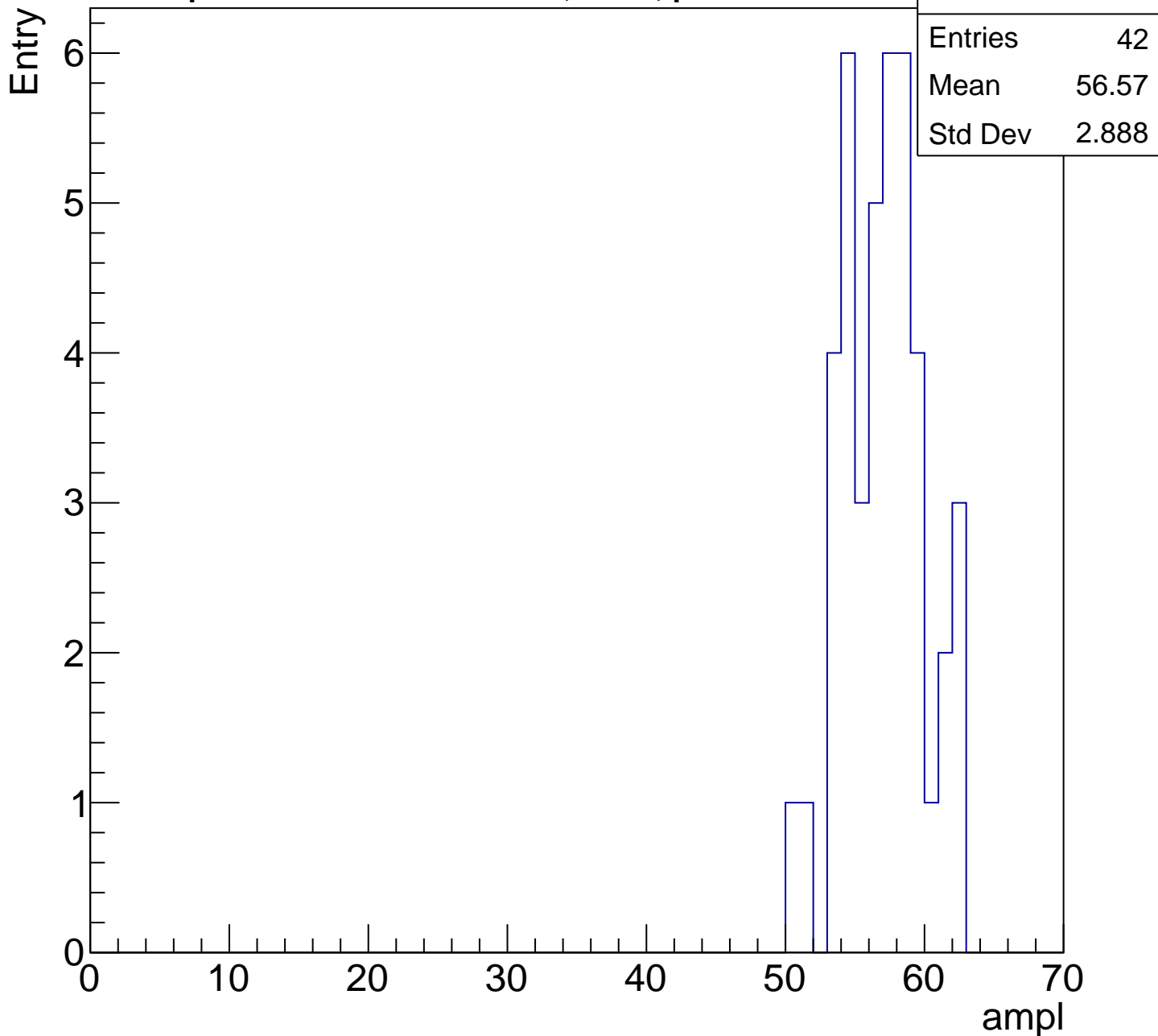
Entry

Entries	75
Mean	51.36
Std Dev	3.979



B1L101S, U26-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch12, adc5

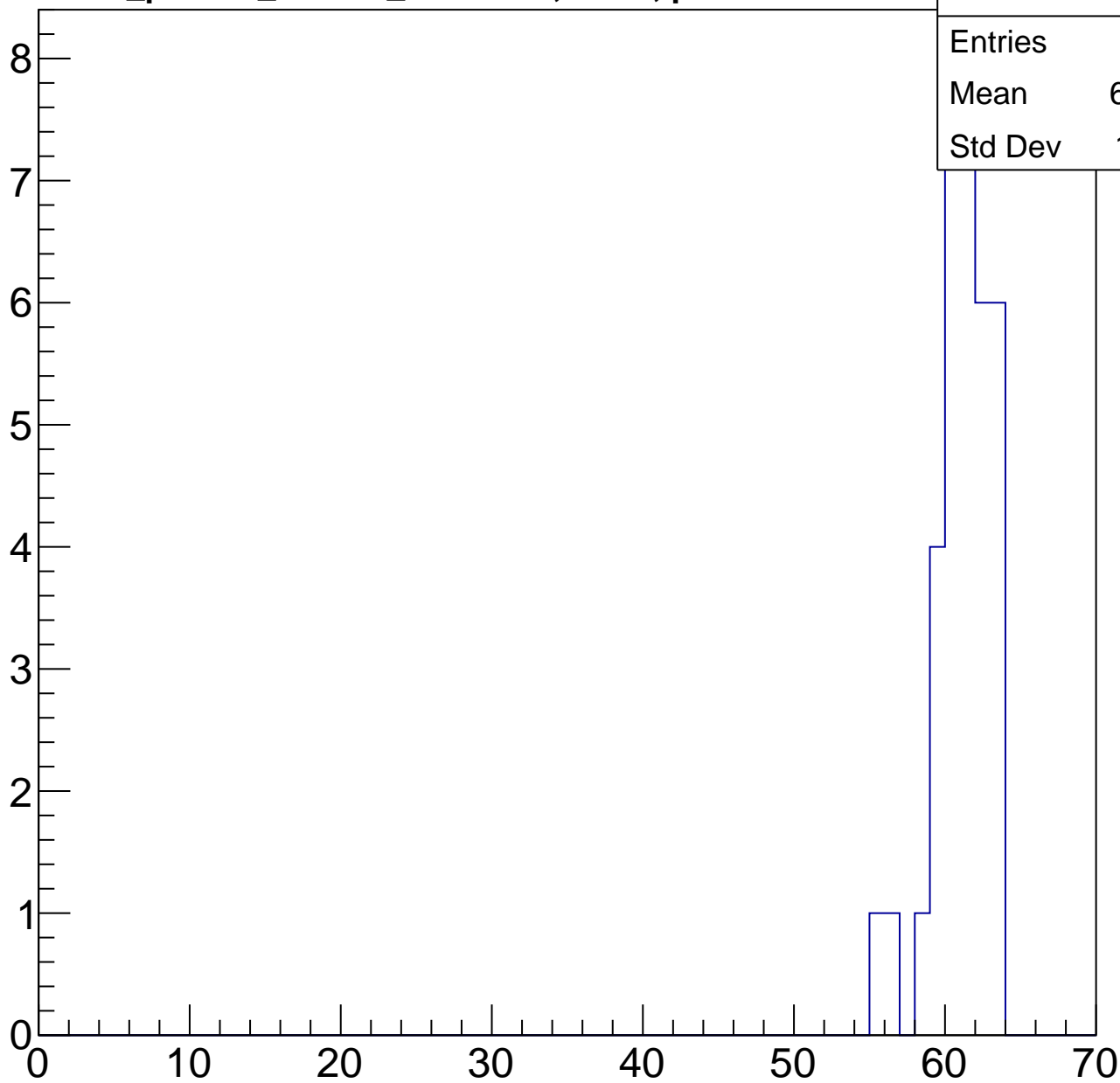
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.66
Std Dev	1.851

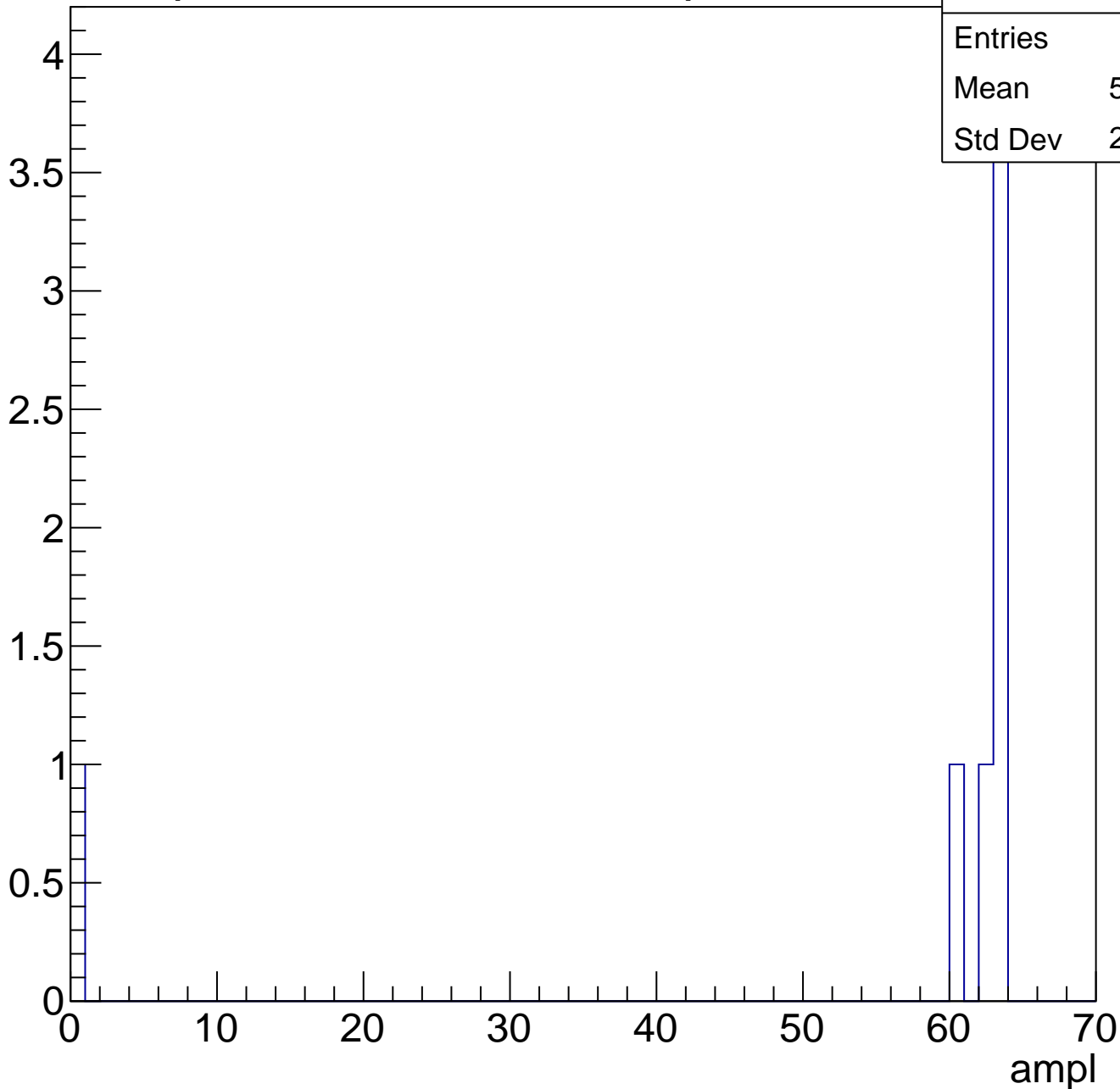
ampl



B1L101S, U26-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L101S, U26-ch13, adc0

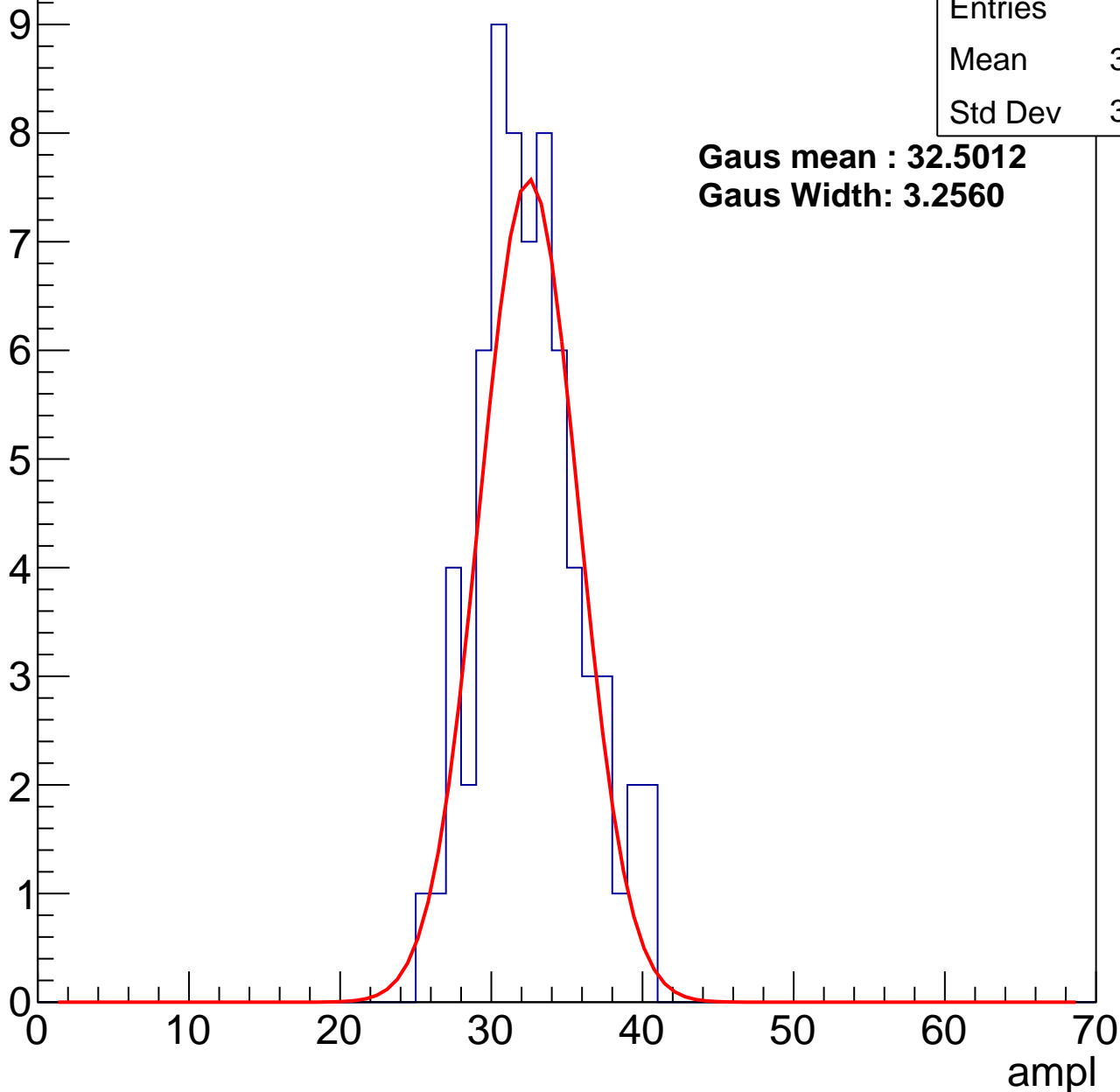
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	32.15
Std Dev	3.382

Gaus mean : 32.5012

Gaus Width: 3.2560



B1L101S, U26-ch13, adc1

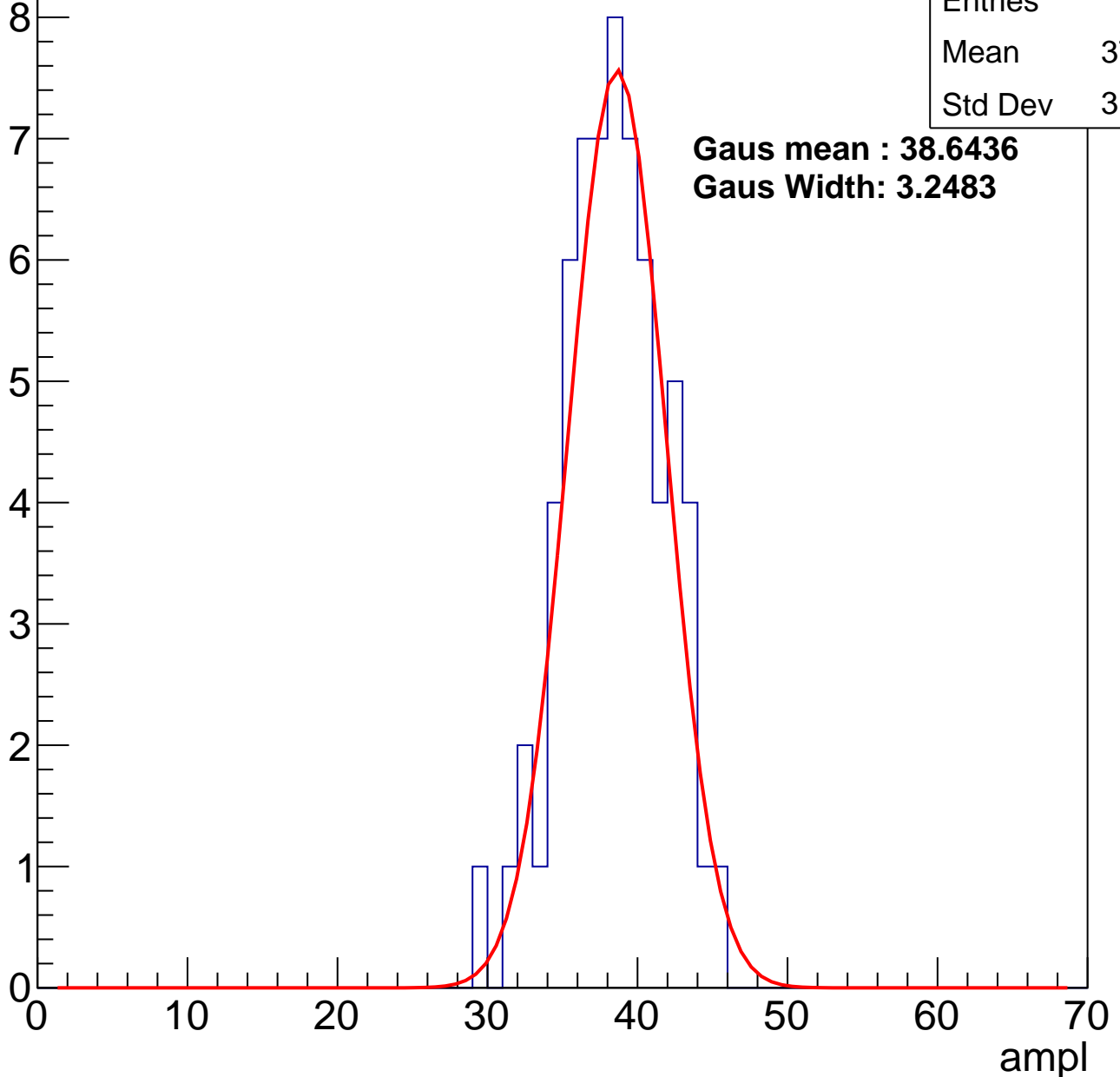
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.94
Std Dev	3.309

Gaus mean : 38.6436

Gaus Width: 3.2483



B1L101S, U26-ch13, adc2

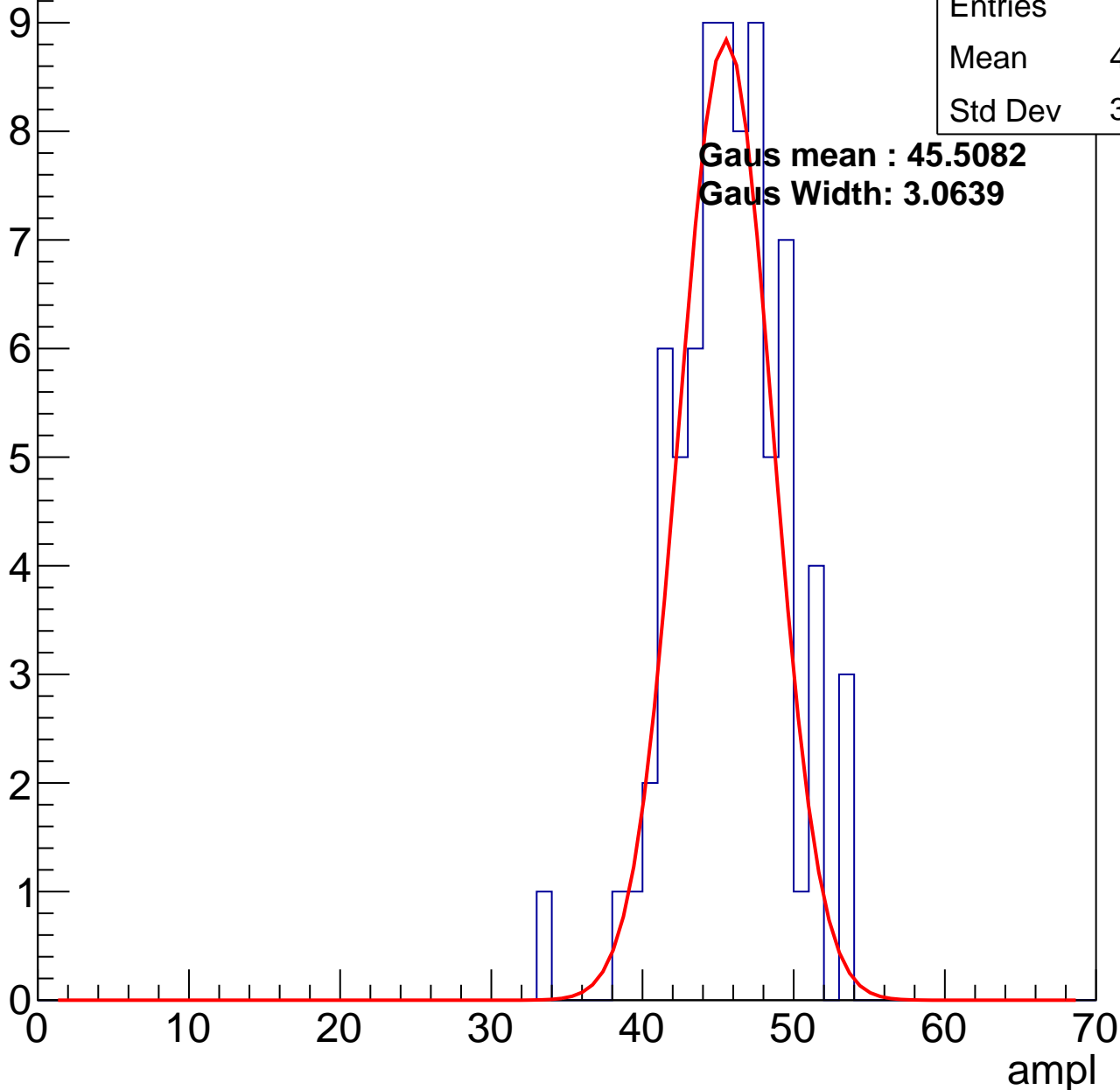
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	45.35
Std Dev	3.614

Gaus mean : 45.5082

Gaus Width: 3.0639

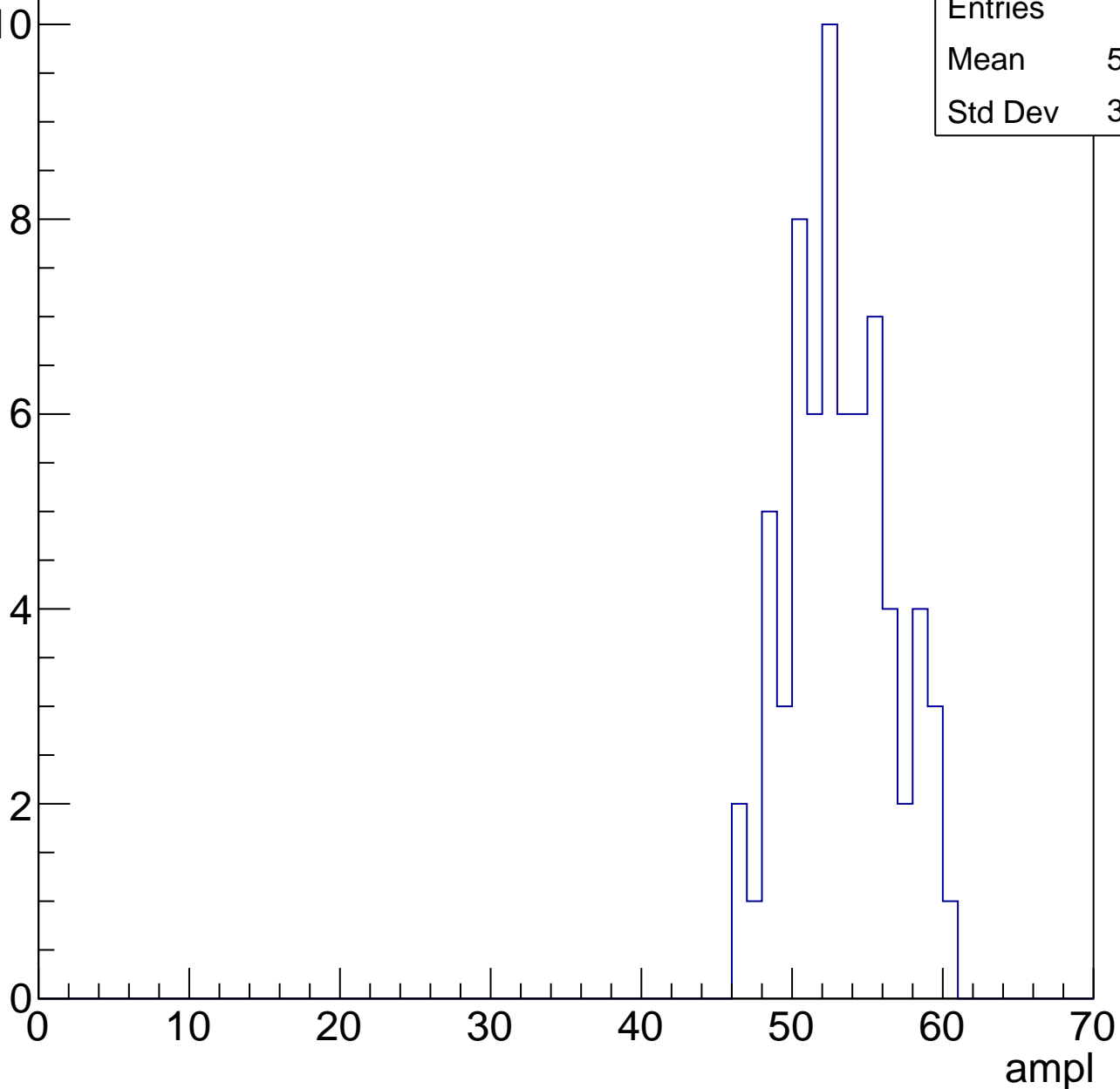


B1L101S, U26-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	52.74
Std Dev	3.359

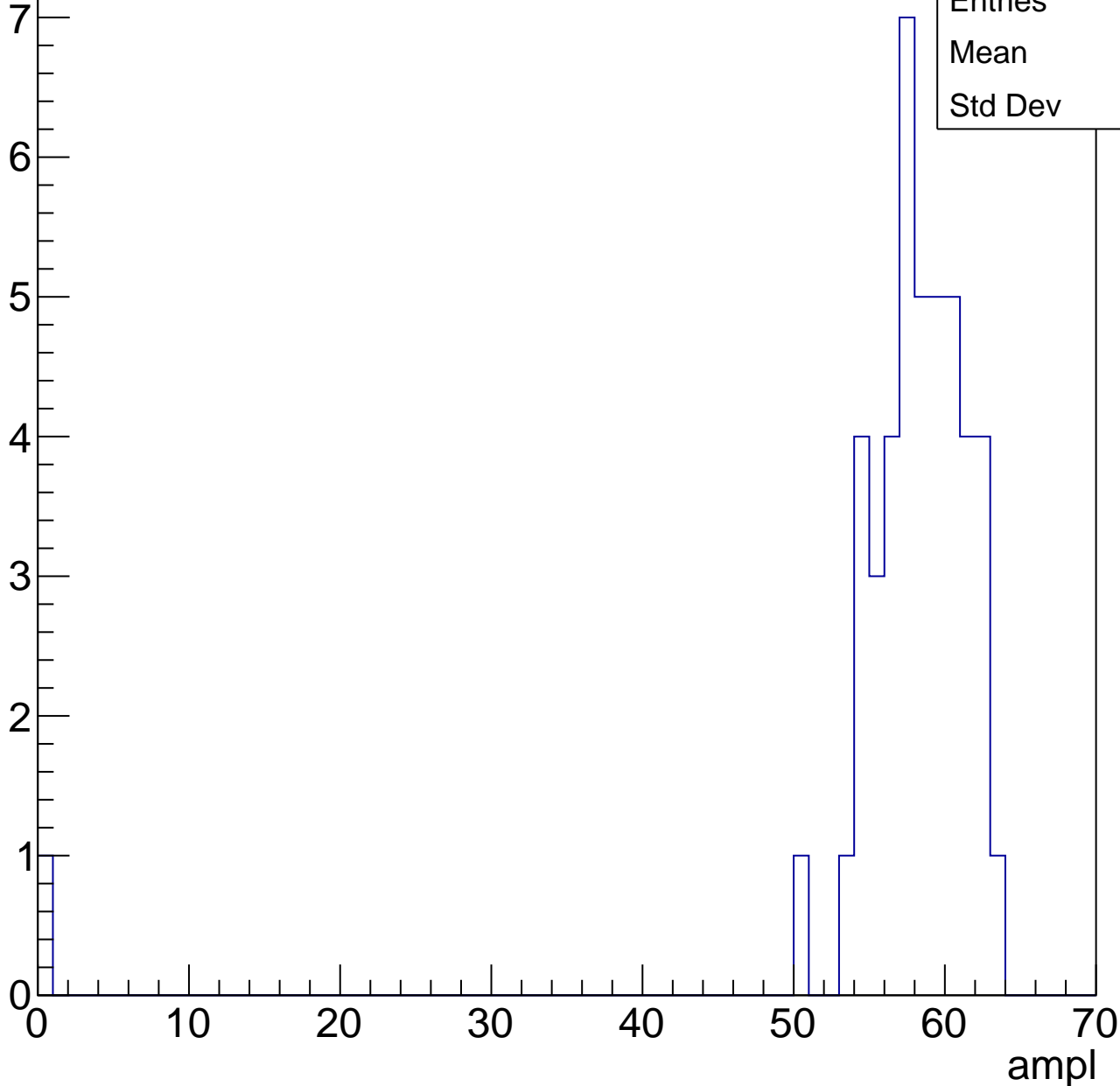


B1L101S, U26-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

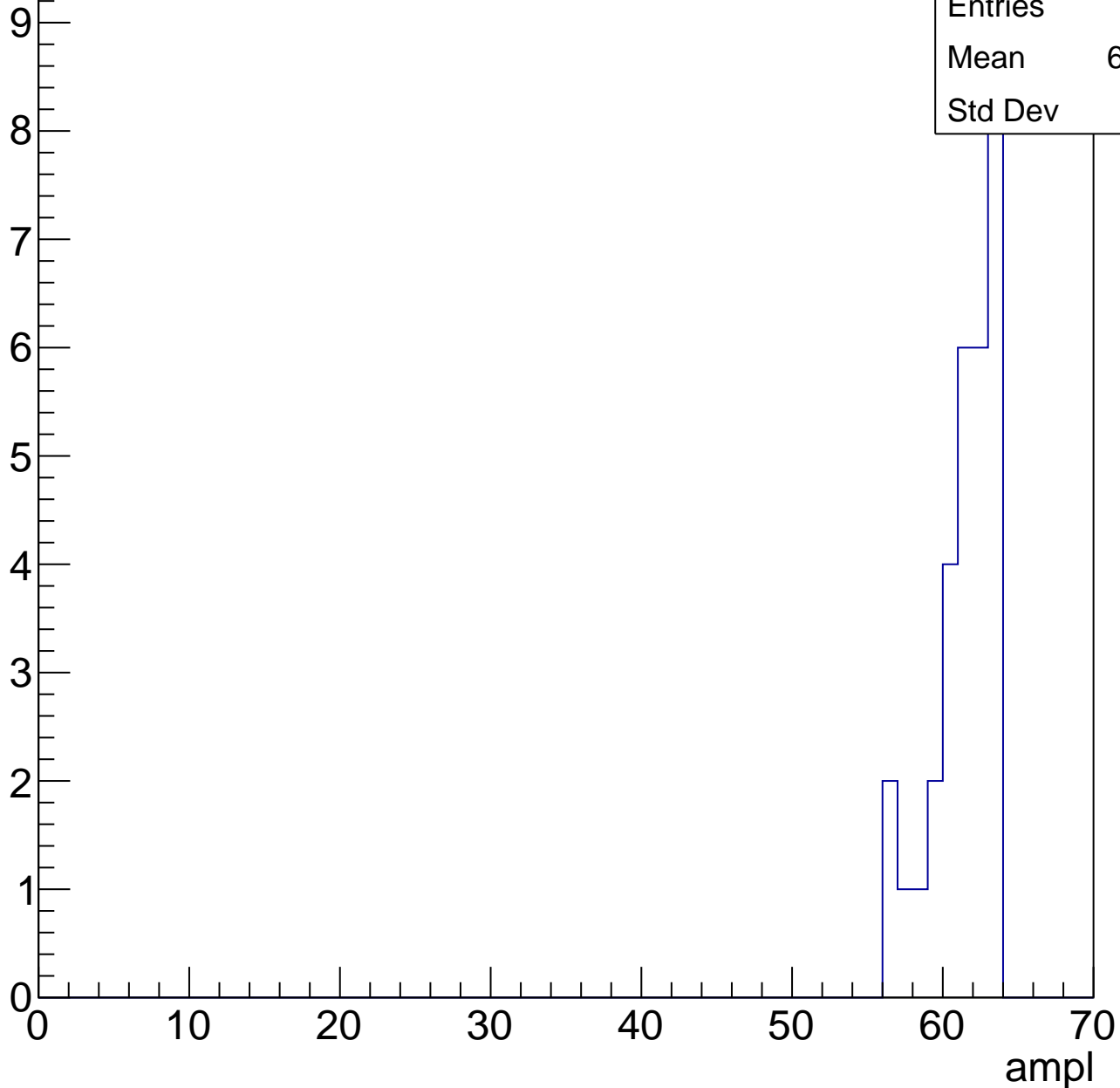
Entries	45
Mean	56.6
Std Dev	8.98



B1L101S, U26-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

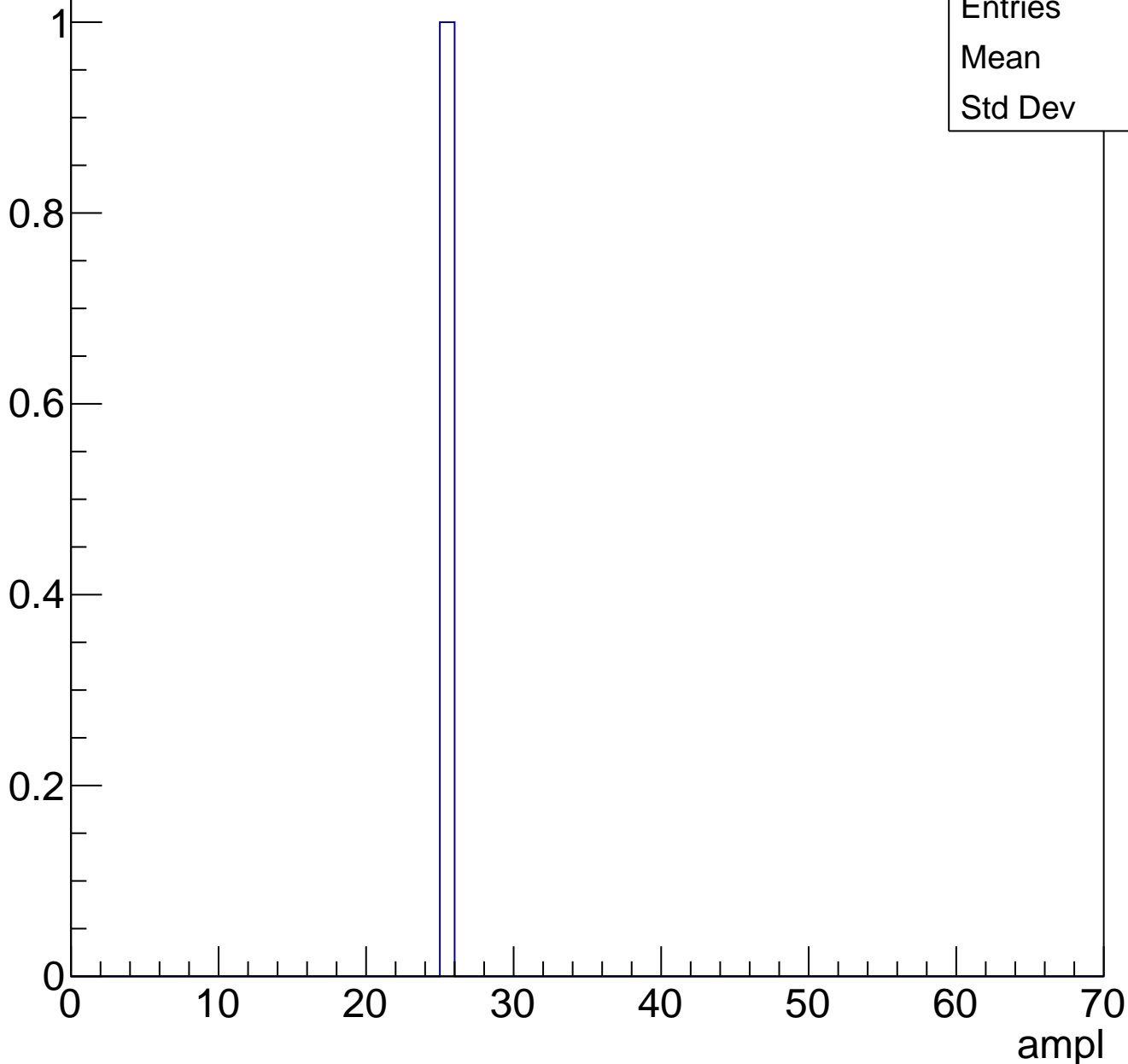


Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch14, adc0

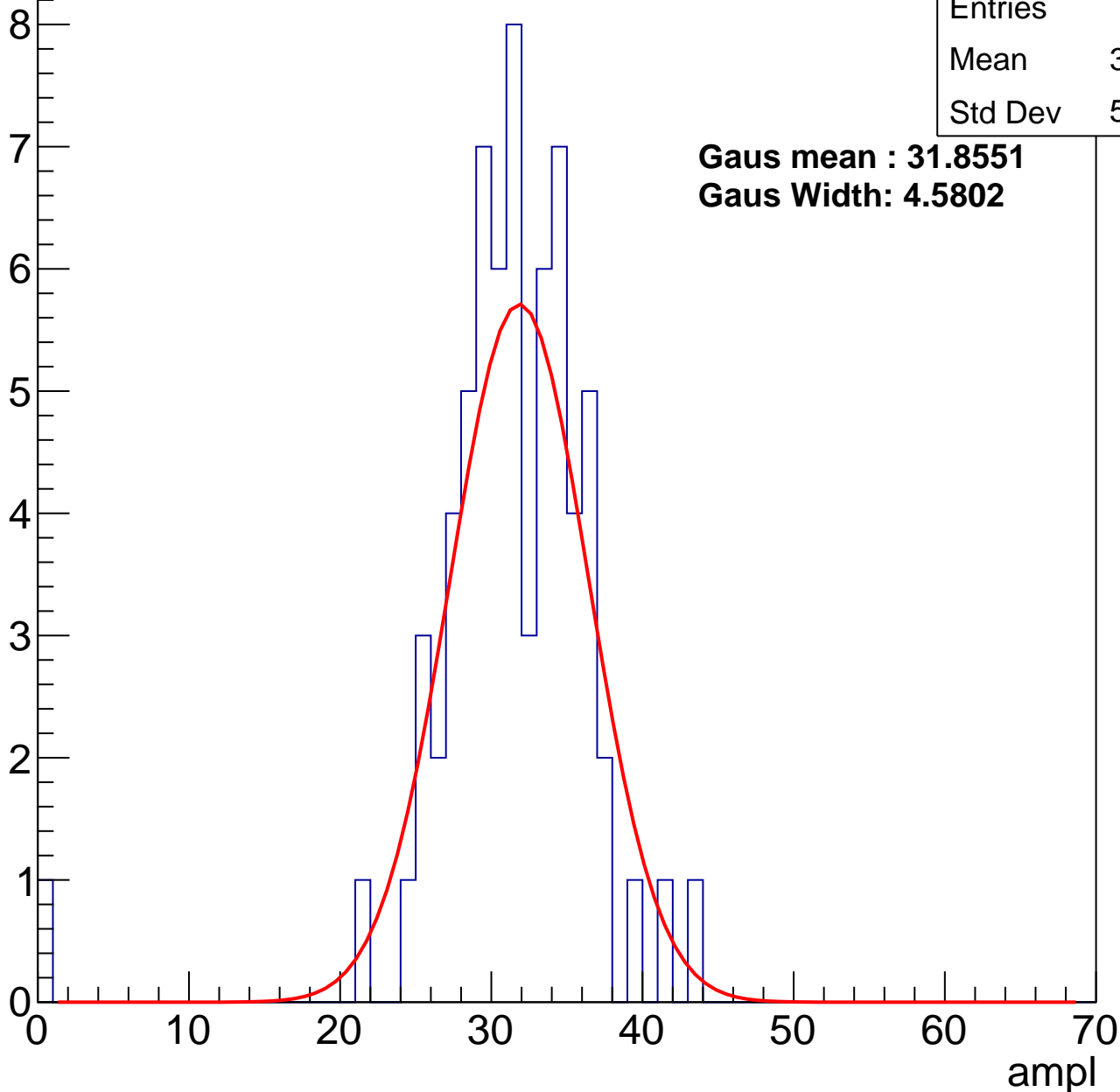
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.88
Std Dev	5.516

Gaus mean : 31.8551

Gaus Width: 4.5802



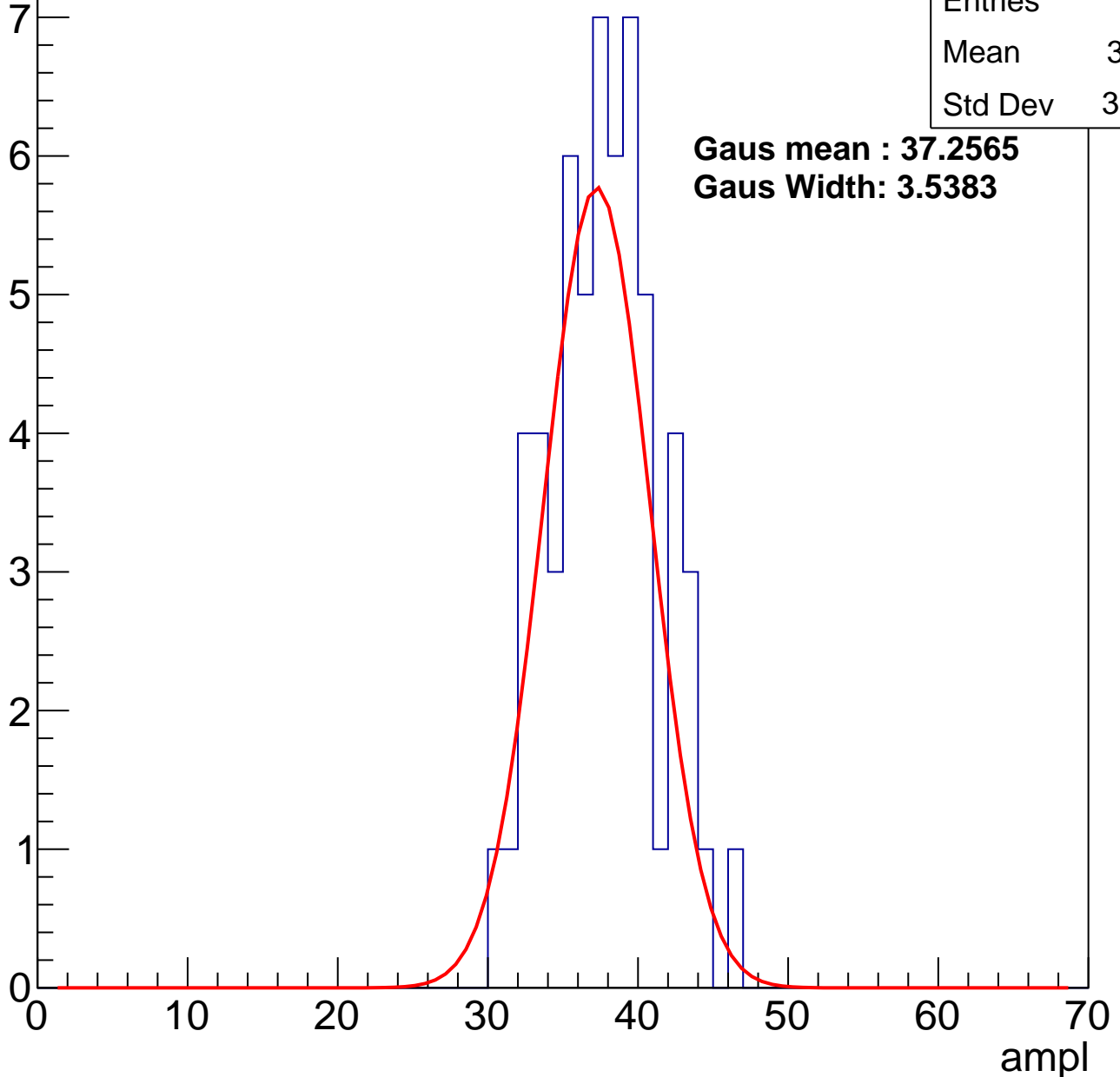
B1L101S, U26-ch14, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.31
Std Dev	3.524

Gaus mean : 37.2565
Gaus Width: 3.5383



B1L101S, U26-ch14, adc2

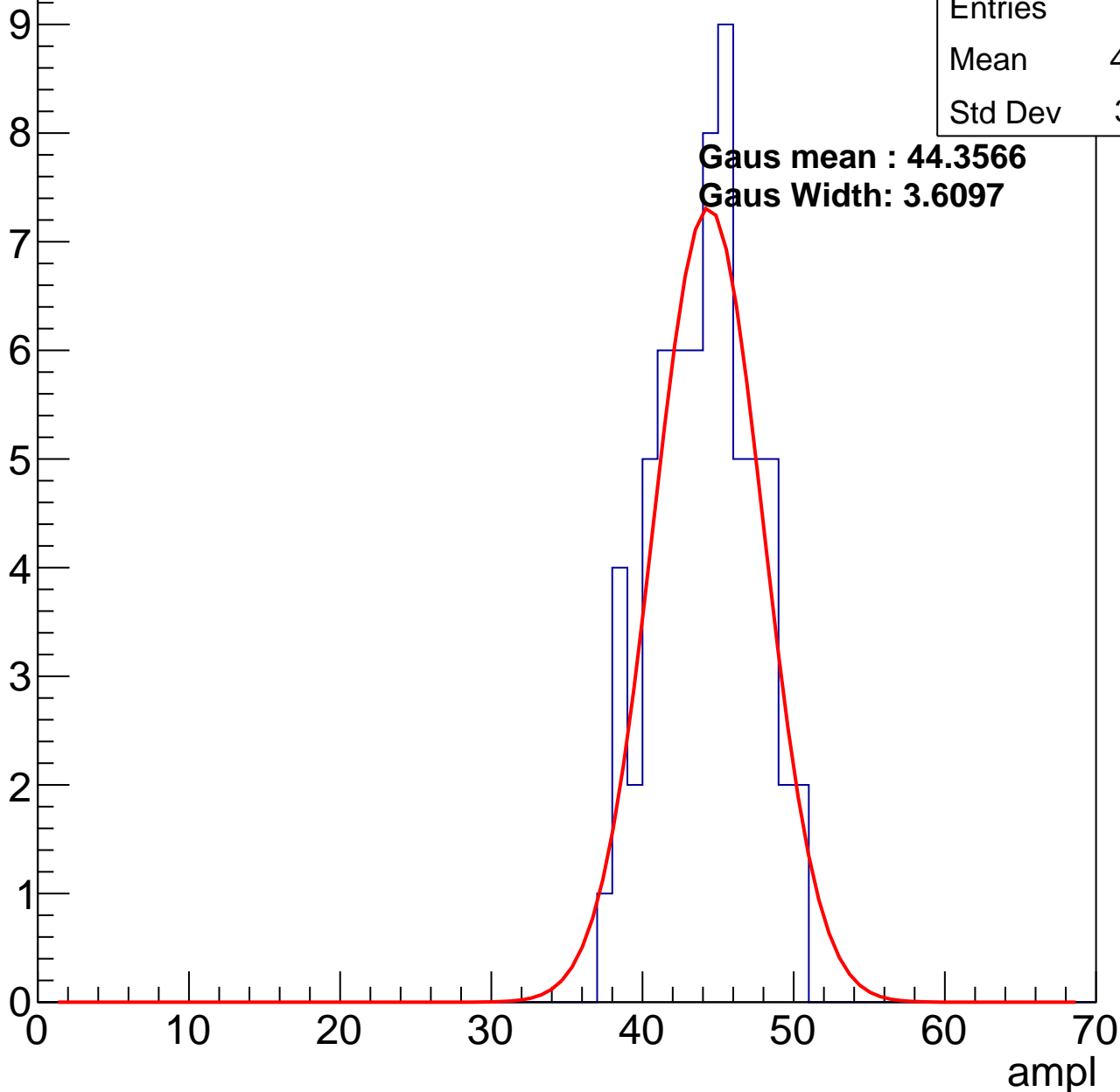
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.68
Std Dev	3.201

Gaus mean : 44.3566

Gaus Width: 3.6097

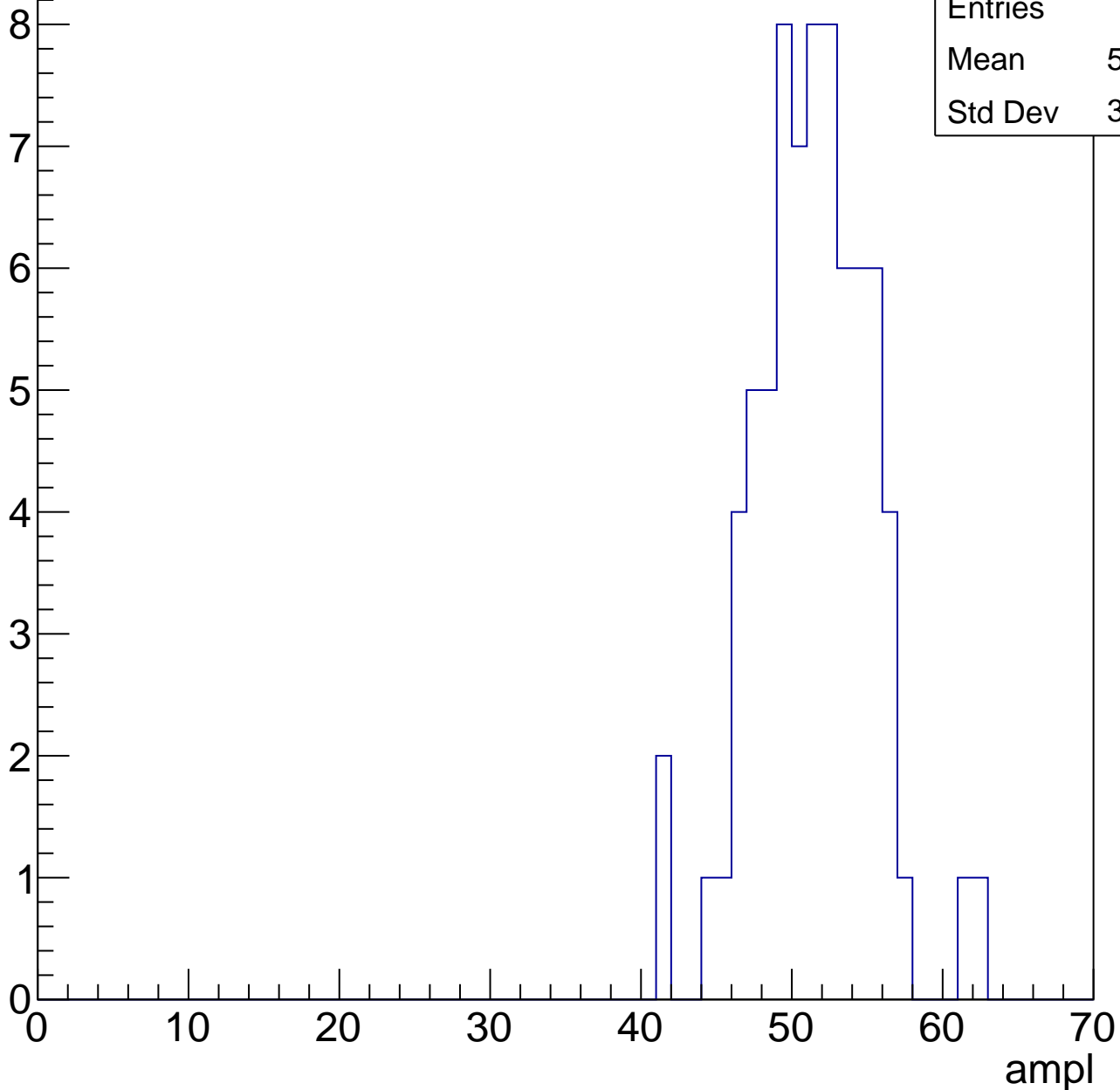


B1L101S, U26-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

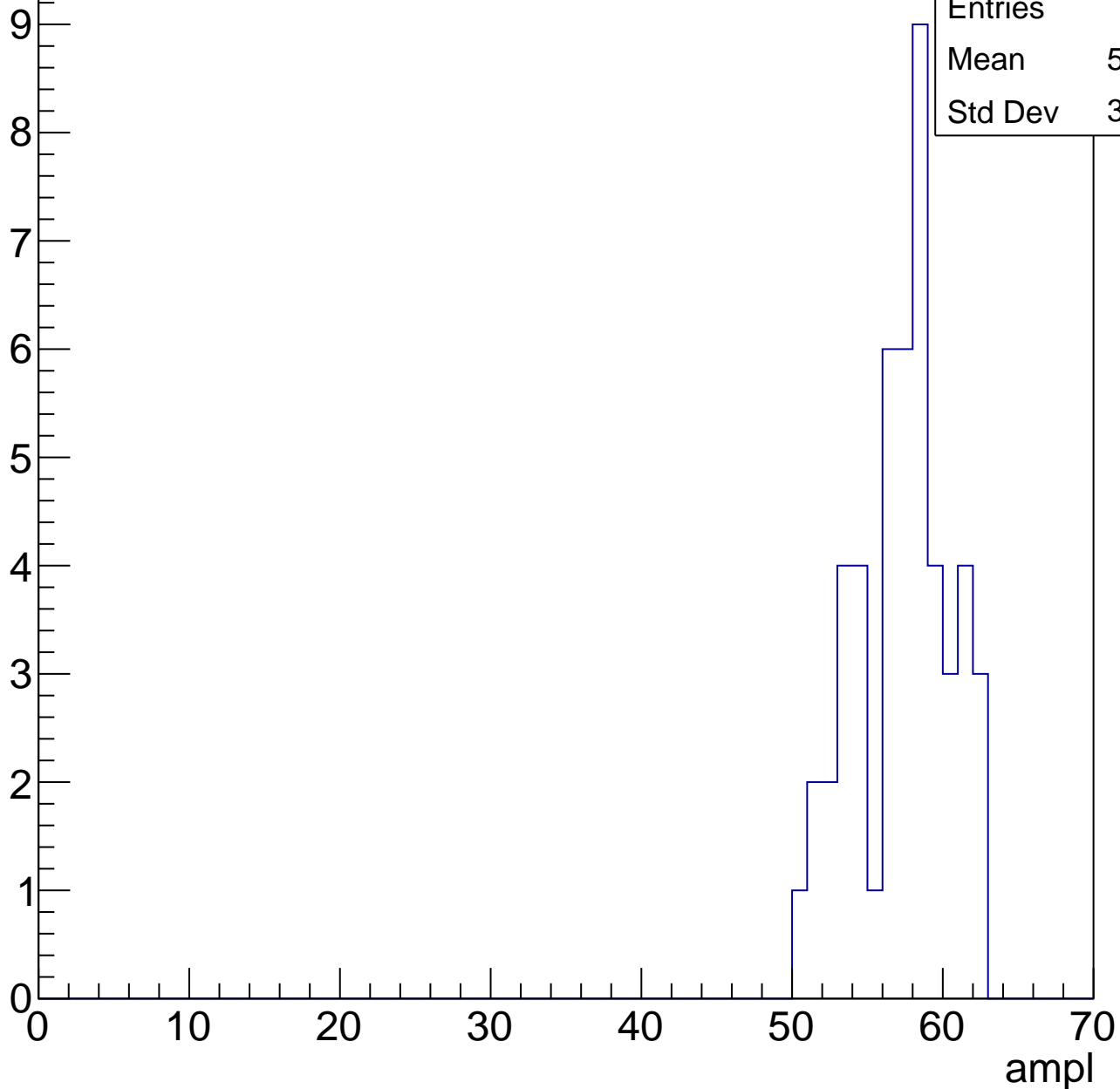
Entries	74
Mean	50.97
Std Dev	3.834



B1L101S, U26-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

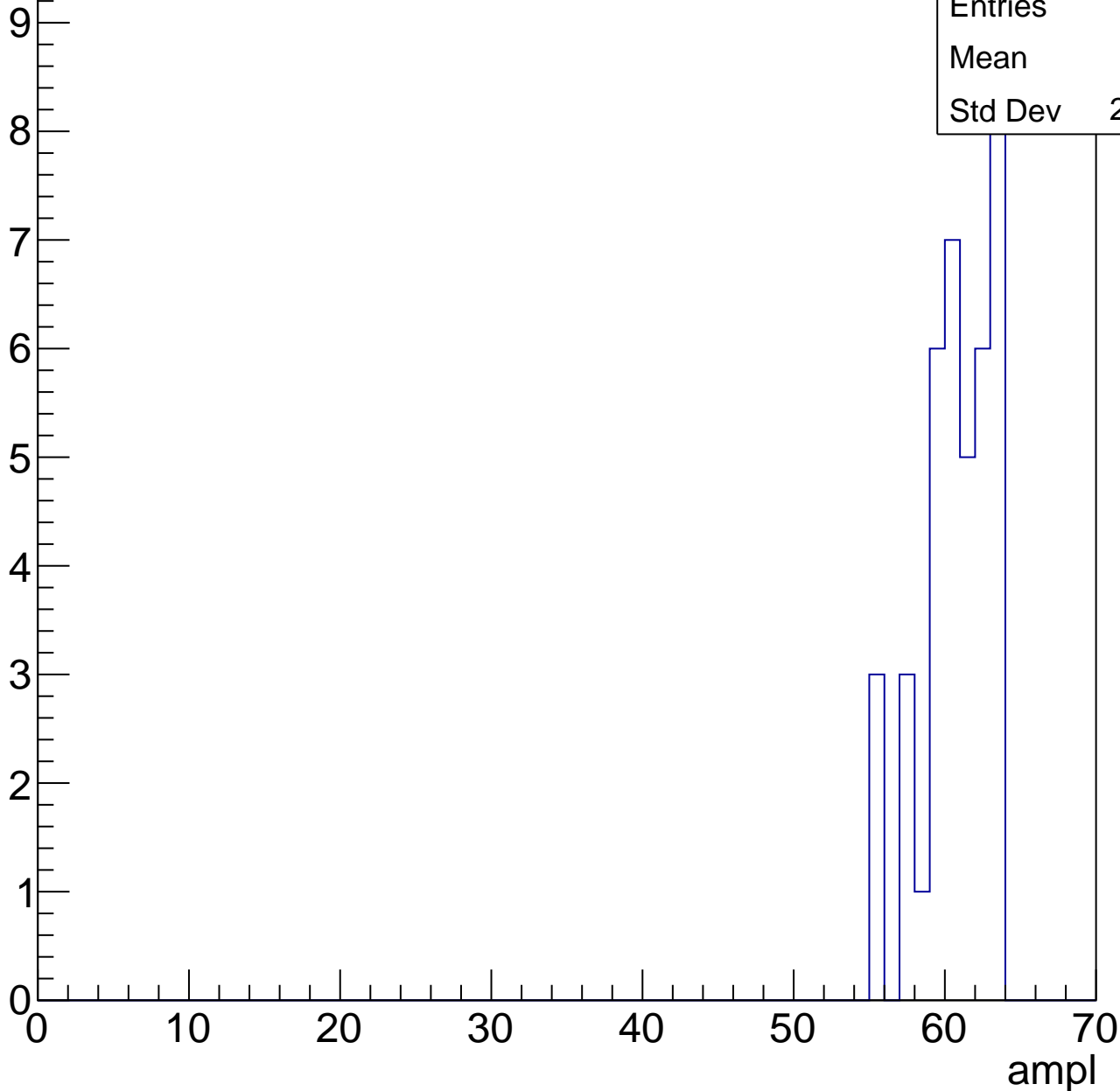


B1L101S, U26-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	60.3
Std Dev	2.337



B1L101S, U26-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch15, adc0

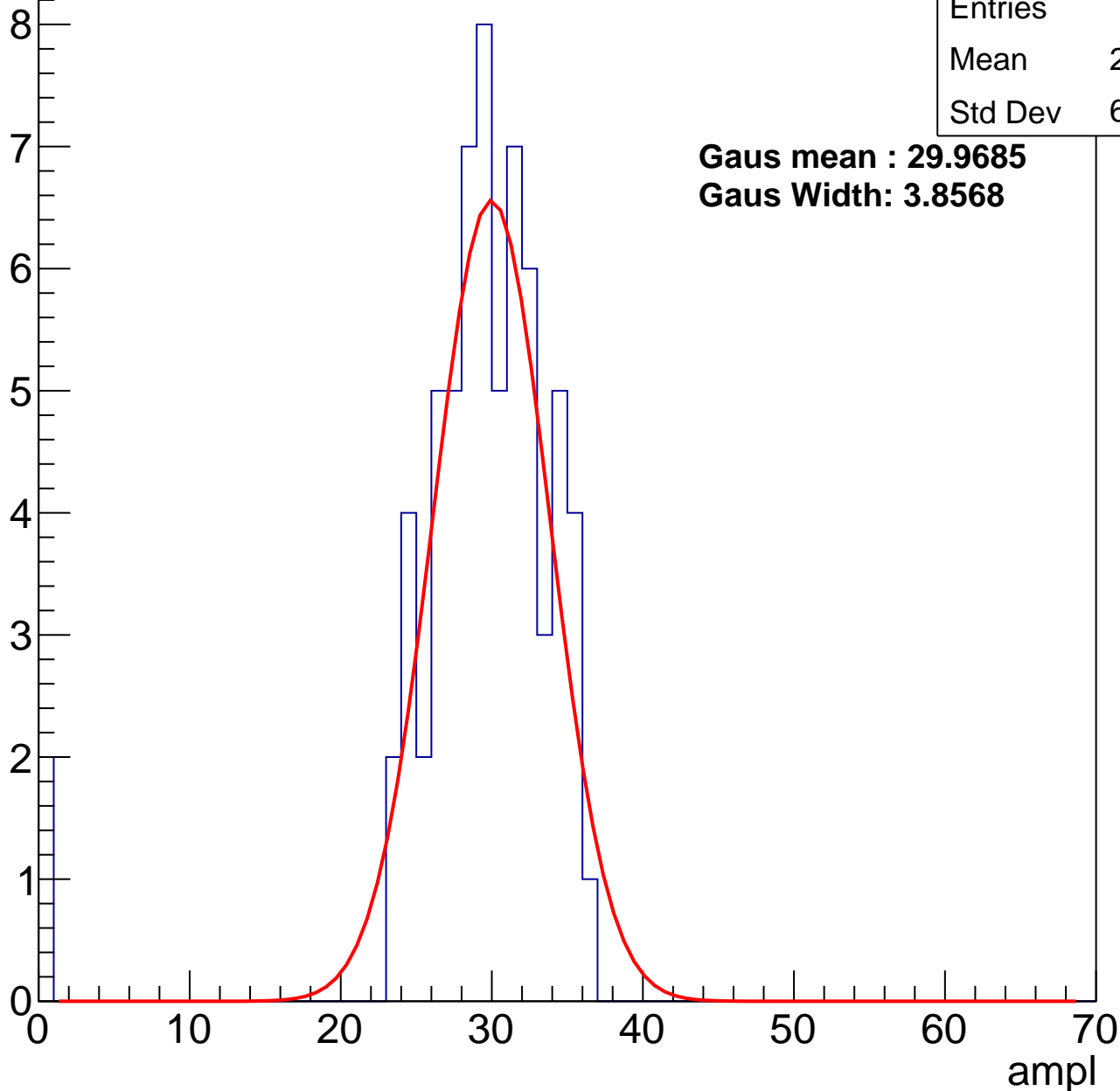
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.62
Std Dev	6.032

Gaus mean : 29.9685

Gaus Width: 3.8568



B1L101S, U26-ch15, adc1

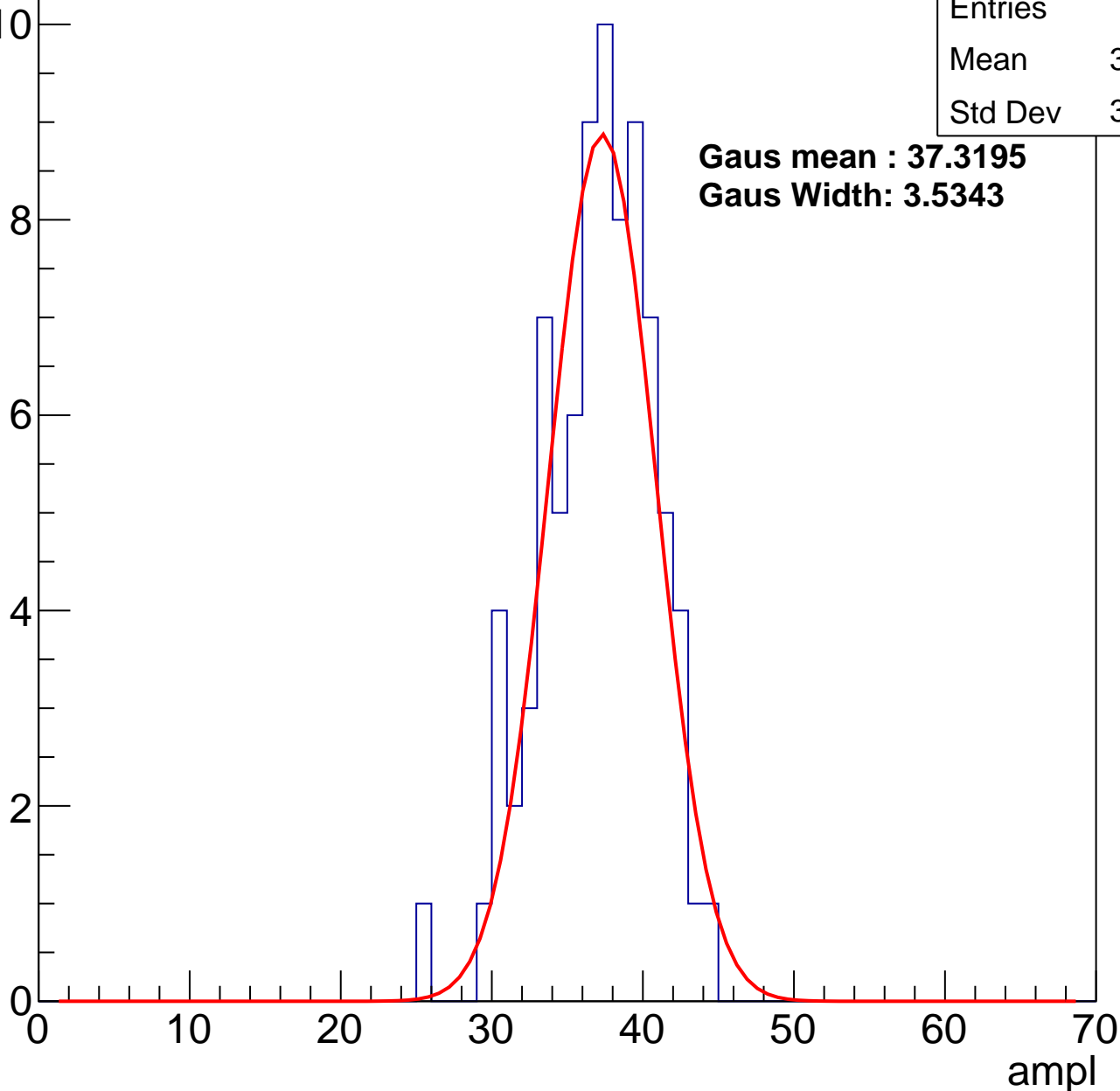
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.53
Std Dev	3.632

Gaus mean : 37.3195

Gaus Width: 3.5343



B1L101S, U26-ch15, adc2

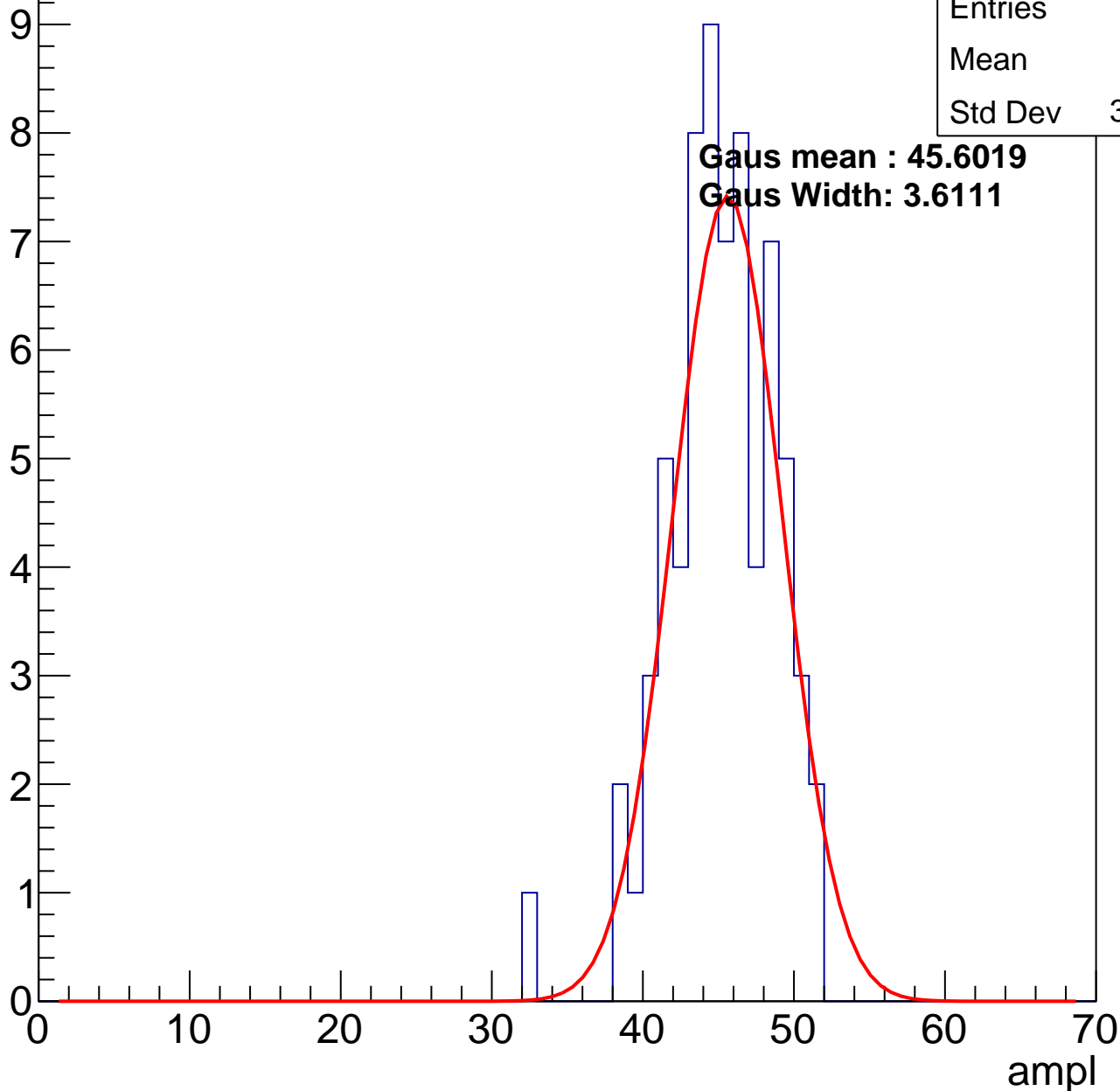
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.7
Std Dev	3.495

Gaus mean : 45.6019

Gaus Width: 3.6111

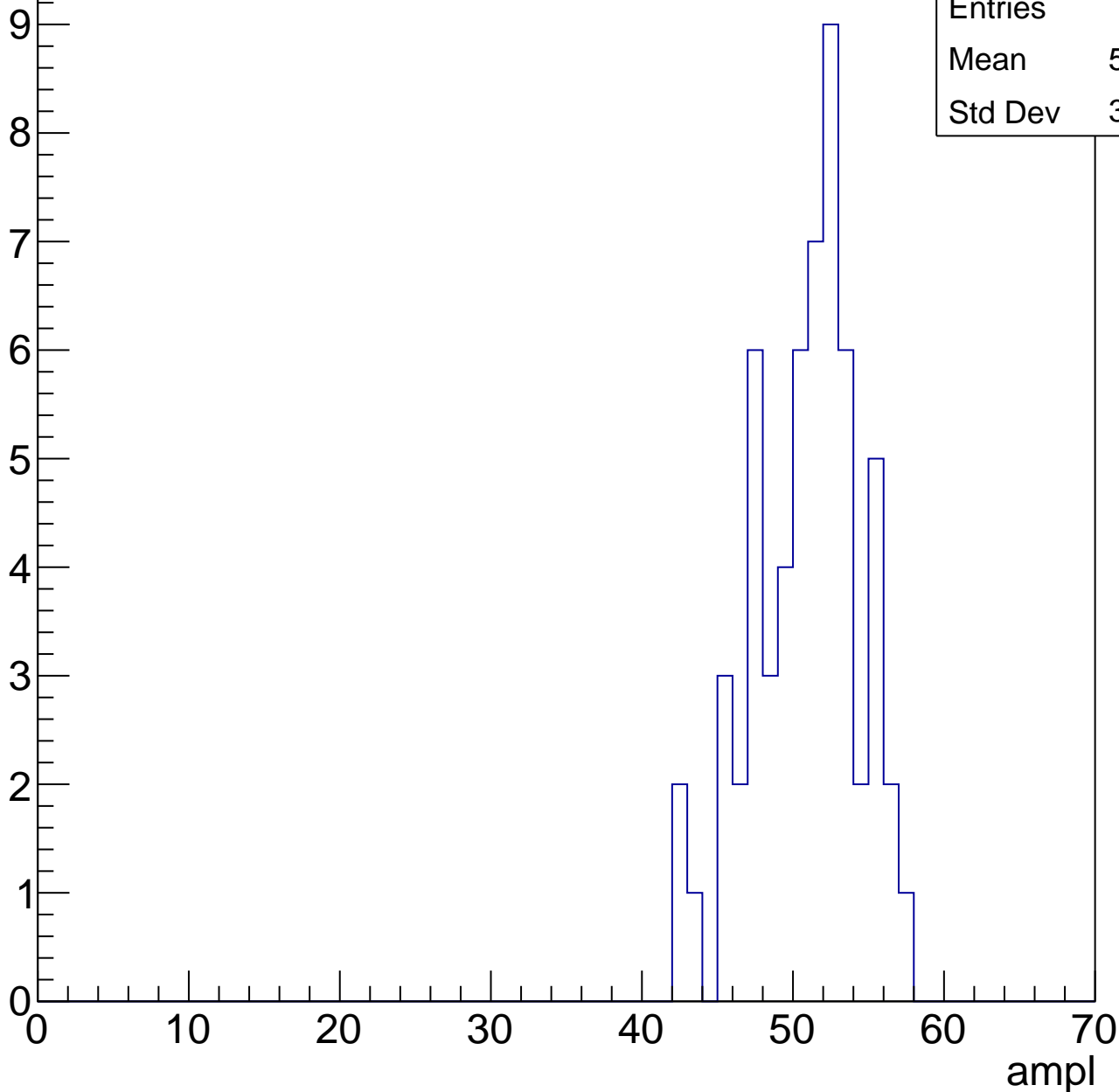


B1L101S, U26-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.36
Std Dev	3.497



B1L101S, U26-ch15, adc4

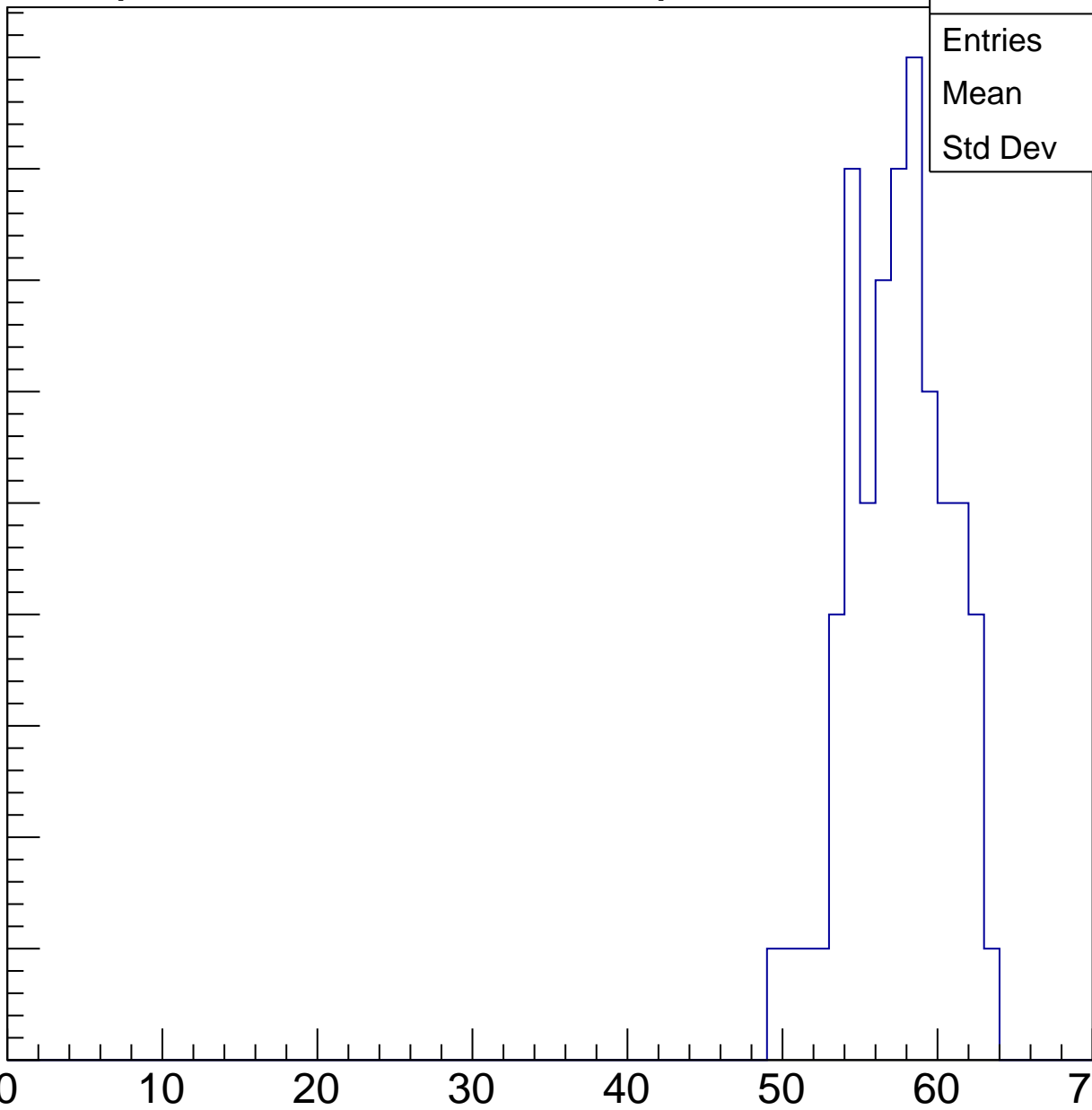
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	56.98
Std Dev	3.092

ampl

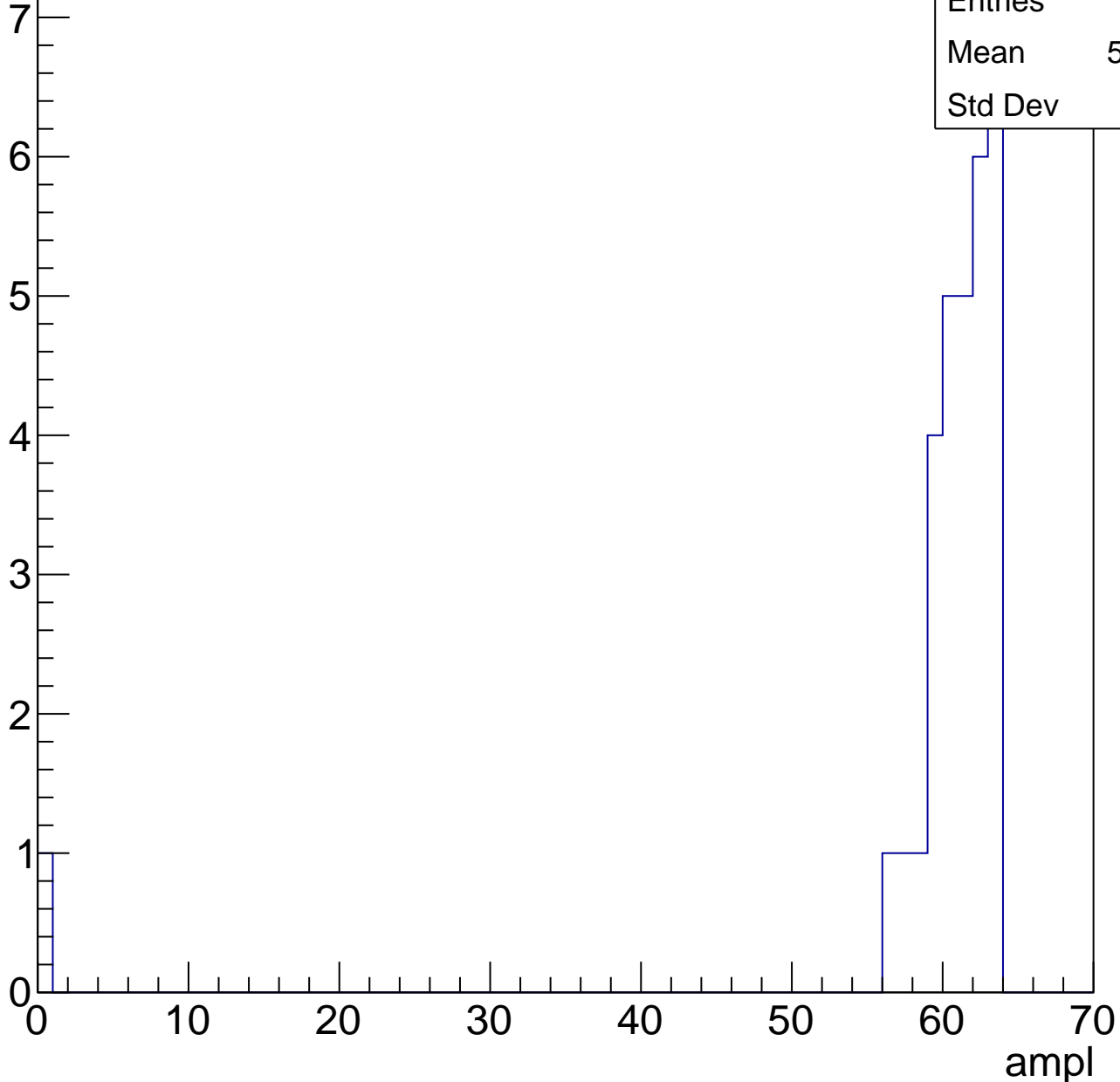


B1L101S, U26-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.87
Std Dev	10.9



B1L101S, U26-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	63
Std Dev	0

B1L101S, U26-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch16, adc0

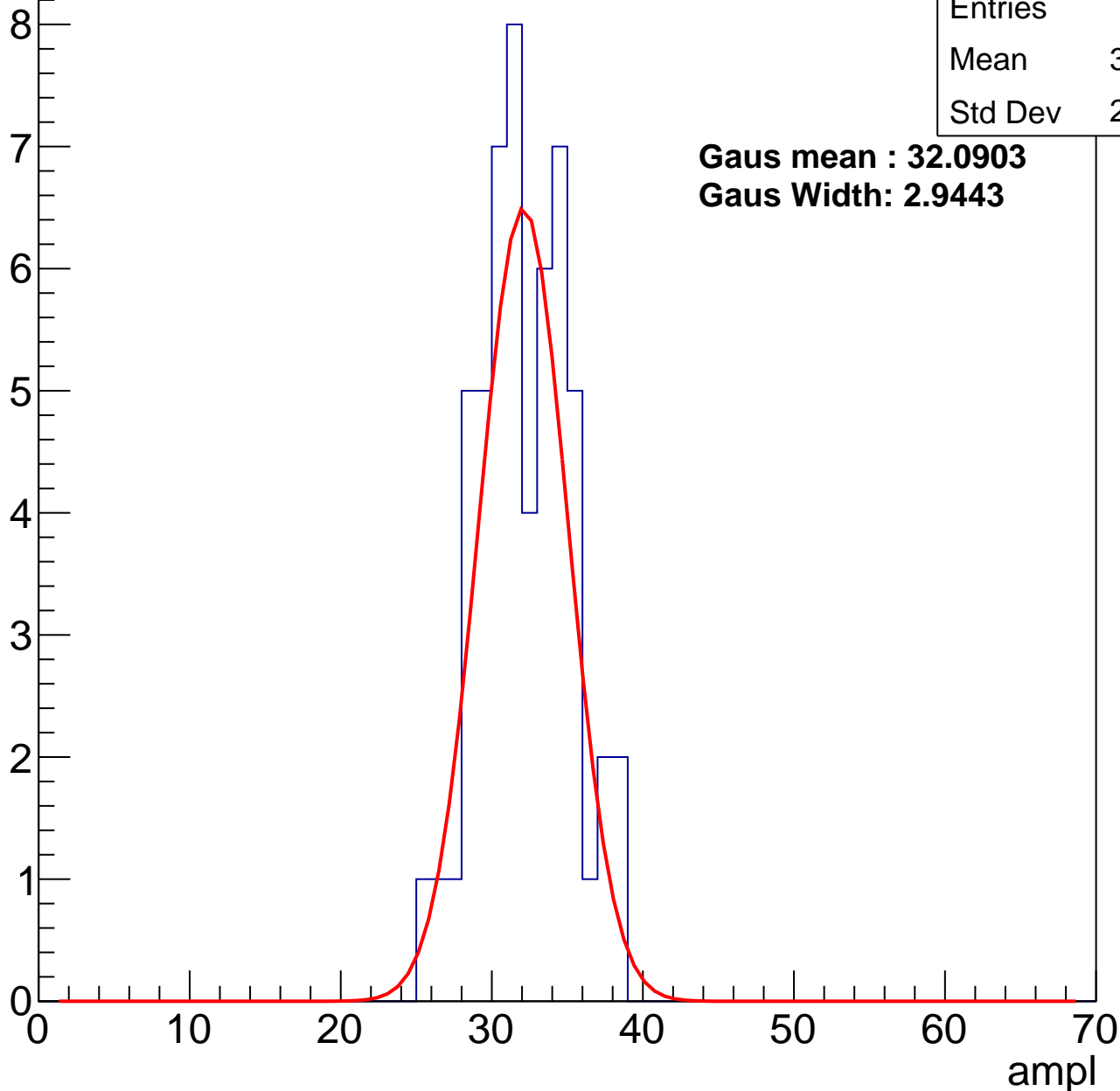
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	31.75
Std Dev	2.968

Gaus mean : 32.0903

Gaus Width: 2.9443



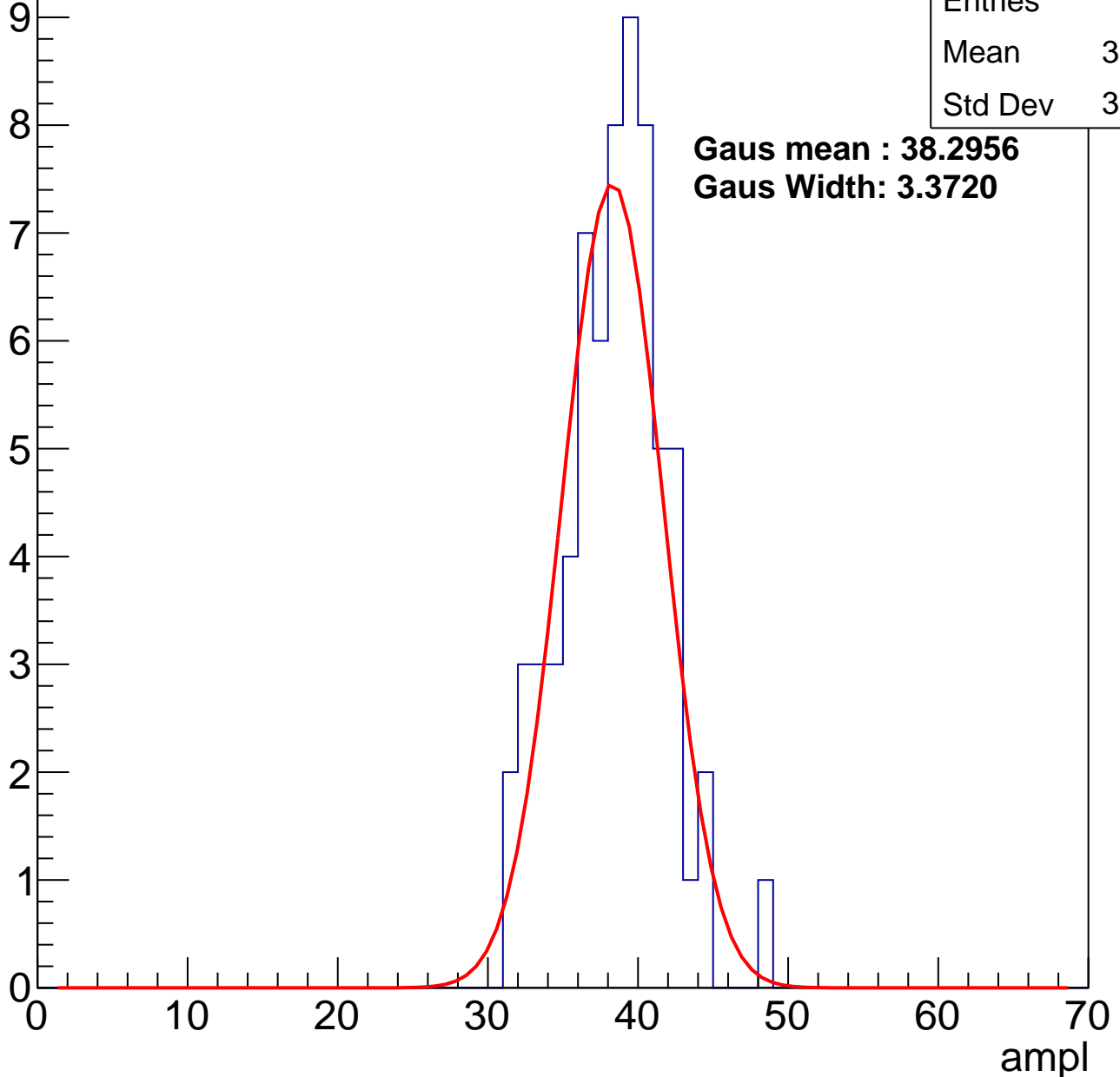
B1L101S, U26-ch16, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.94
Std Dev	3.372

Gaus mean : 38.2956
Gaus Width: 3.3720



B1L101S, U26-ch16, adc2

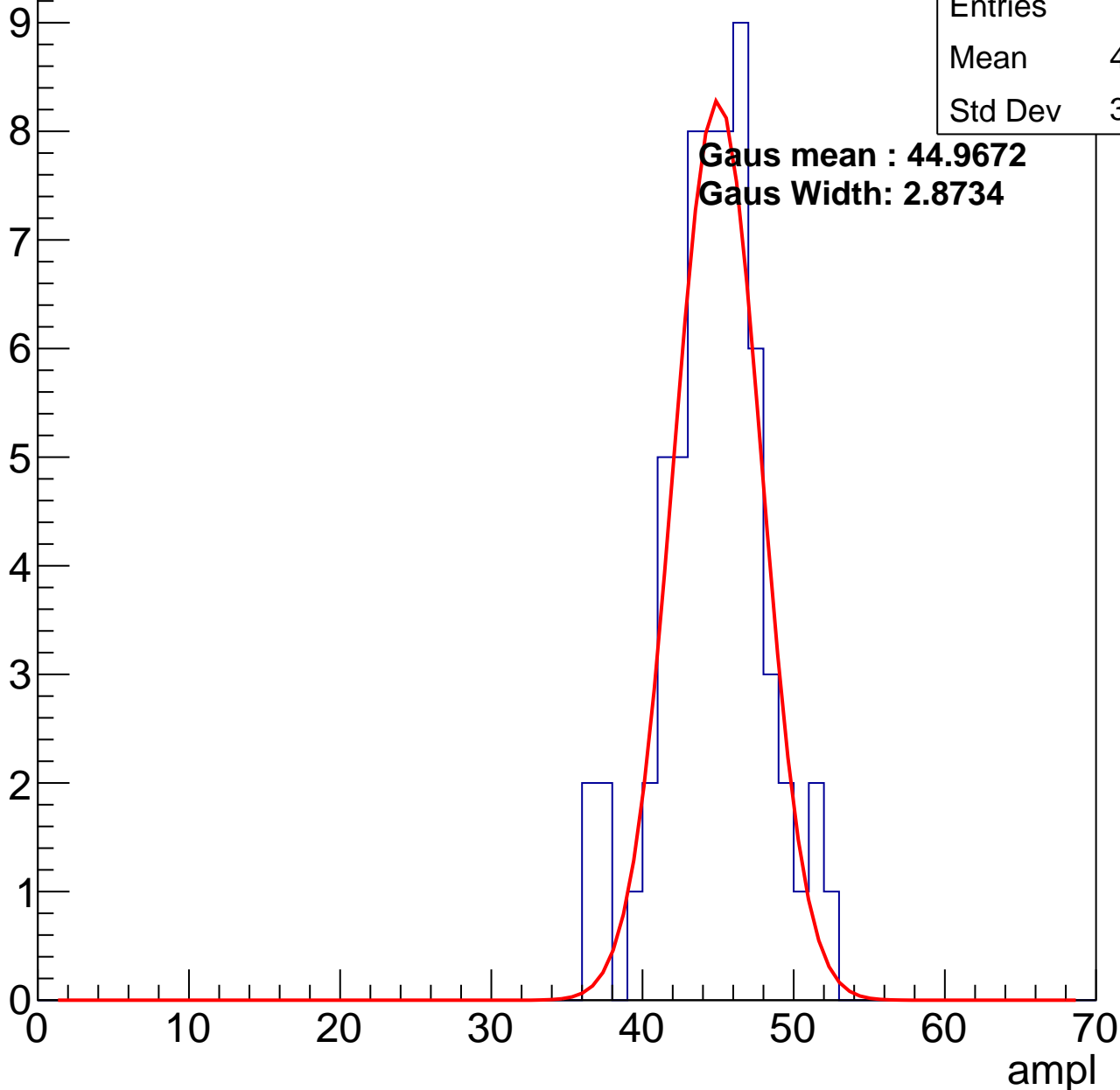
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.28
Std Dev	3.386

Gaus mean : 44.9672

Gaus Width: 2.8734

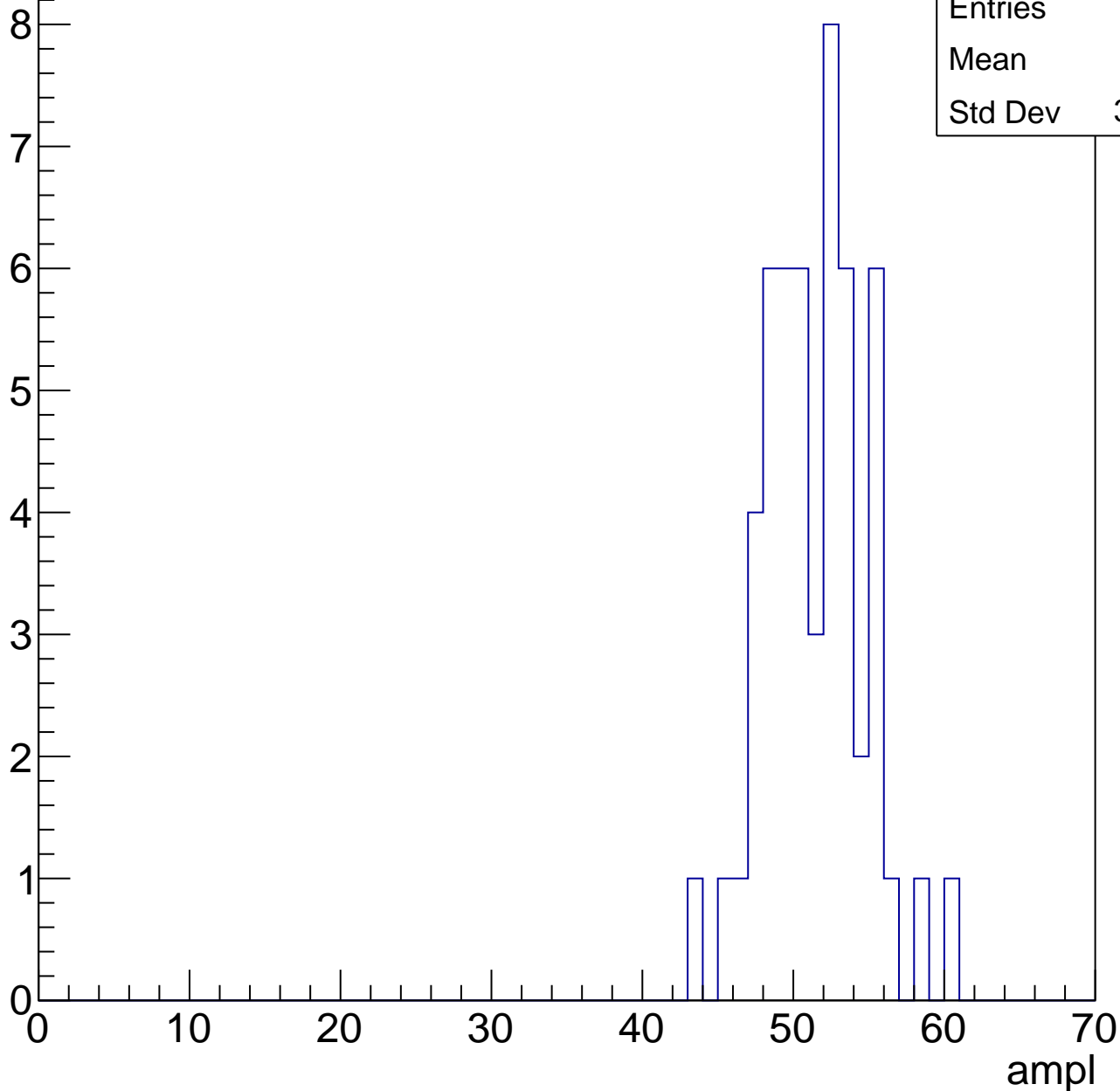


B1L101S, U26-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	51
Std Dev	3.291

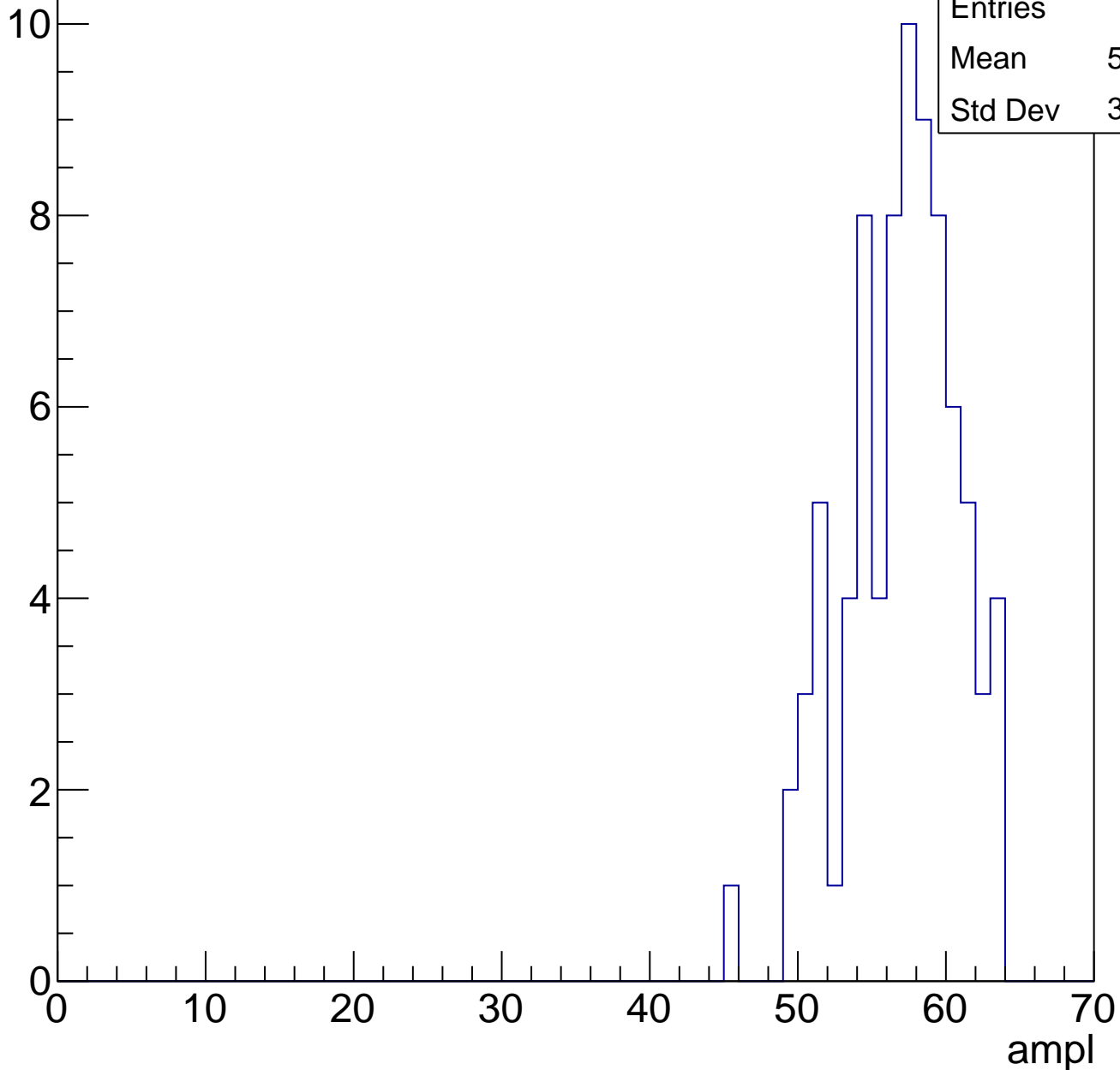


B1L101S, U26-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	56.53
Std Dev	3.785

Entry

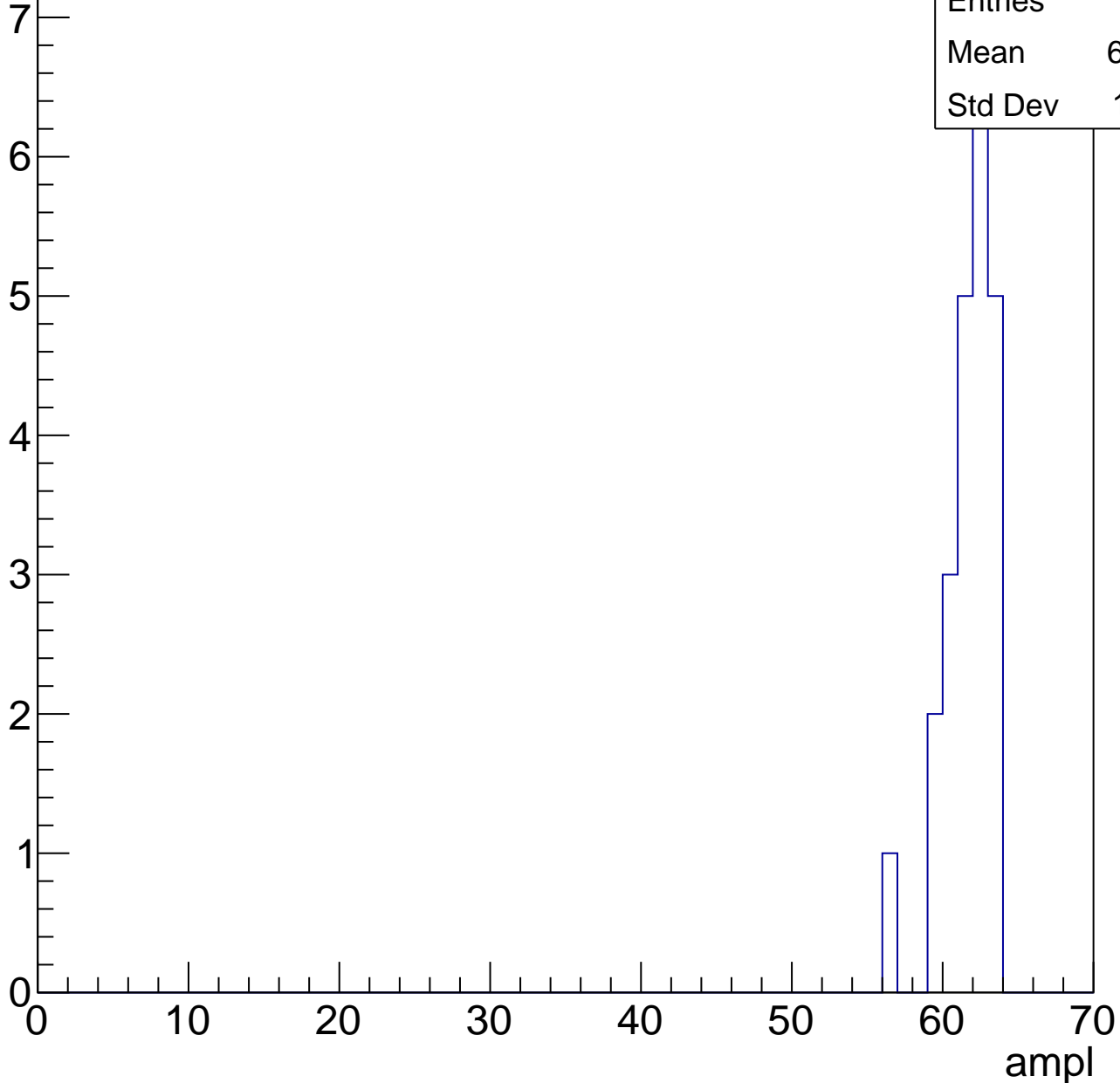


B1L101S, U26-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

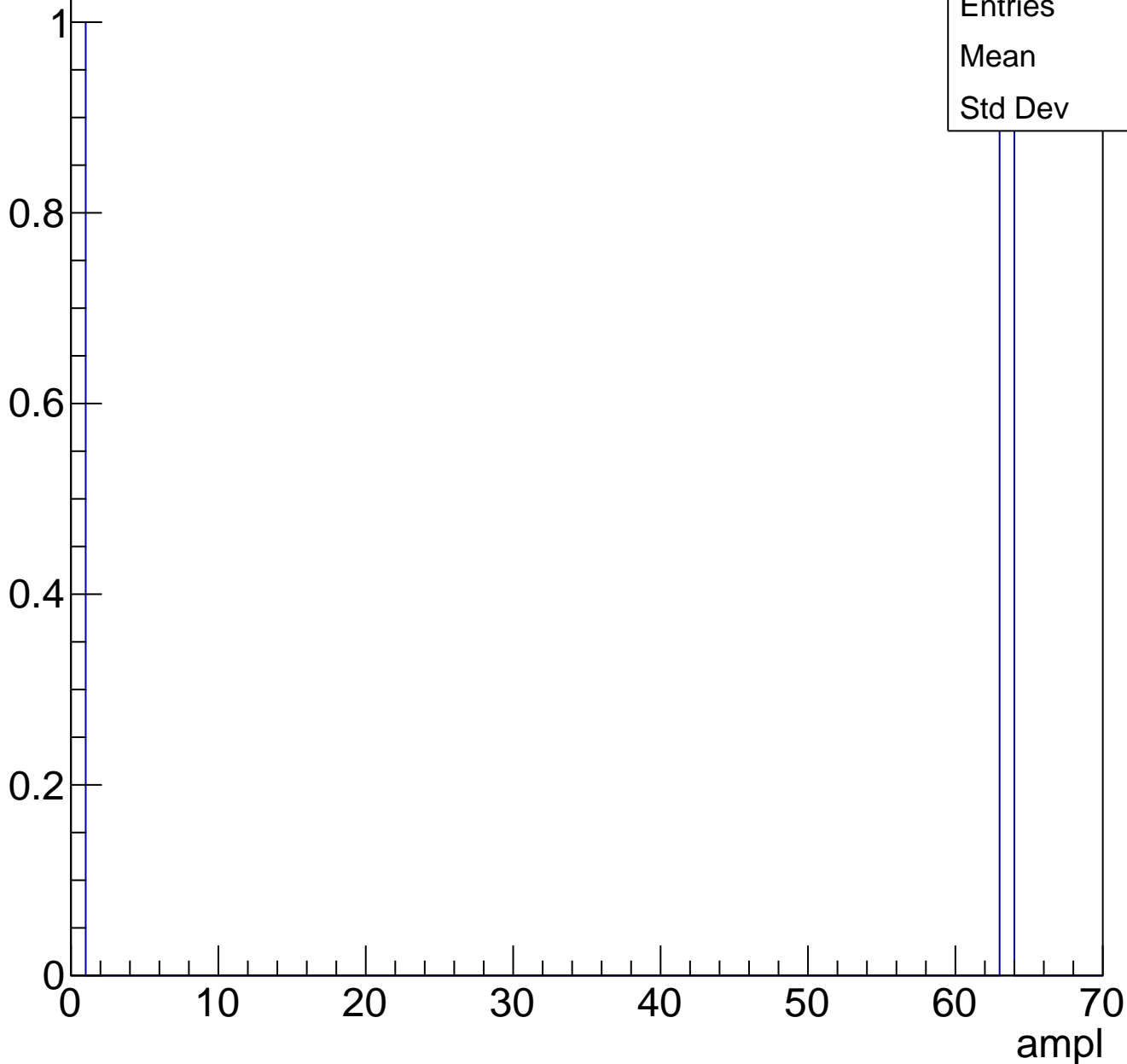
Entries	23
Mean	61.22
Std Dev	1.641



B1L101S, U26-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

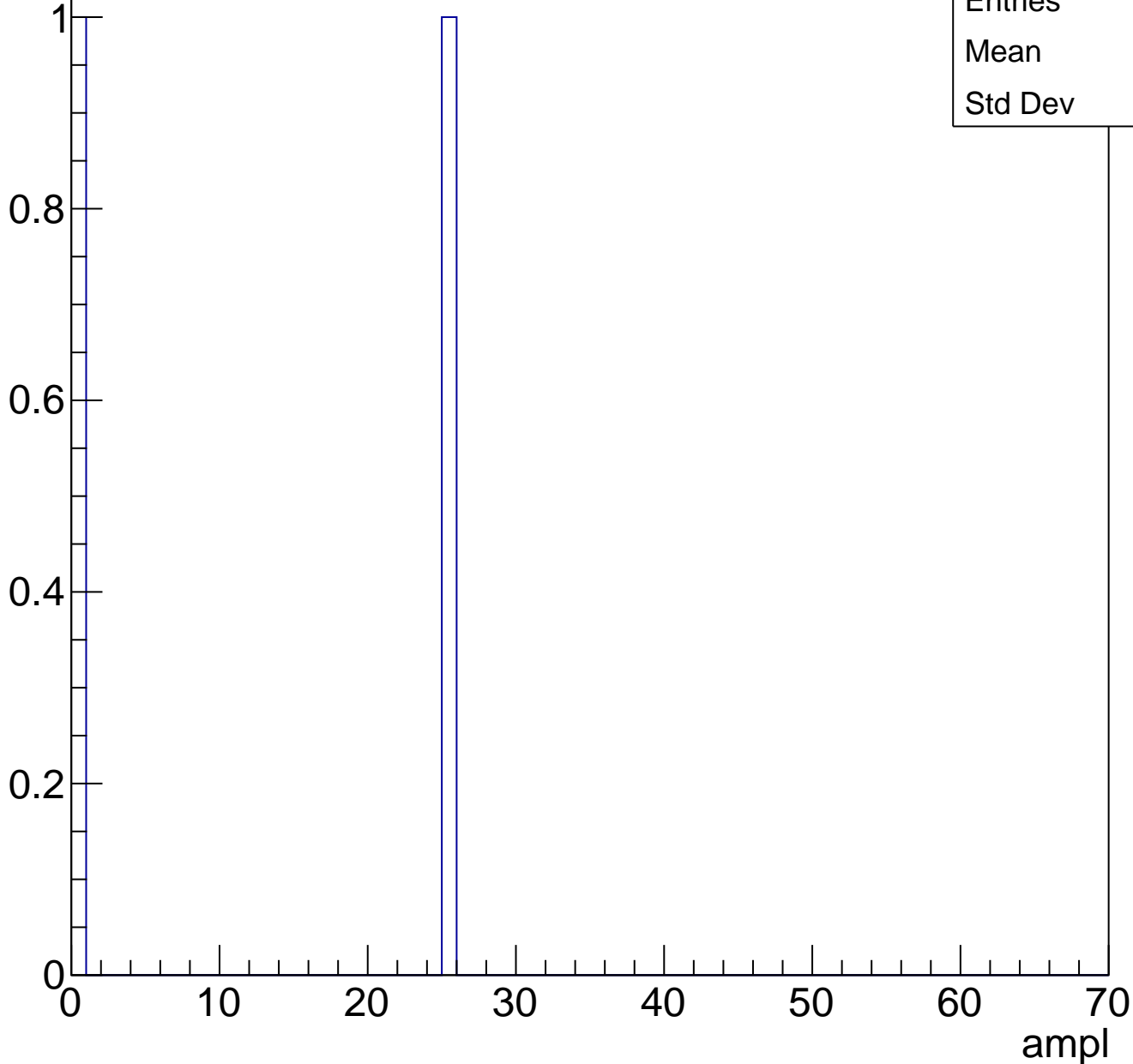
Entry



B1L101S, U26-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch17, adc0

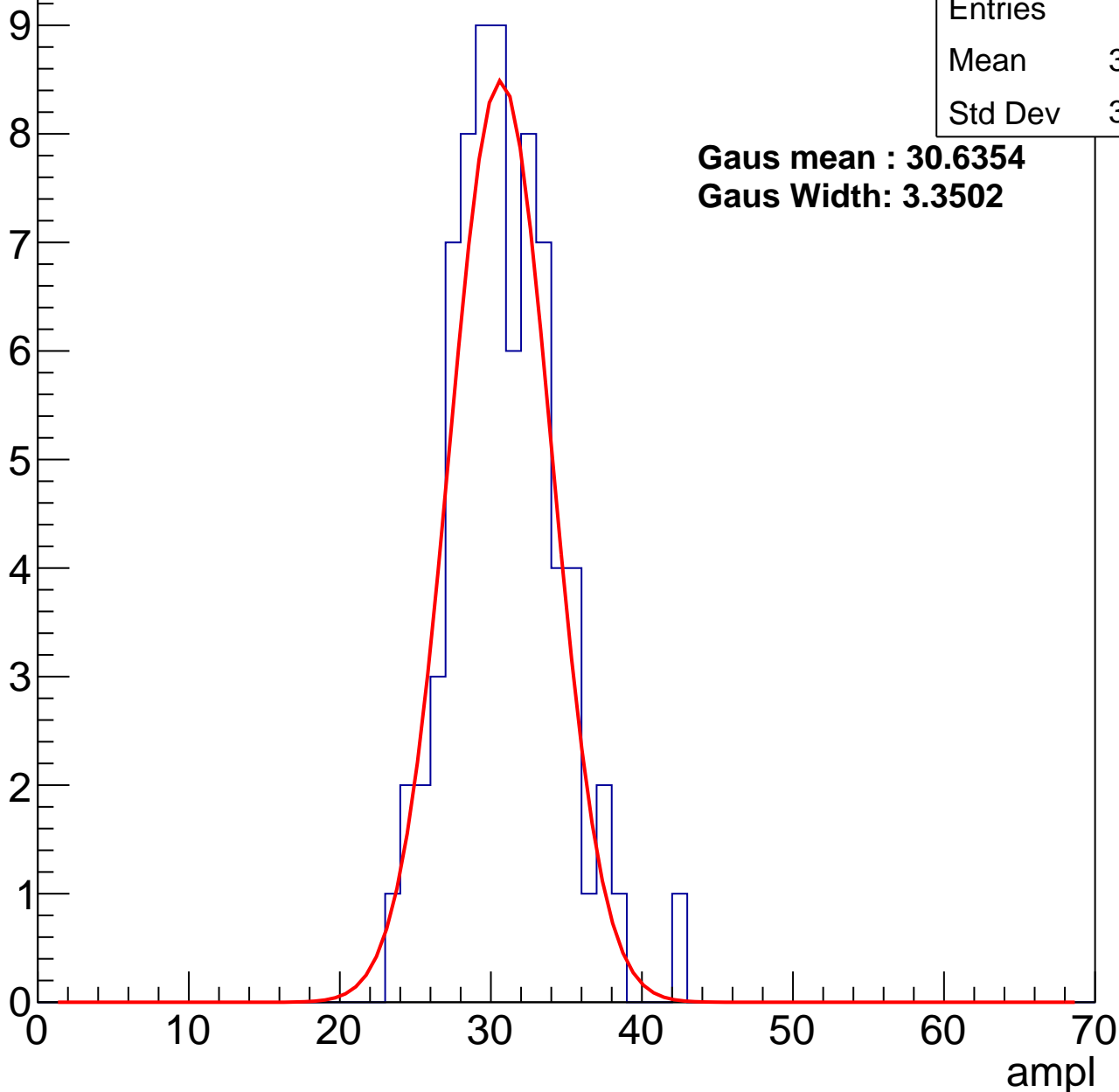
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.43
Std Dev	3.495

Gaus mean : 30.6354

Gaus Width: 3.3502



B1L101S, U26-ch17, adc1

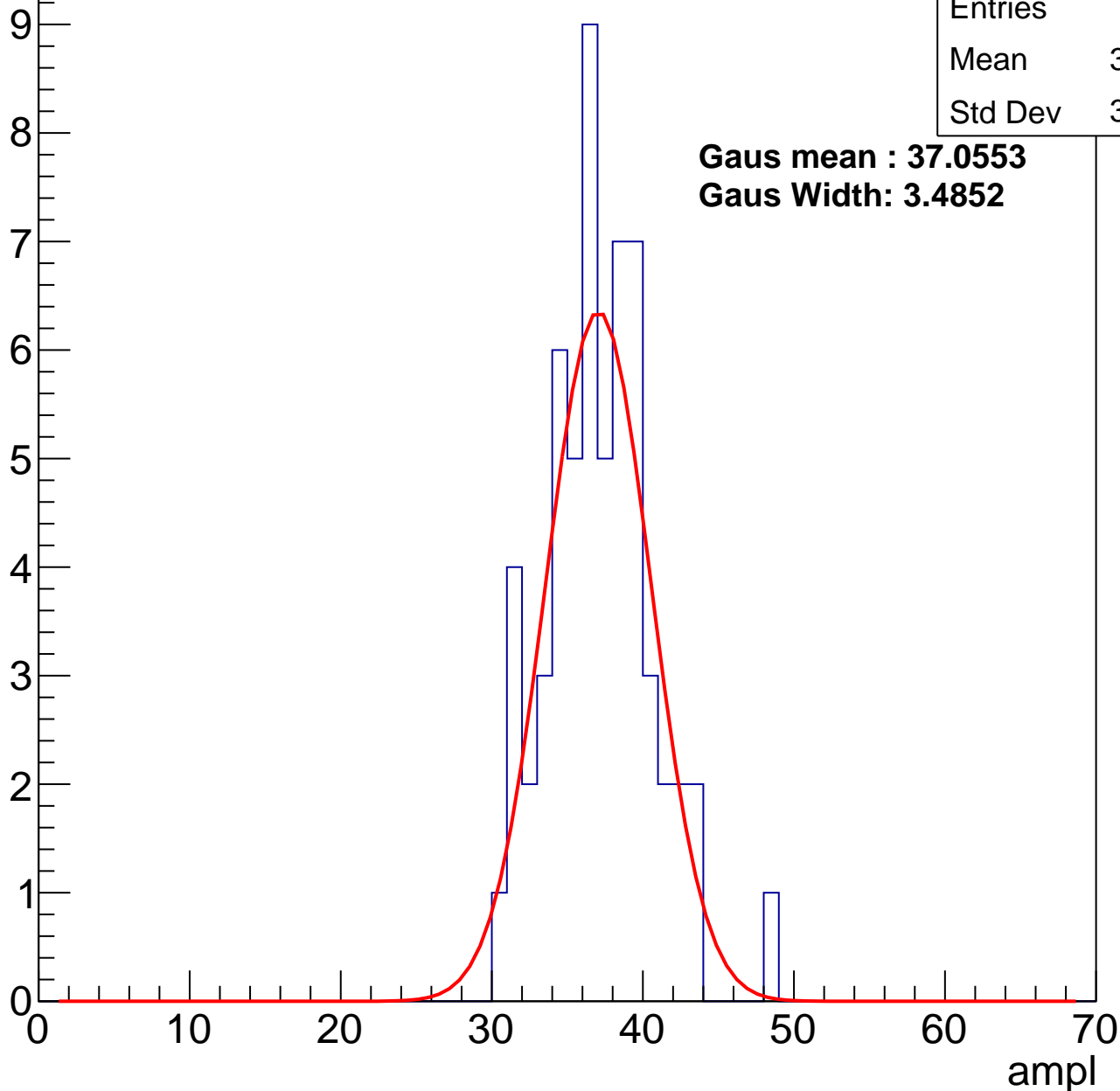
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.68
Std Dev	3.456

Gaus mean : 37.0553

Gaus Width: 3.4852



B1L101S, U26-ch17, adc2

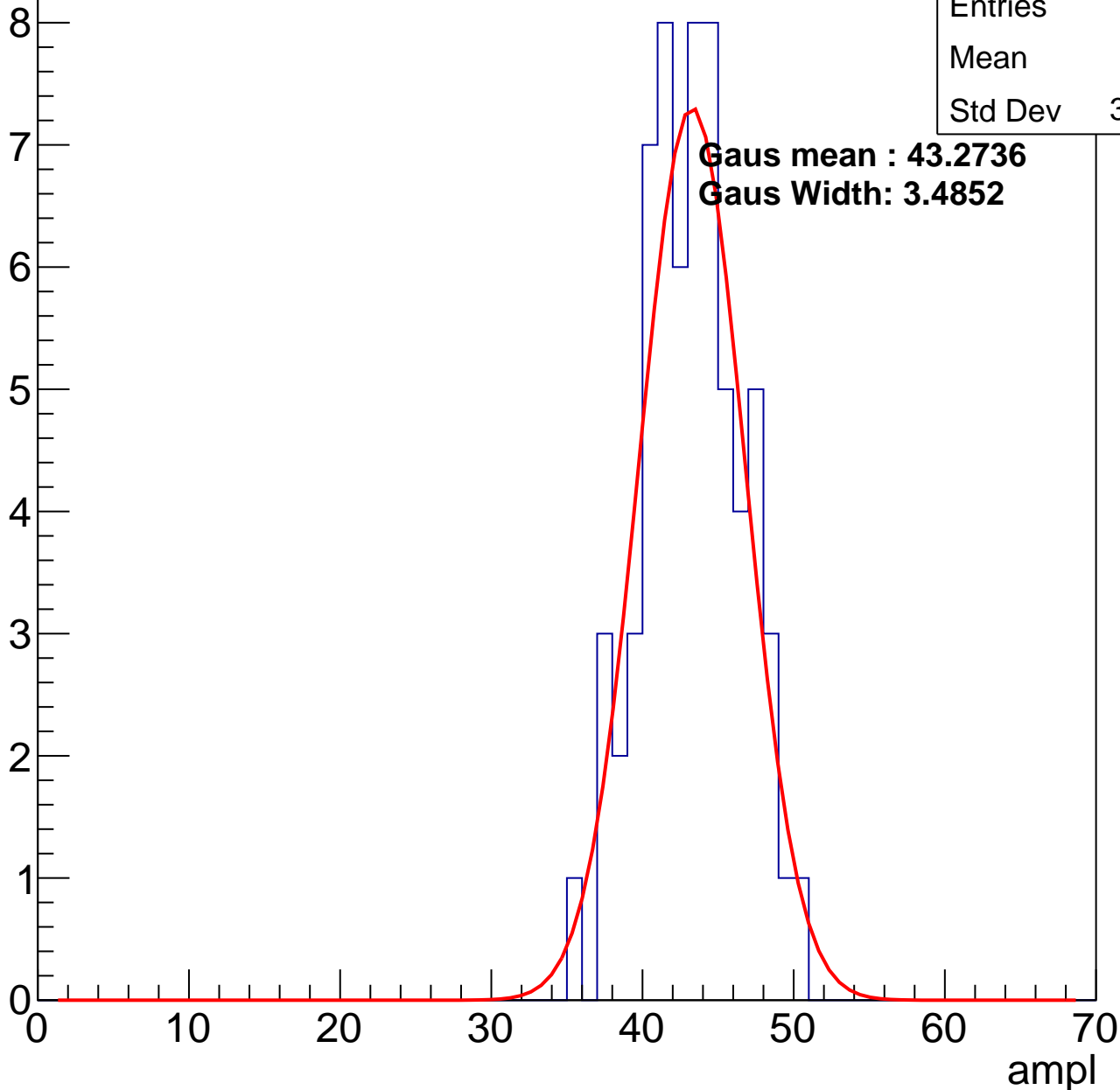
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.8
Std Dev	3.216

Gaus mean : 43.2736

Gaus Width: 3.4852

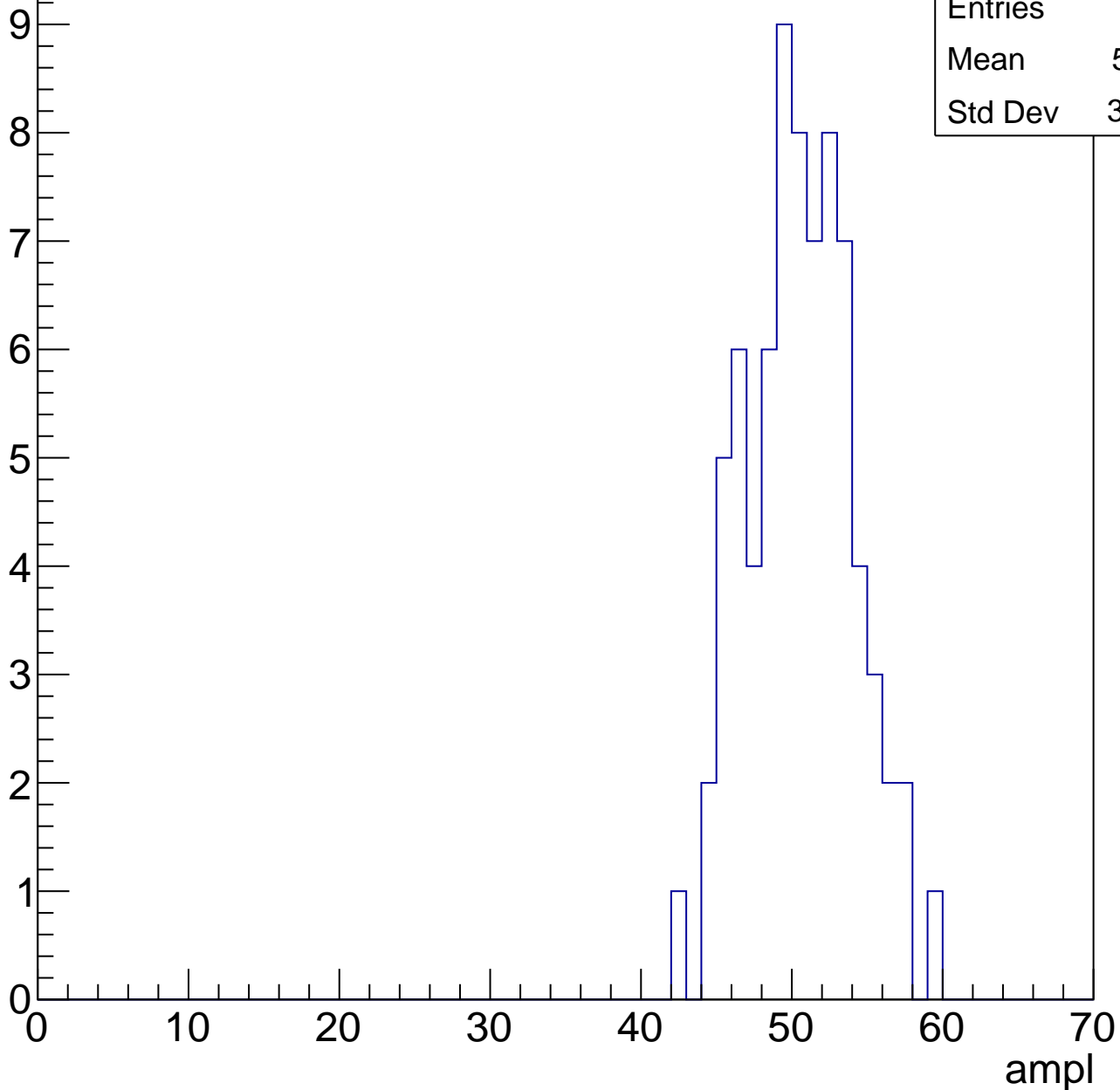


B1L101S, U26-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	50.11
Std Dev	3.497

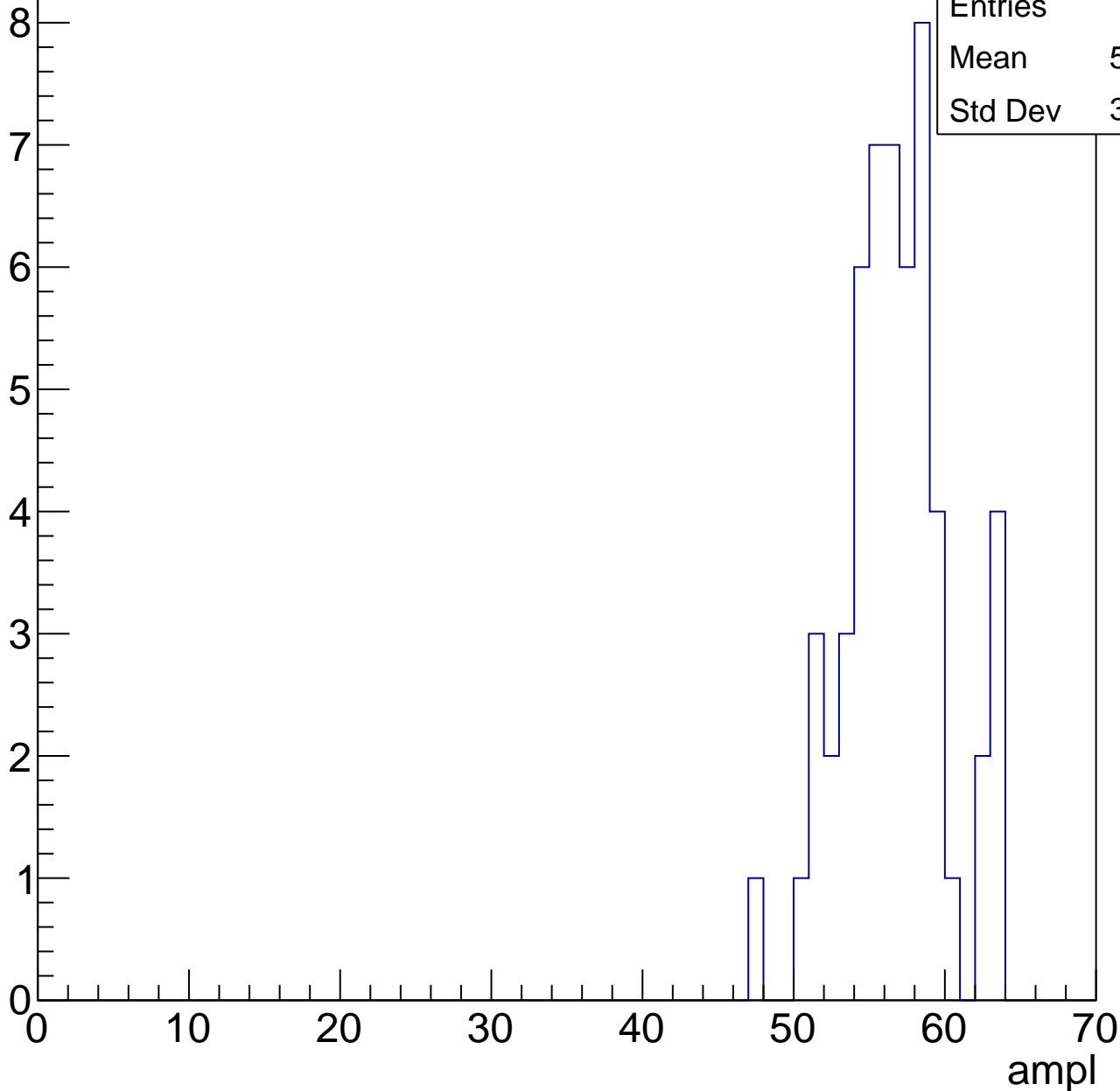


B1L101S, U26-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.22
Std Dev	3.404



B1L101S, U26-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

41

Mean

58.93

Std Dev

9.46

Entries	41
Mean	58.93
Std Dev	9.46

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

B1L101S, U26-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch18, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	28.42
Std Dev	5.825

Gaus mean : 29.6783

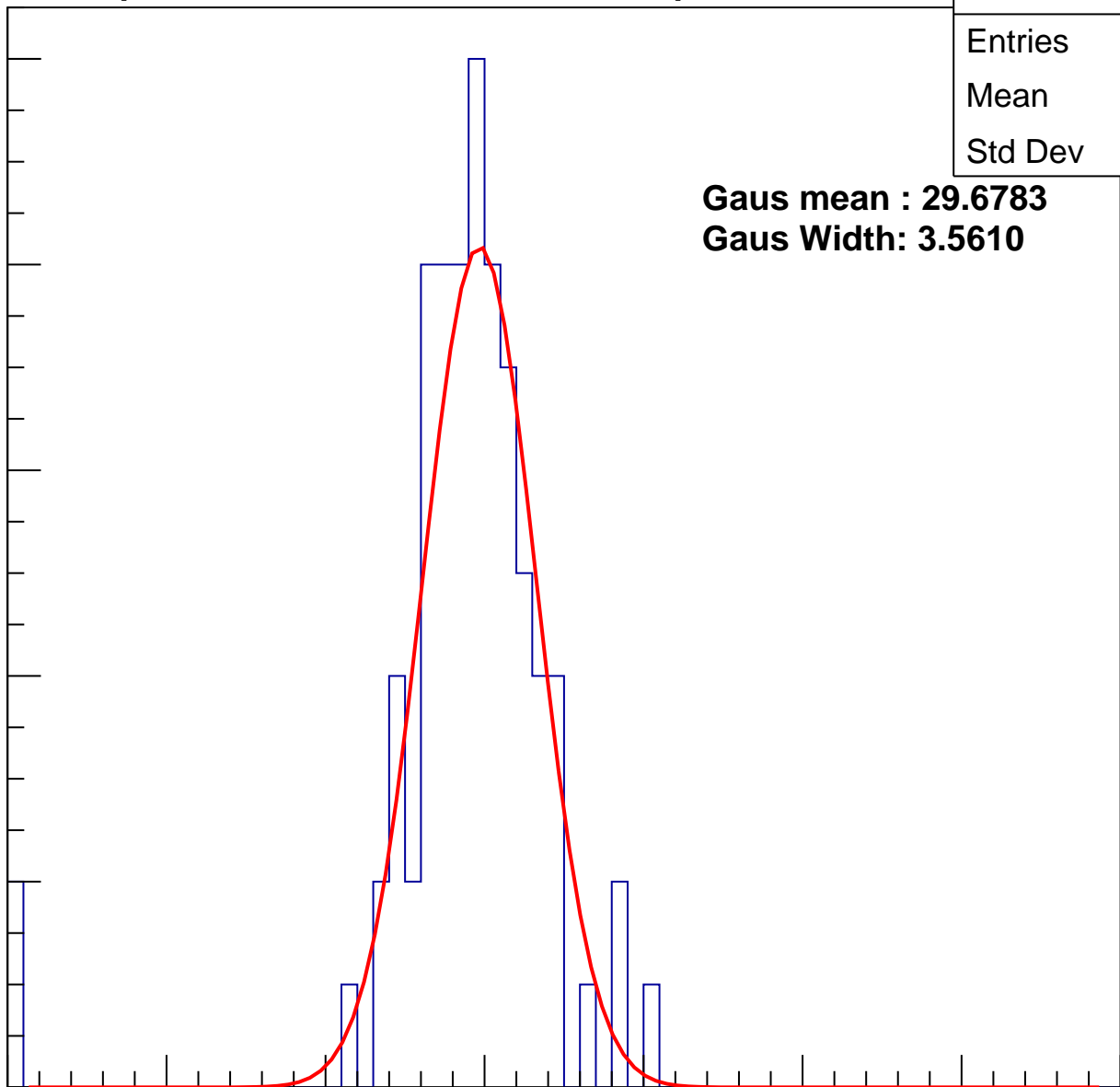
Gaus Width: 3.5610

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch18, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.27
Std Dev	3.82

Gaus mean : 36.5577

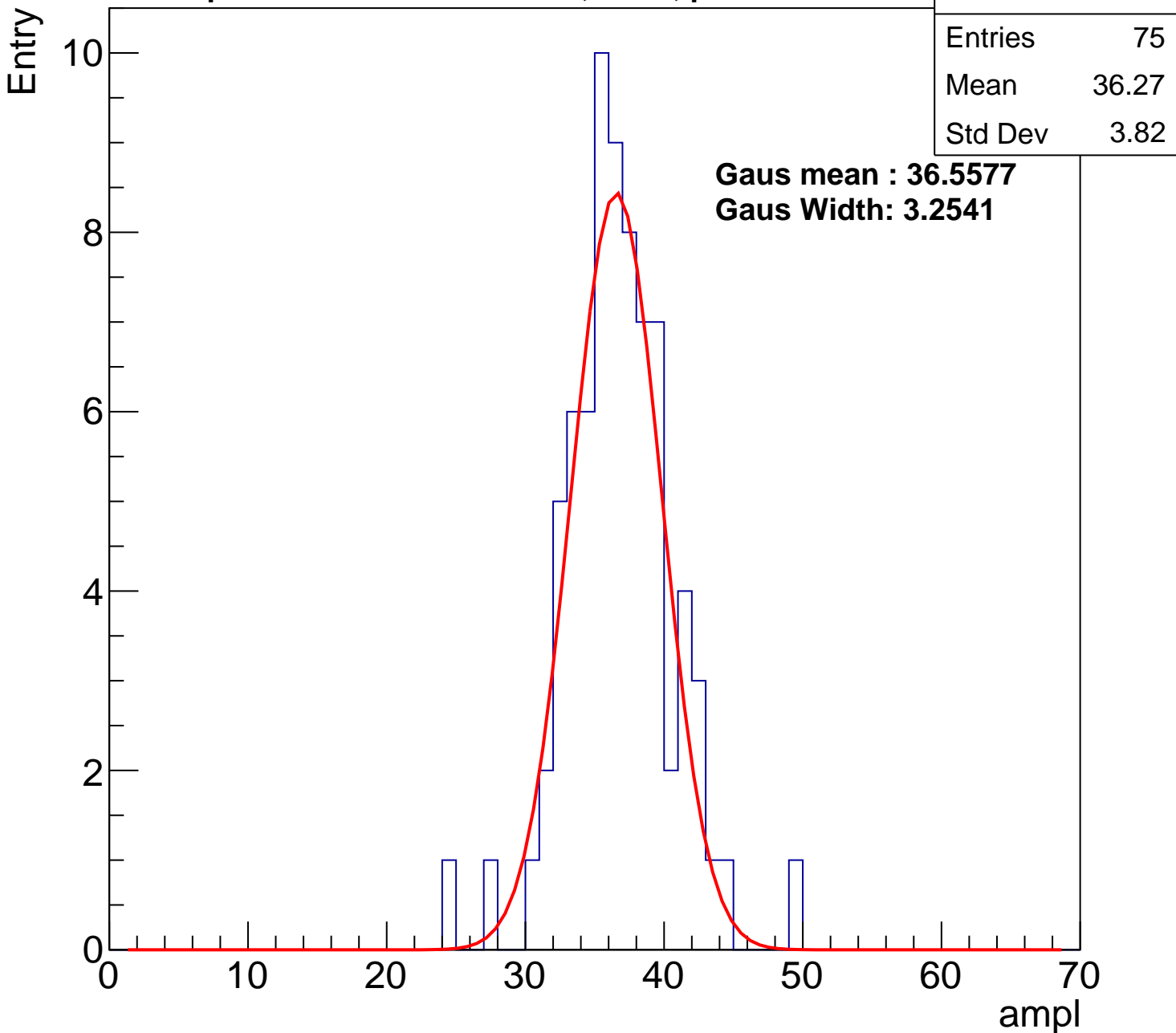
Gaus Width: 3.2541

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch18, adc2

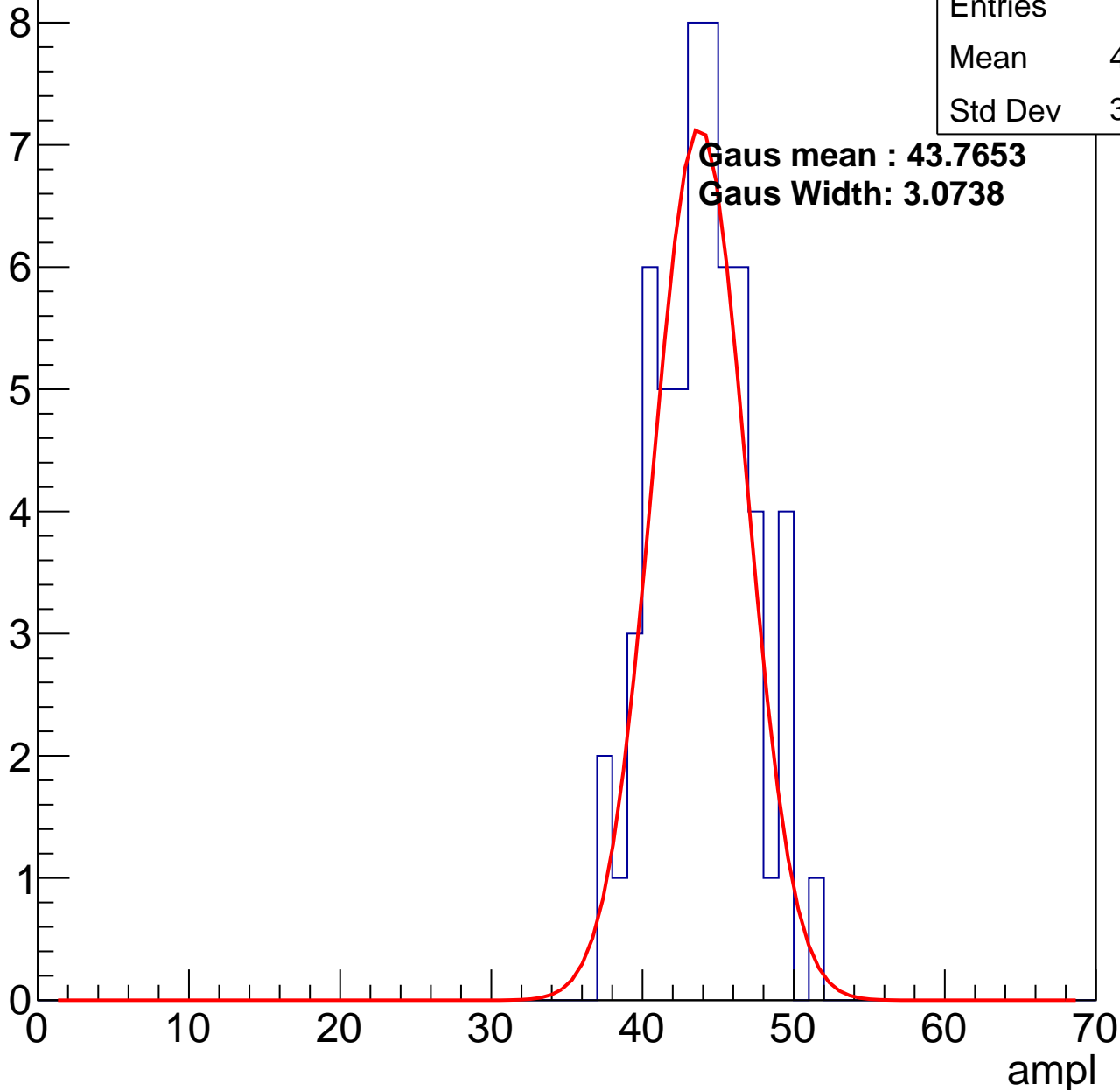
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.48
Std Dev	3.144

Gaus mean : 43.7653

Gaus Width: 3.0738

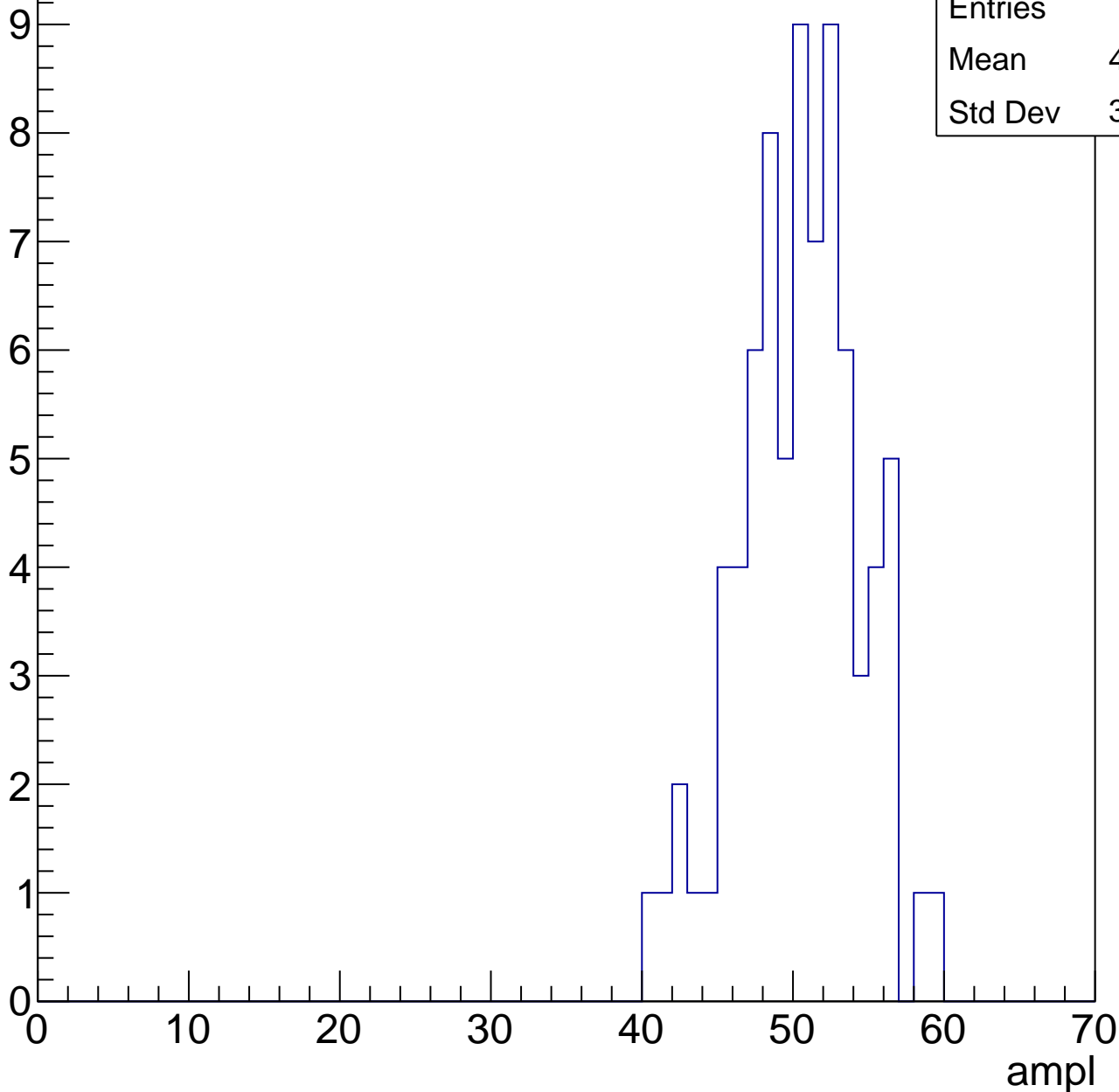


B1L101S, U26-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	49.99
Std Dev	3.953

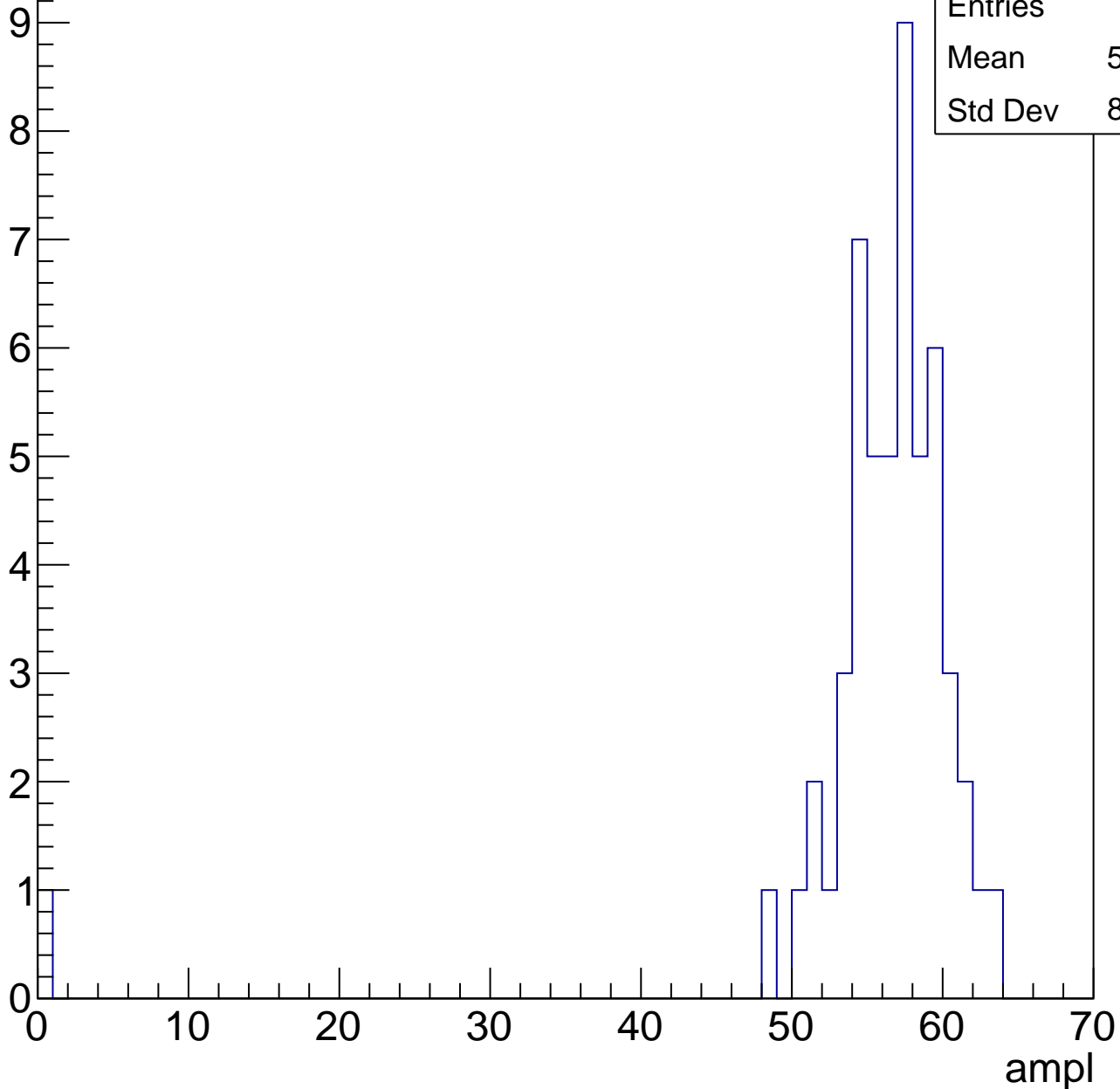


B1L101S, U26-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.25
Std Dev	8.239



B1L101S, U26-ch18, adc5

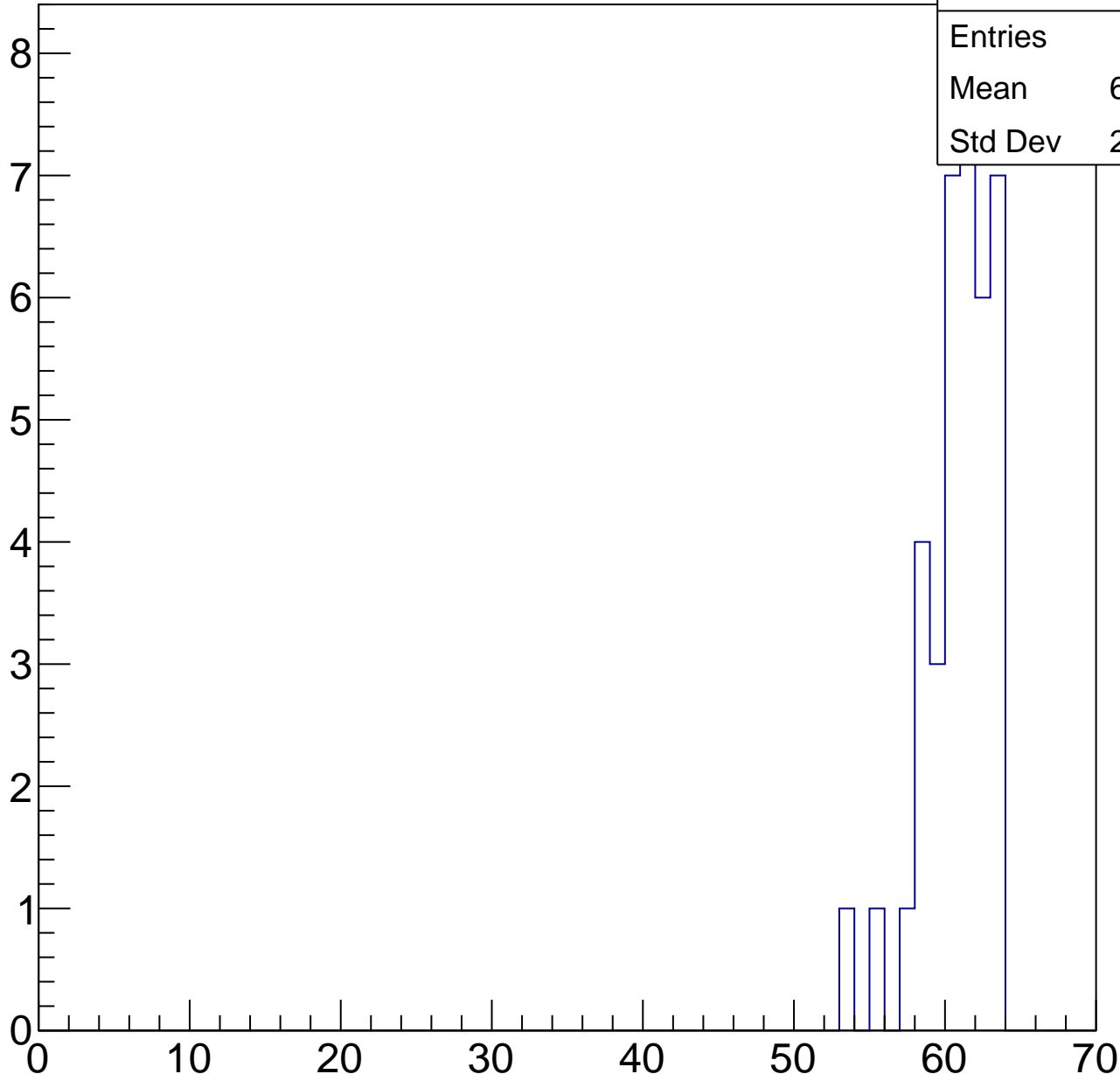
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.39
Std Dev	2.242

ampl



B1L101S, U26-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

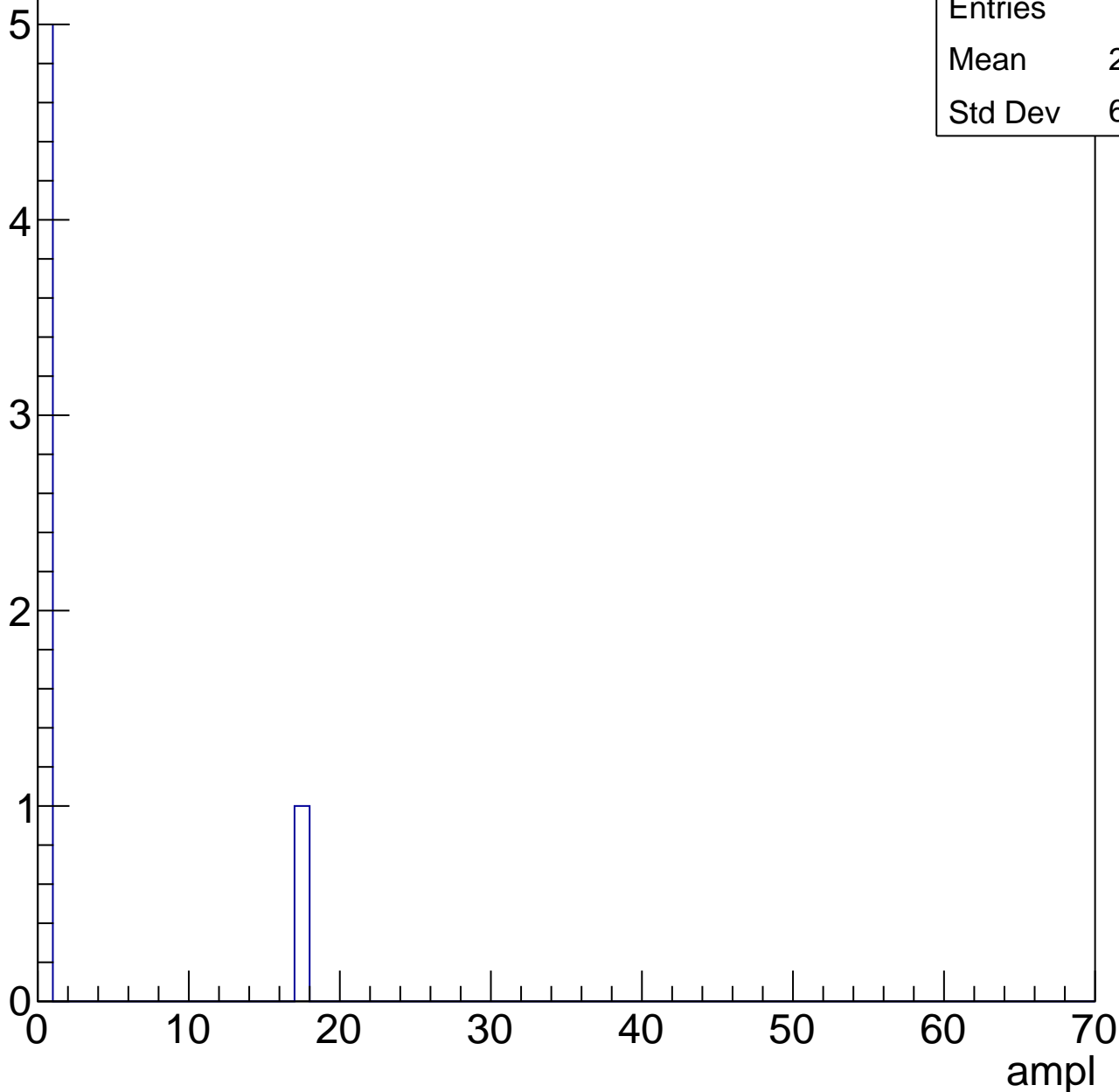


B1L101S, U26-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	6
Mean	2.833
Std Dev	6.336



B1L101S, U26-ch19, adc0

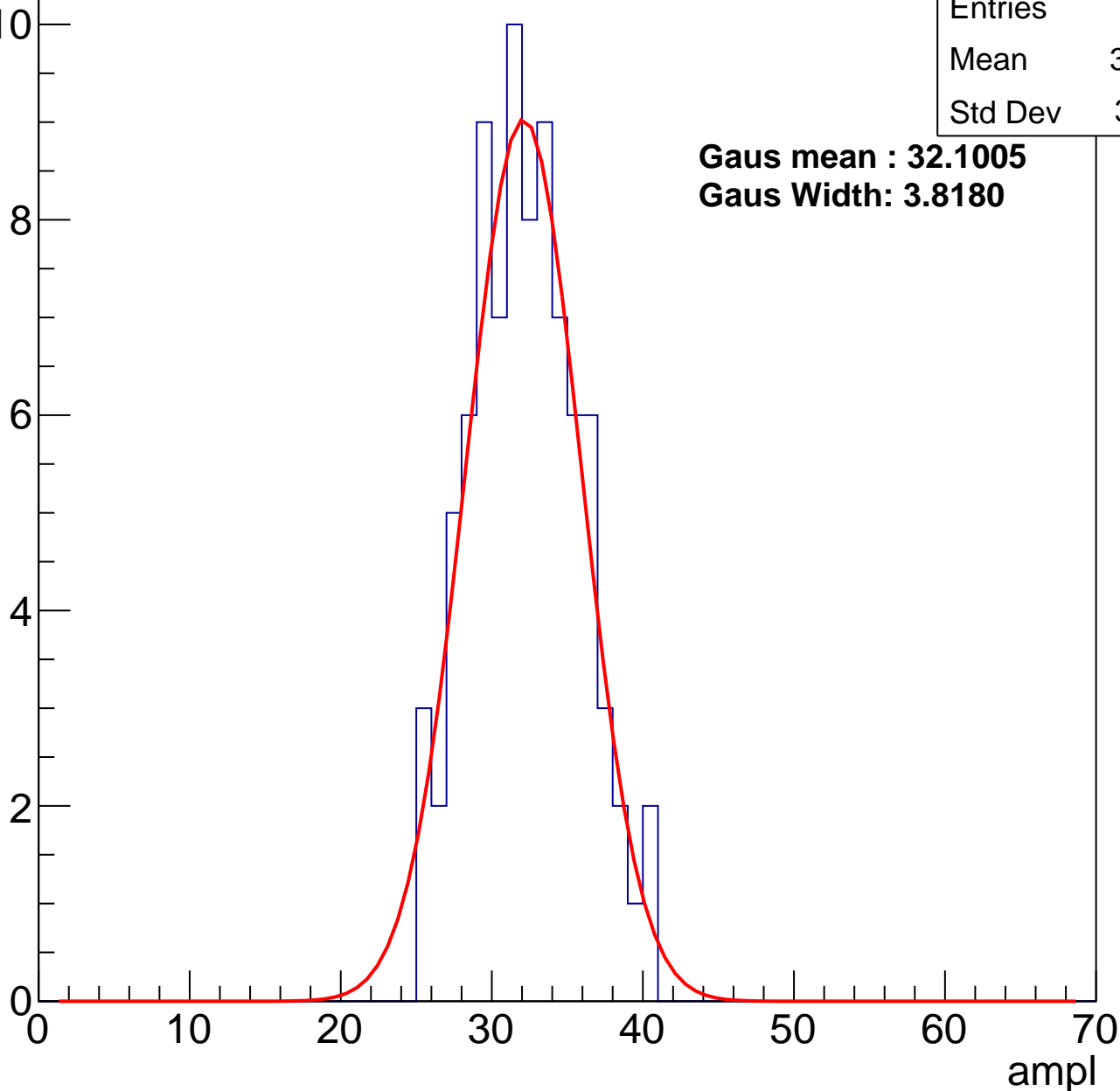
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	31.79
Std Dev	3.521

Gaus mean : 32.1005

Gaus Width: 3.8180



B1L101S, U26-ch19, adc1

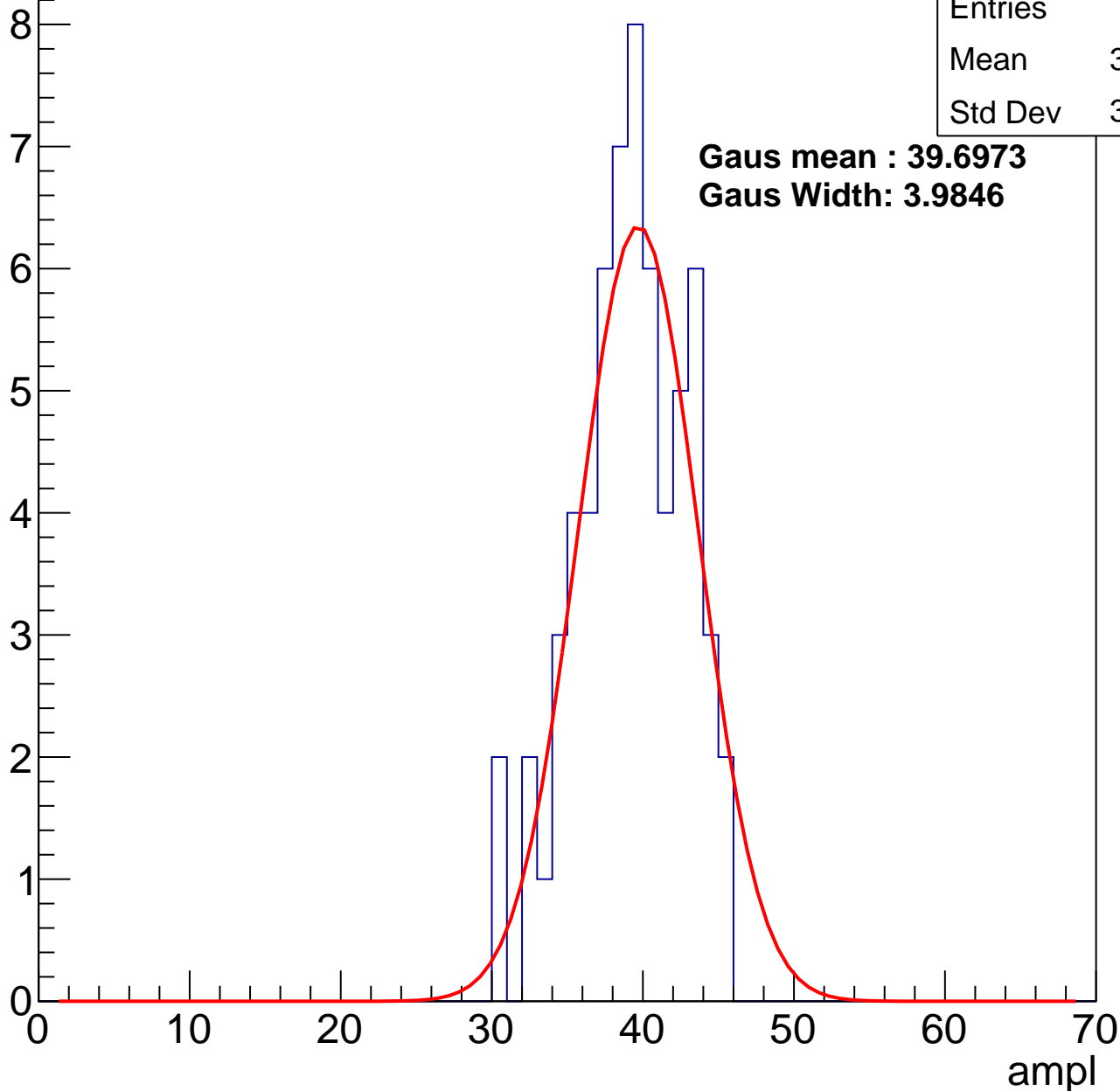
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.68
Std Dev	3.576

Gaus mean : 39.6973

Gaus Width: 3.9846



B1L101S, U26-ch19, adc2

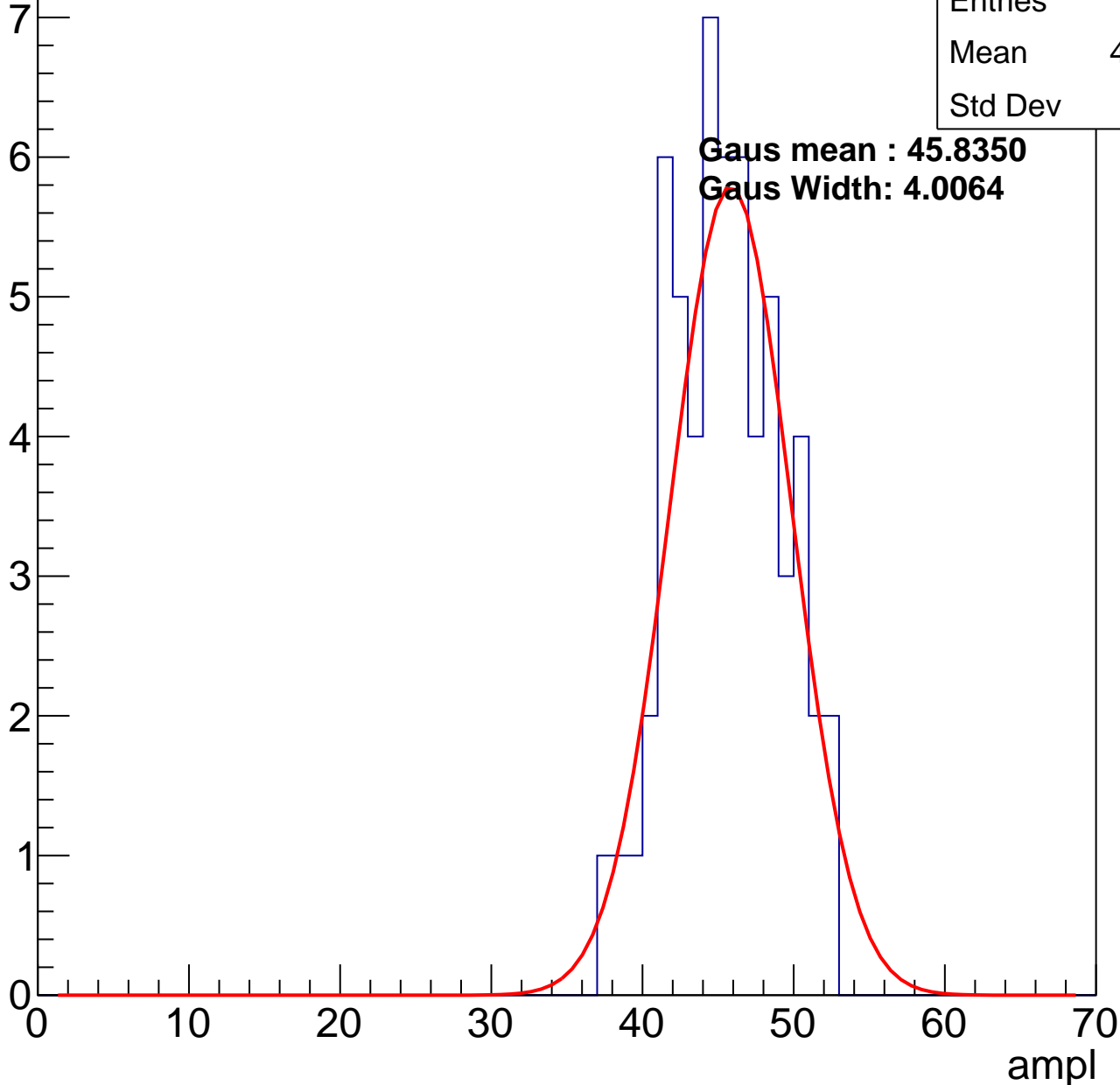
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	45.03
Std Dev	3.56

Gaus mean : 45.8350

Gaus Width: 4.0064



B1L101S, U26-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	51.45
Std Dev	3.394

Entry

10

8

6

4

2

0

0

10

20

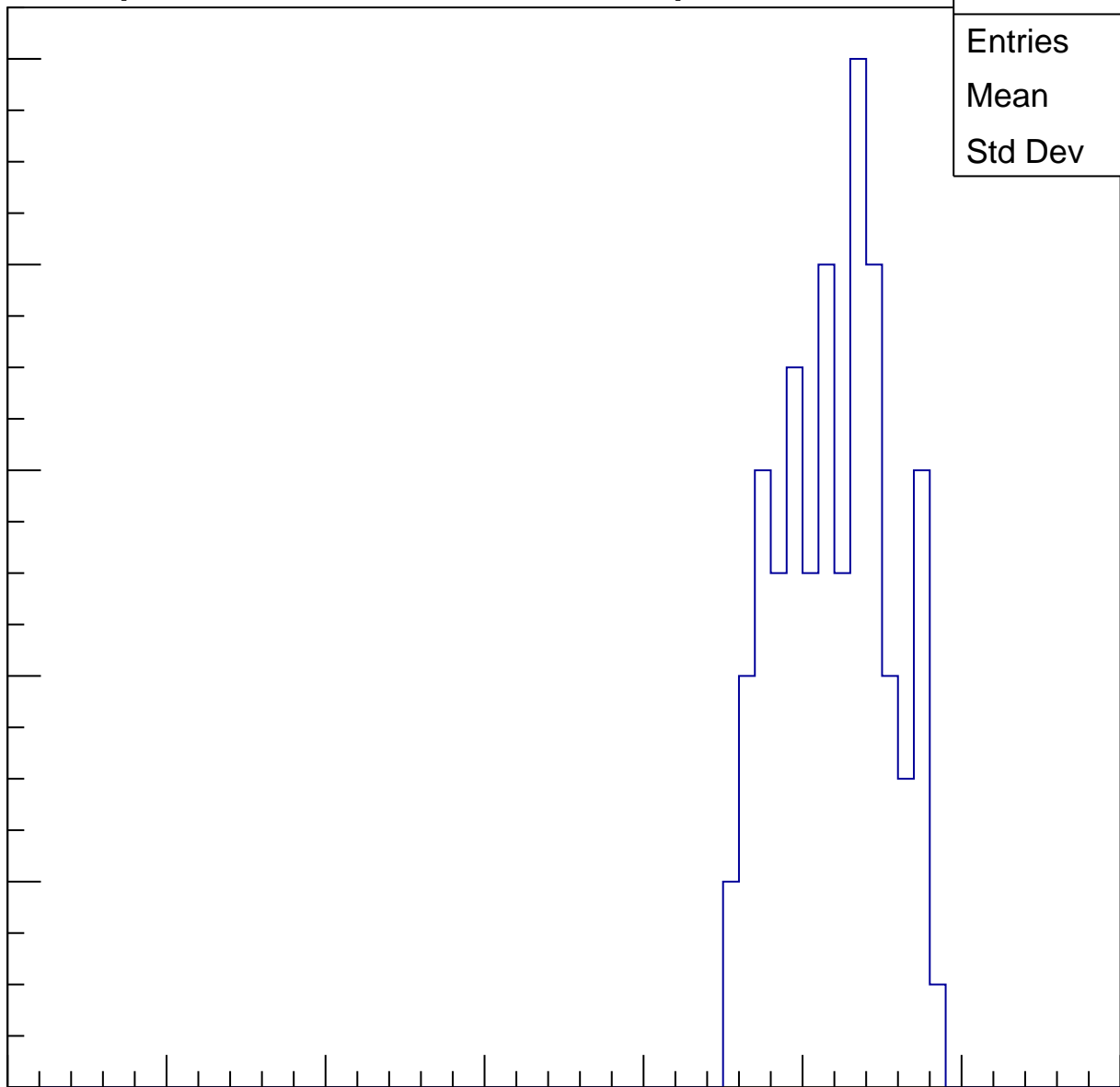
30

40

50

60

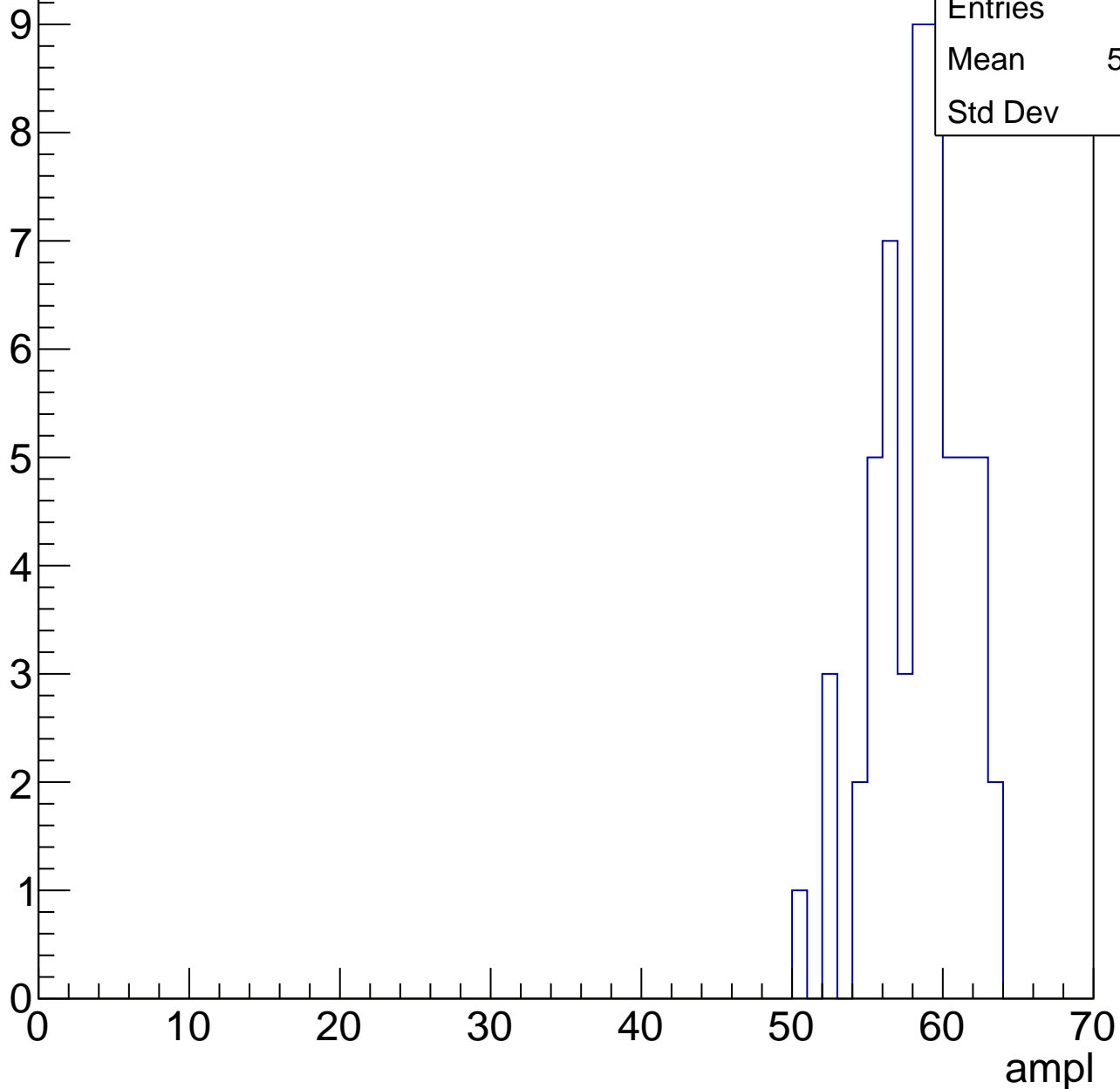
ampl



B1L101S, U26-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

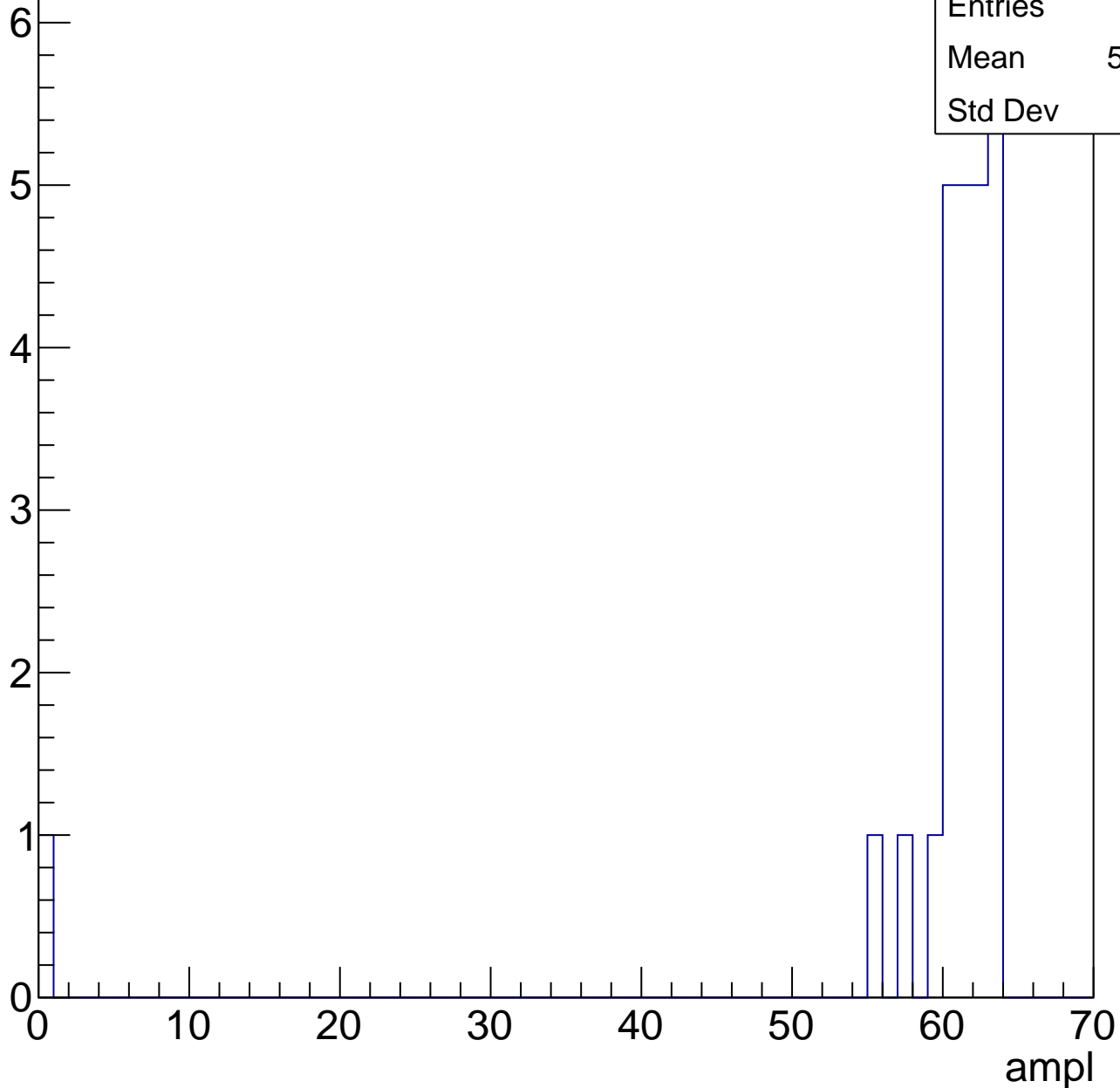


B1L101S, U26-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.56
Std Dev	12.1



B1L101S, U26-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	12
Std Dev	12

B1L101S, U26-ch20, adc0

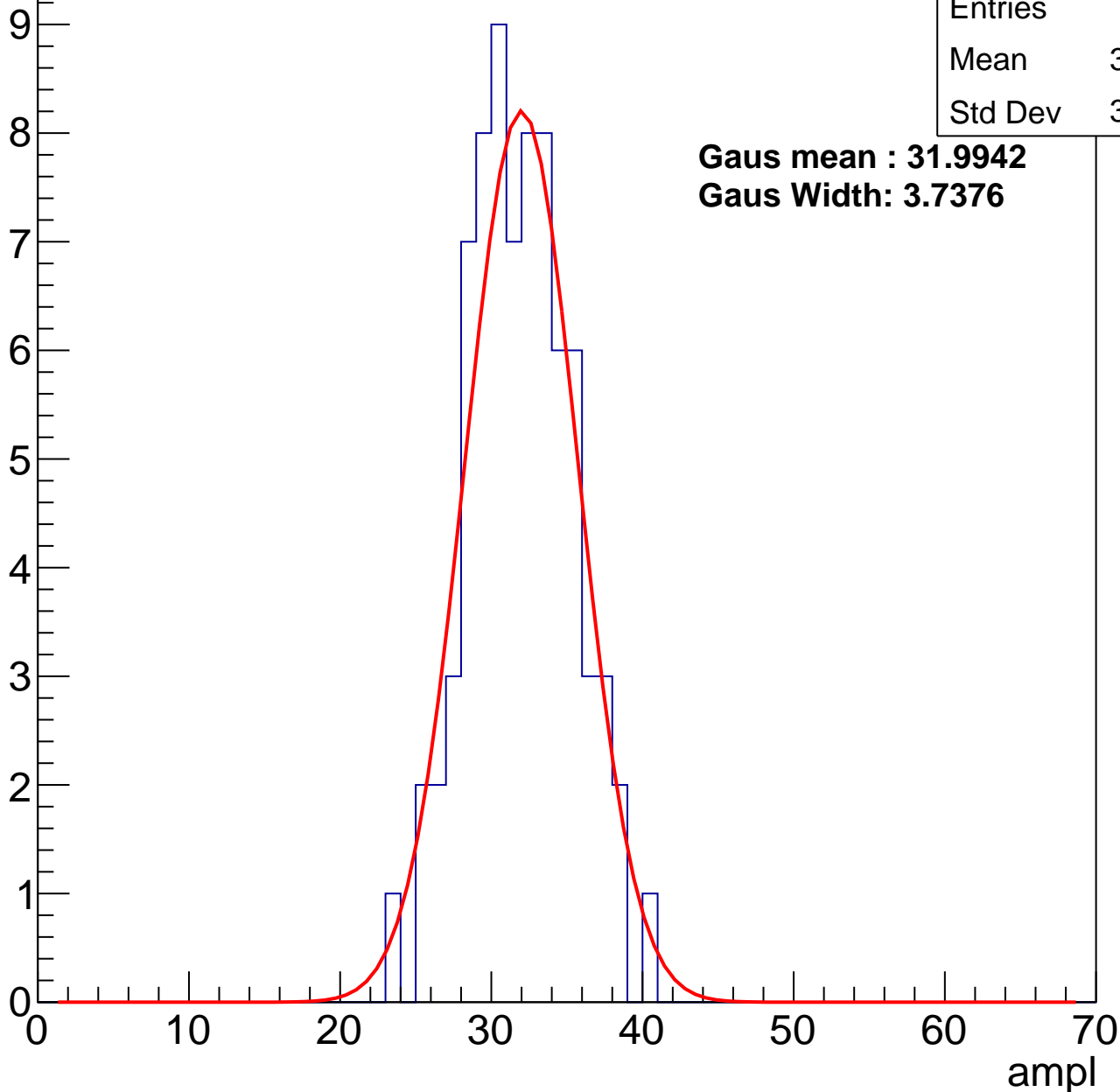
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	31.45
Std Dev	3.393

Gaus mean : 31.9942

Gaus Width: 3.7376



B1L101S, U26-ch20, adc1

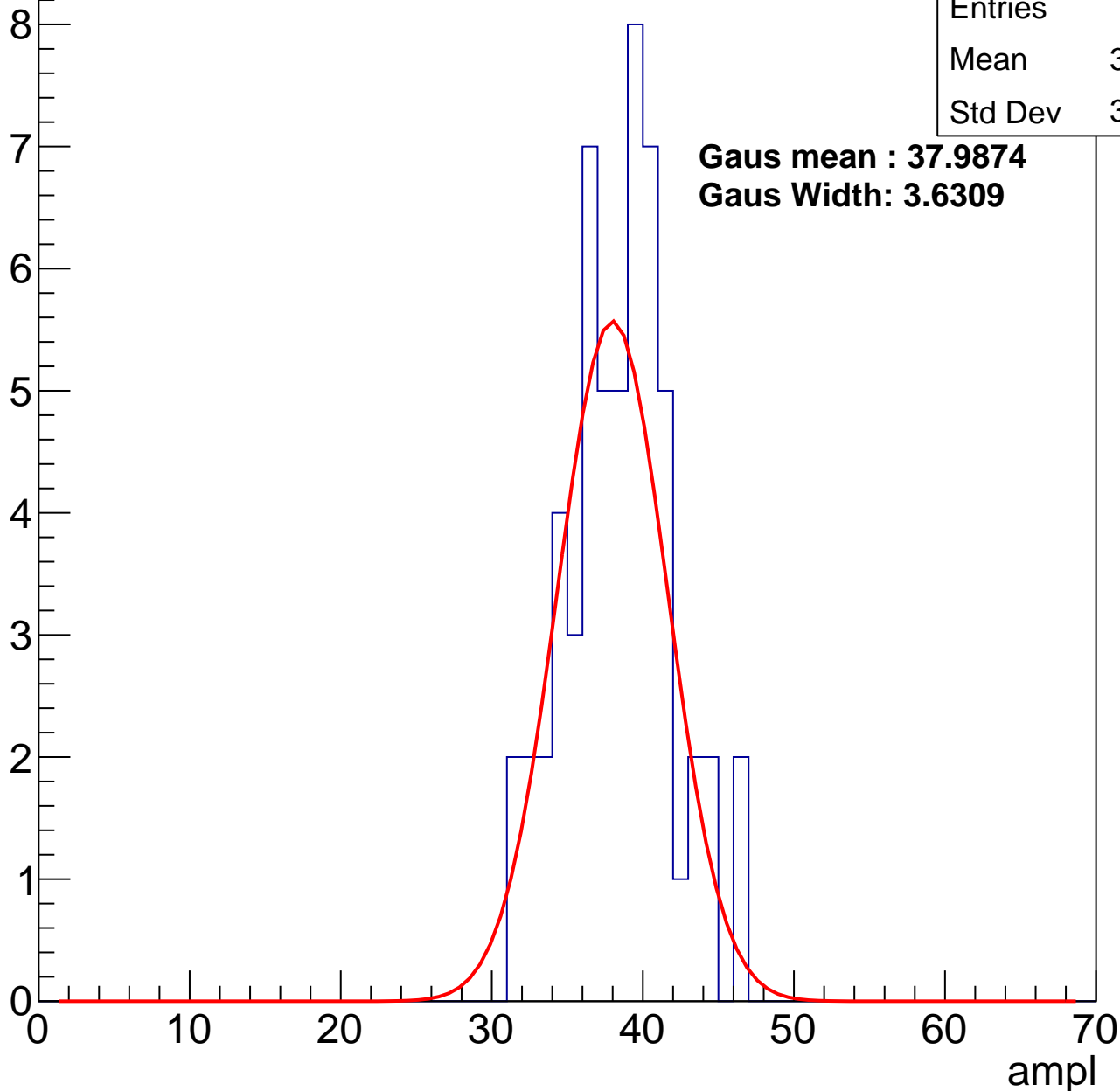
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.98
Std Dev	3.482

Gaus mean : 37.9874

Gaus Width: 3.6309



B1L101S, U26-ch20, adc2

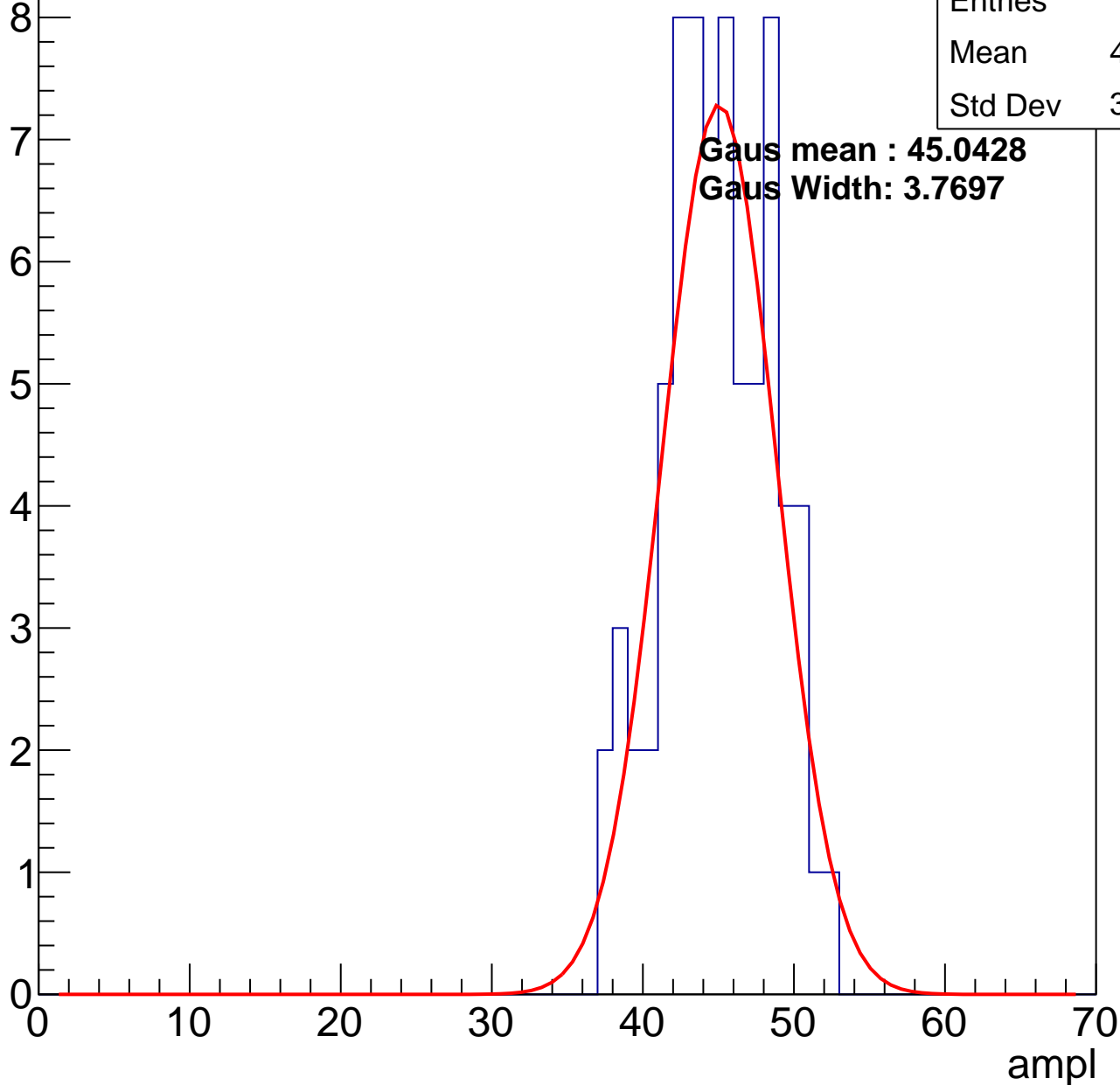
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.48
Std Dev	3.539

Gaus mean : 45.0428

Gaus Width: 3.7697

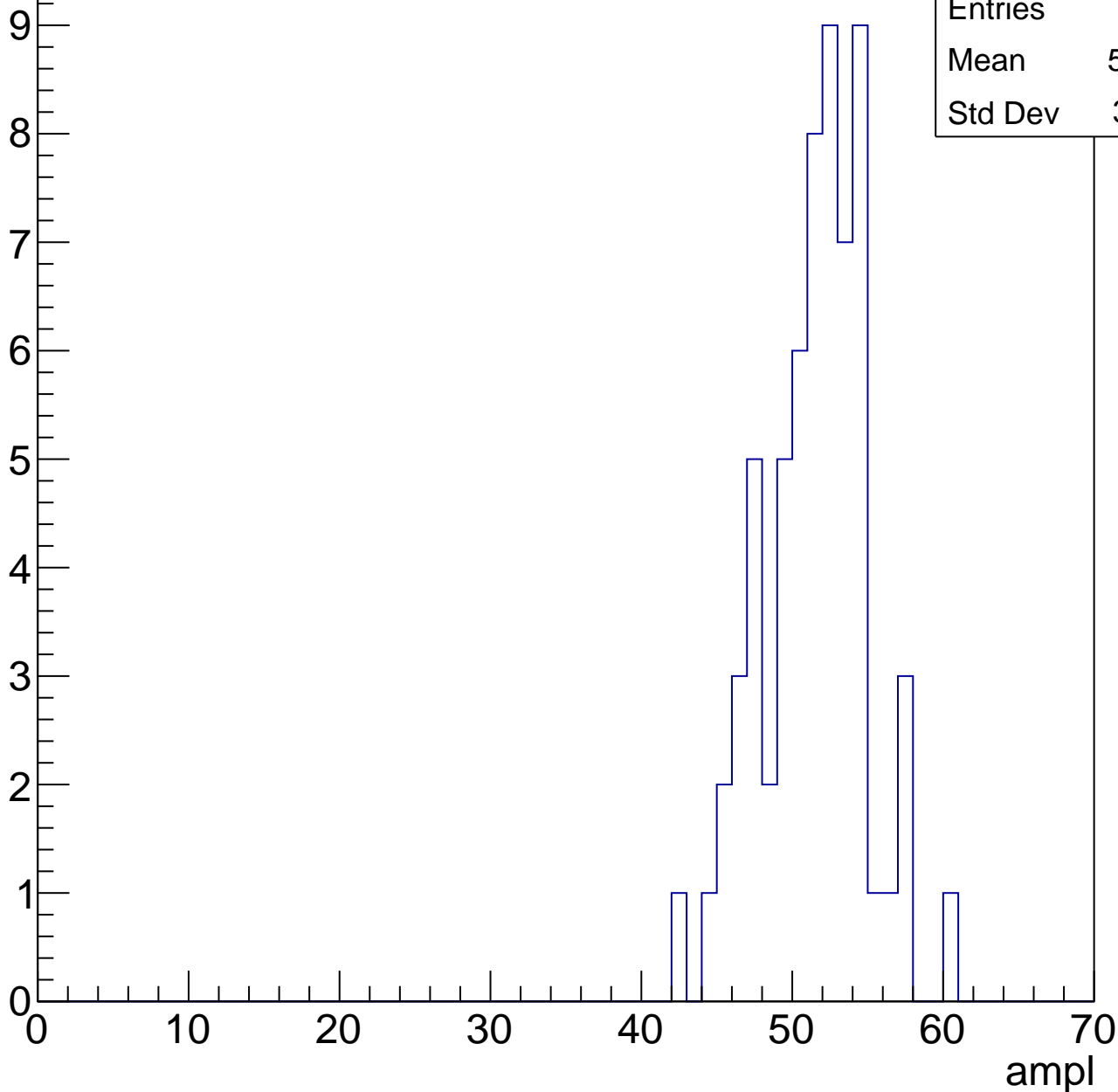


B1L101S, U26-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.02
Std Dev	3.421

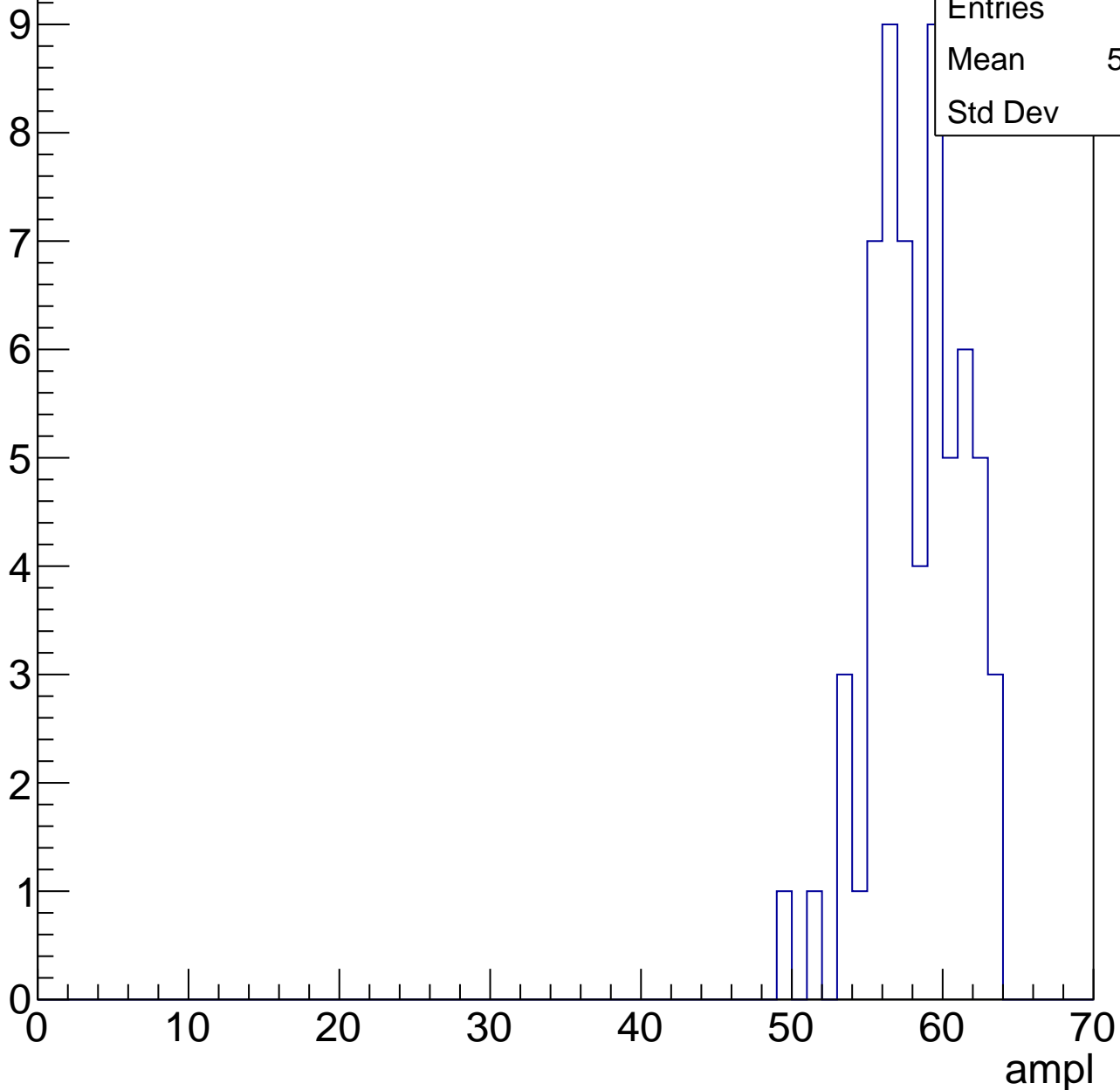


B1L101S, U26-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	57.85
Std Dev	3.04

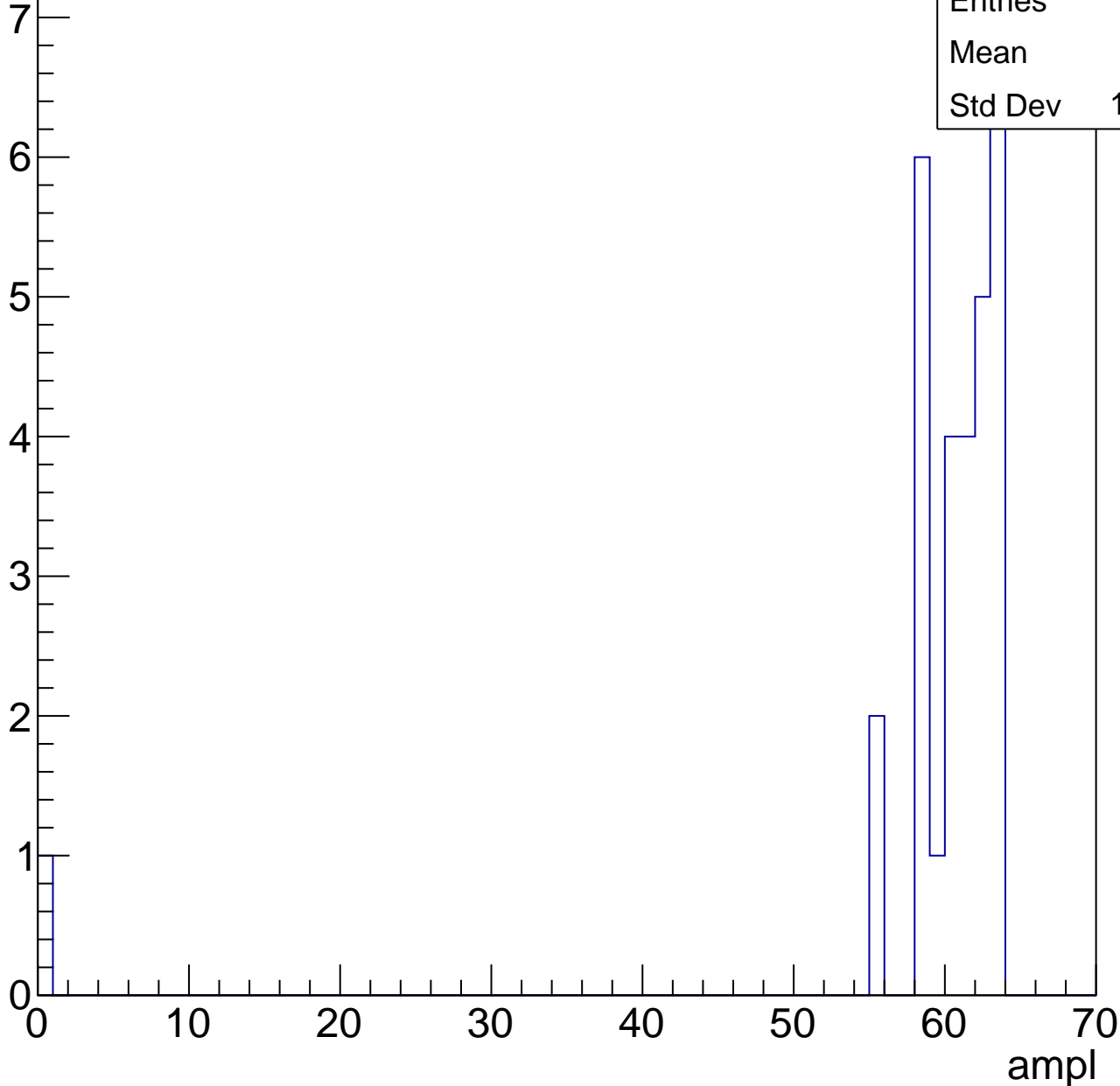


B1L101S, U26-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	58.4
Std Dev	11.08



B1L101S, U26-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch21, adc0

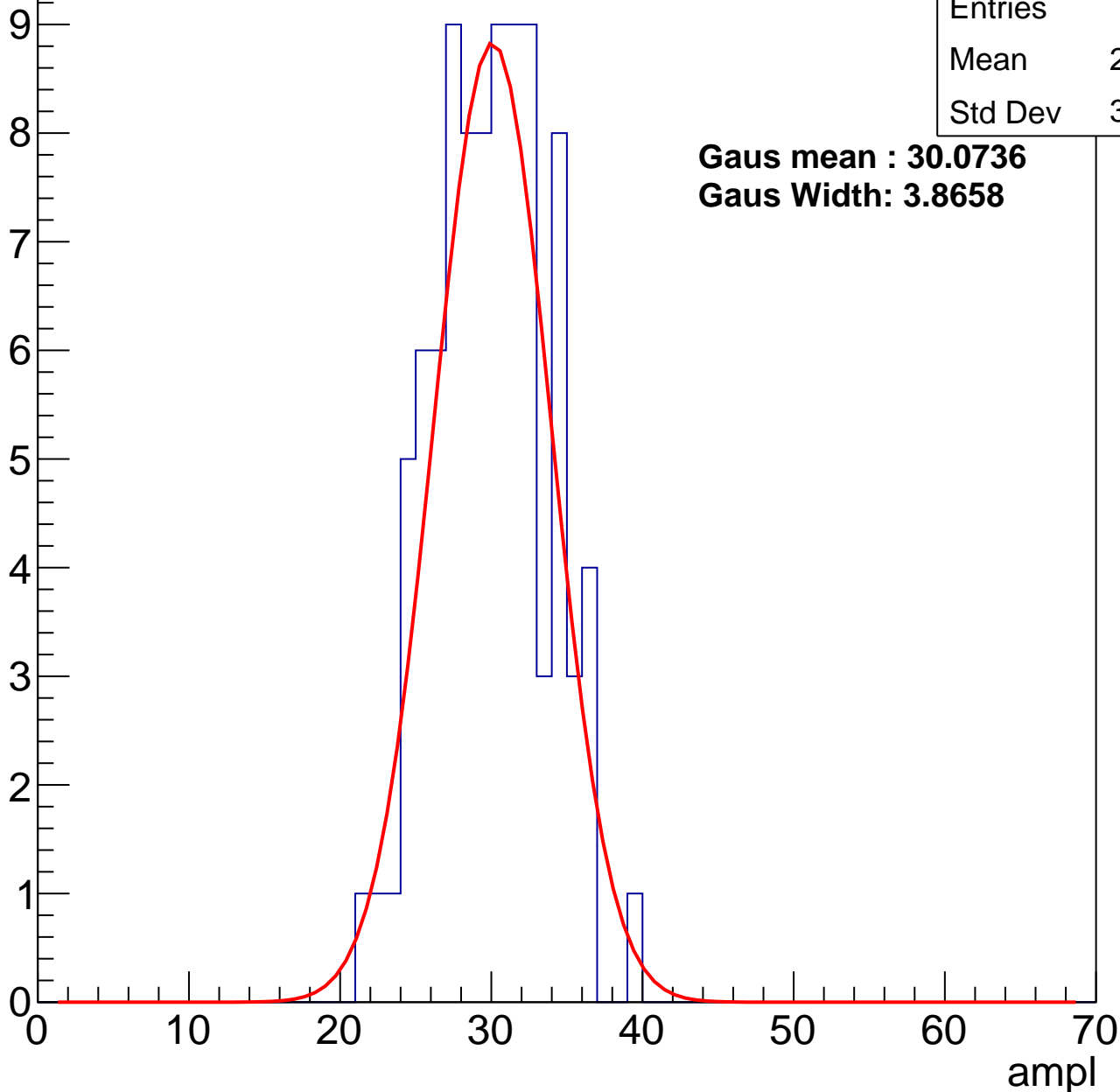
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	91
Mean	29.53
Std Dev	3.663

Gaus mean : 30.0736

Gaus Width: 3.8658



B1L101S, U26-ch21, adc1

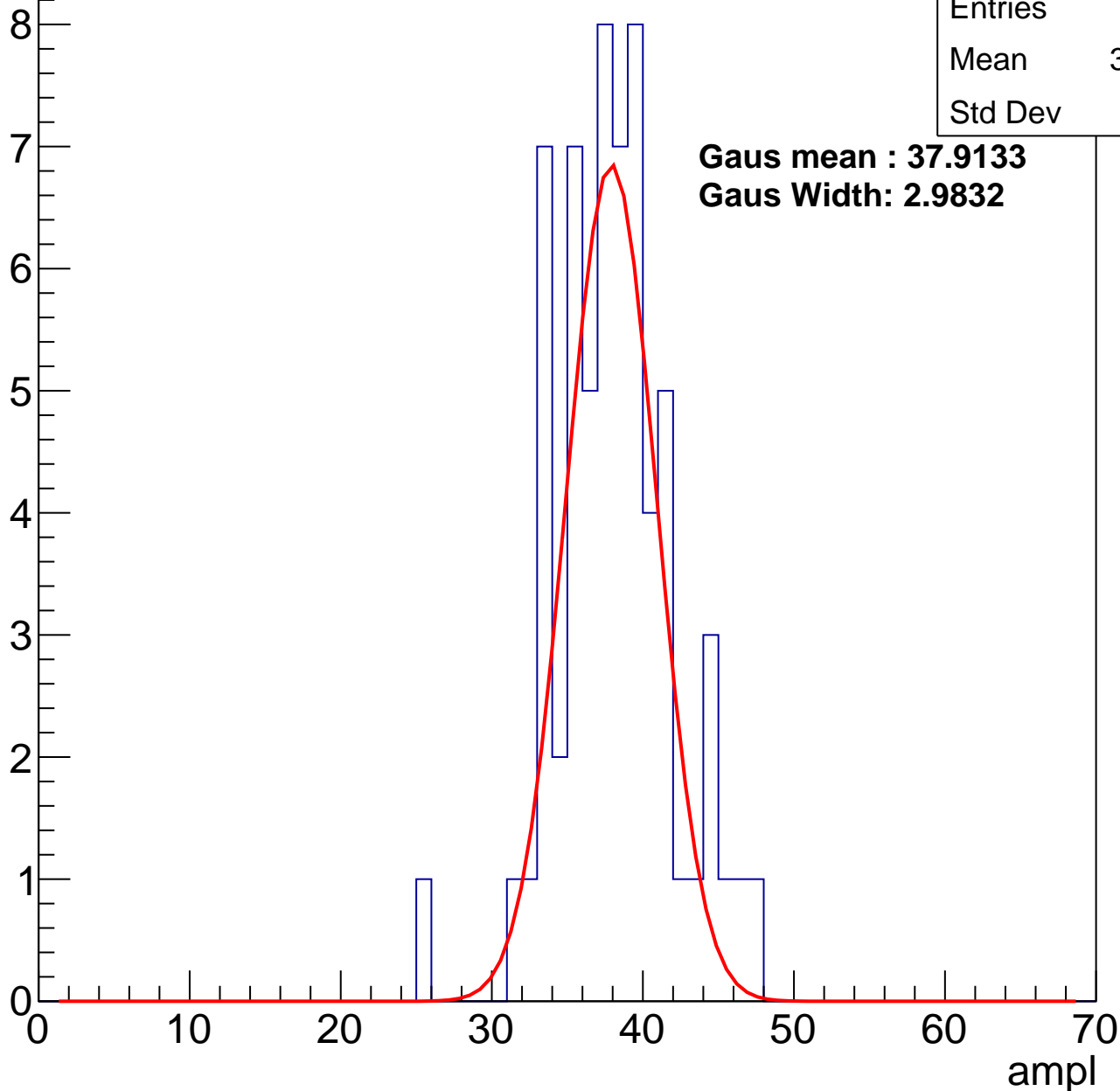
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.59
Std Dev	3.86

Gaus mean : 37.9133

Gaus Width: 2.9832



B1L101S, U26-ch21, adc2

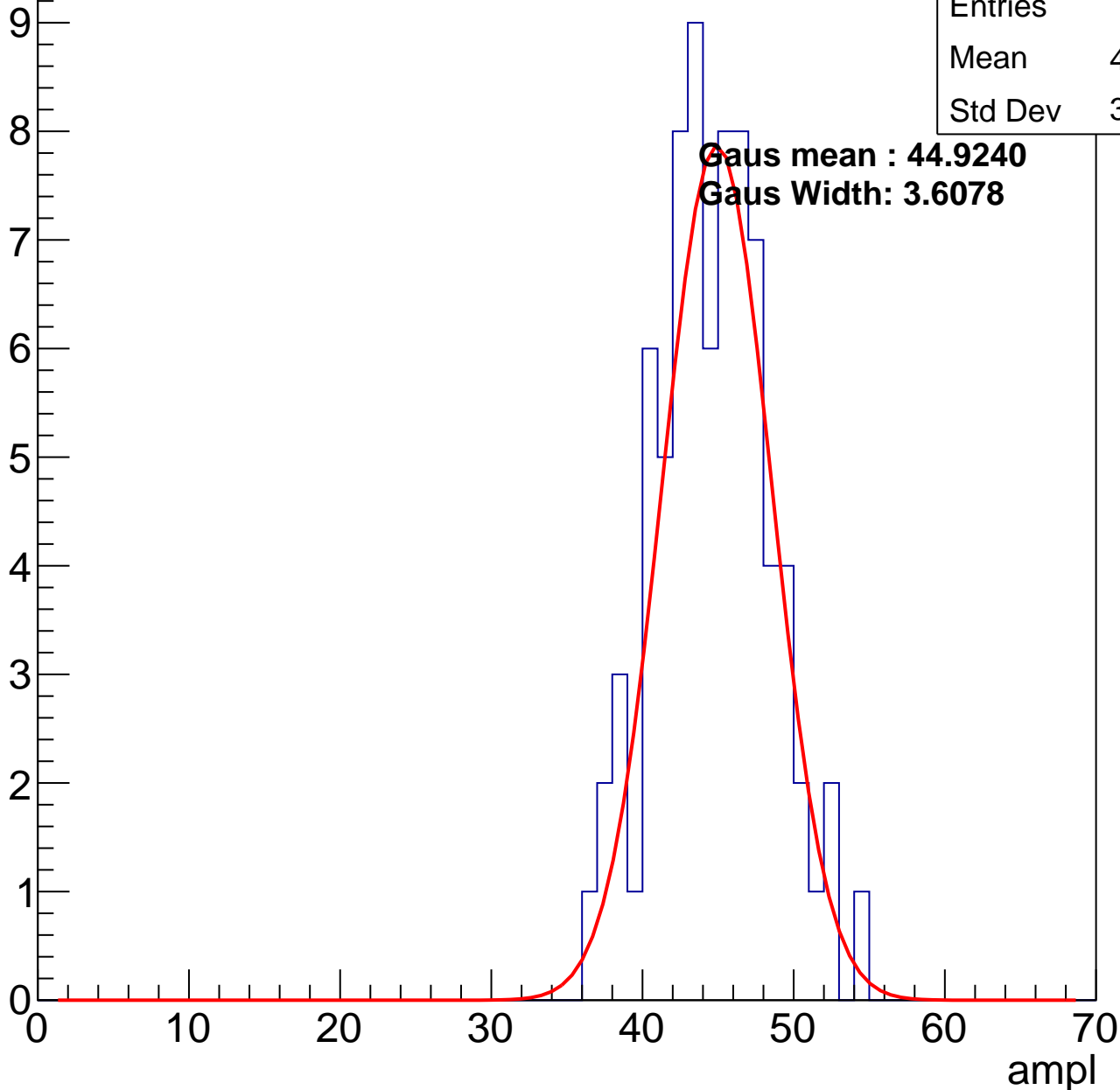
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.22
Std Dev	3.737

Gaus mean : 44.9240

Gaus Width: 3.6078

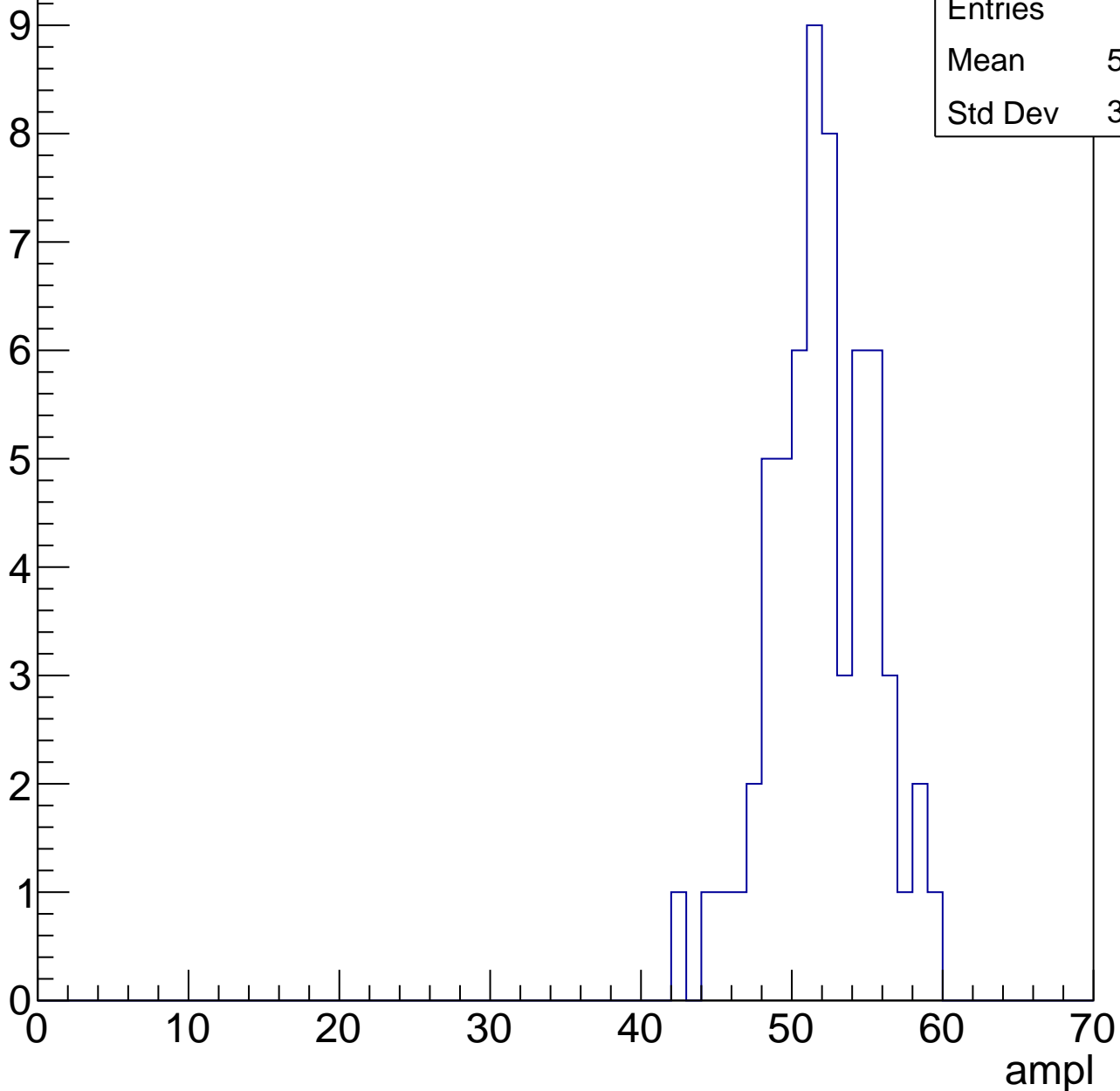


B1L101S, U26-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	51.54
Std Dev	3.434

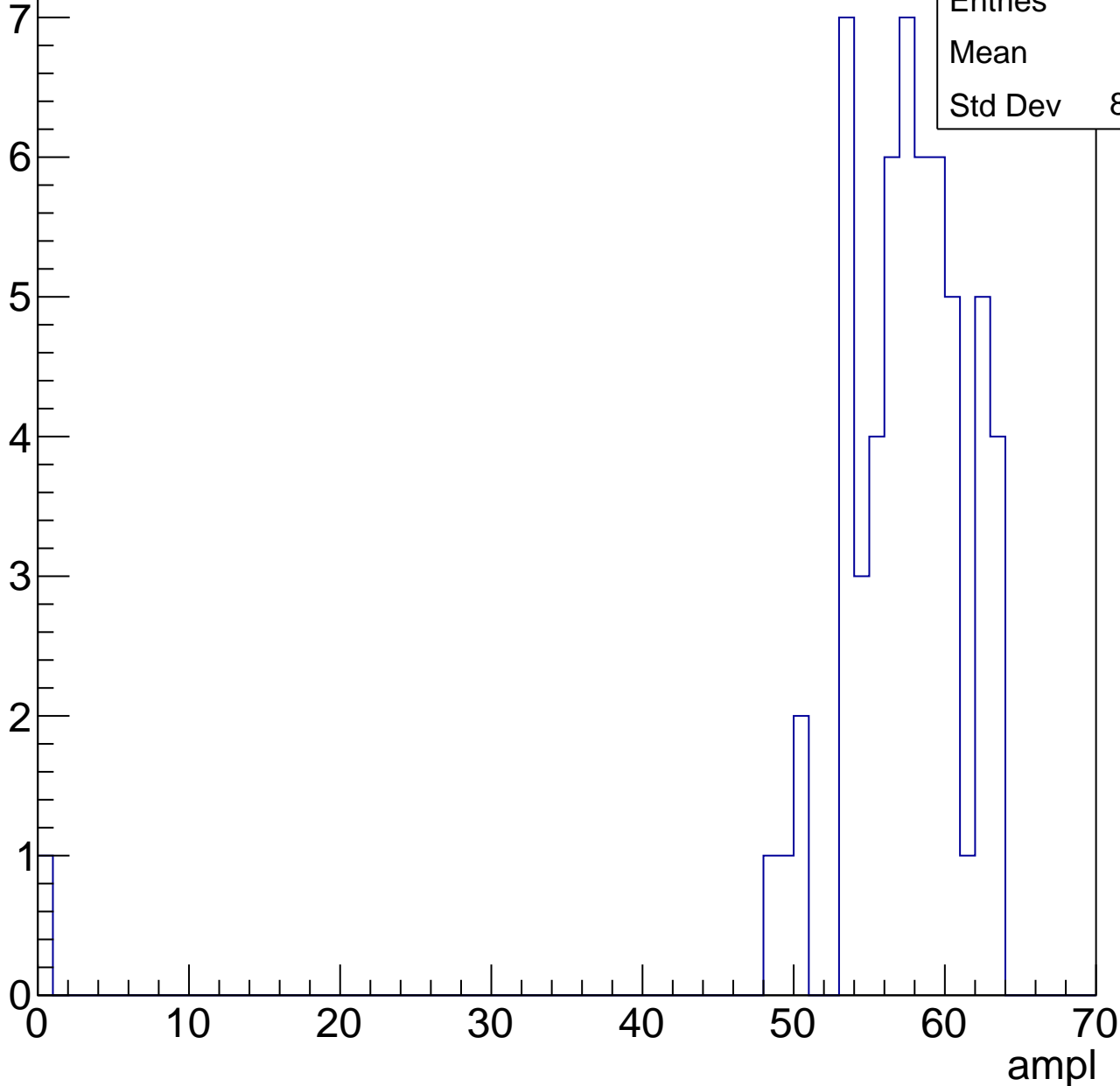


B1L101S, U26-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.1
Std Dev	8.198

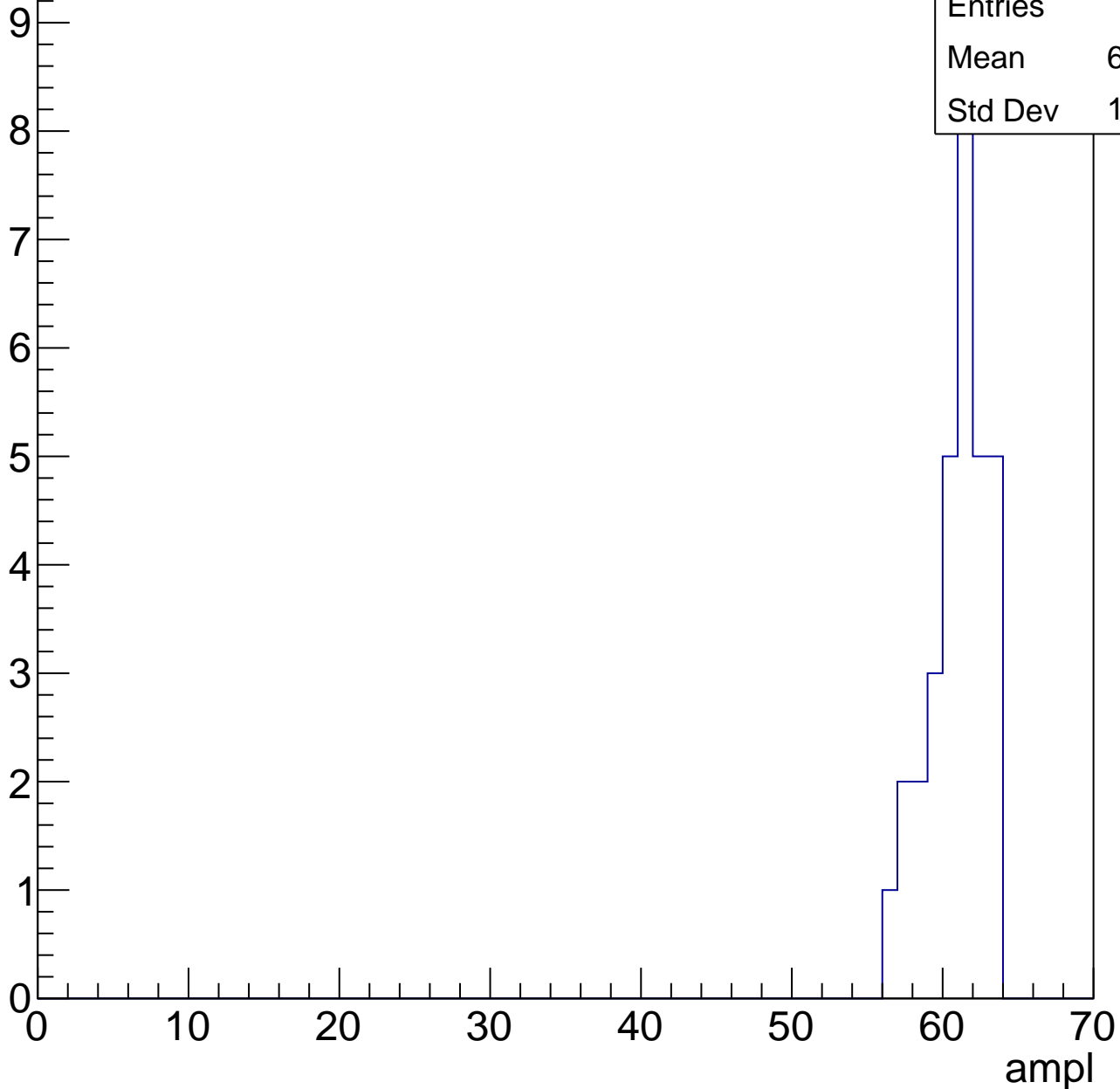


B1L101S, U26-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	60.53
Std Dev	1.854



B1L101S, U26-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch22, adc0

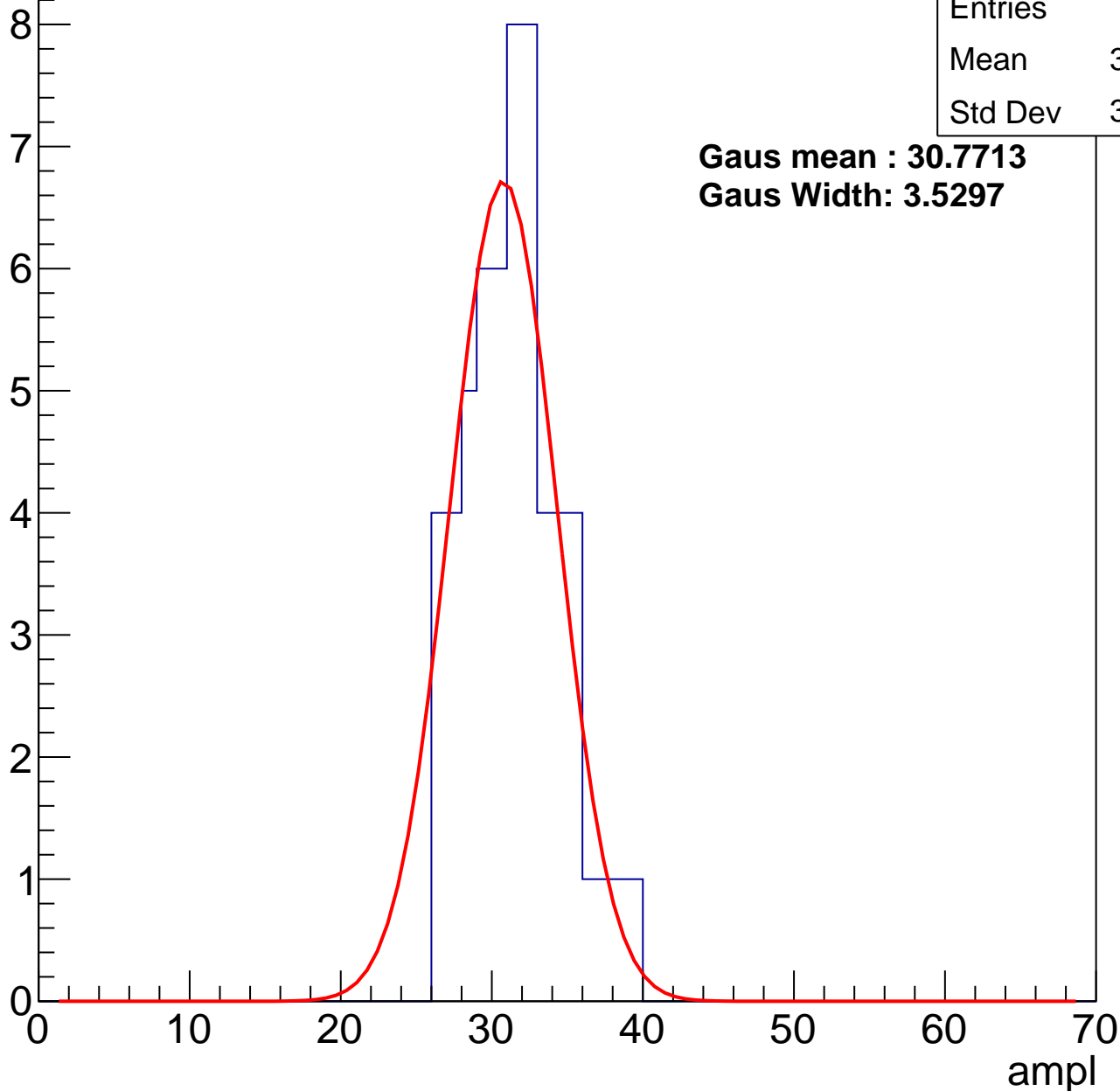
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	31.02
Std Dev	3.069

Gaus mean : 30.7713

Gaus Width: 3.5297



B1L101S, U26-ch22, adc1

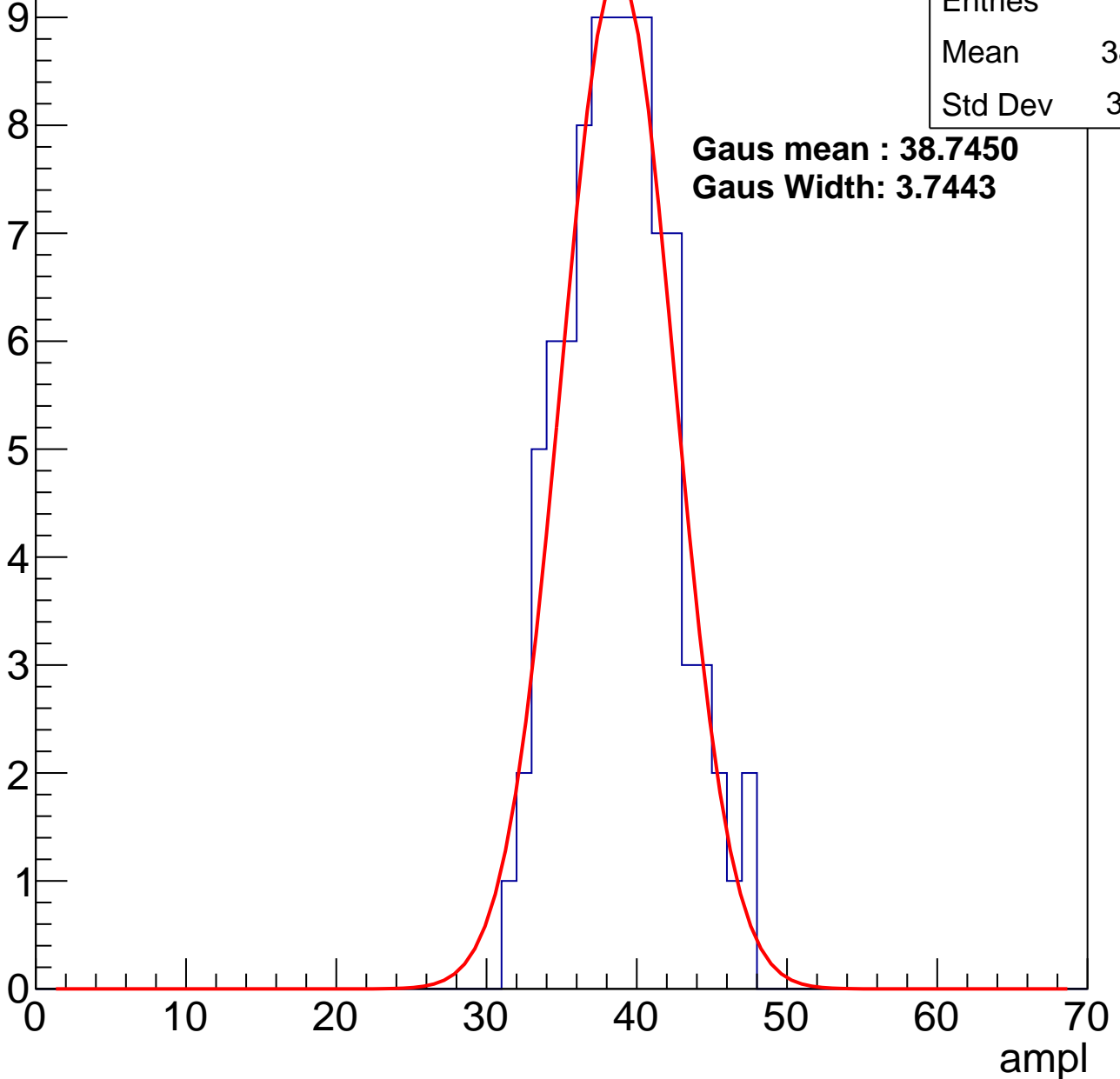
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	38.43
Std Dev	3.591

Gaus mean : 38.7450

Gaus Width: 3.7443



B1L101S, U26-ch22, adc2

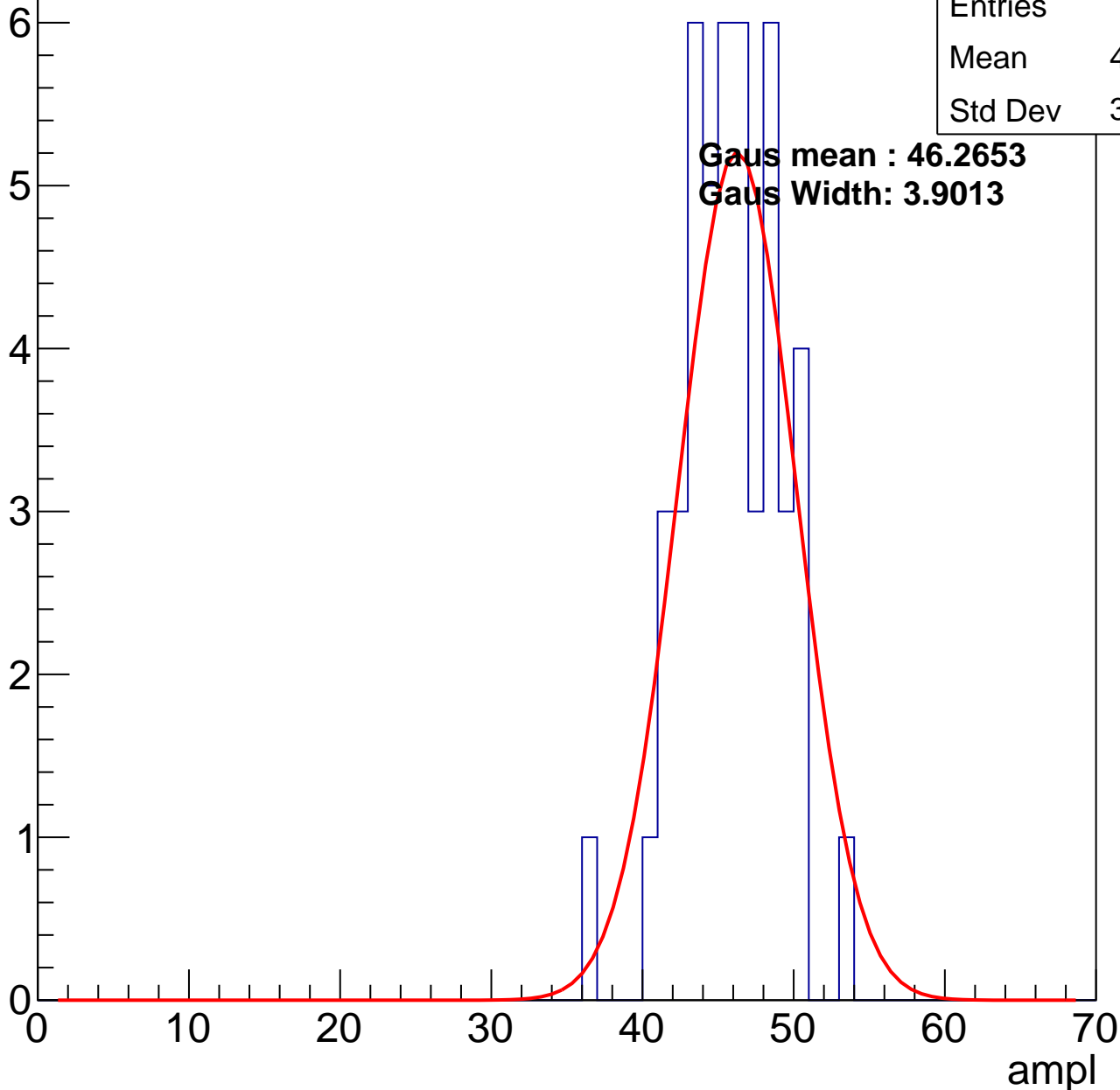
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	45.38
Std Dev	3.186

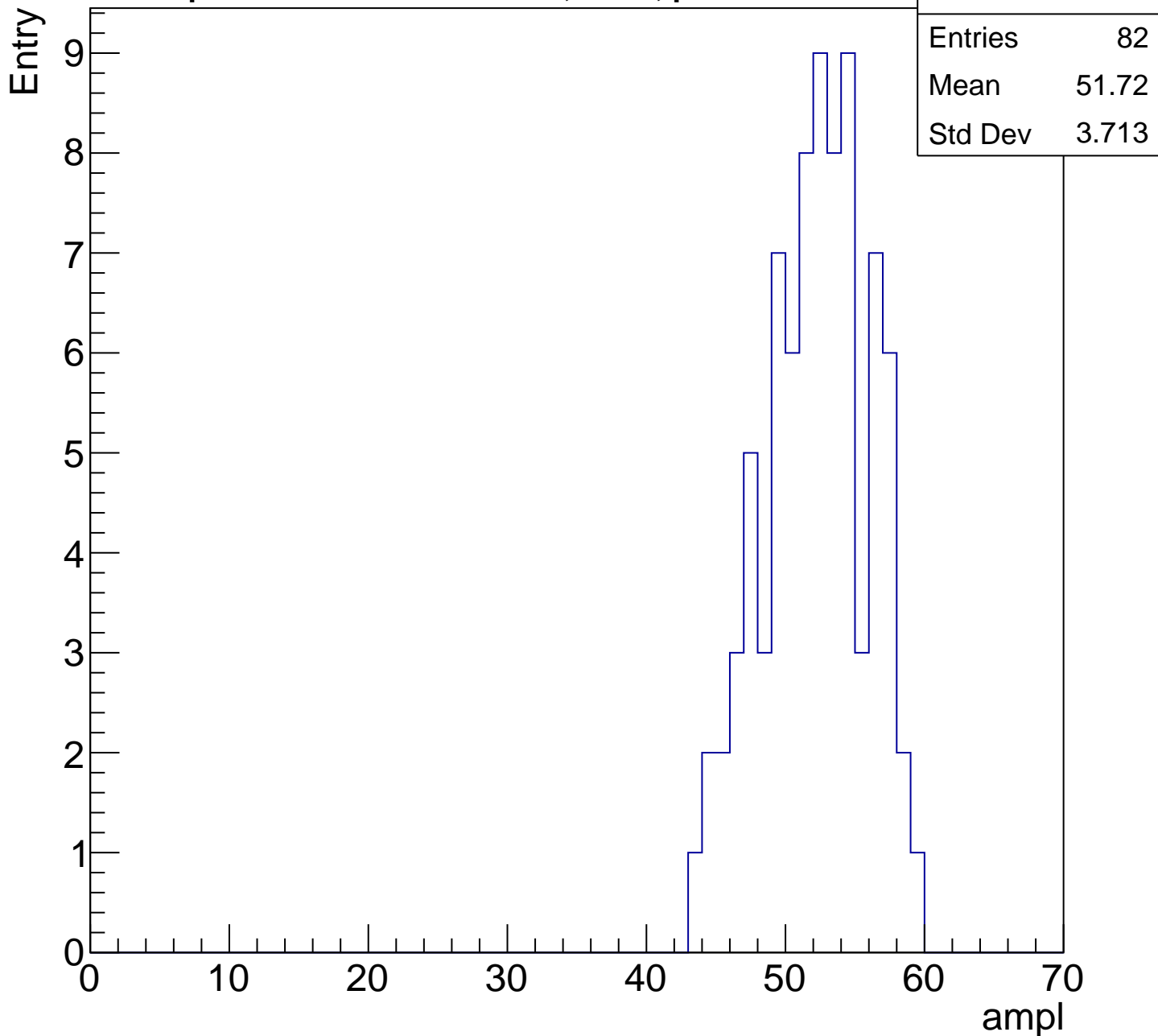
Gaus mean : 46.2653

Gaus Width: 3.9013



B1L101S, U26-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

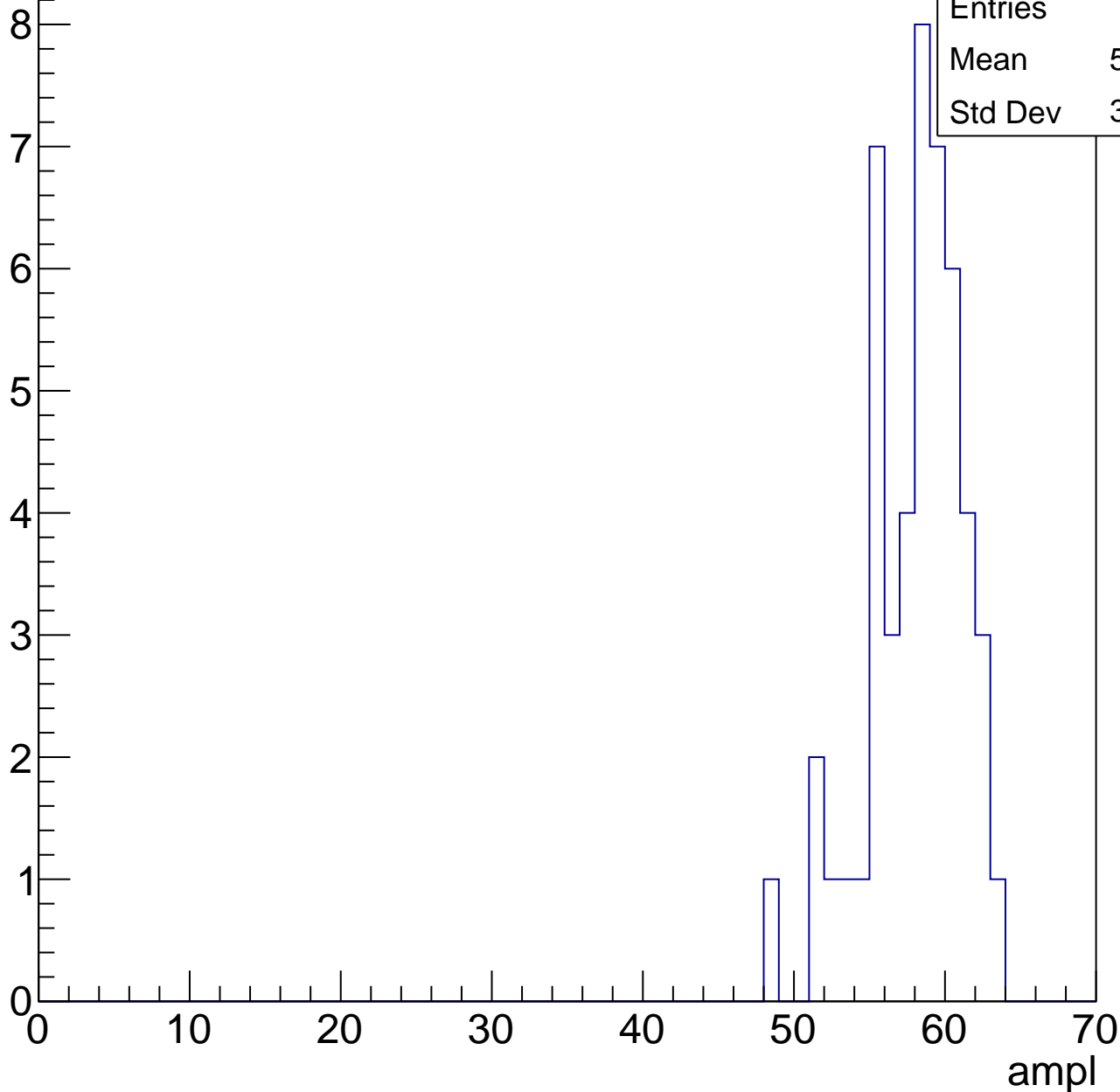


B1L101S, U26-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

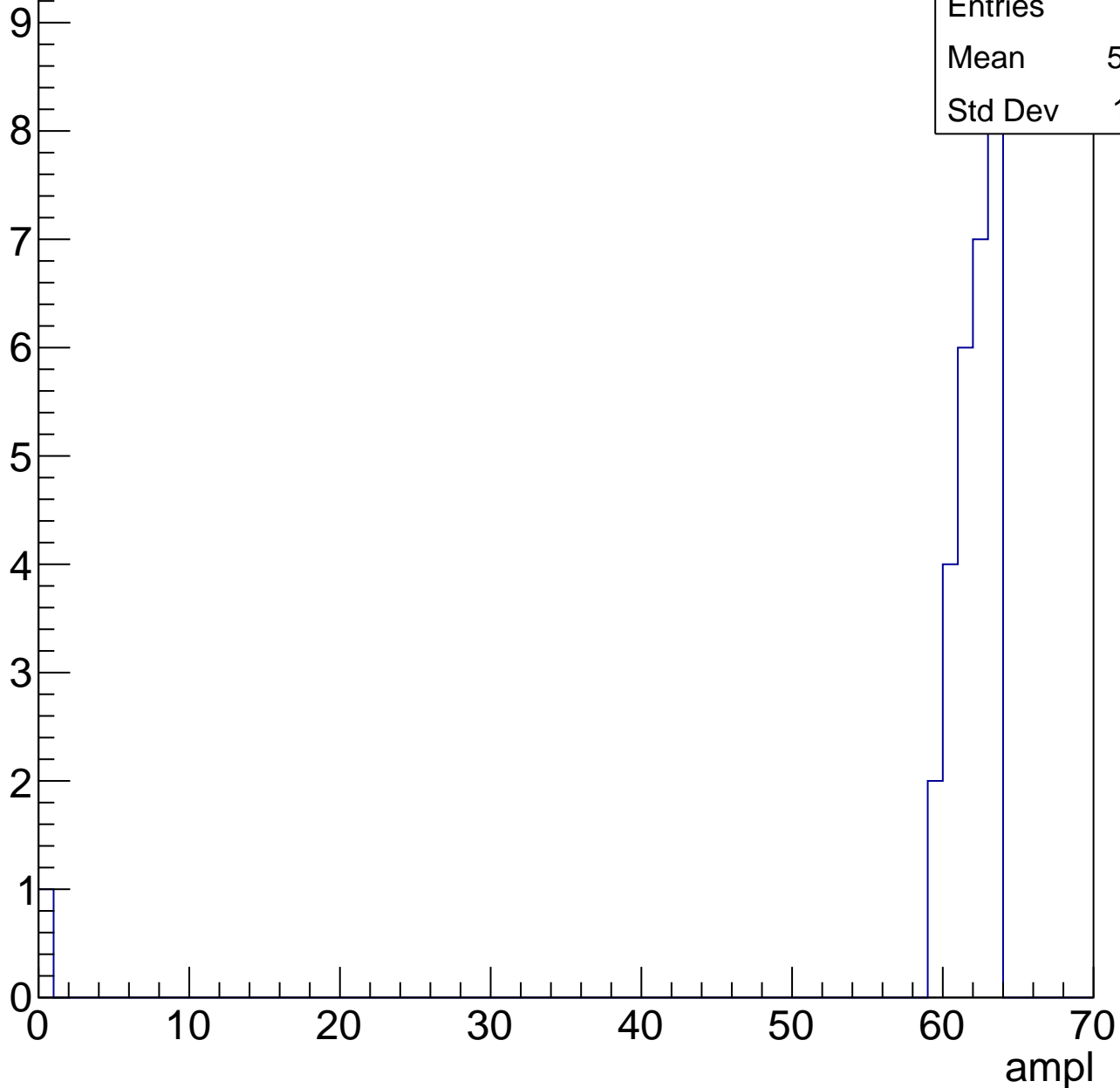
Entries	49
Mean	57.55
Std Dev	3.143



B1L101S, U26-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch23, adc0

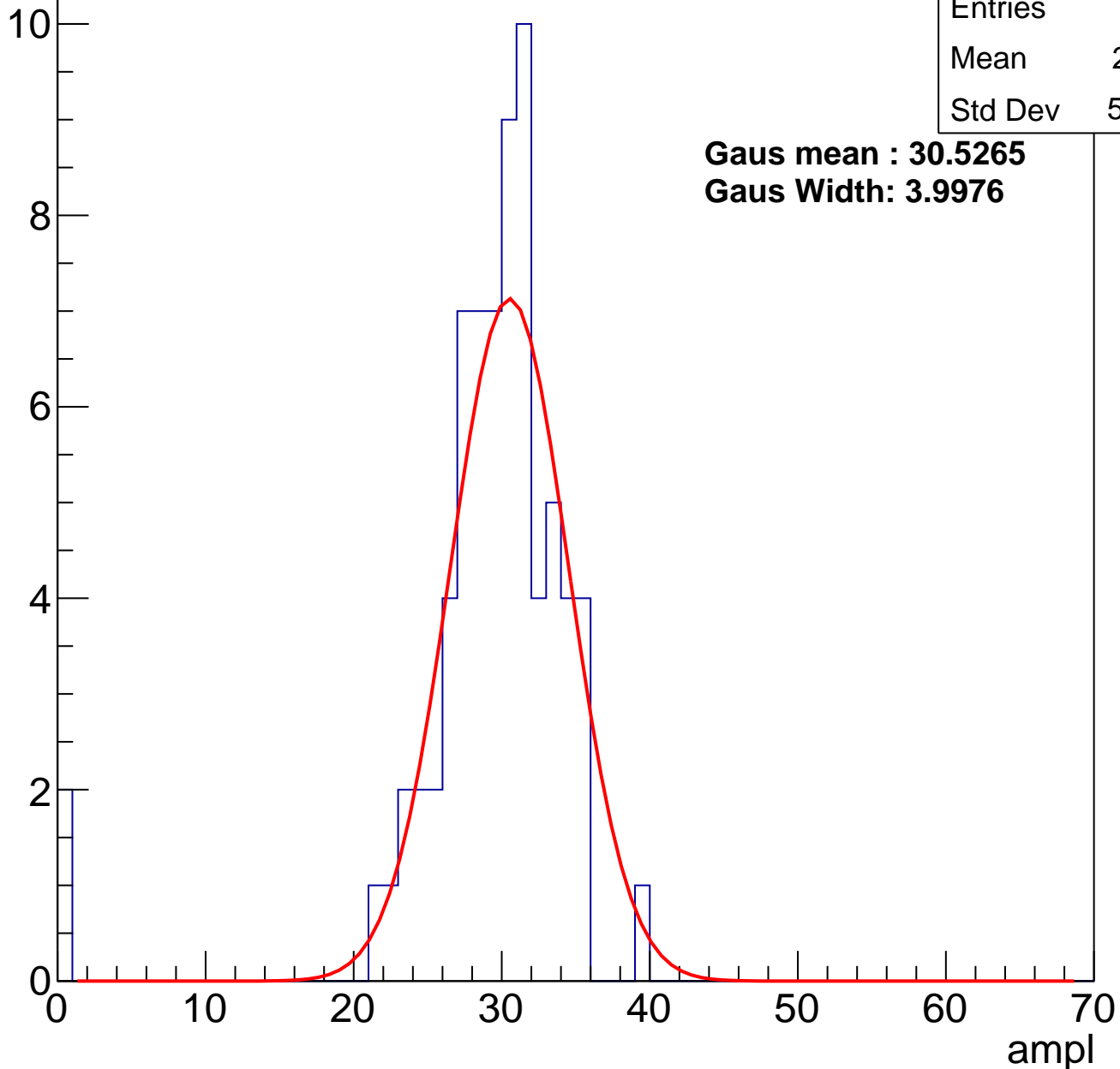
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	28.71
Std Dev	5.919

Gaus mean : 30.5265

Gaus Width: 3.9976

Entry



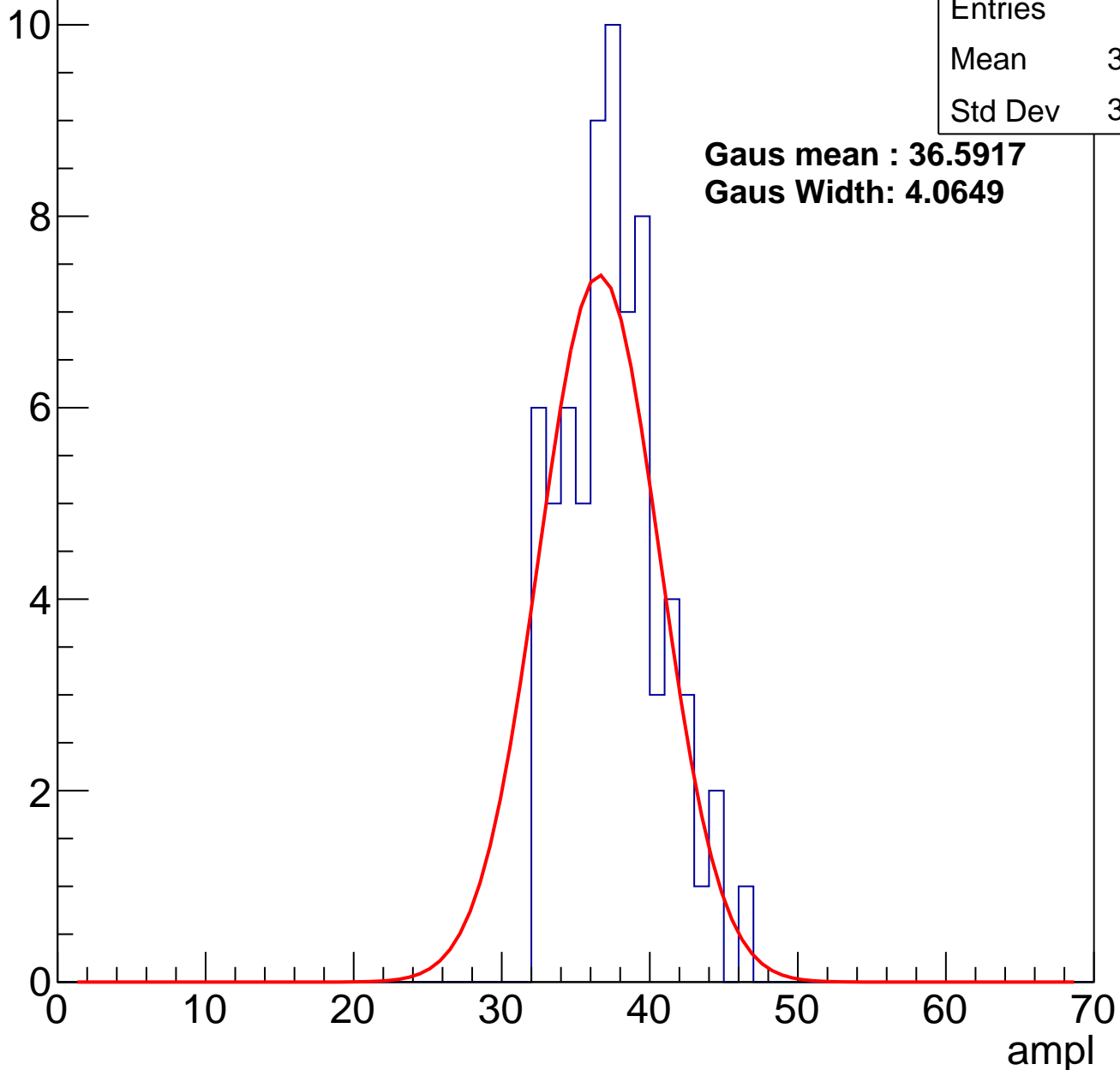
B1L101S, U26-ch23, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	37.07
Std Dev	3.235

Gaus mean : 36.5917
Gaus Width: 4.0649

Entry



B1L101S, U26-ch23, adc2

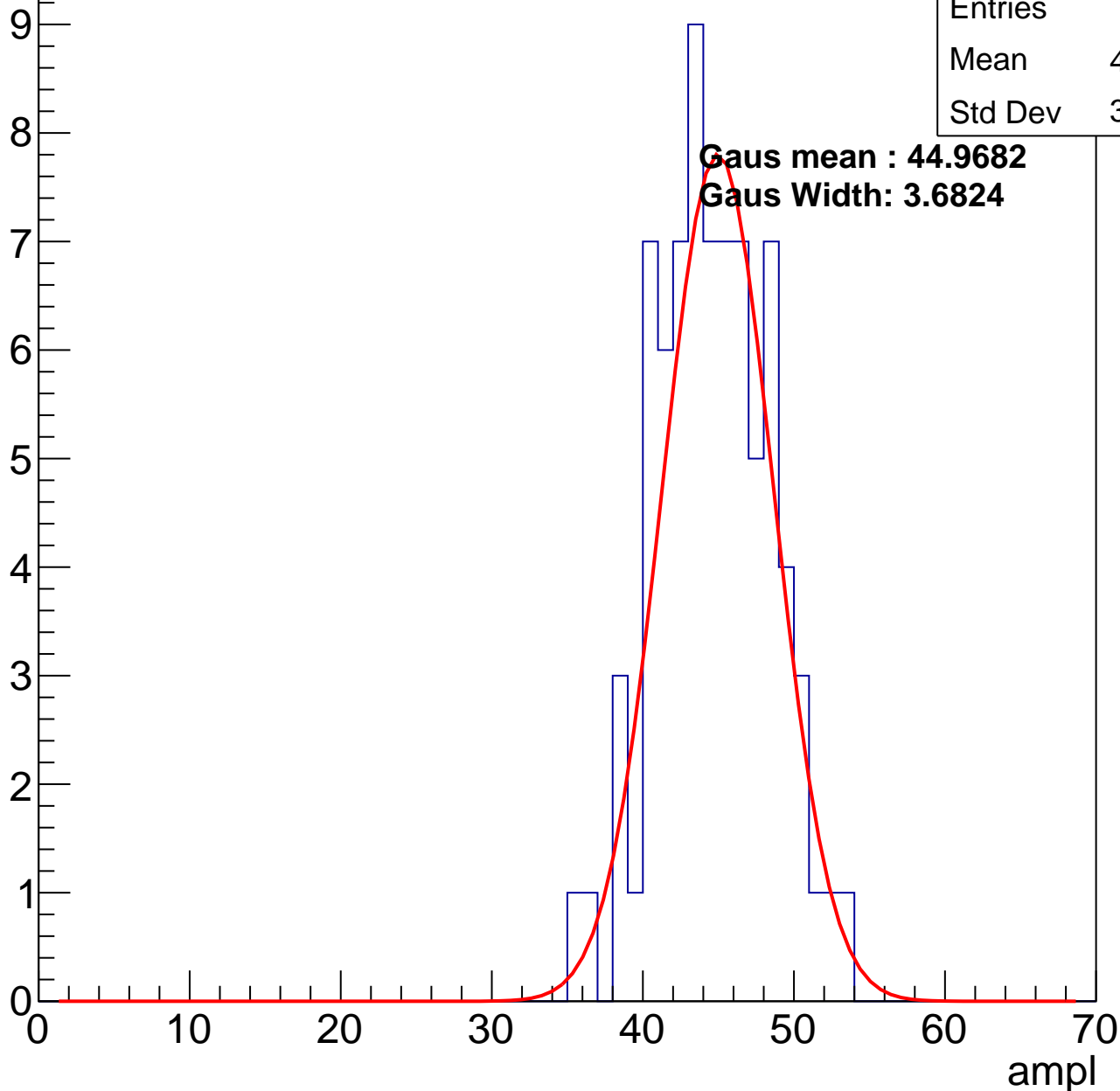
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.22
Std Dev	3.706

Gaus mean : 44.9682

Gaus Width: 3.6824

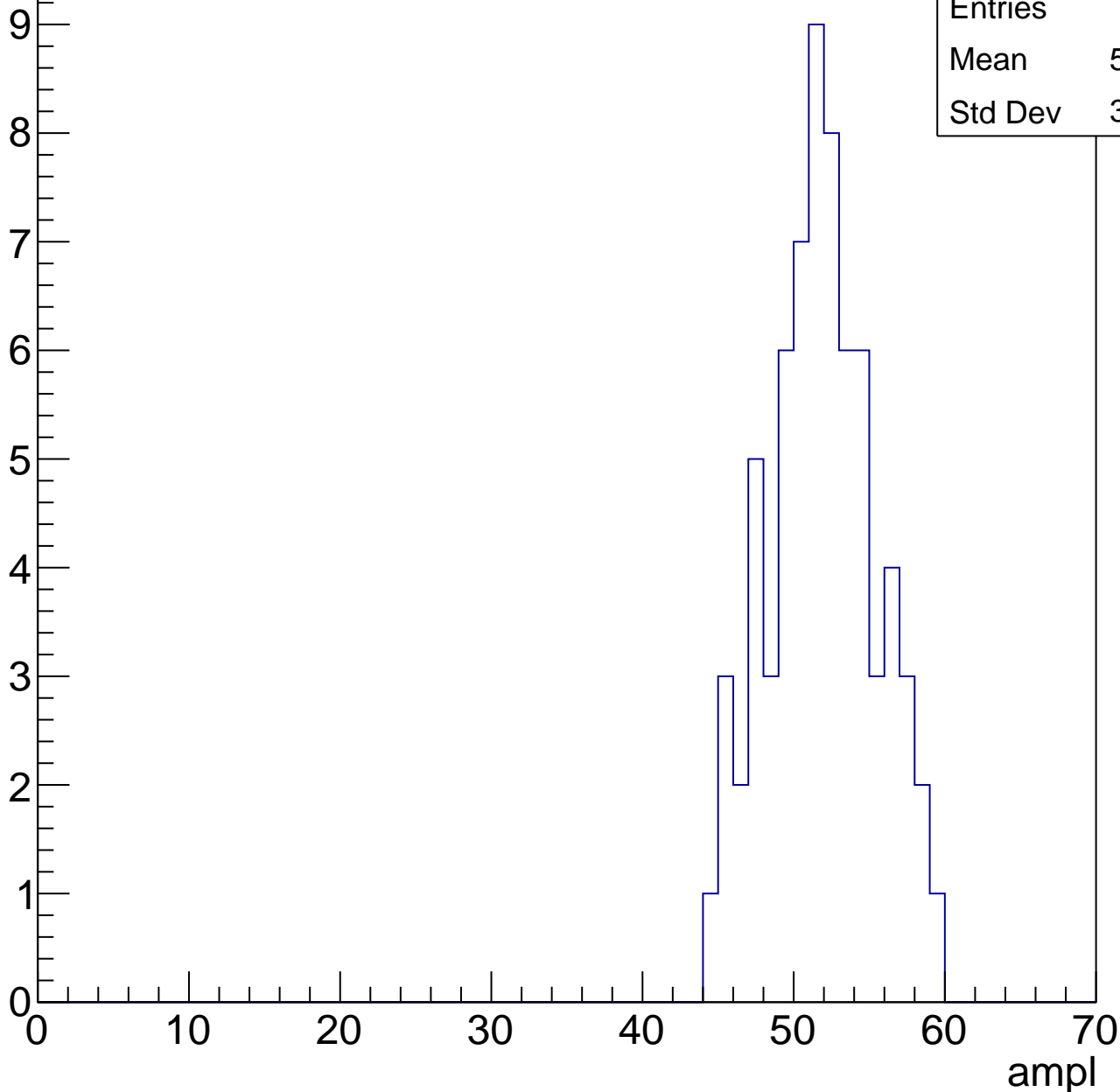


B1L101S, U26-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	51.39
Std Dev	3.486

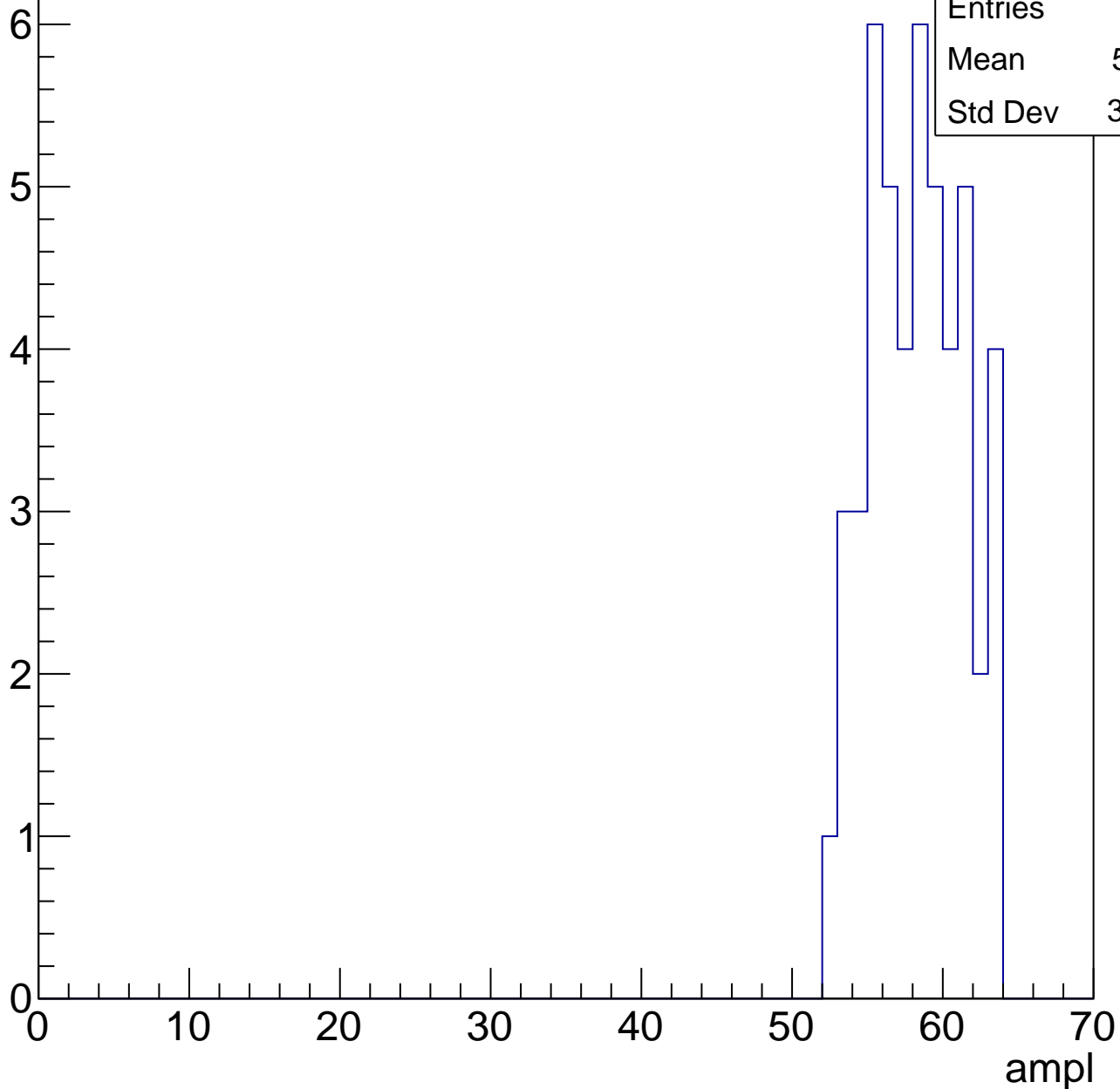


B1L101S, U26-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.81
Std Dev	3.005

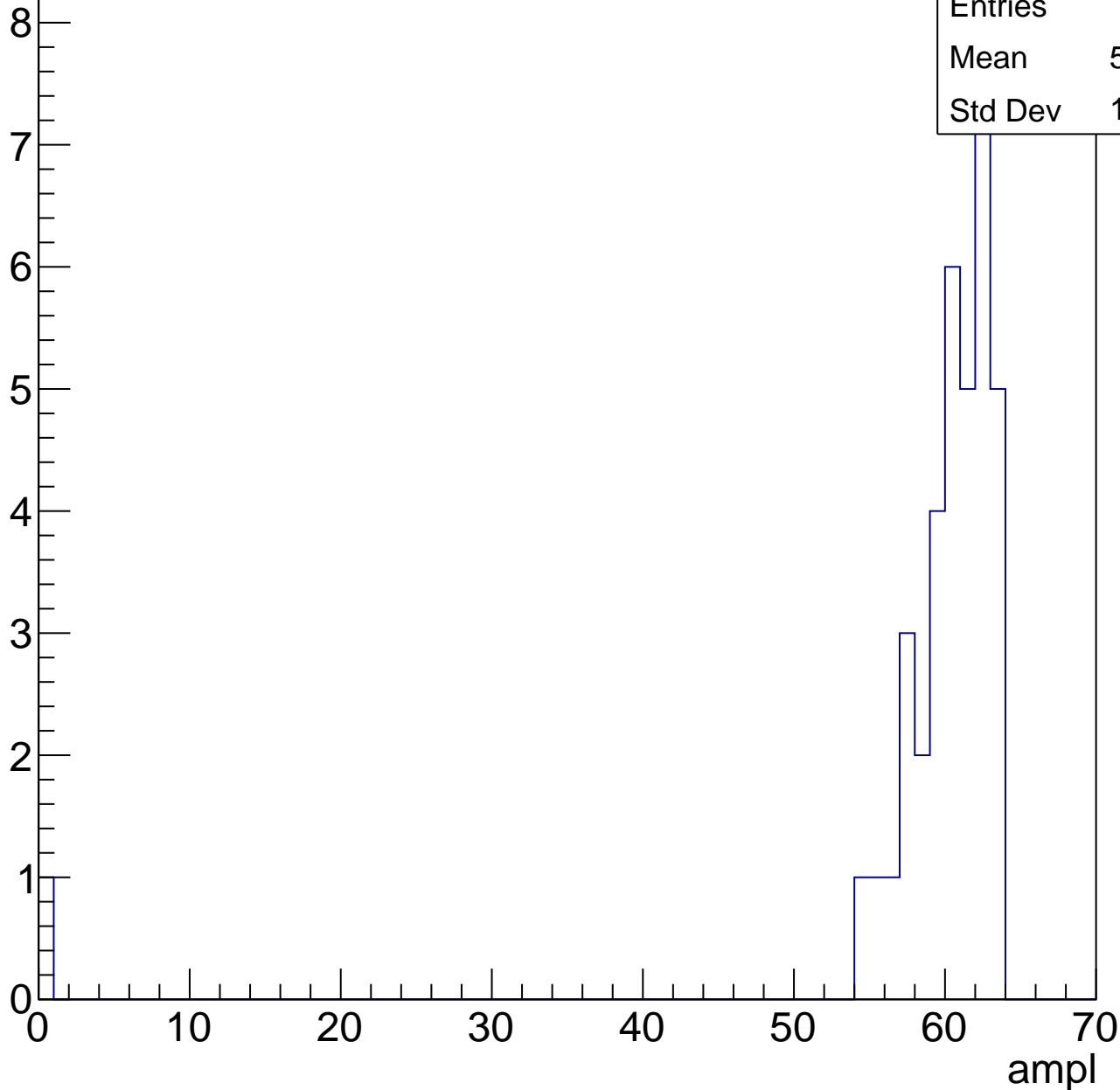


B1L101S, U26-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.49
Std Dev	10.02



B1L101S, U26-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U26-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch24, adc0

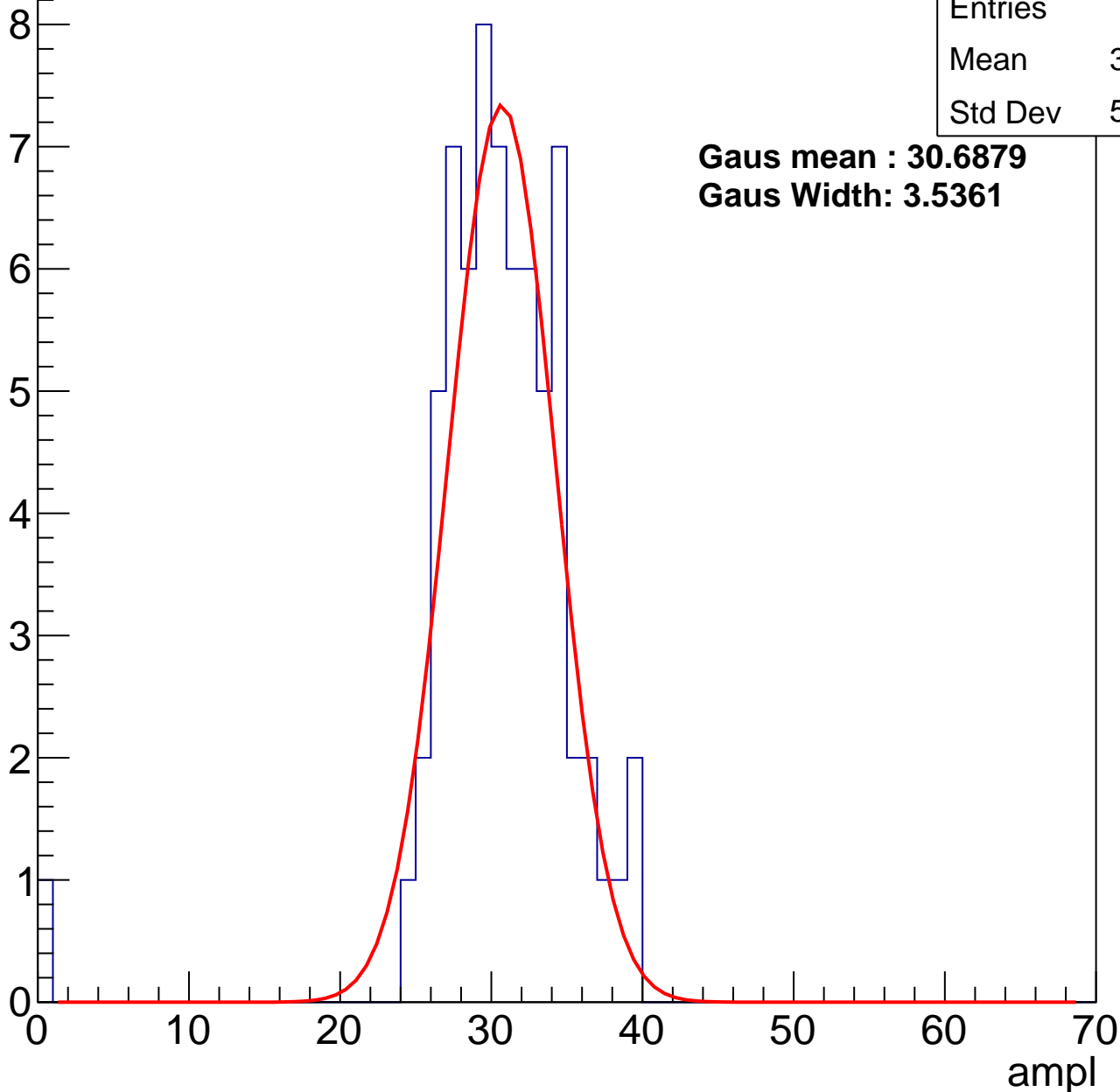
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.13
Std Dev	5.027

Gaus mean : 30.6879

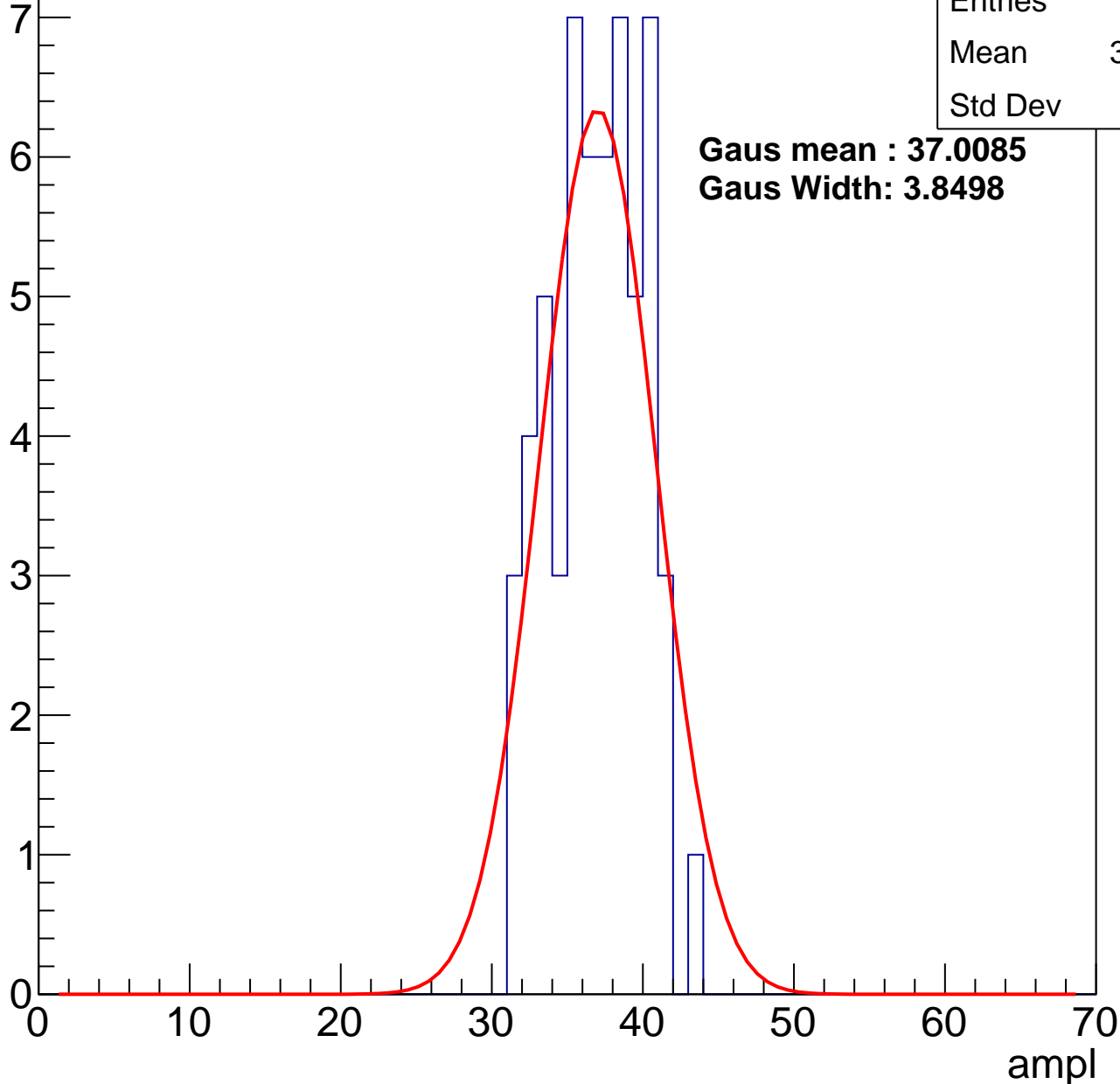
Gaus Width: 3.5361



B1L101S, U26-ch24, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch24, adc2

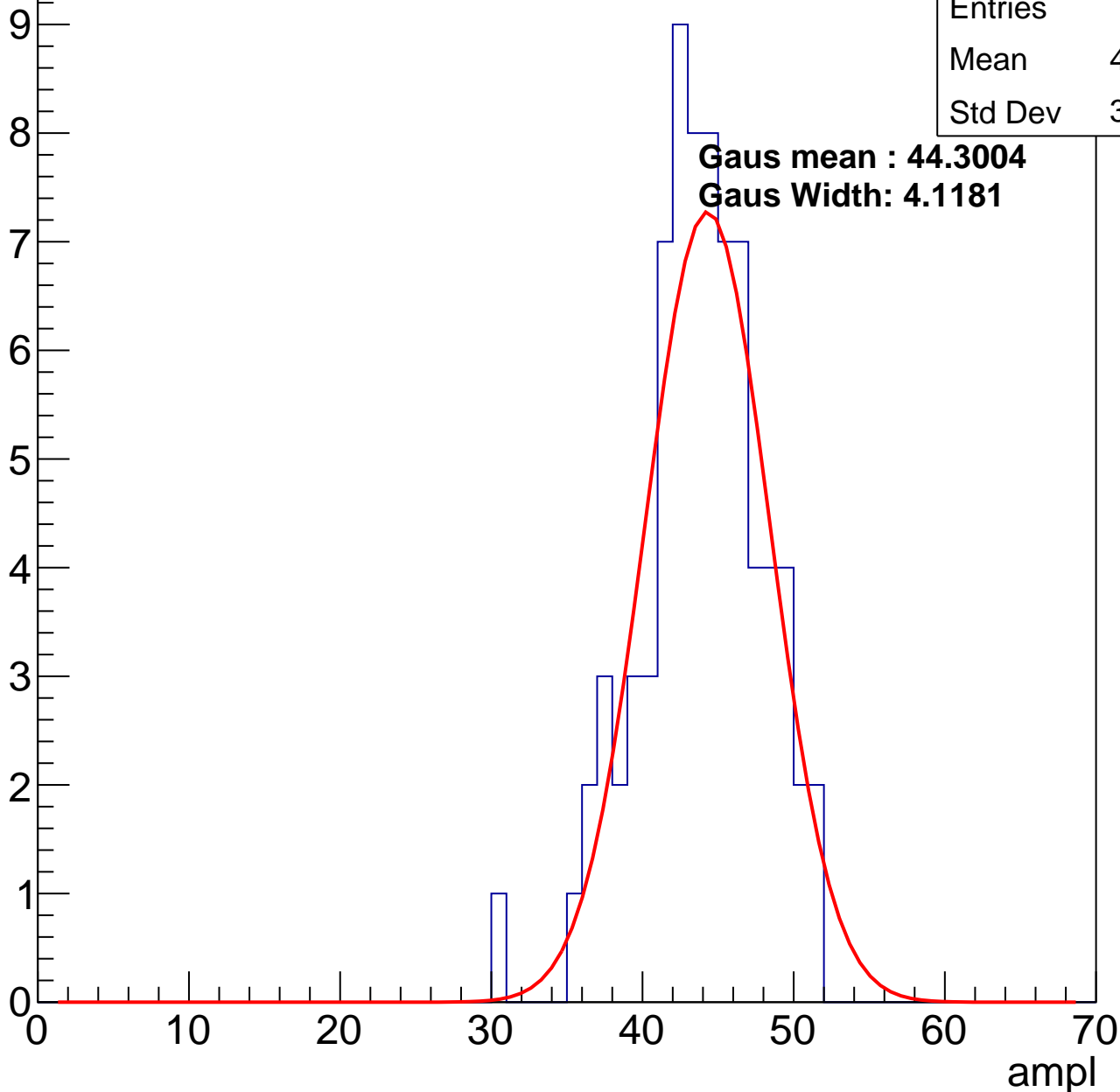
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.34
Std Dev	3.989

Gaus mean : 44.3004

Gaus Width: 4.1181

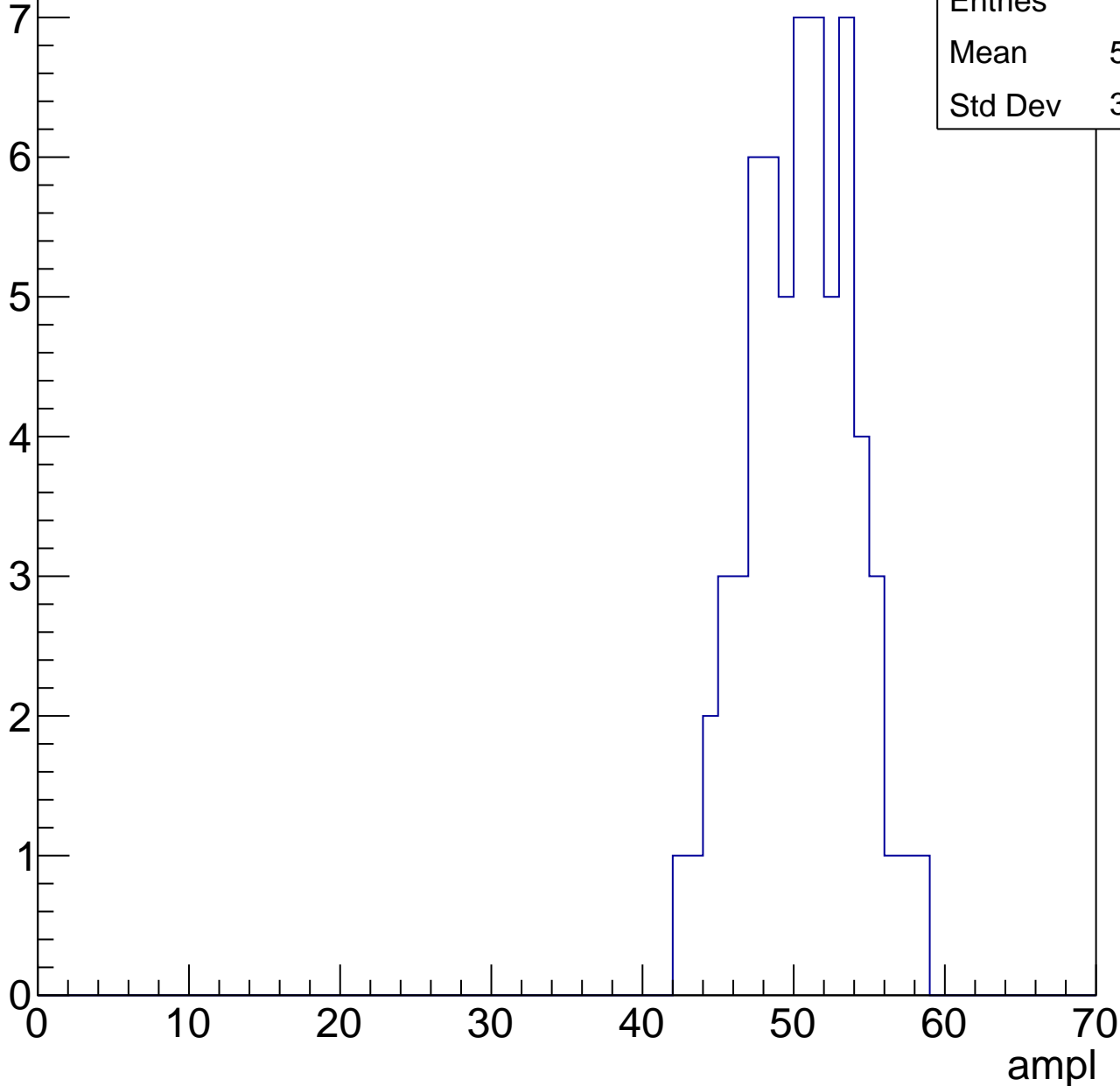


B1L101S, U26-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.02
Std Dev	3.494

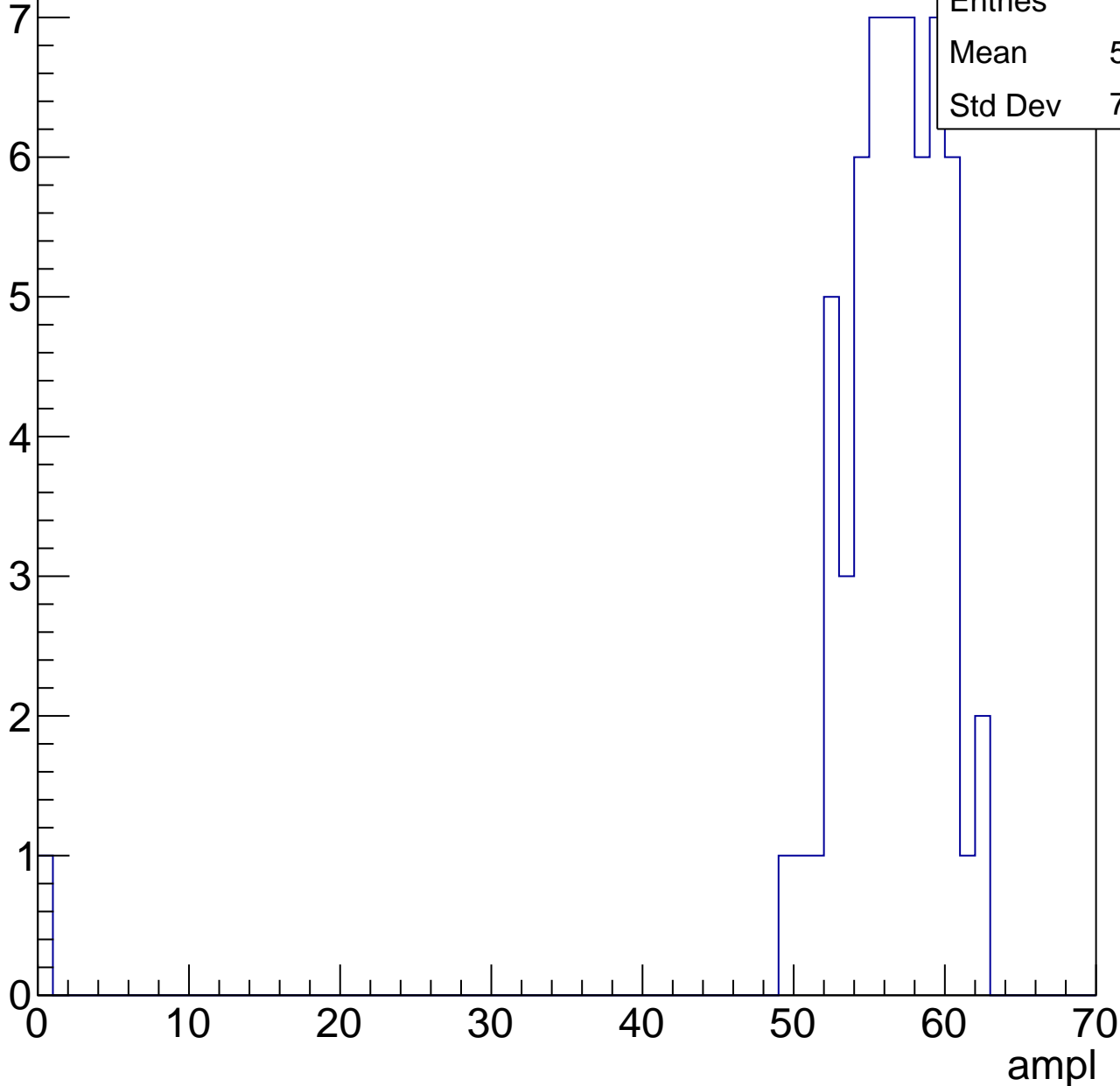


B1L101S, U26-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.33
Std Dev	7.728



B1L101S, U26-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	60.62
Std Dev	2.071

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L101S, U26-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch25, adc0

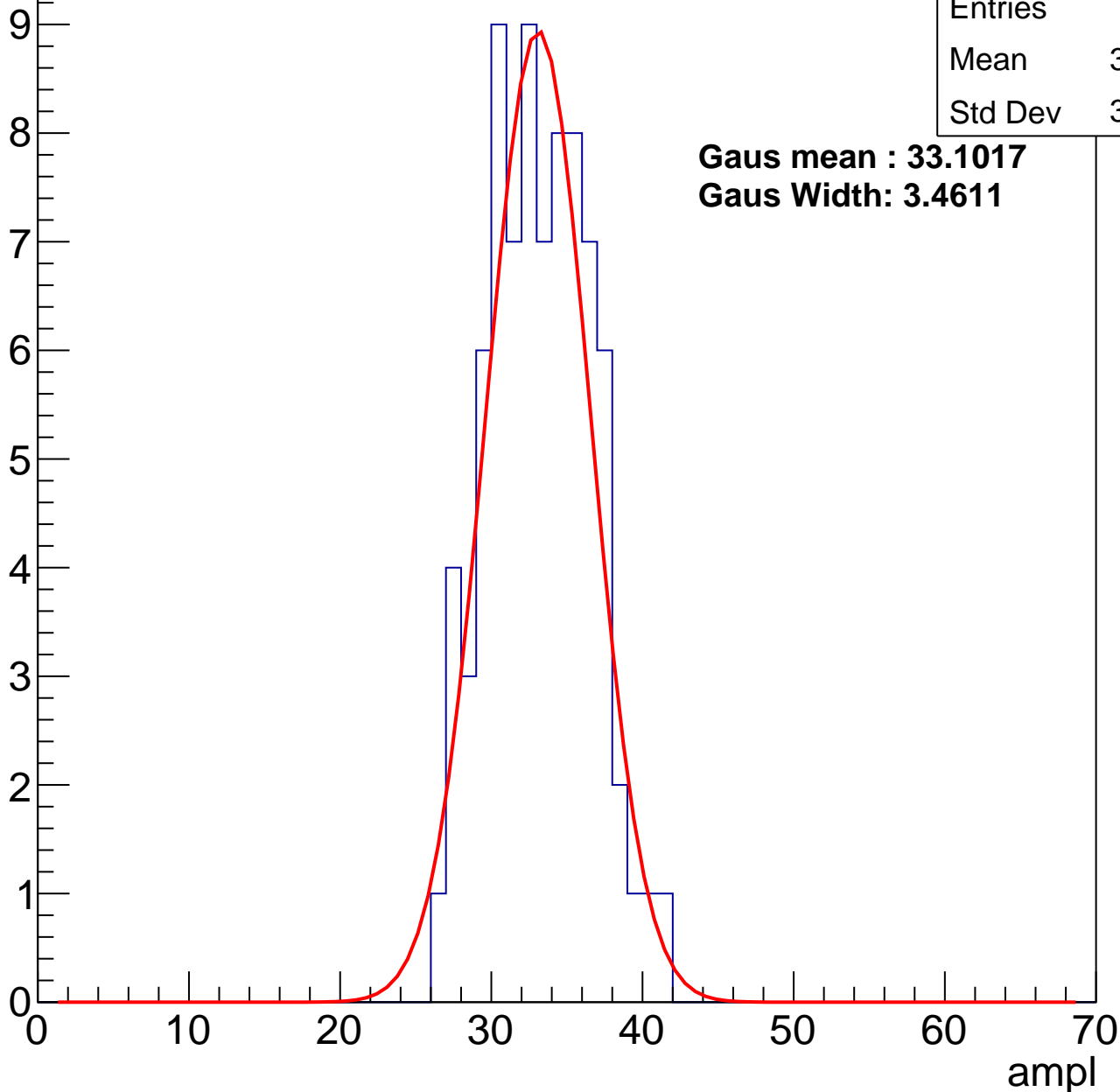
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	32.75
Std Dev	3.307

Gaus mean : 33.1017

Gaus Width: 3.4611



B1L101S, U26-ch25, adc1

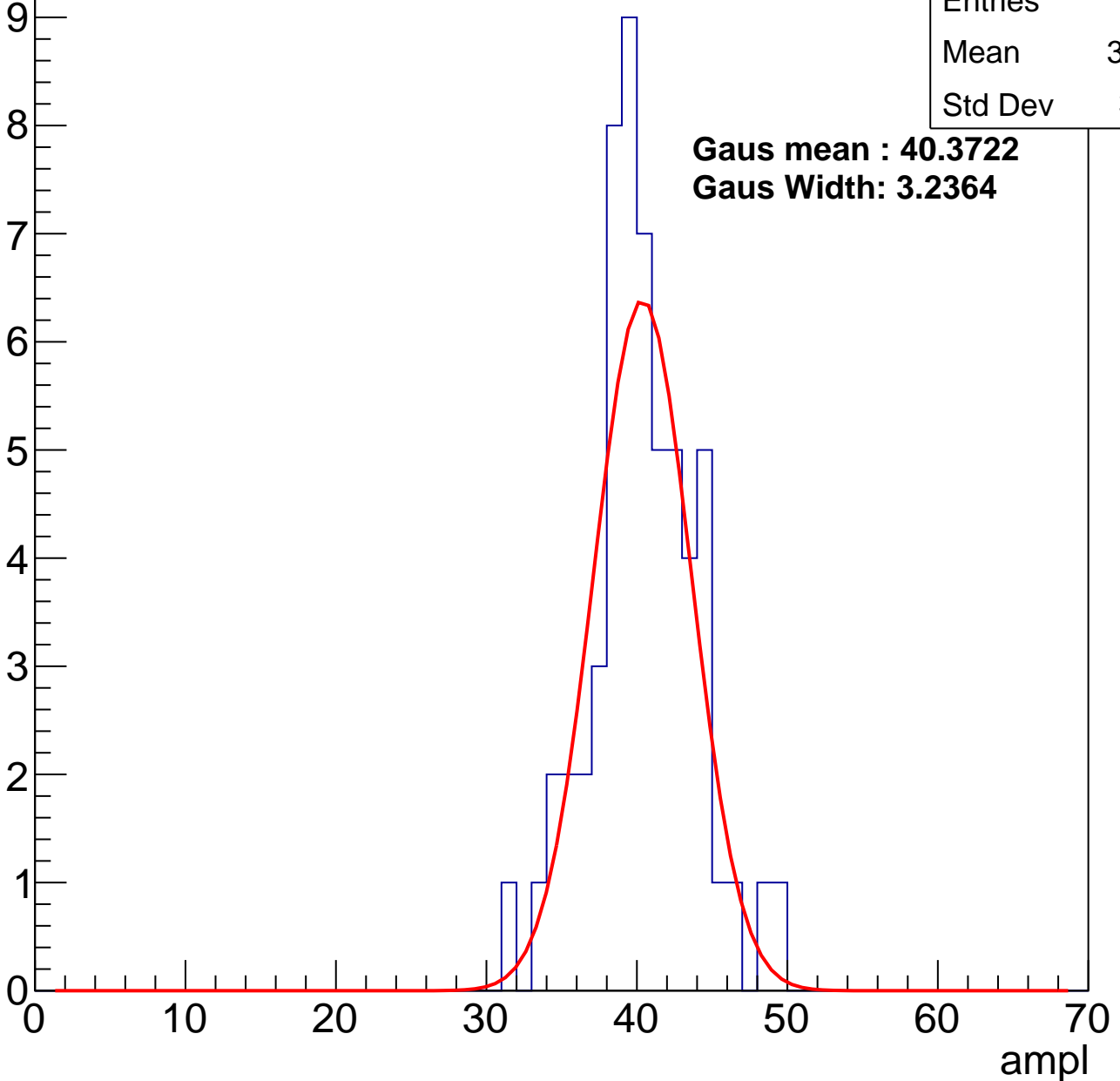
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	39.91
Std Dev	3.47

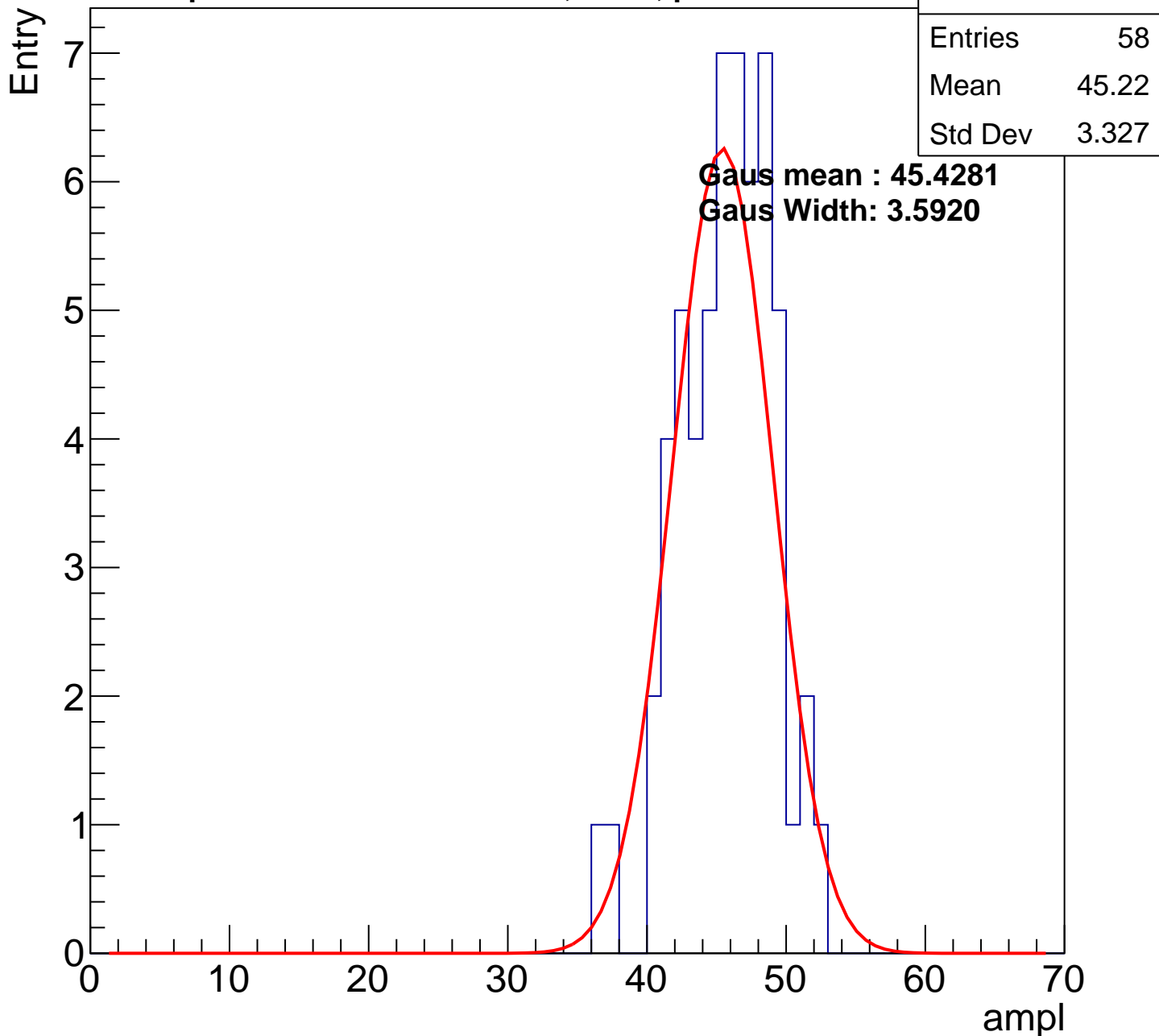
Gaus mean : 40.3722

Gaus Width: 3.2364



B1L101S, U26-ch25, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	52.35
Std Dev	3.874

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

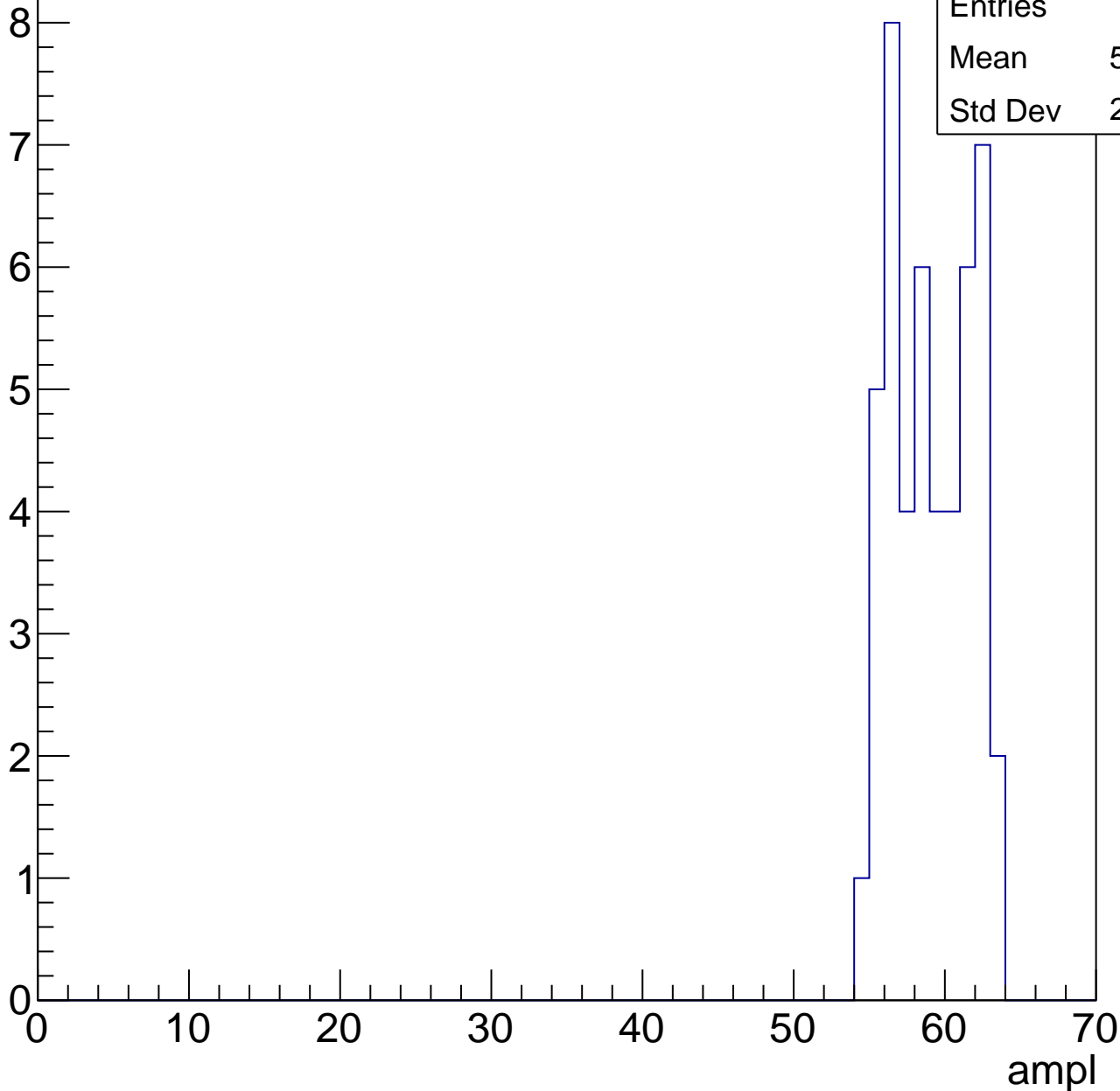
70

B1L101S, U26-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.62
Std Dev	2.589

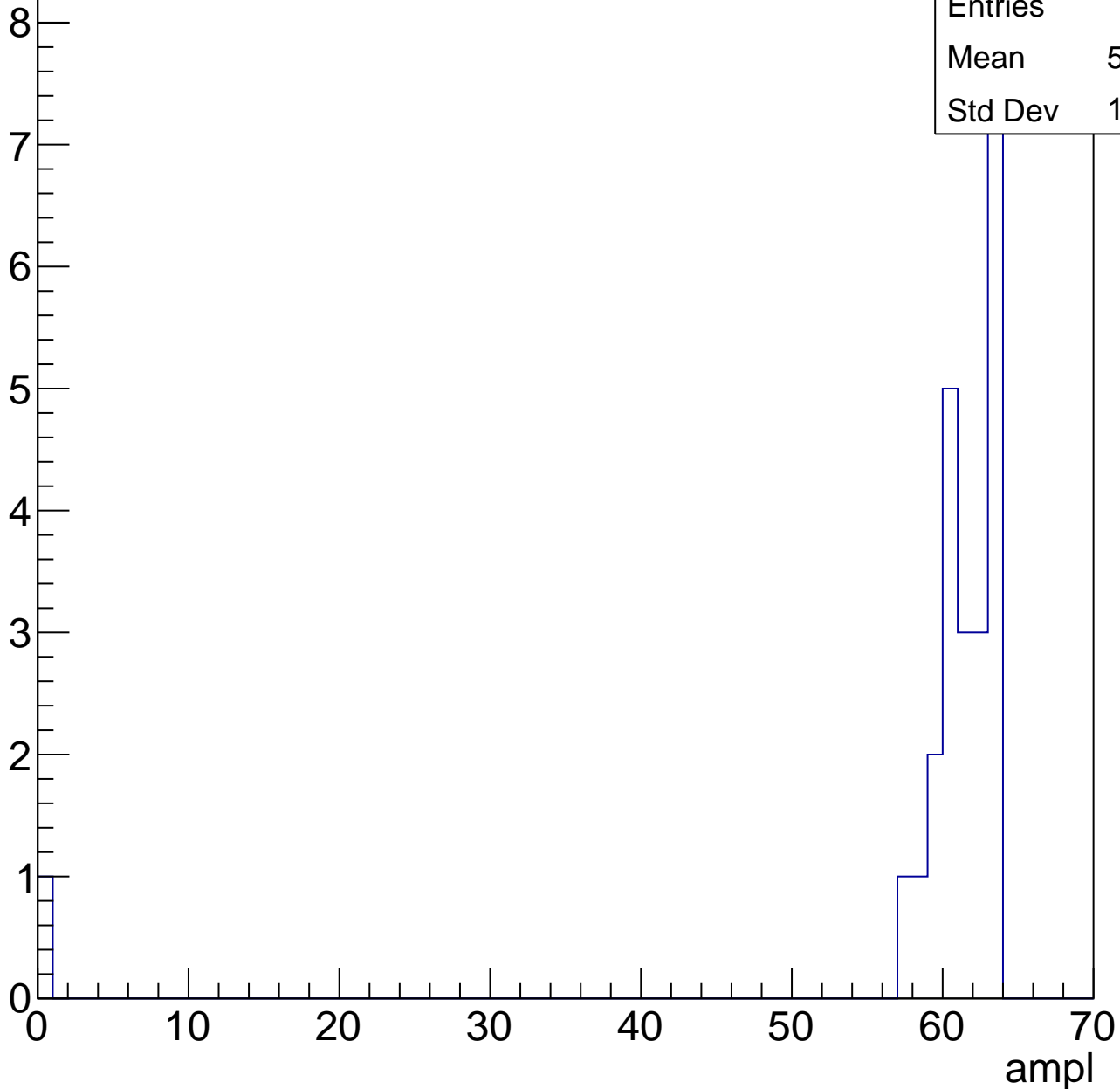


B1L101S, U26-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	58.58
Std Dev	12.34



B1L101S, U26-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

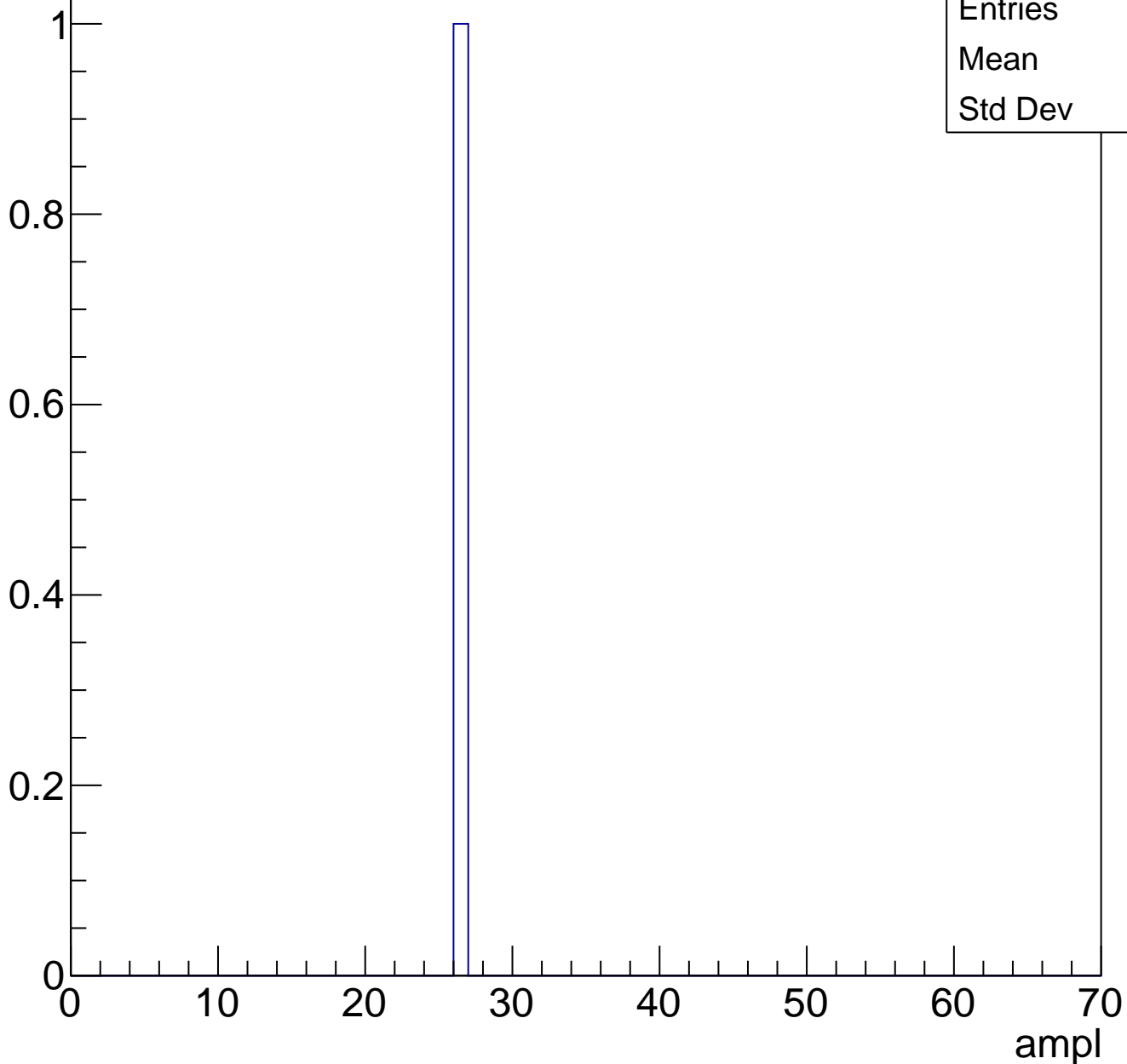
Entry



B1L101S, U26-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch26, adc0

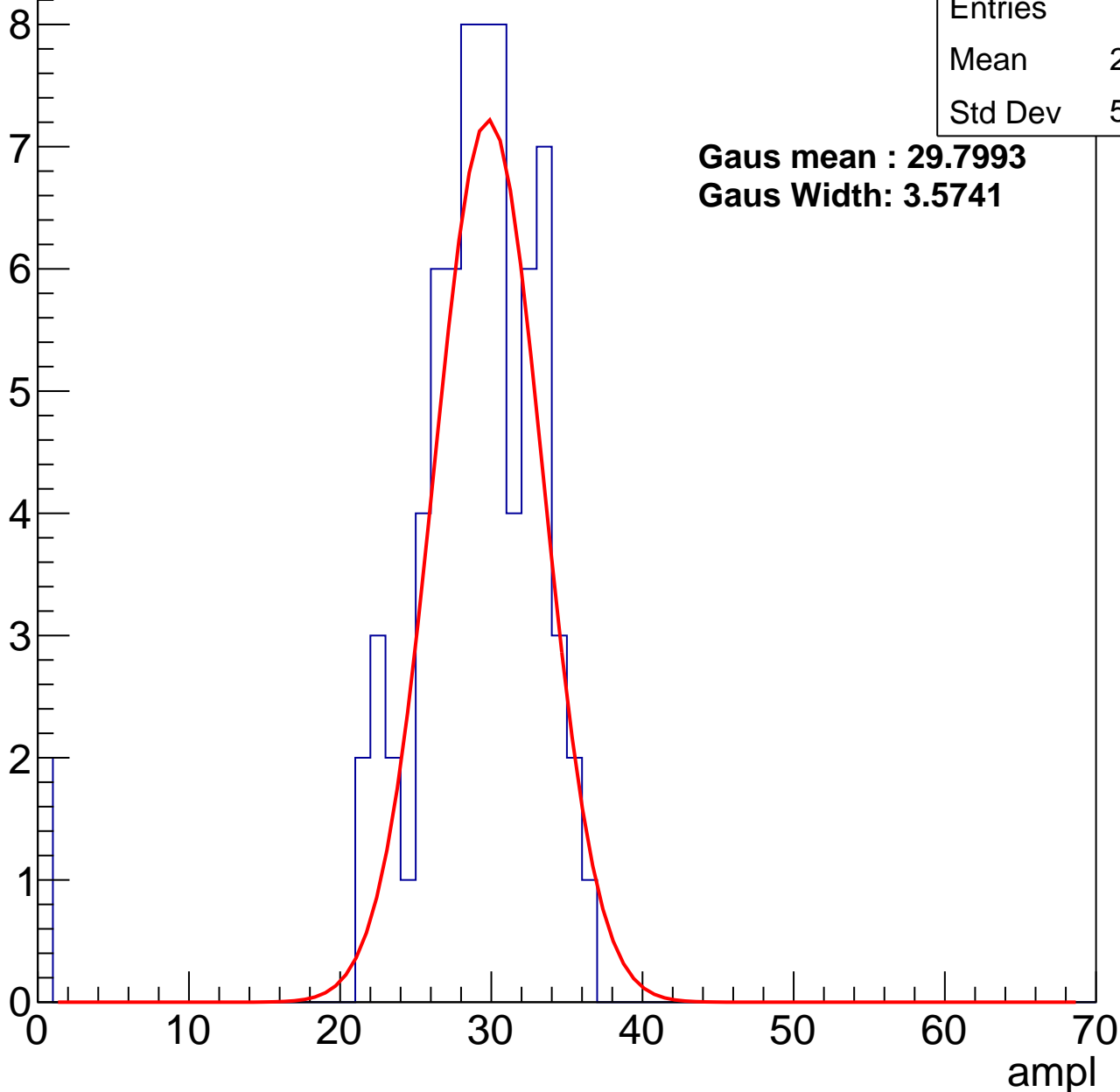
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.04
Std Dev	5.879

Gaus mean : 29.7993

Gaus Width: 3.5741



B1L101S, U26-ch26, adc1

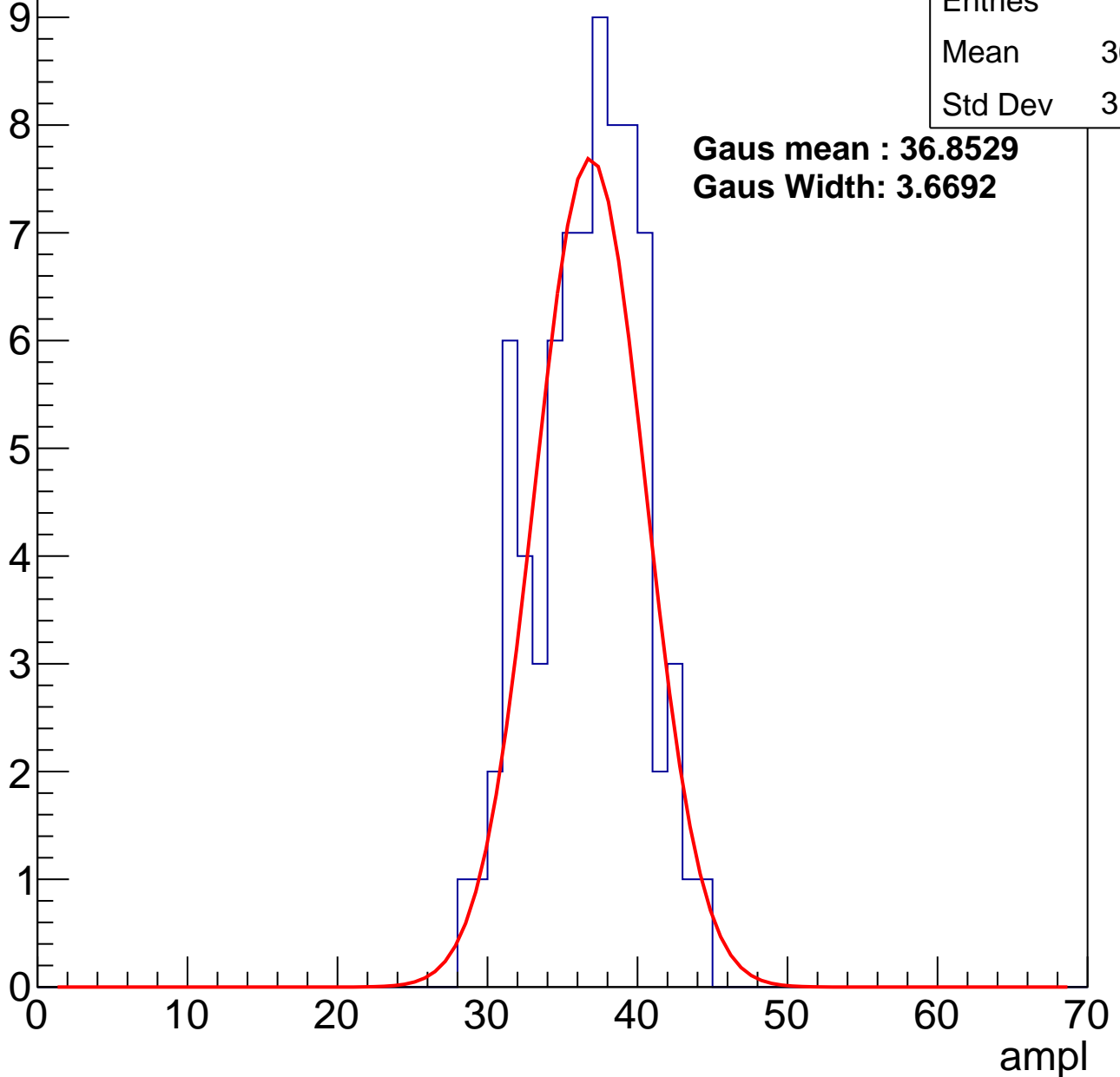
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.25
Std Dev	3.532

Gaus mean : 36.8529

Gaus Width: 3.6692



B1L101S, U26-ch26, adc2

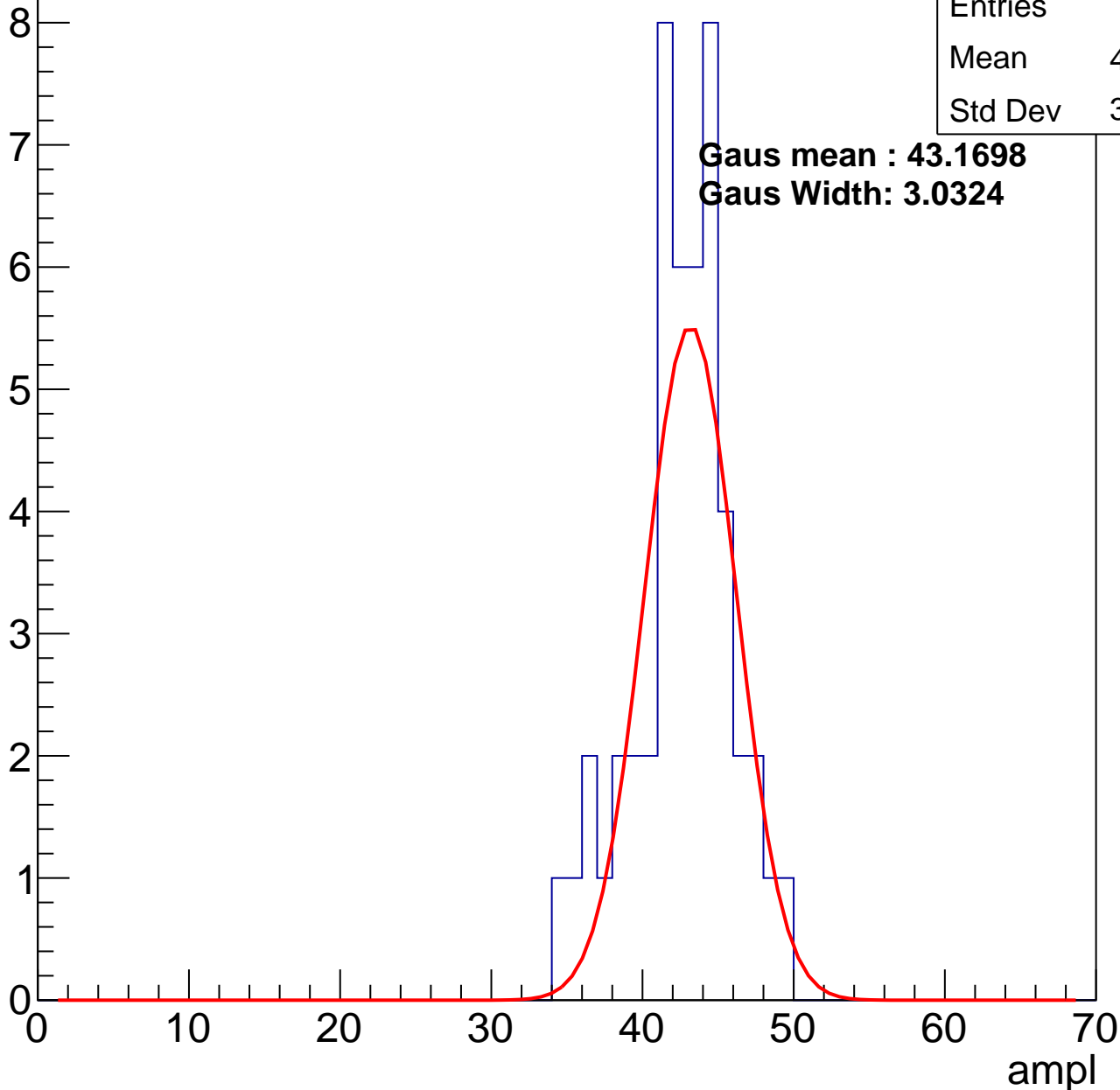
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	42.14
Std Dev	3.245

Gaus mean : 43.1698

Gaus Width: 3.0324

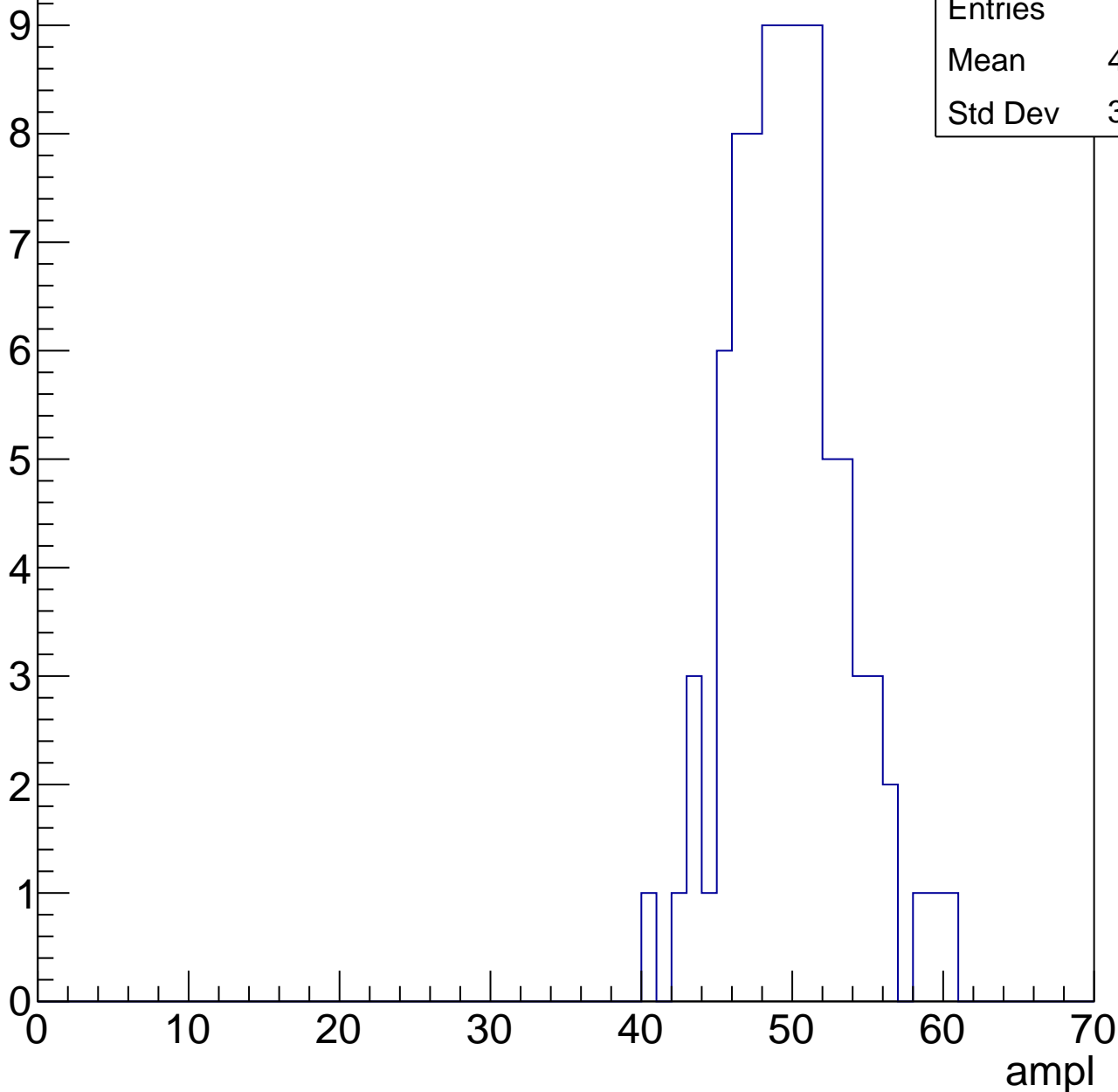


B1L101S, U26-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	49.32
Std Dev	3.783

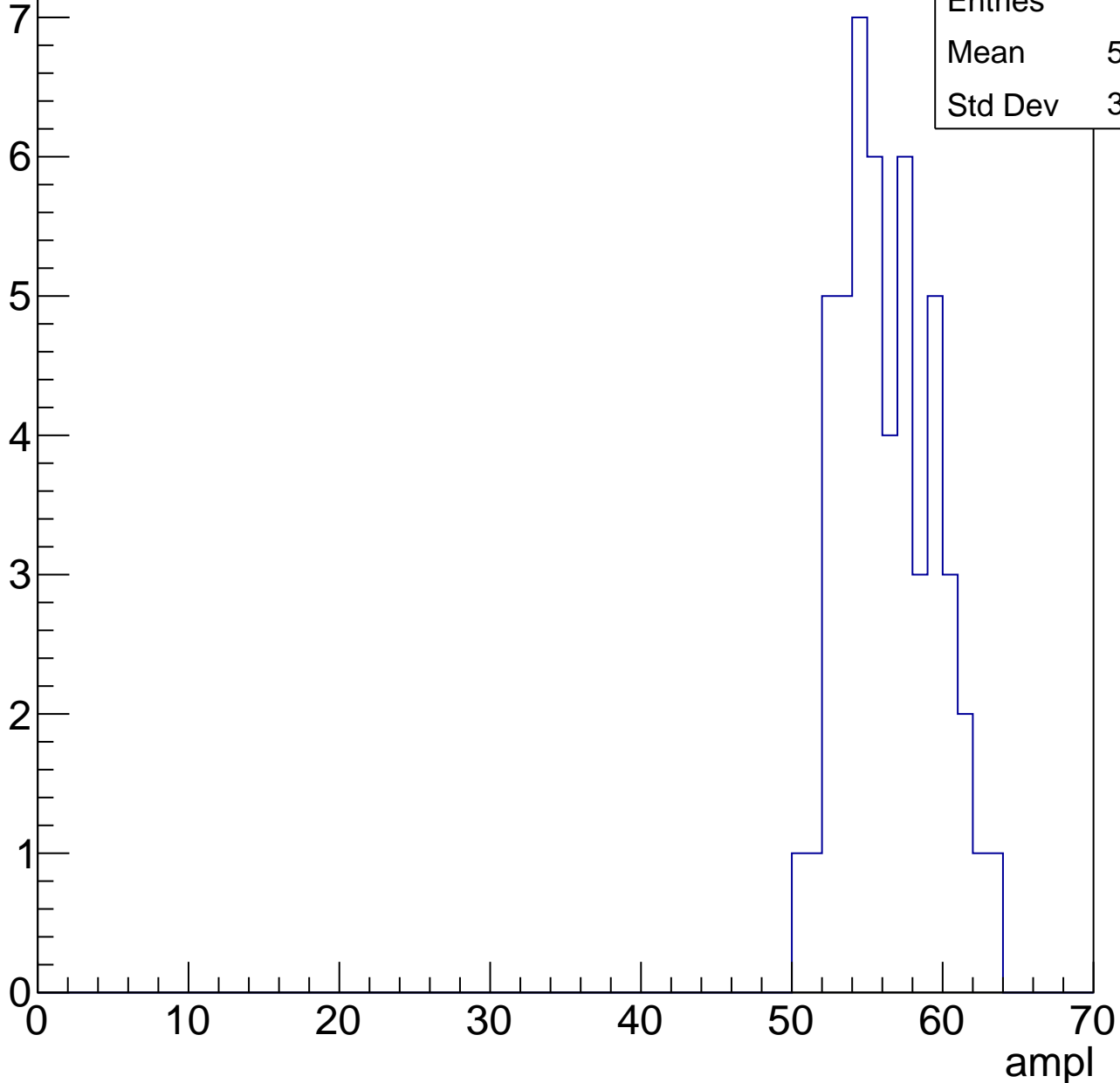


B1L101S, U26-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.92
Std Dev	3.052

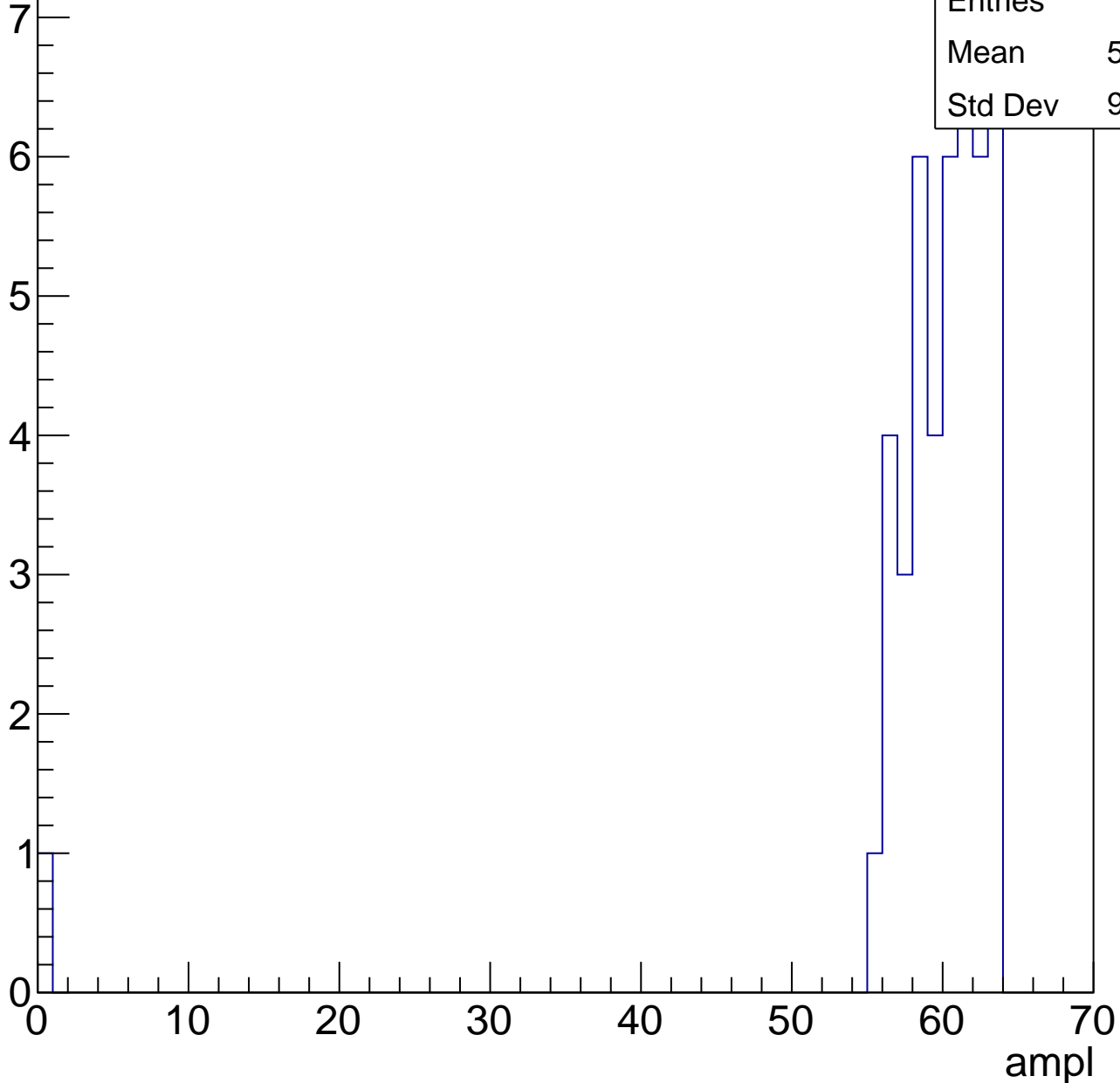


B1L101S, U26-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

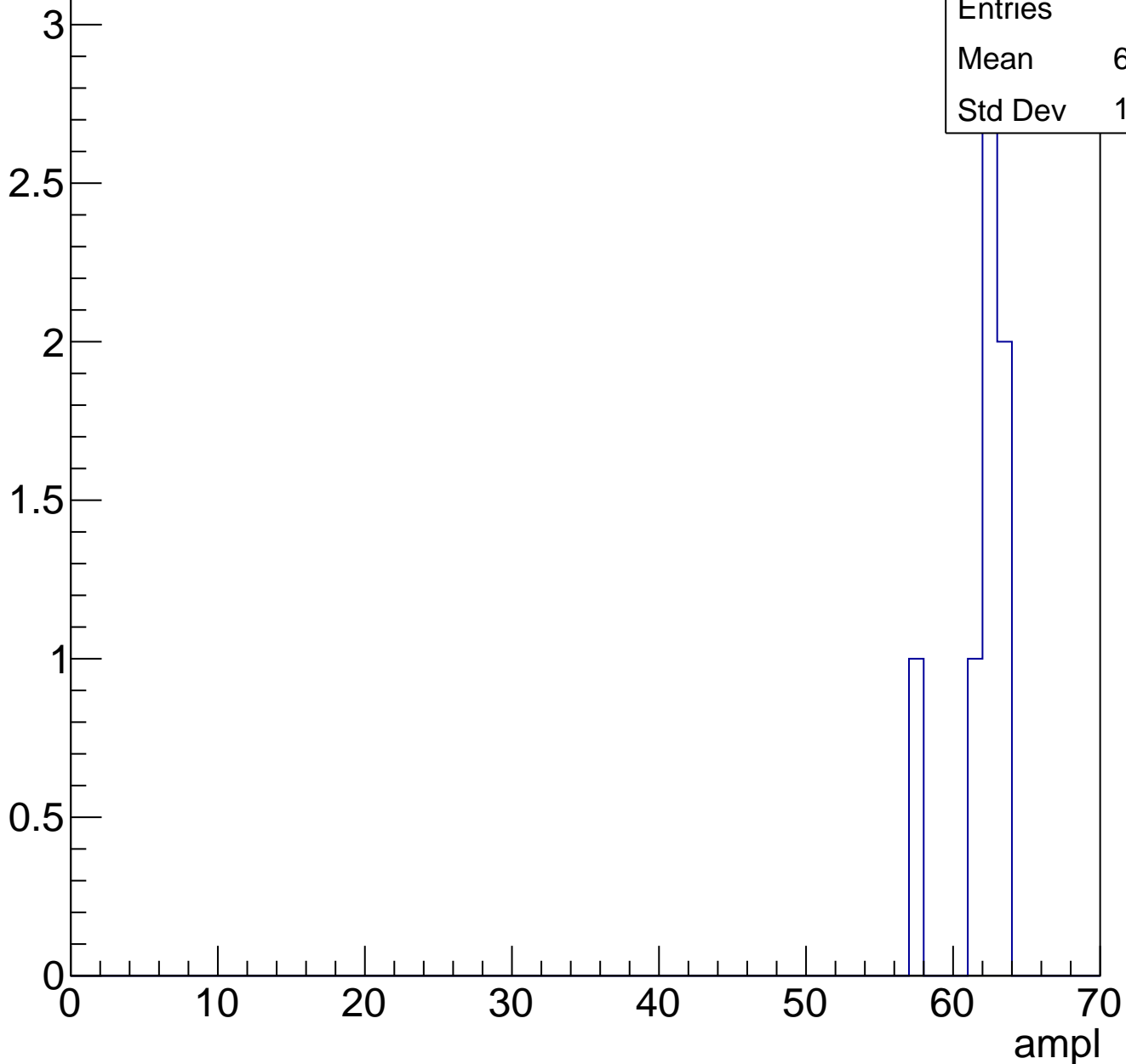
Entries	45
Mean	58.53
Std Dev	9.118



B1L101S, U26-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch27, adc0

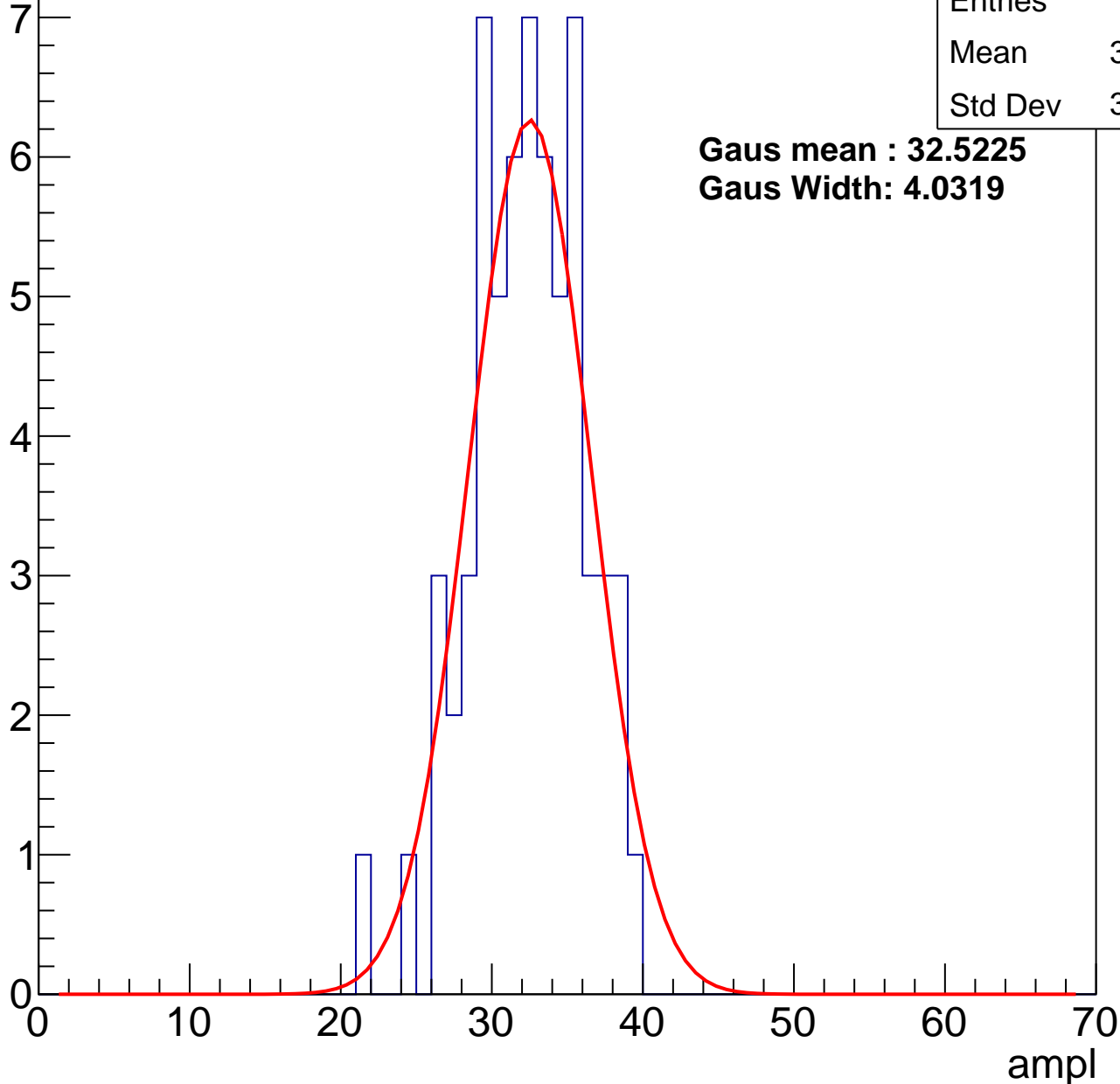
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.89
Std Dev	3.669

Gaus mean : 32.5225

Gaus Width: 4.0319



B1L101S, U26-ch27, adc1

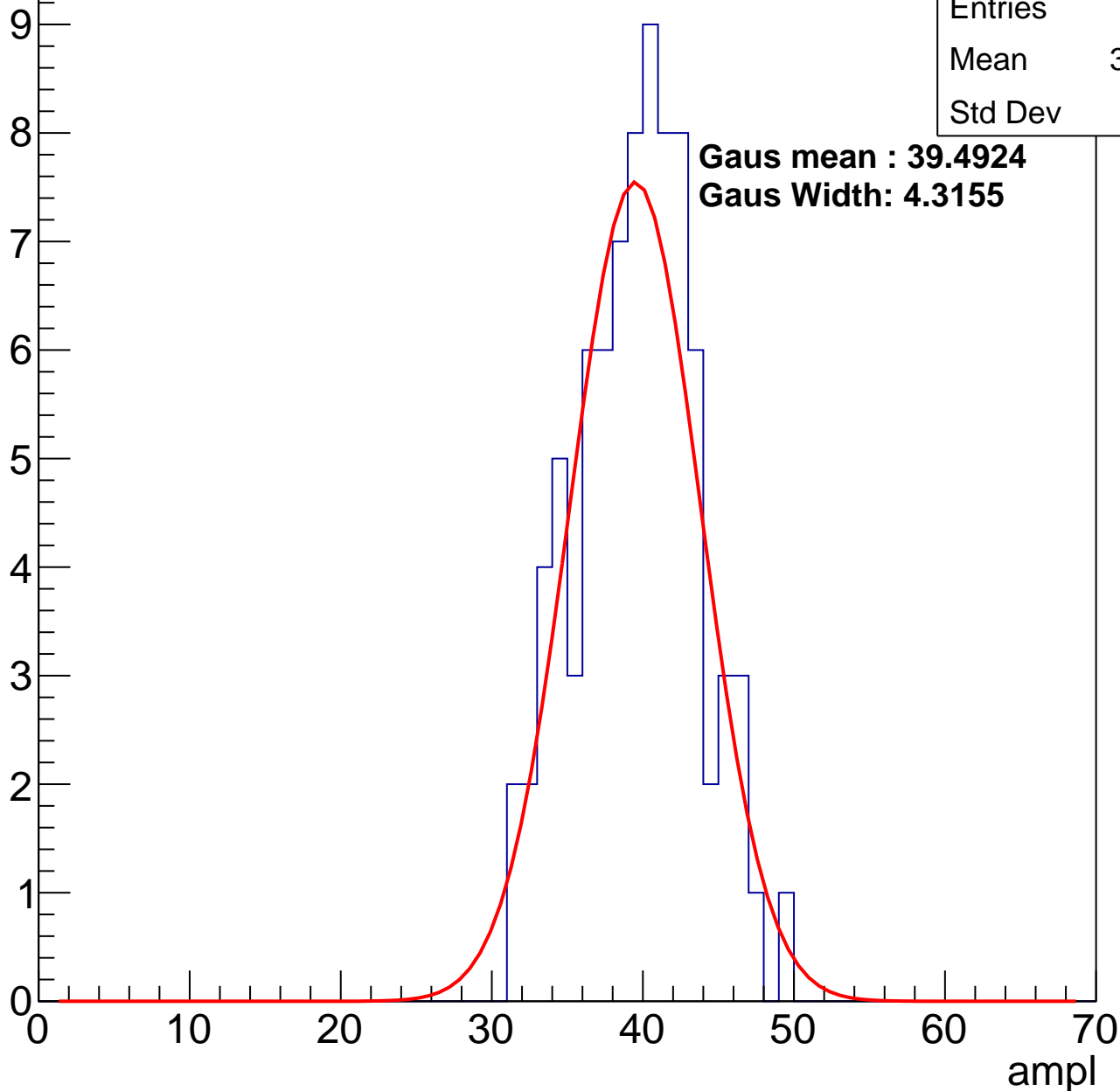
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	39.14
Std Dev	3.94

Gaus mean : 39.4924

Gaus Width: 4.3155



B1L101S, U26-ch27, adc2

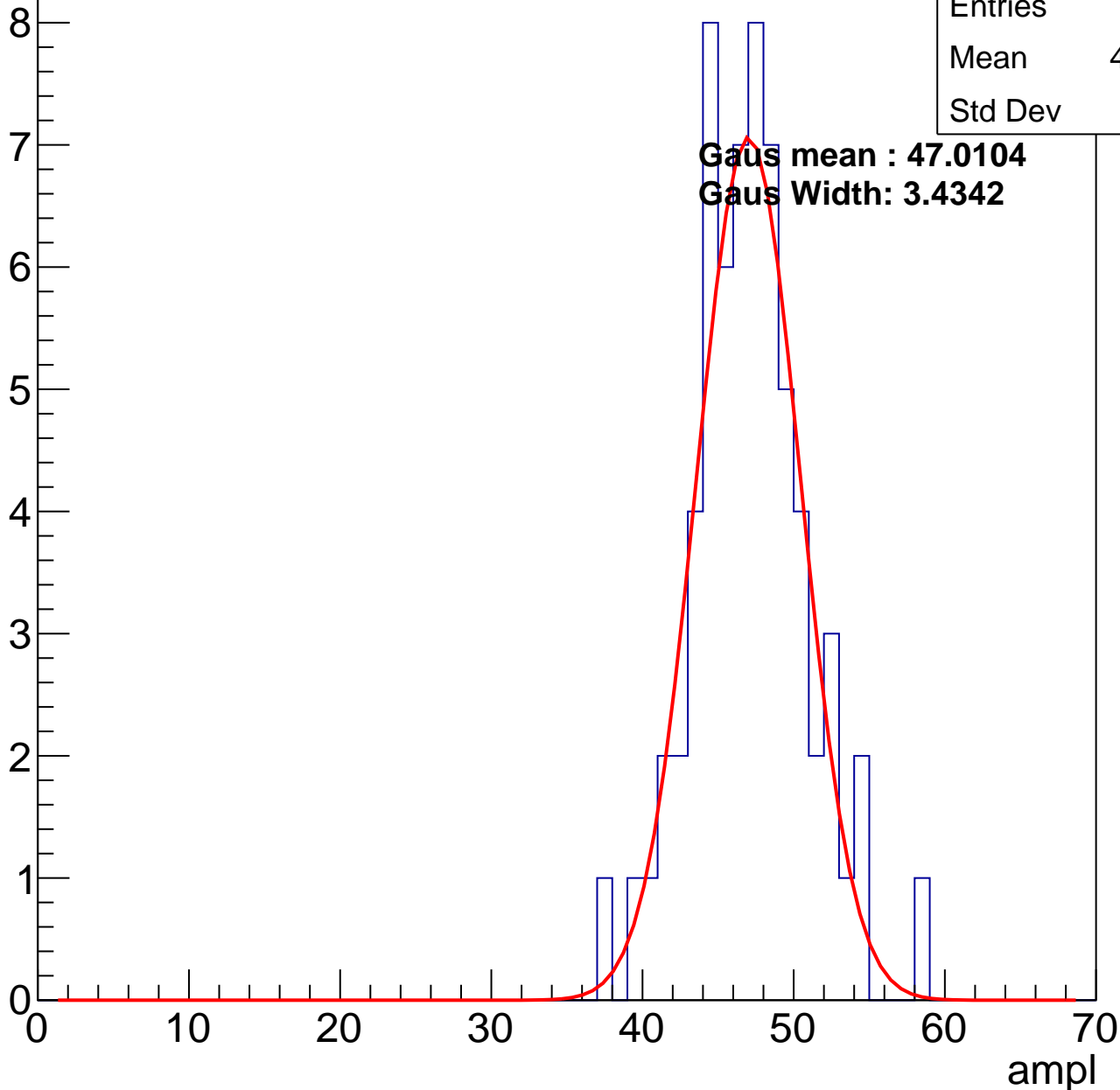
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	46.65
Std Dev	3.76

Gaus mean : 47.0104

Gaus Width: 3.4342

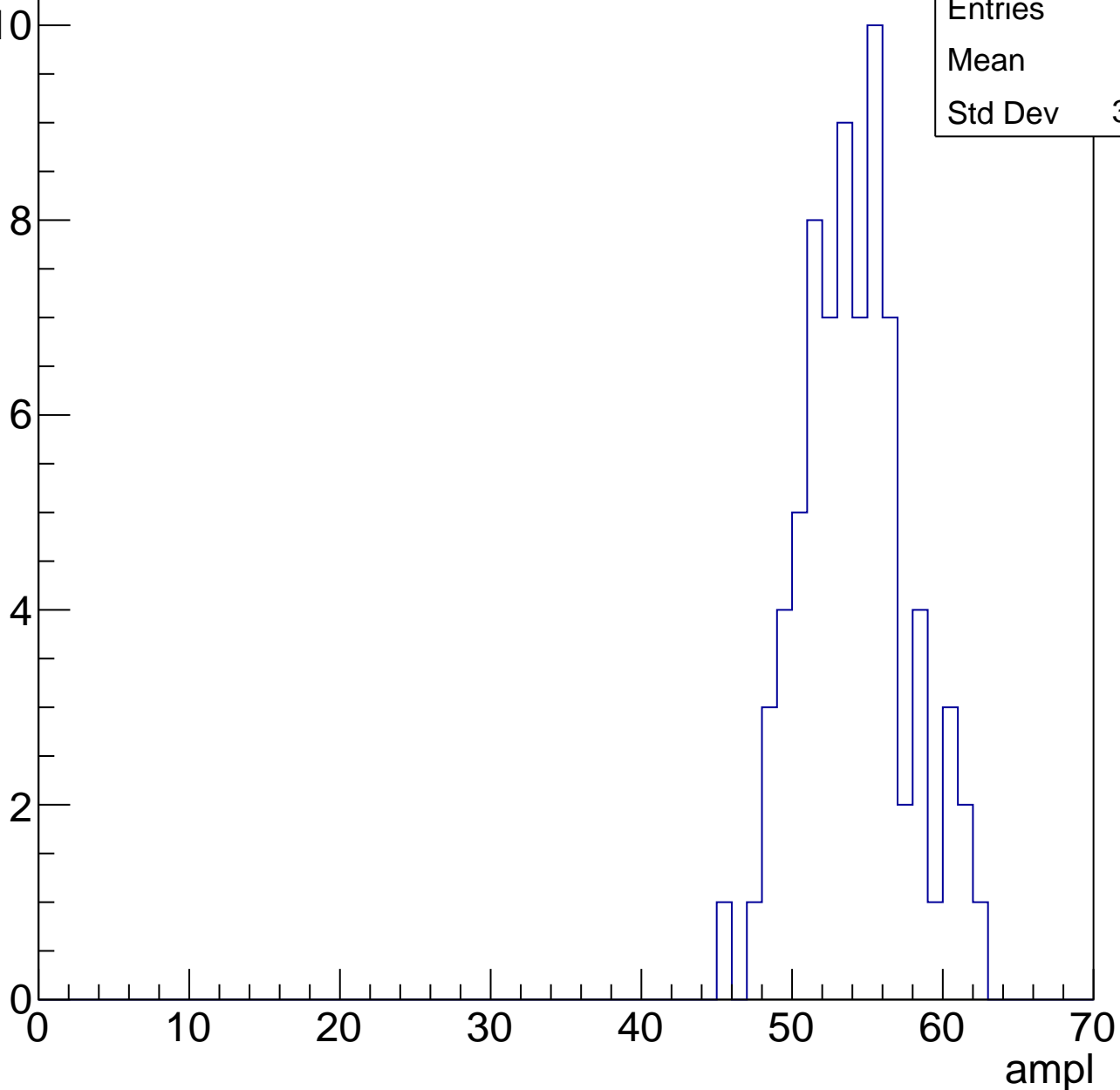


B1L101S, U26-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	53.6
Std Dev	3.521

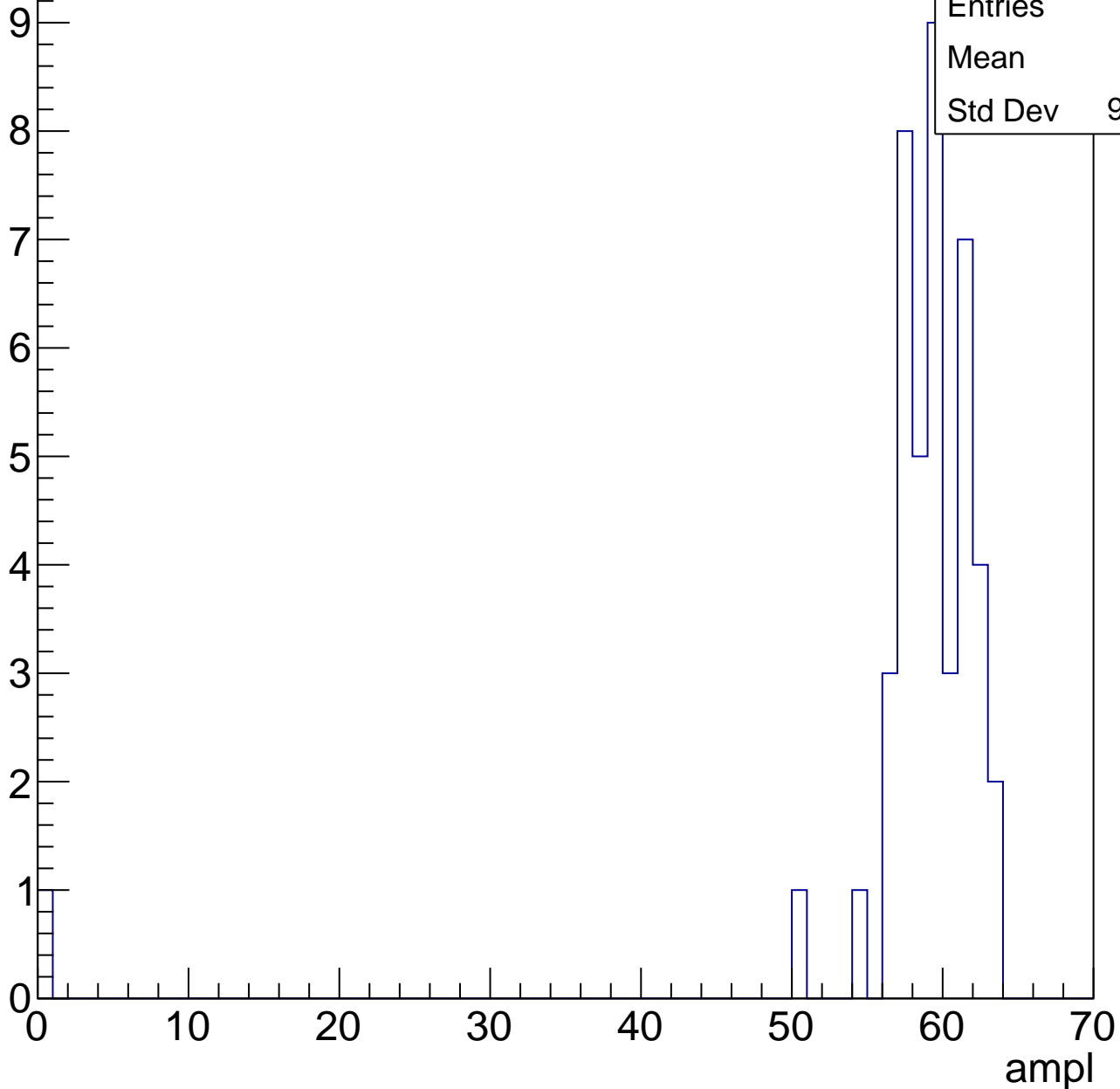


B1L101S, U26-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	57.5
Std Dev	9.109

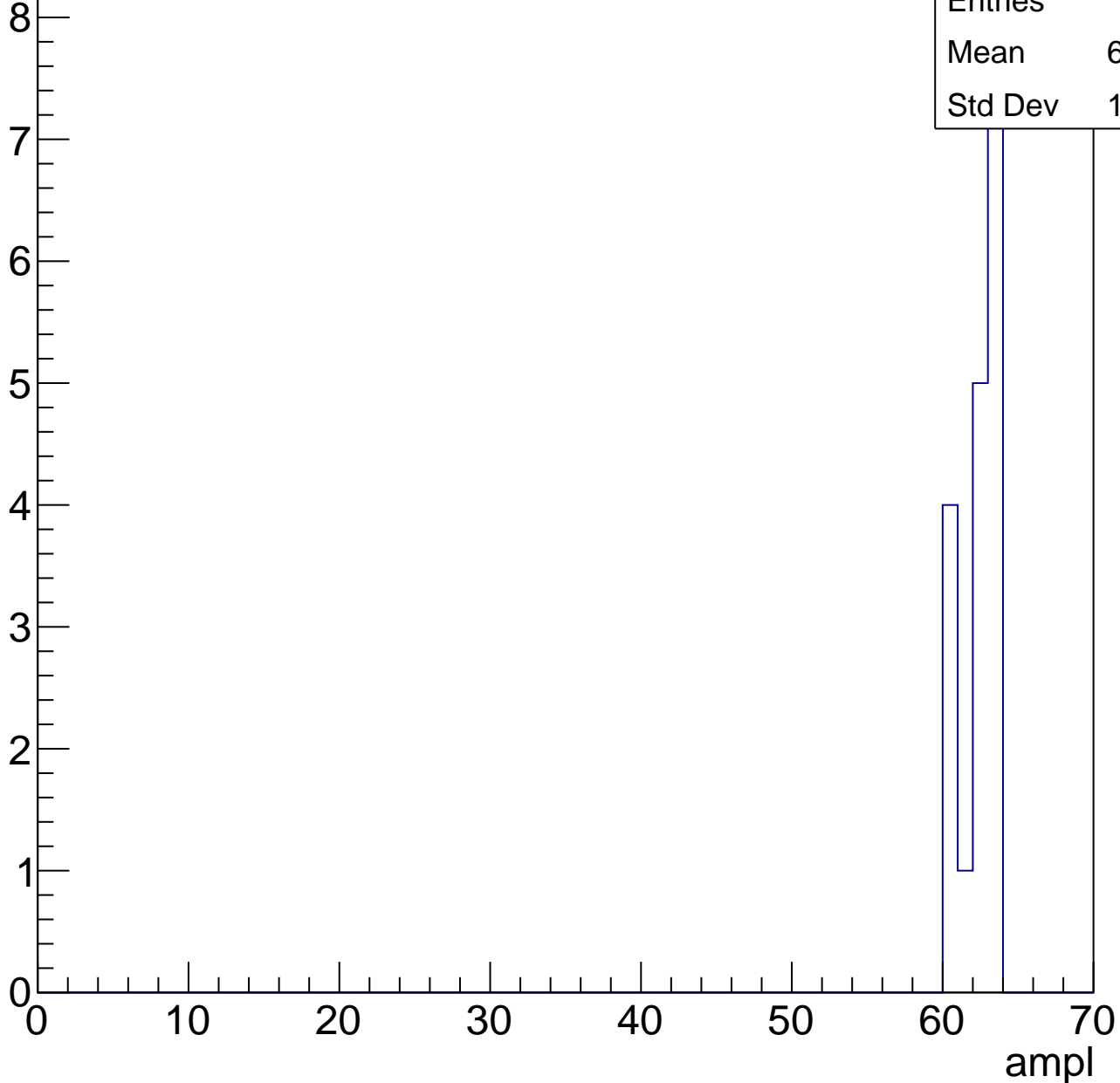


B1L101S, U26-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.94
Std Dev	1.177



B1L101S, U26-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch28, adc0

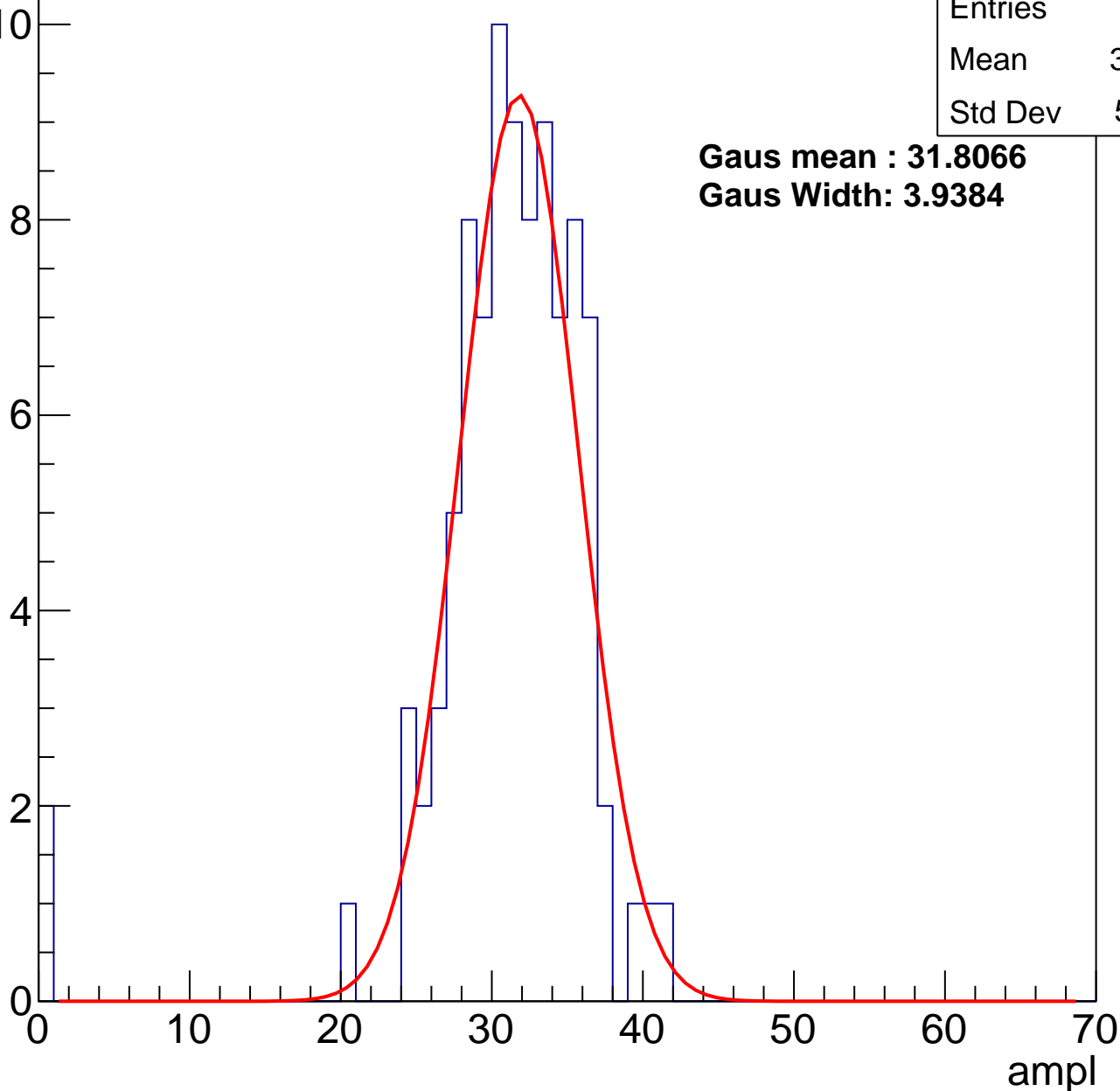
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	94
Mean	30.62
Std Dev	5.871

Gaus mean : 31.8066

Gaus Width: 3.9384



B1L101S, U26-ch28, adc1

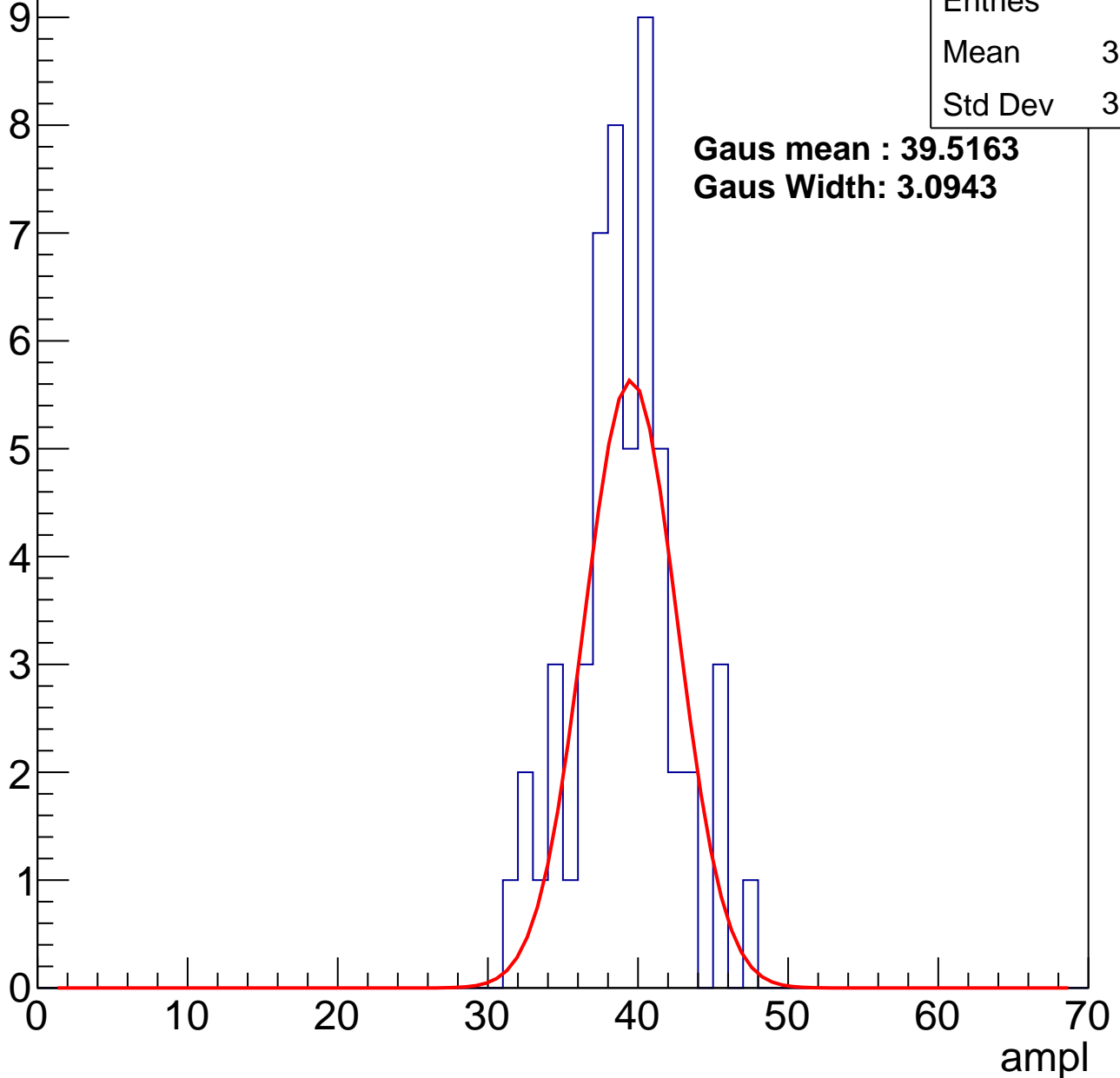
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	38.64
Std Dev	3.354

Gaus mean : 39.5163

Gaus Width: 3.0943



B1L101S, U26-ch28, adc2

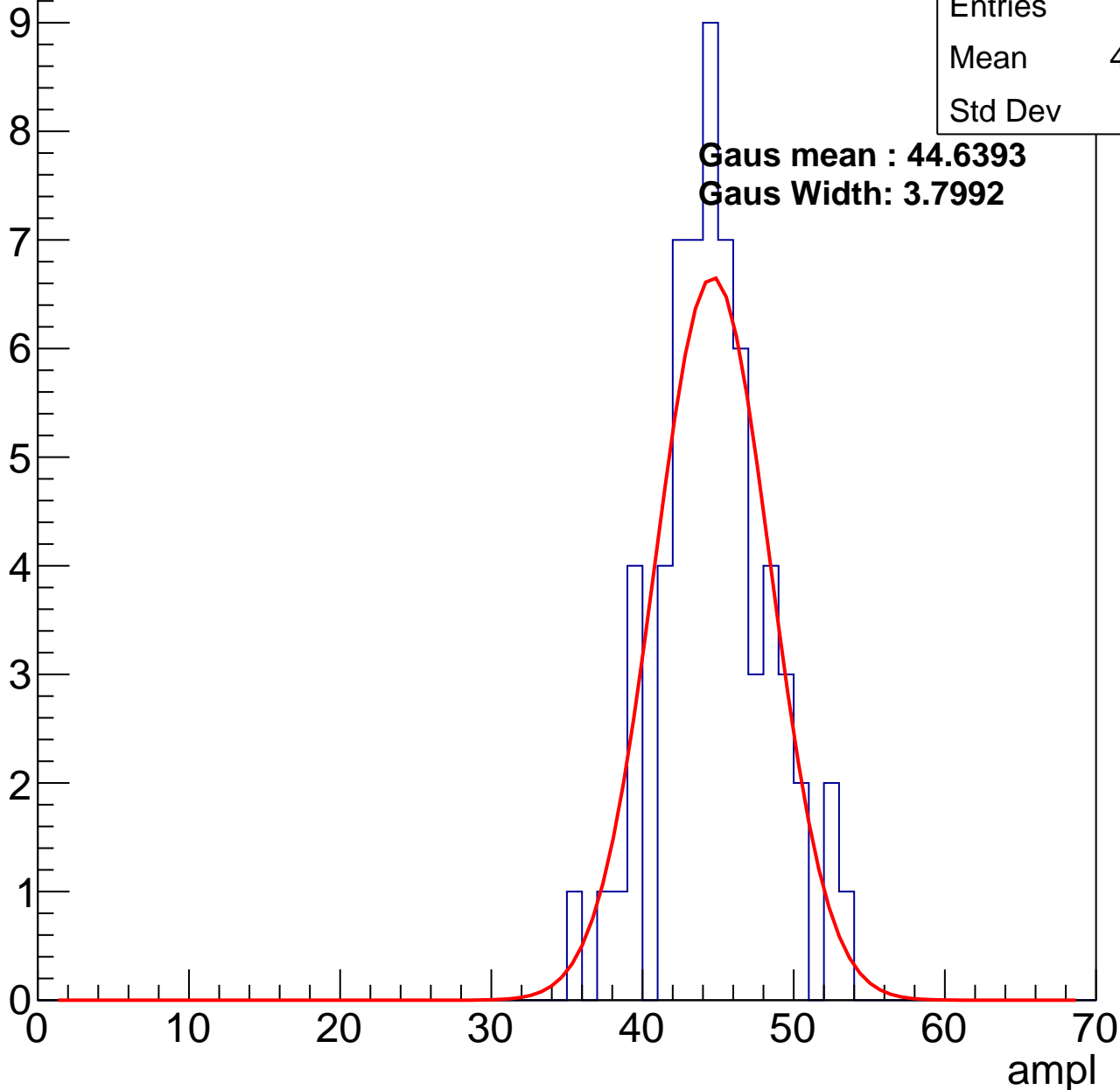
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.34
Std Dev	3.61

Gaus mean : 44.6393

Gaus Width: 3.7992

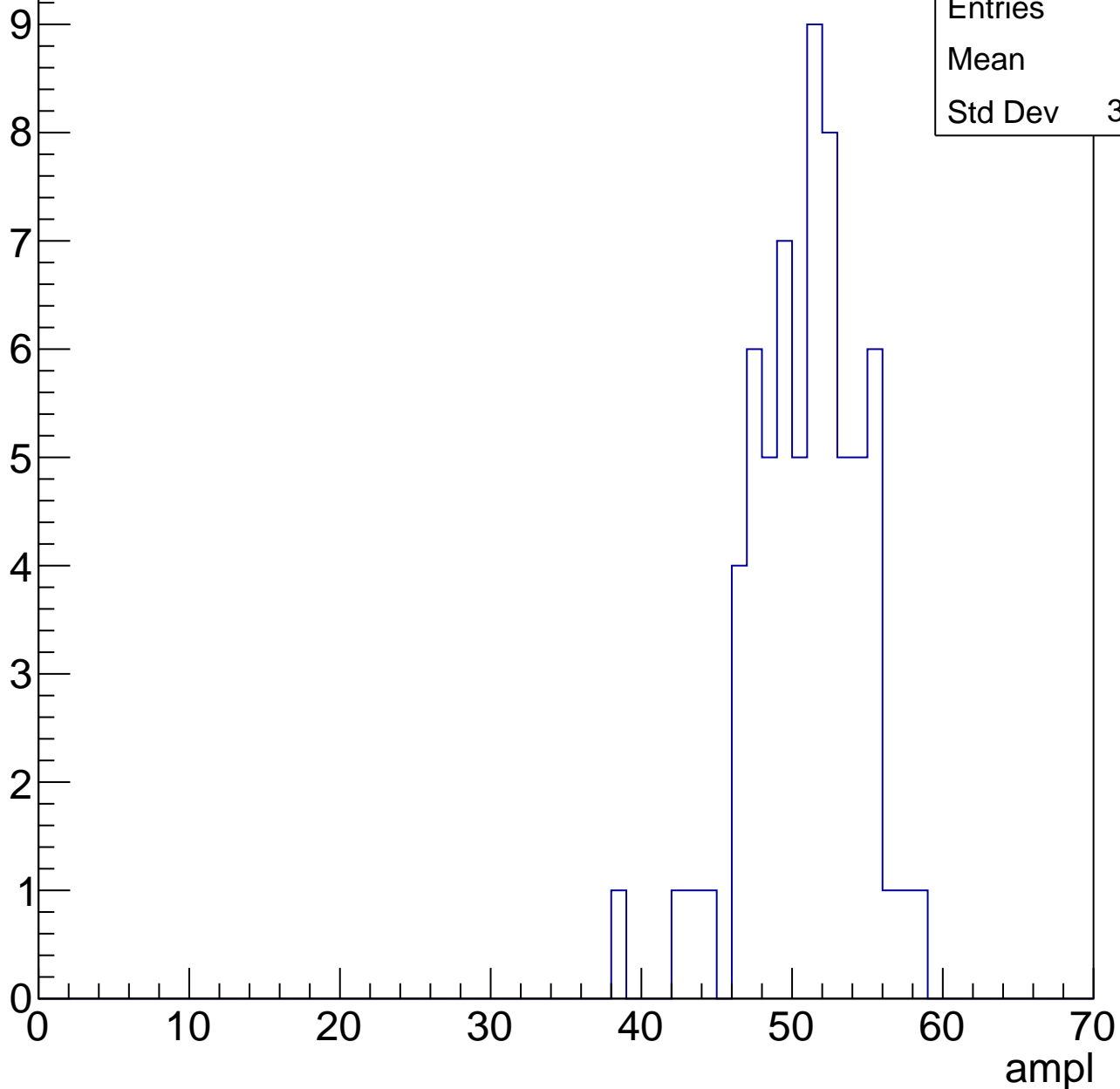


B1L101S, U26-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

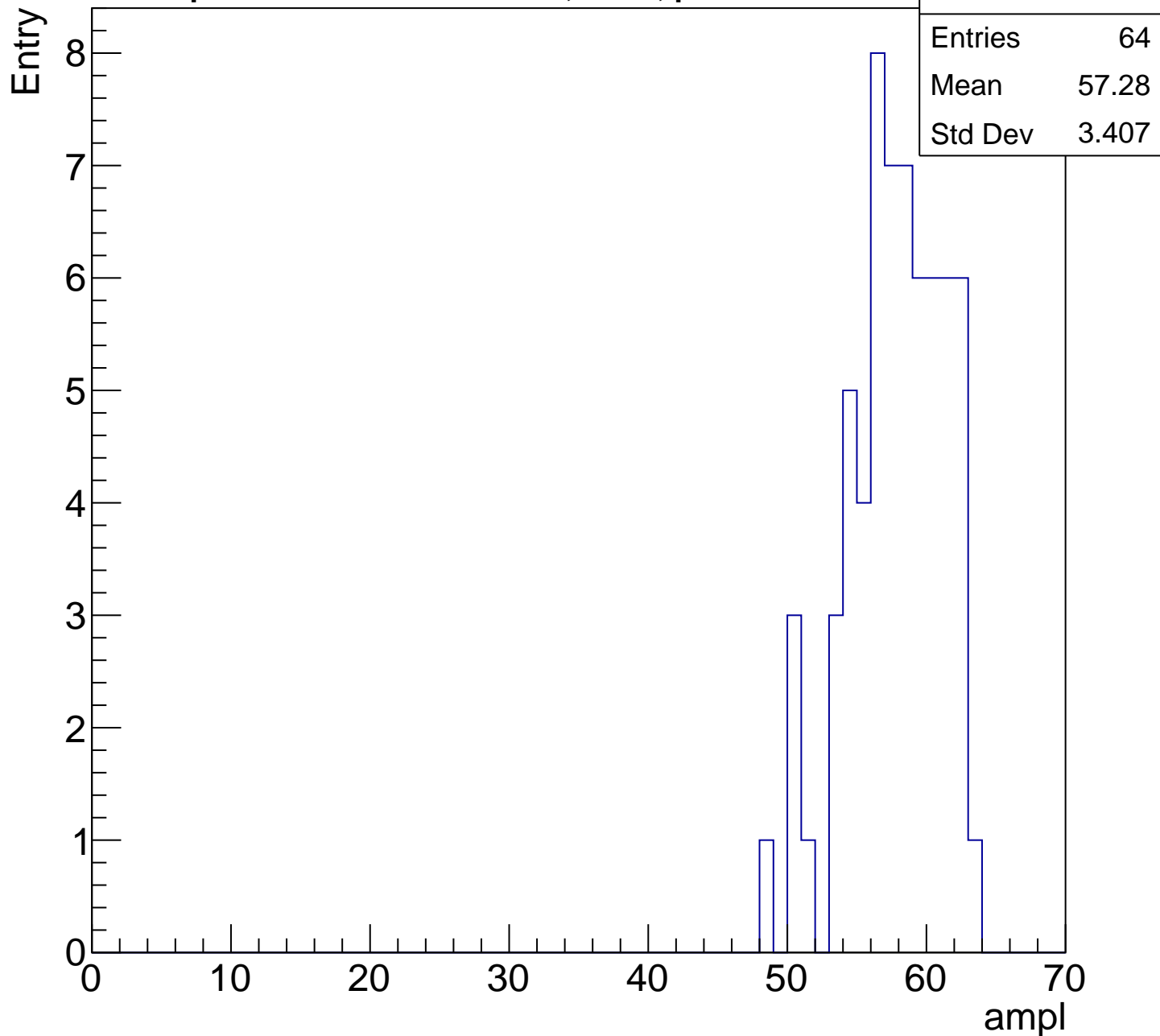
Entry

Entries	67
Mean	50.4
Std Dev	3.649



B1L101S, U26-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

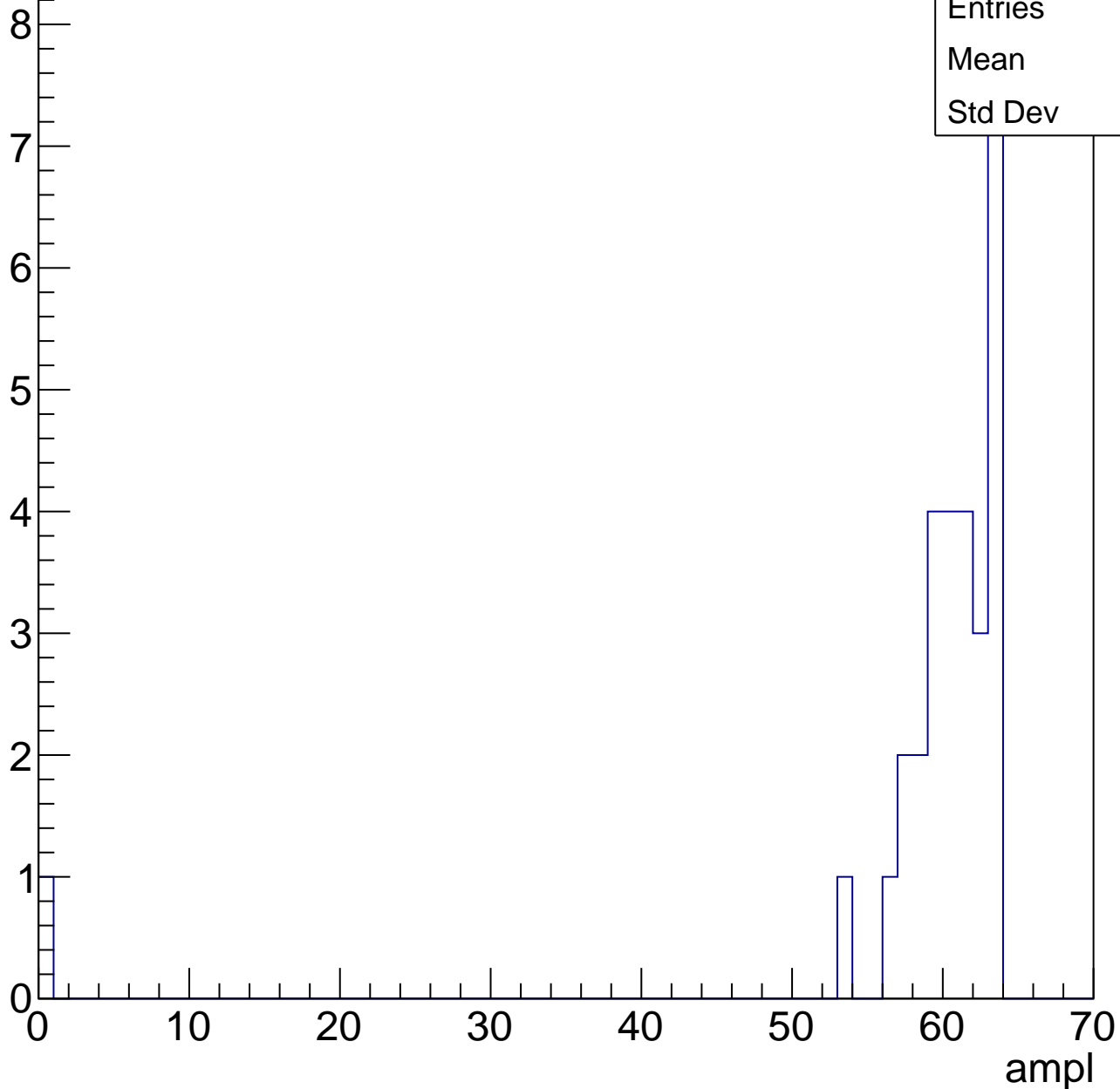


B1L101S, U26-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	58.3
Std Dev	11.1



B1L101S, U26-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch29, adc0

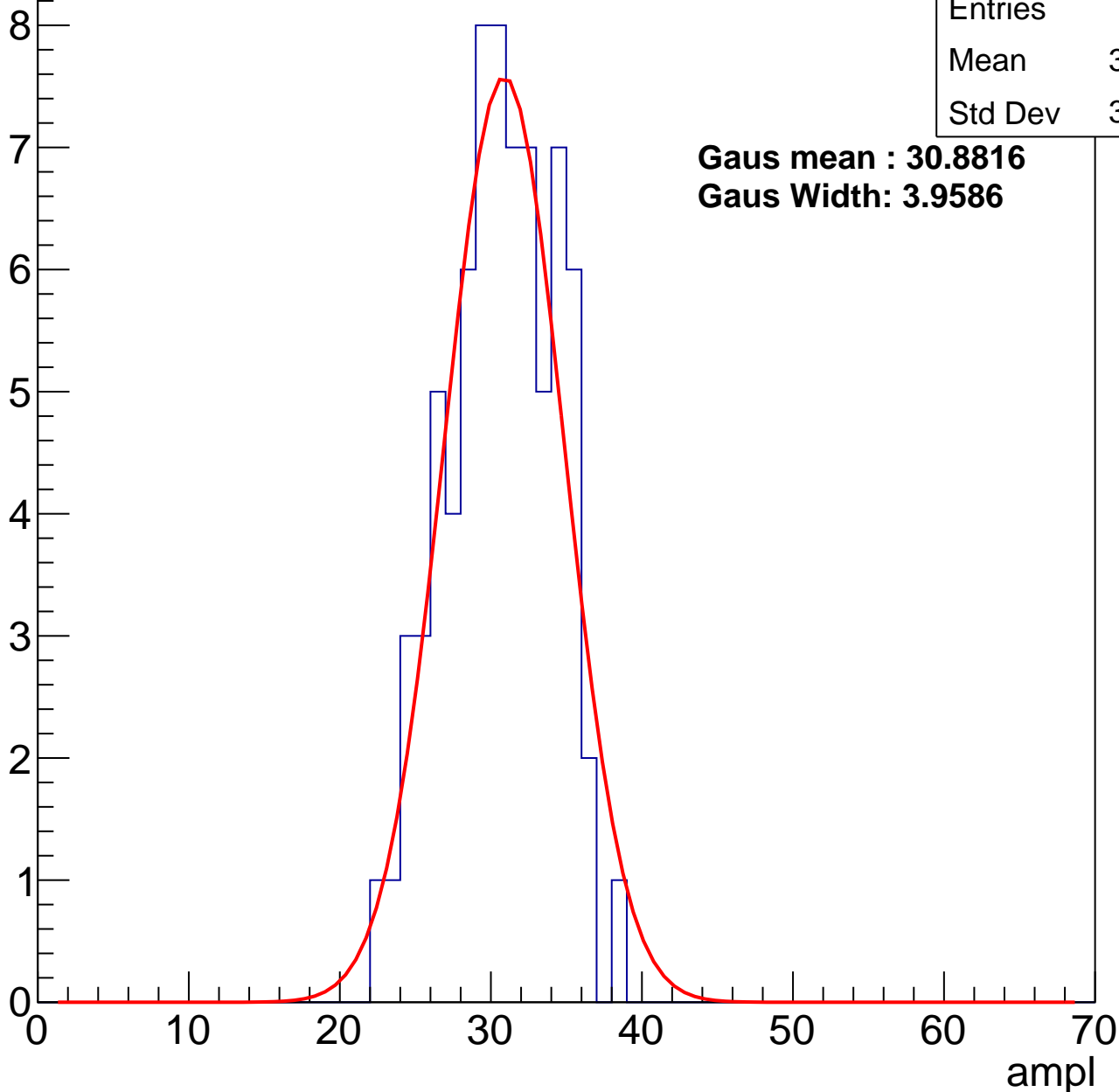
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.19
Std Dev	3.525

Gaus mean : 30.8816

Gaus Width: 3.9586



B1L101S, U26-ch29, adc1

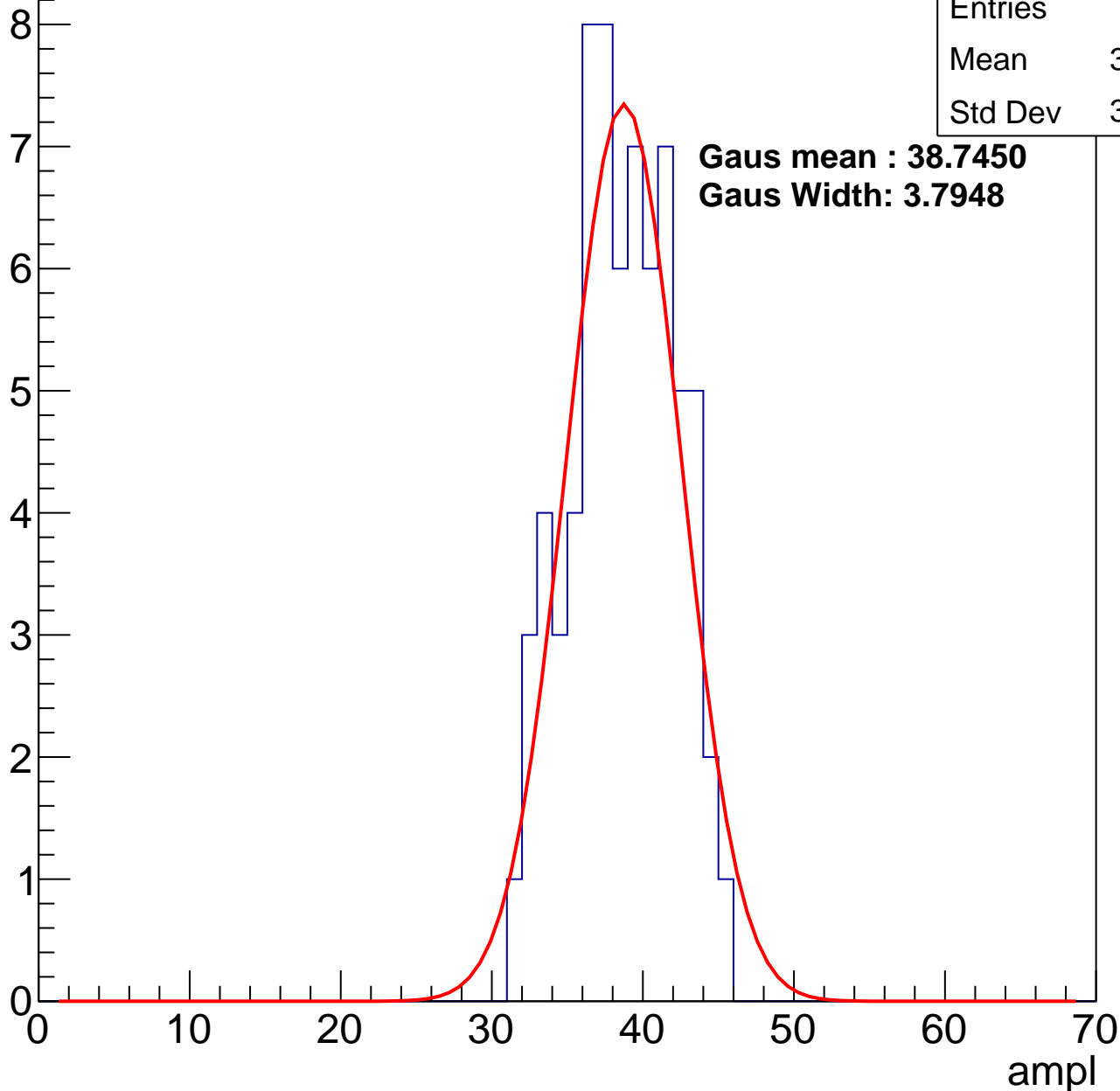
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	38.16
Std Dev	3.379

Gaus mean : 38.7450

Gaus Width: 3.7948



B1L101S, U26-ch29, adc2

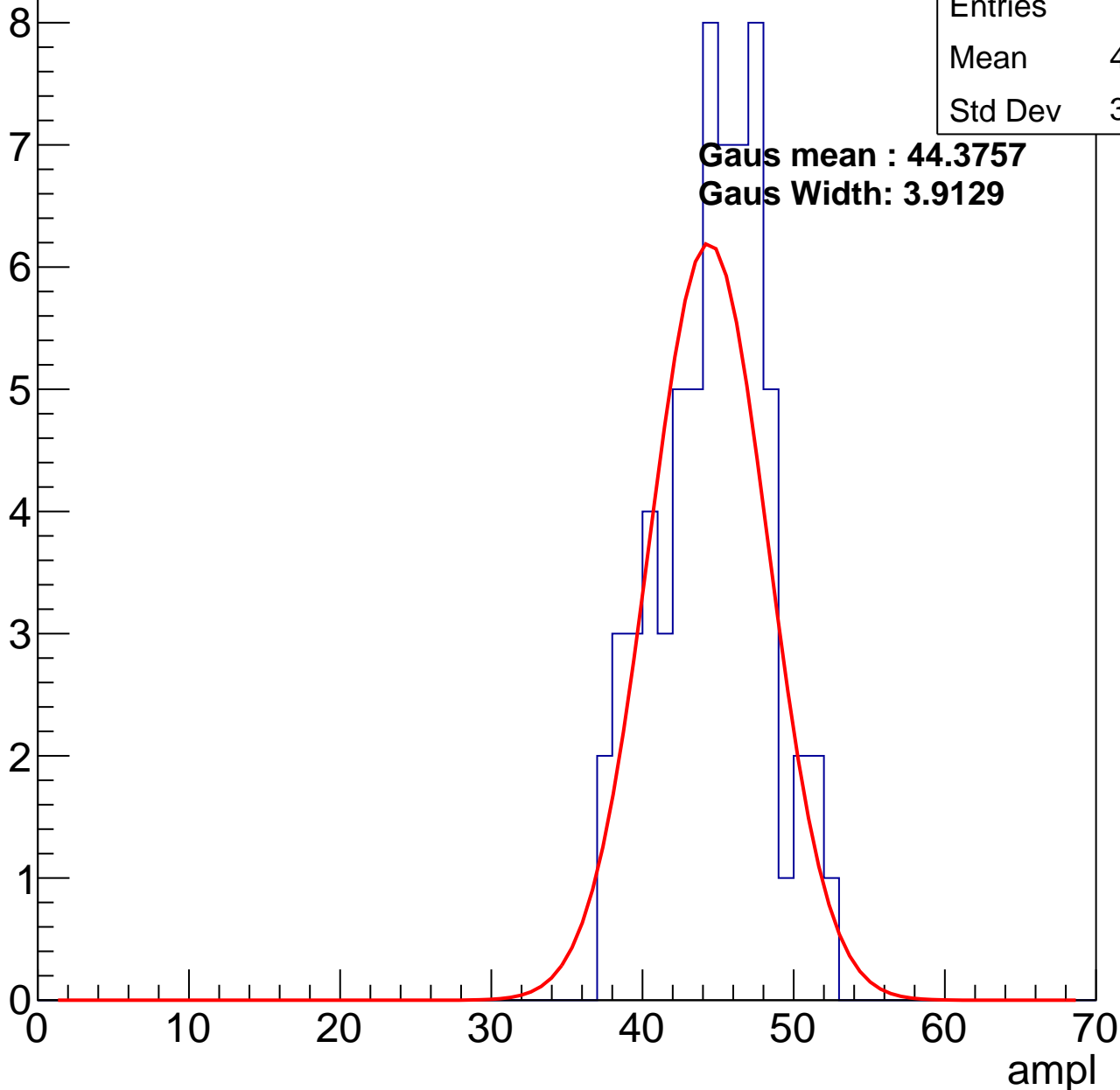
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.26
Std Dev	3.565

Gaus mean : 44.3757

Gaus Width: 3.9129

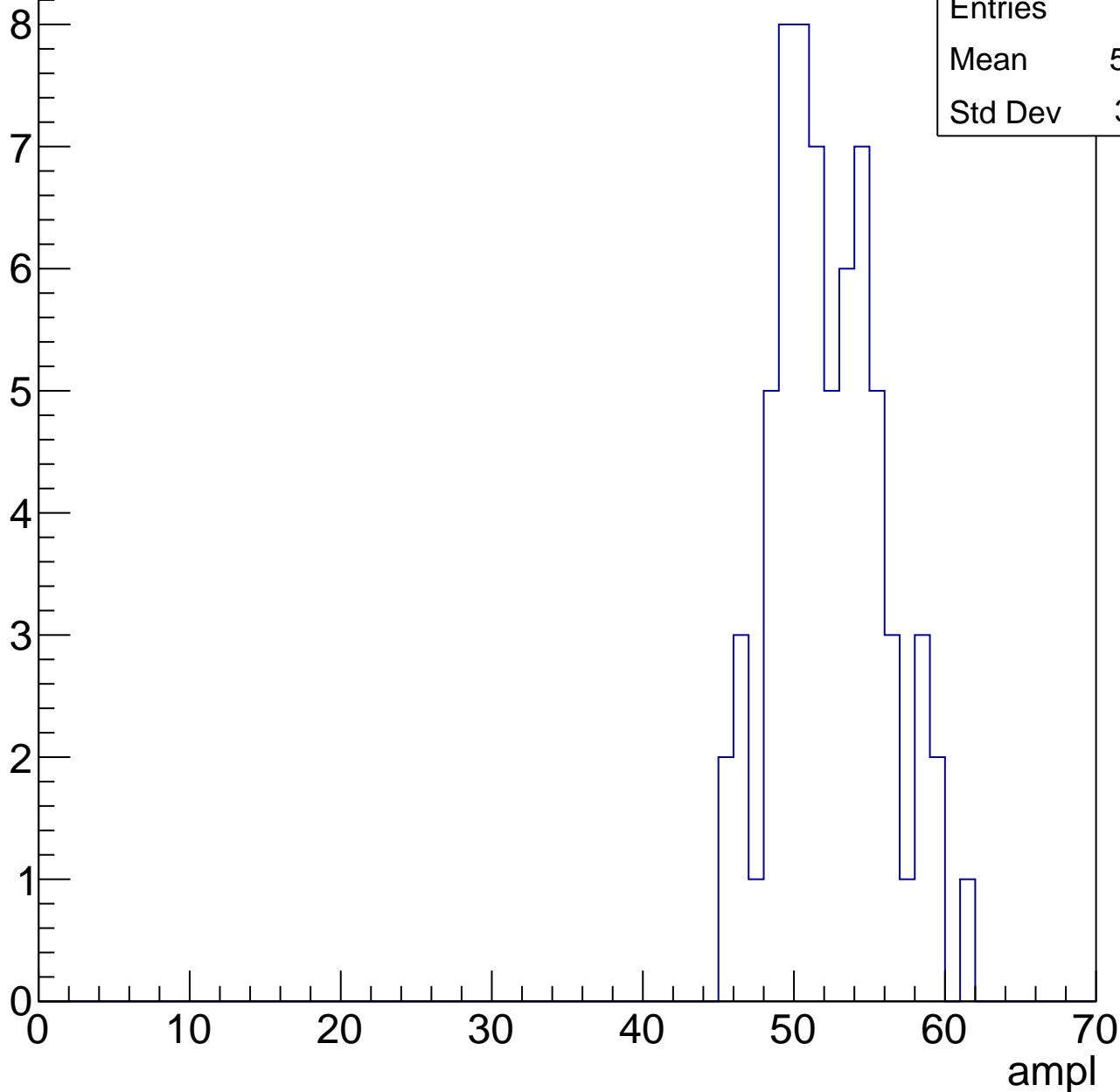


B1L101S, U26-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.84
Std Dev	3.581

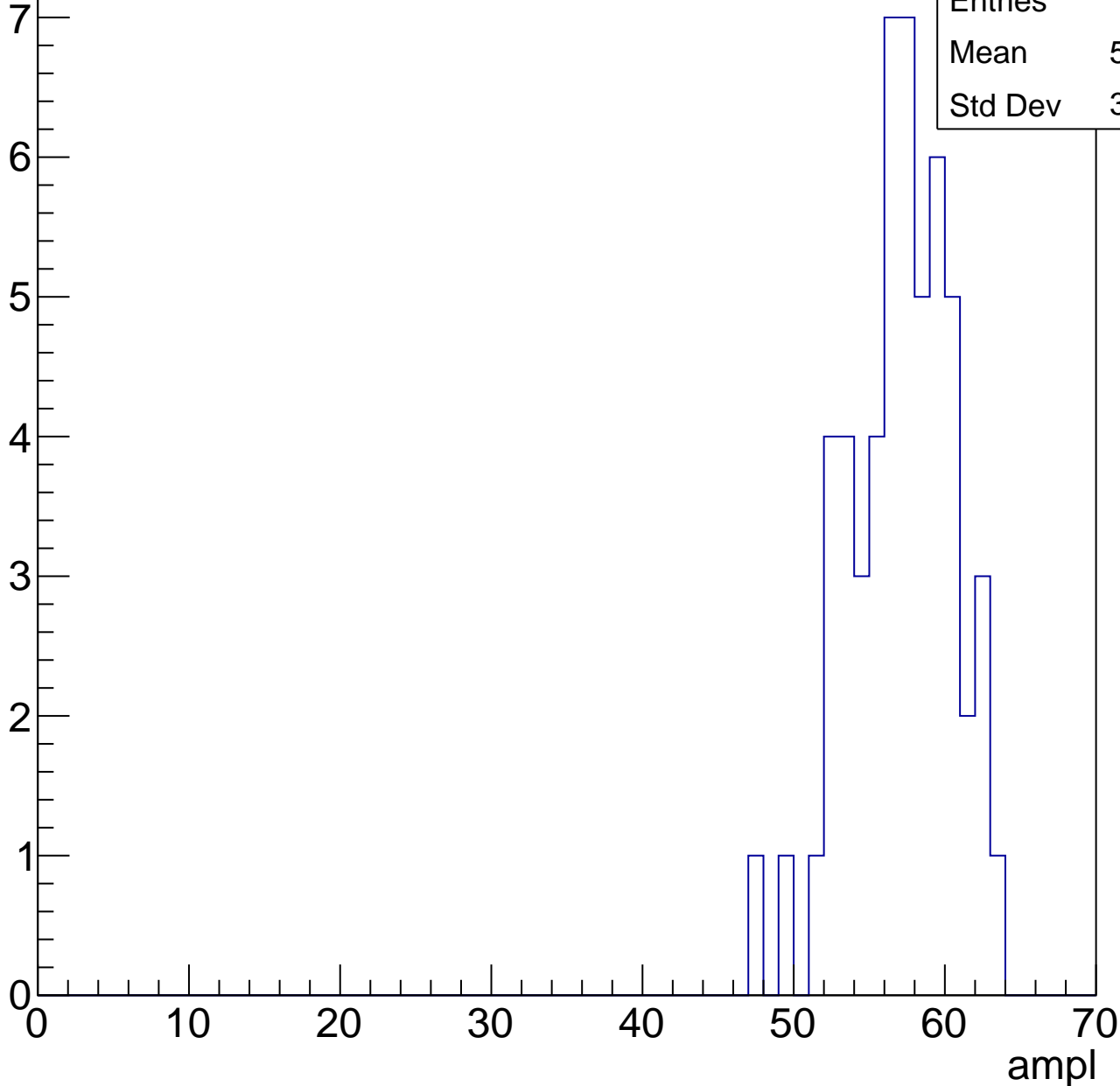


B1L101S, U26-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

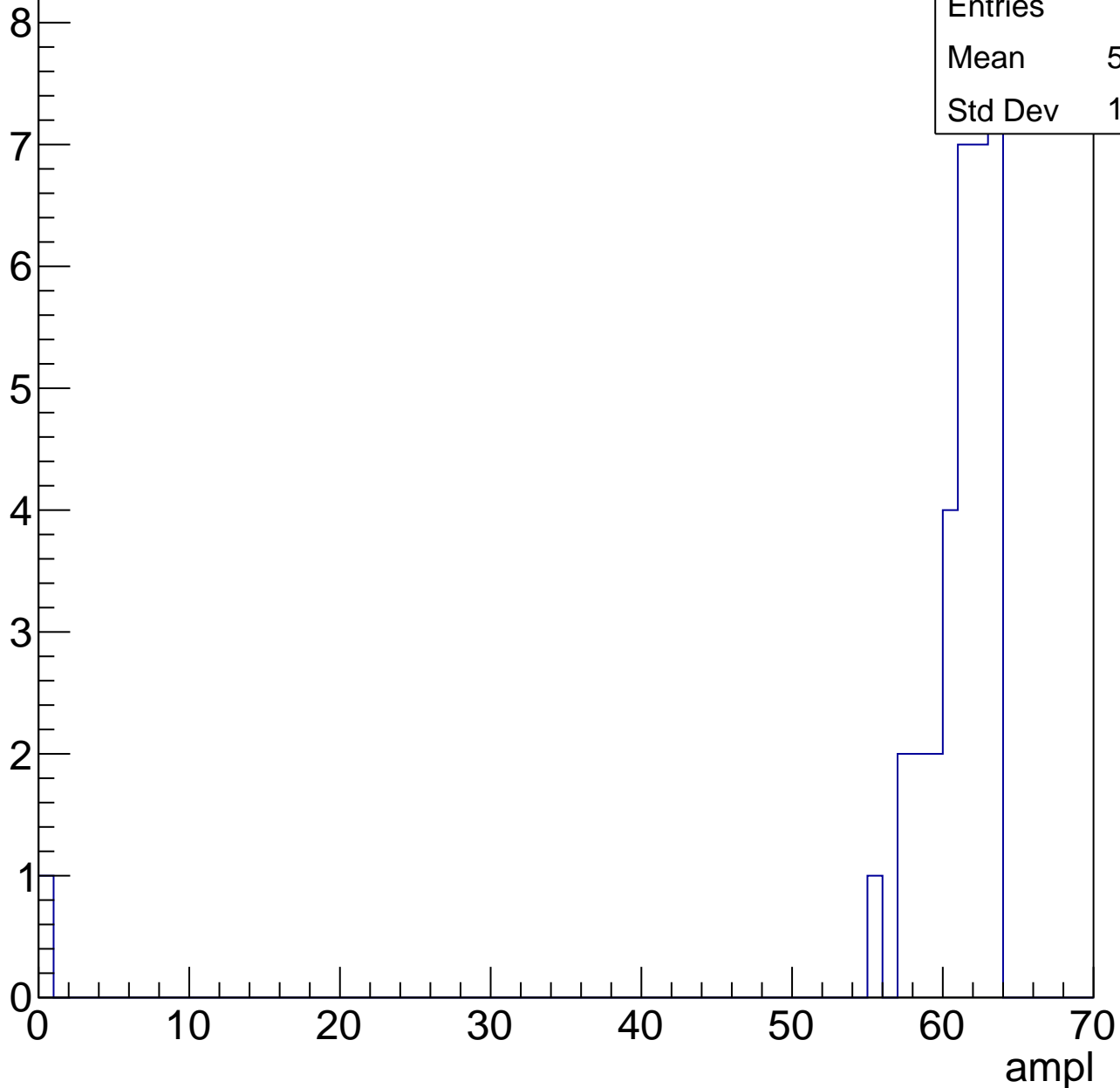
Entries	54
Mean	56.57
Std Dev	3.392



B1L101S, U26-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch30, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	28.27
Std Dev	4.866

Gaus mean : 28.8650

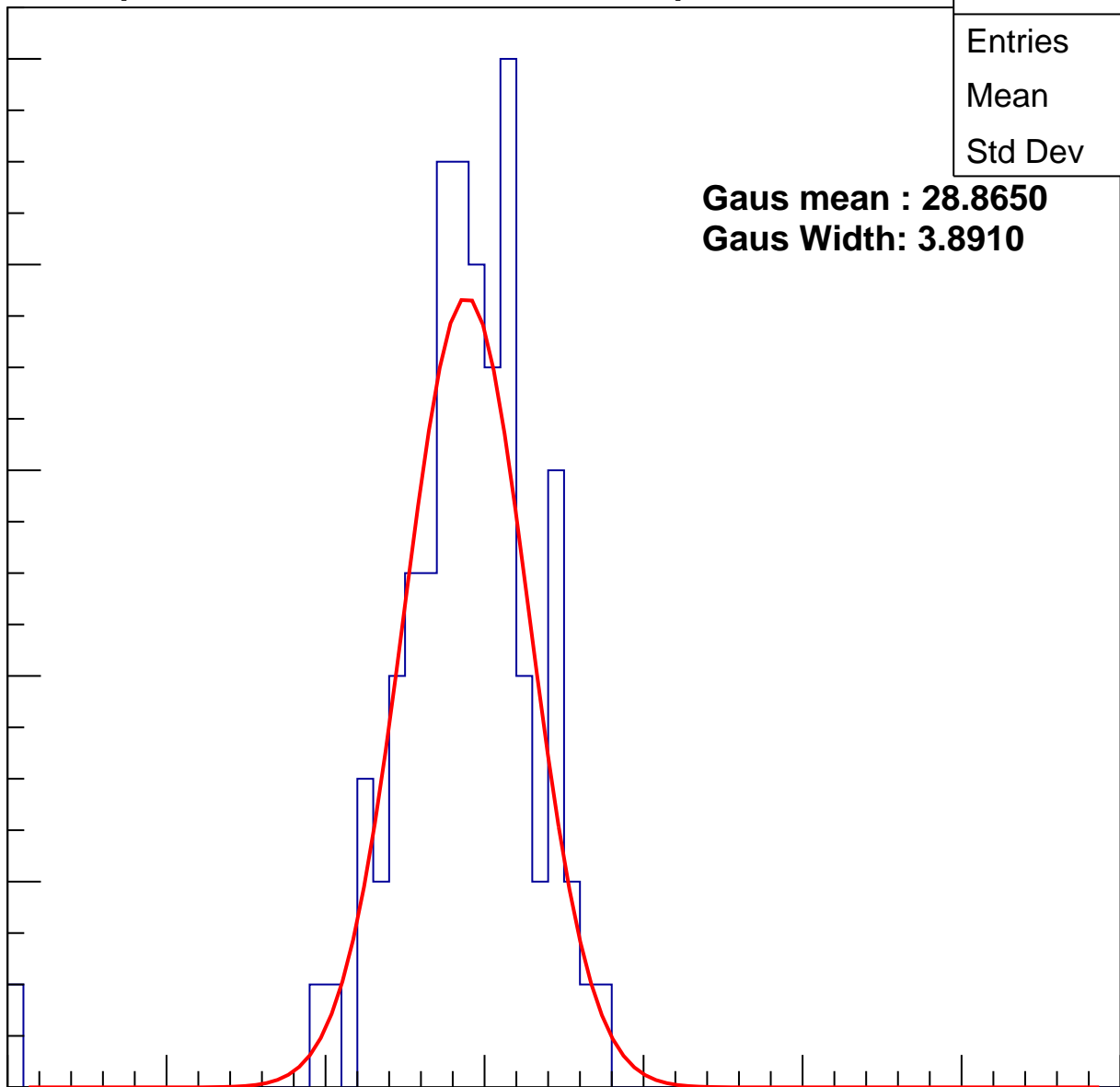
Gaus Width: 3.8910

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch30, adc1

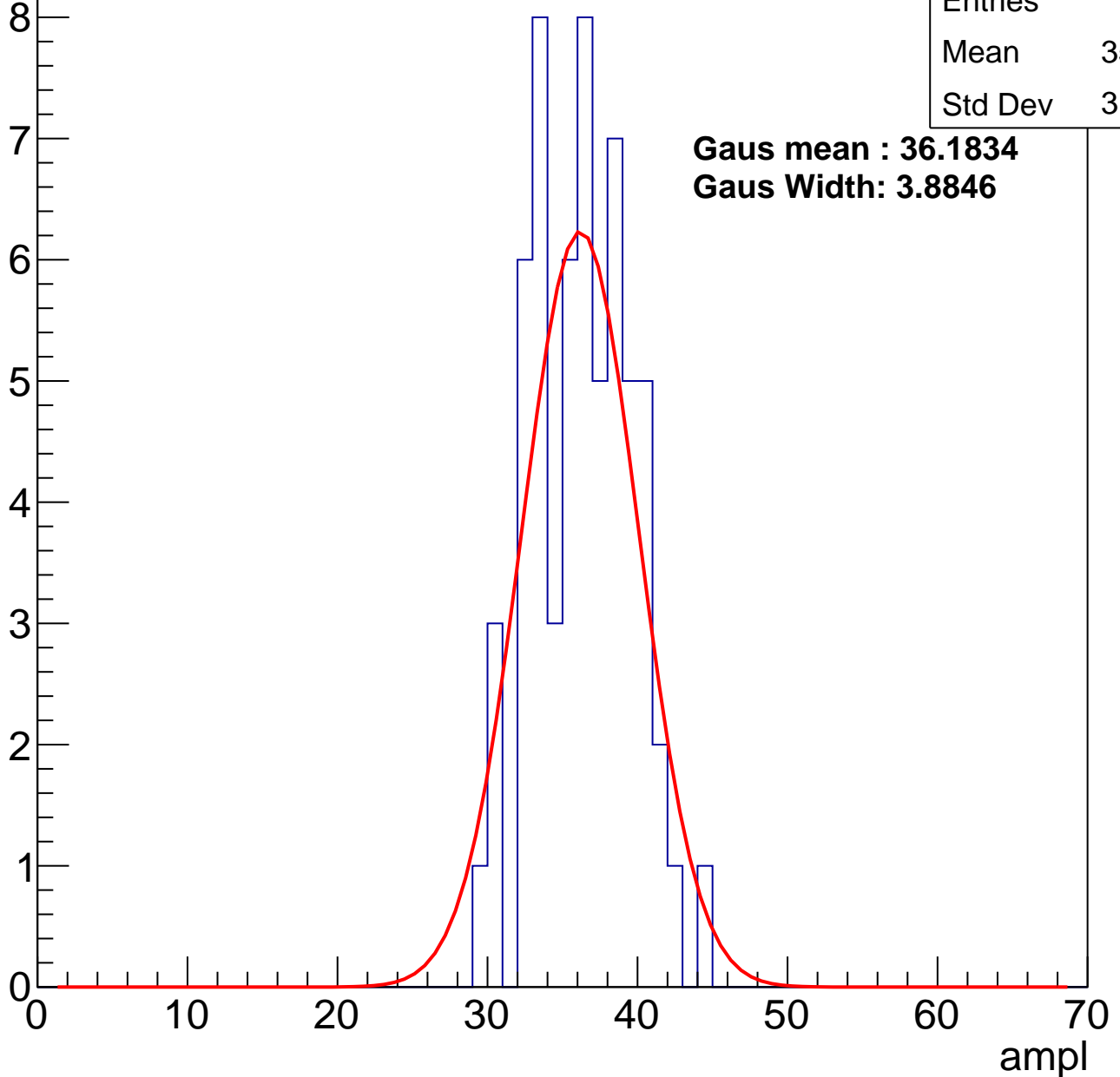
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.89
Std Dev	3.265

Gaus mean : 36.1834

Gaus Width: 3.8846



B1L101S, U26-ch30, adc2

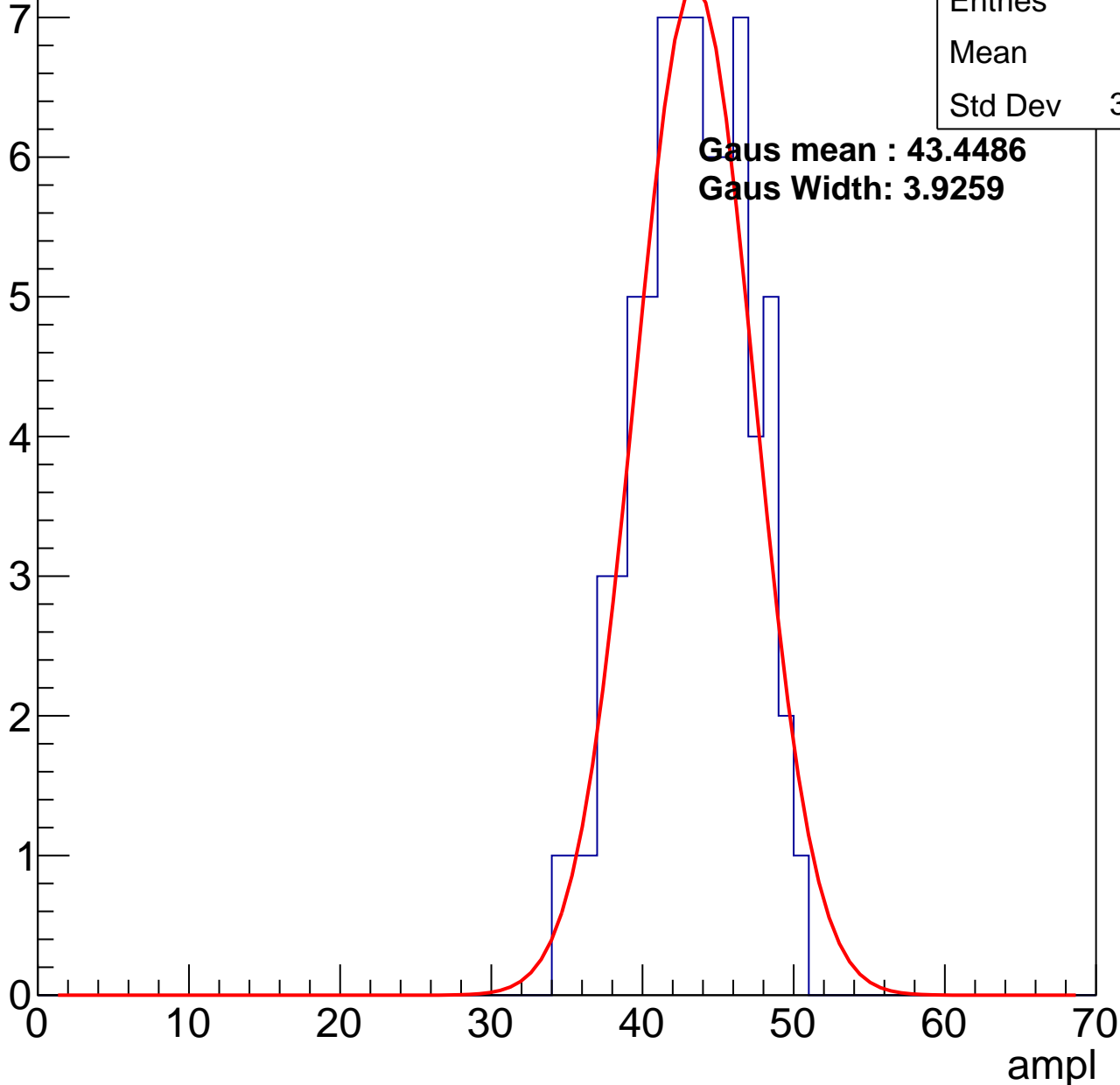
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.8
Std Dev	3.633

Gaus mean : 43.4486

Gaus Width: 3.9259

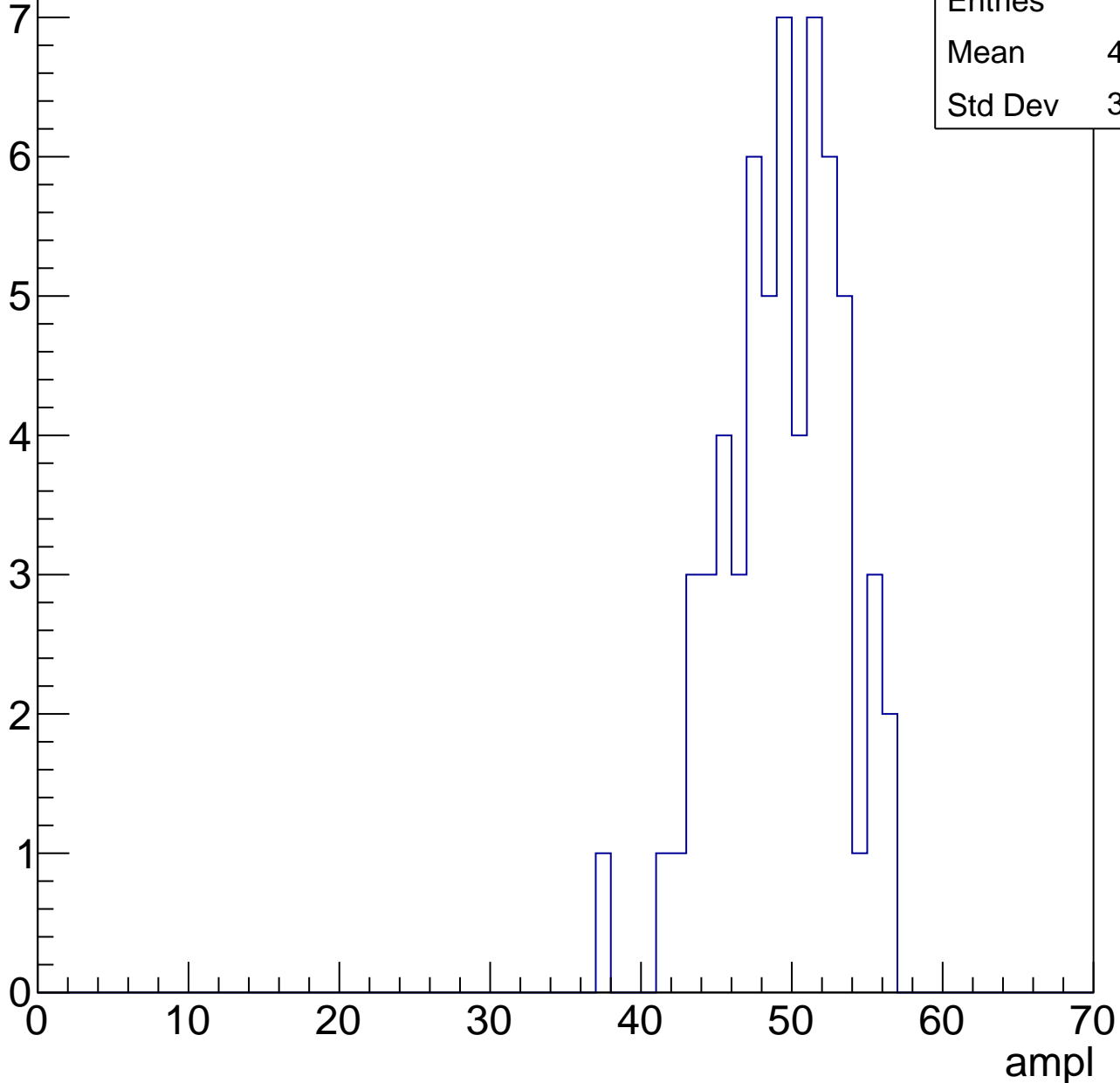


B1L101S, U26-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	48.85
Std Dev	3.926

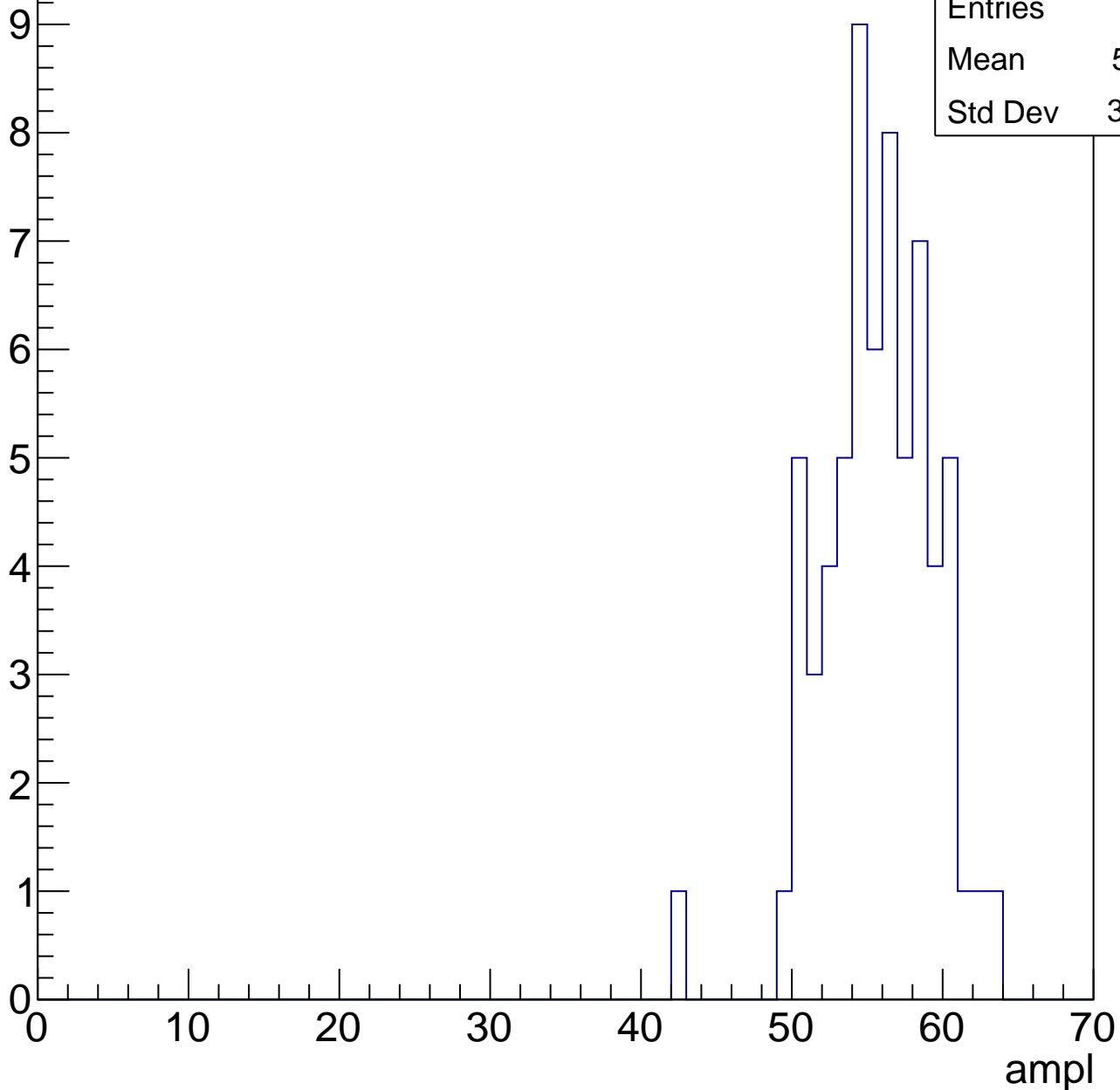


B1L101S, U26-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	55.21
Std Dev	3.629

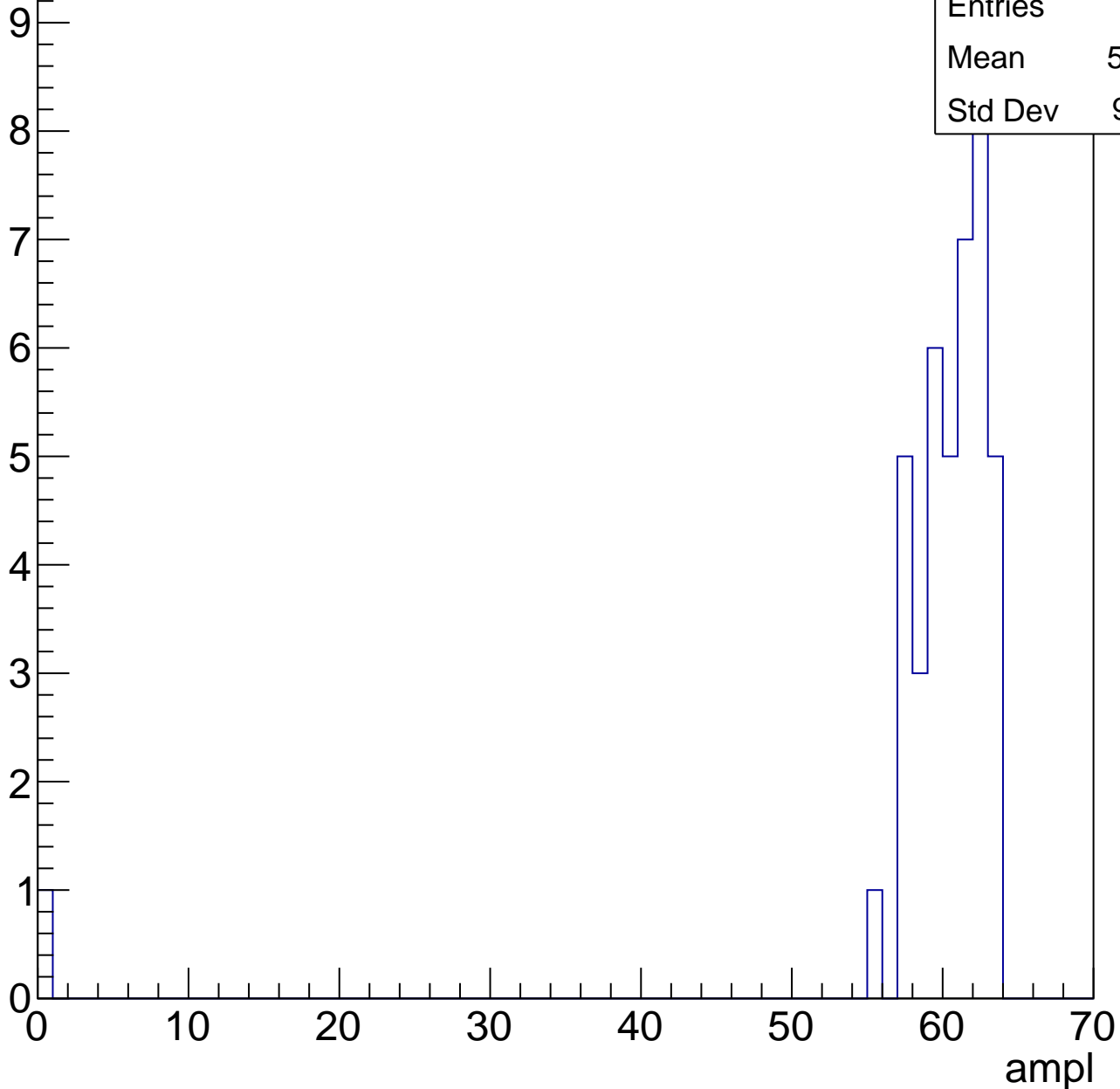


B1L101S, U26-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.76
Std Dev	9.401



B1L101S, U26-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch31, adc0

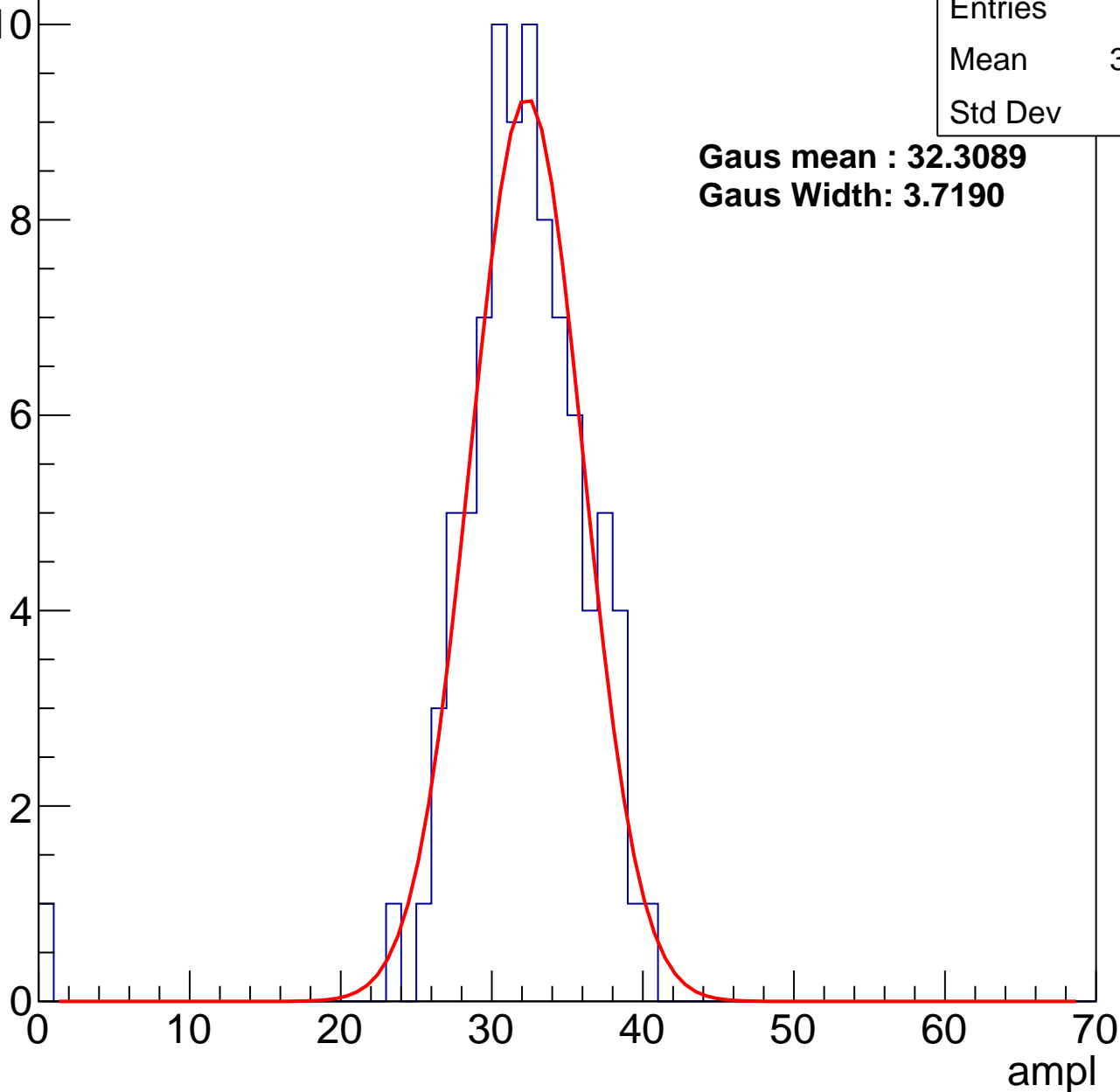
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	31.53
Std Dev	4.88

Gaus mean : 32.3089

Gaus Width: 3.7190



B1L101S, U26-ch31, adc1

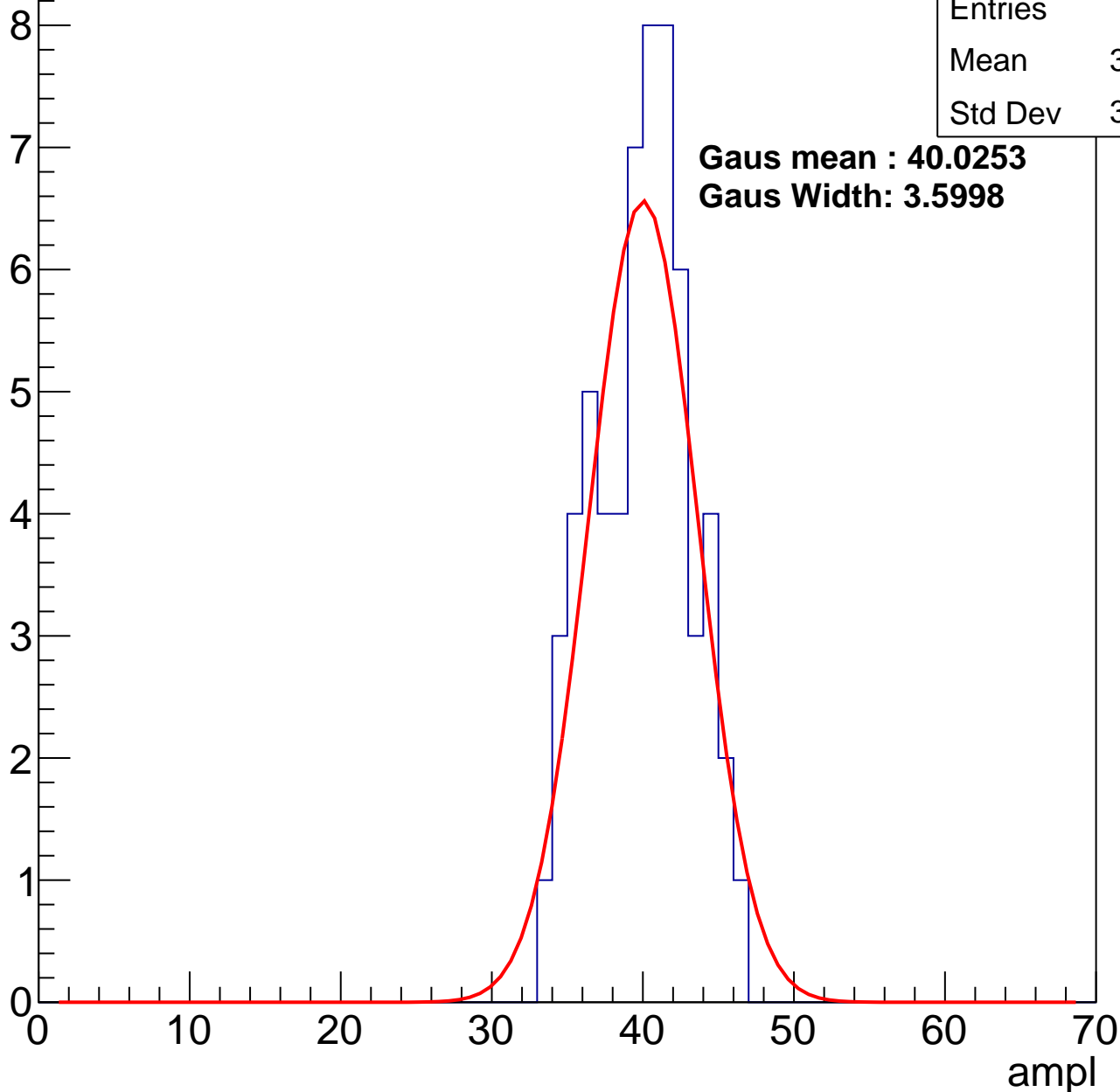
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	39.48
Std Dev	3.133

Gaus mean : 40.0253

Gaus Width: 3.5998



B1L101S, U26-ch31, adc2

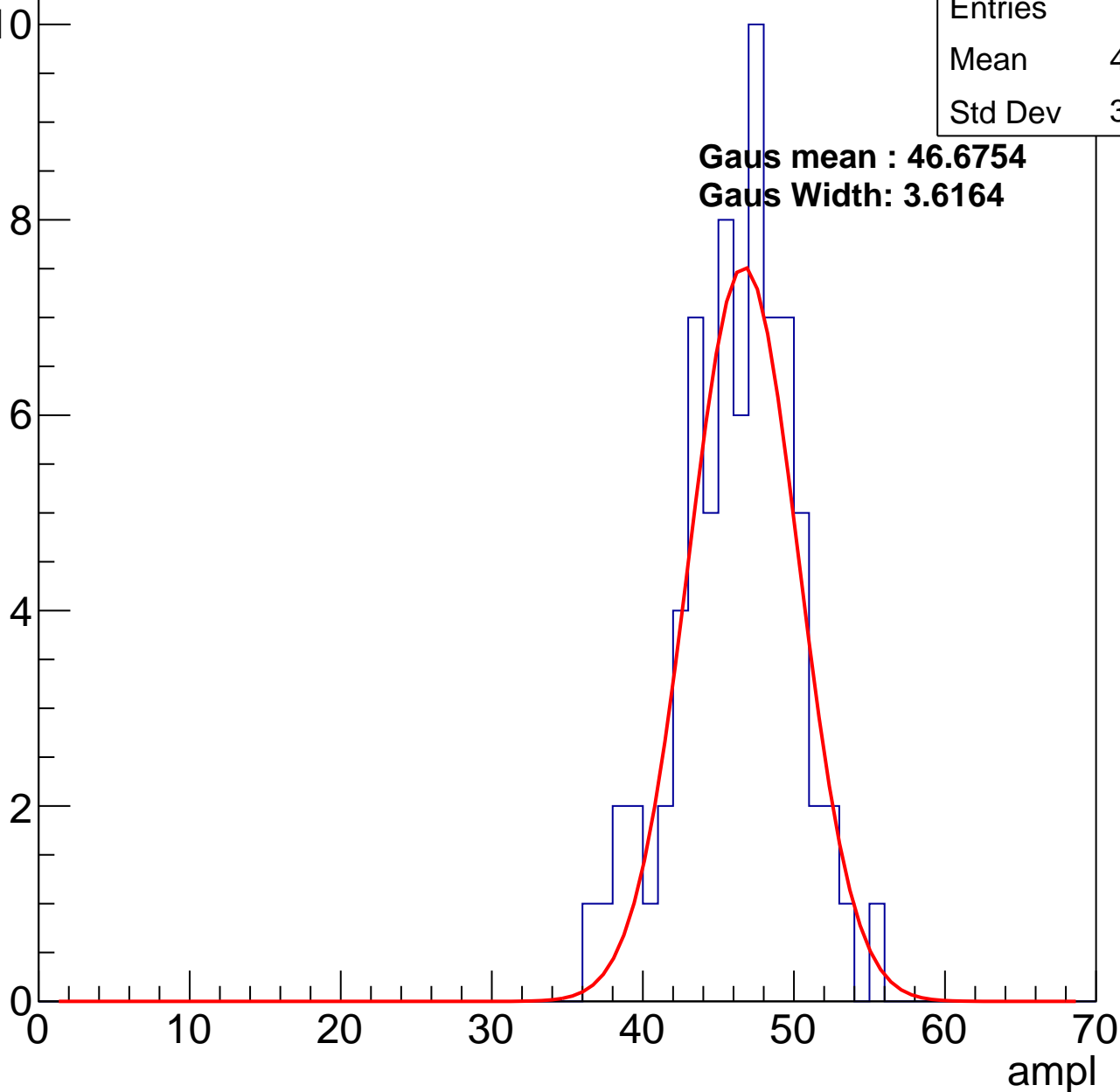
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	45.77
Std Dev	3.815

Gaus mean : 46.6754

Gaus Width: 3.6164

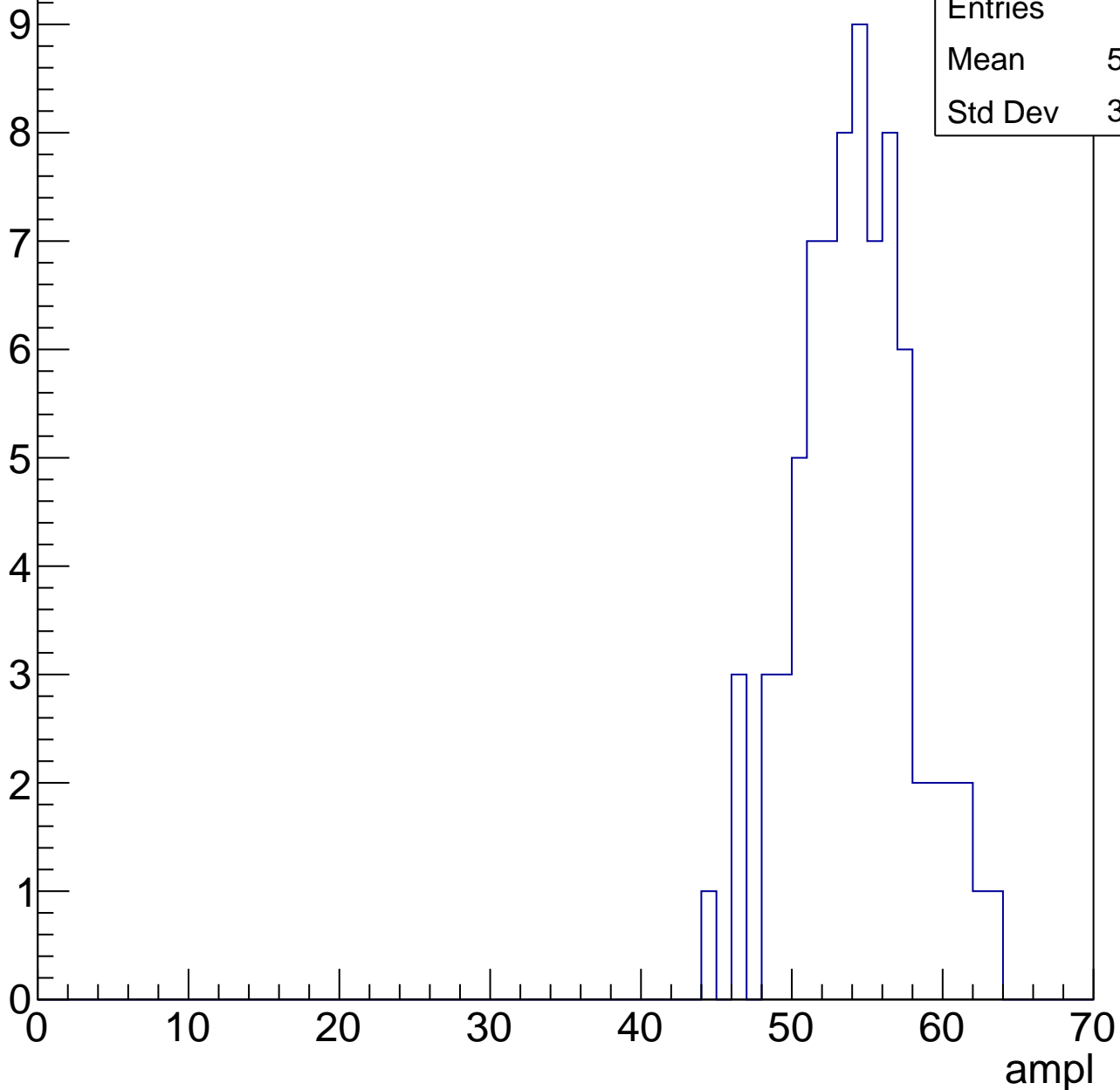


B1L101S, U26-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	53.64
Std Dev	3.837

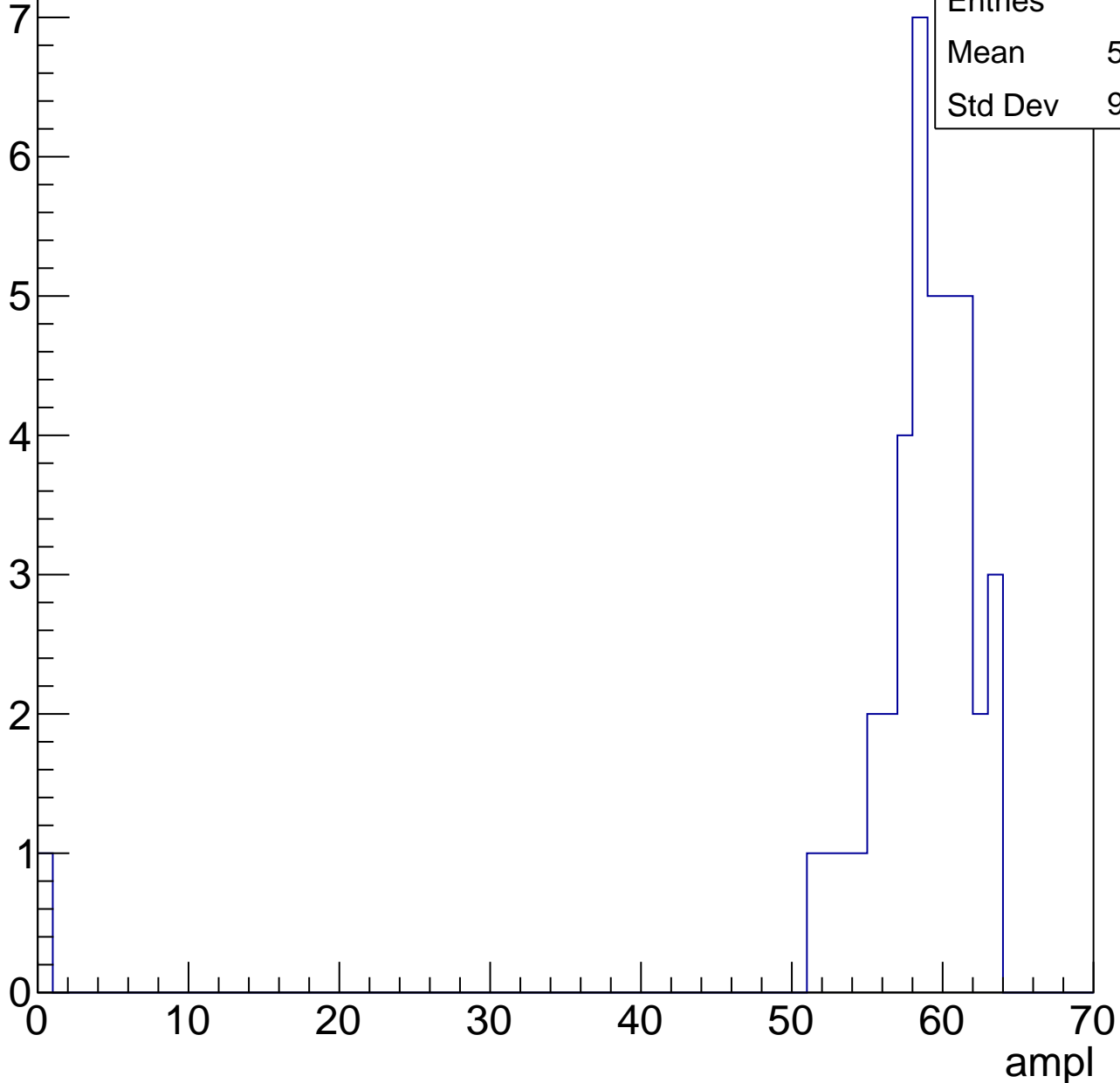


B1L101S, U26-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

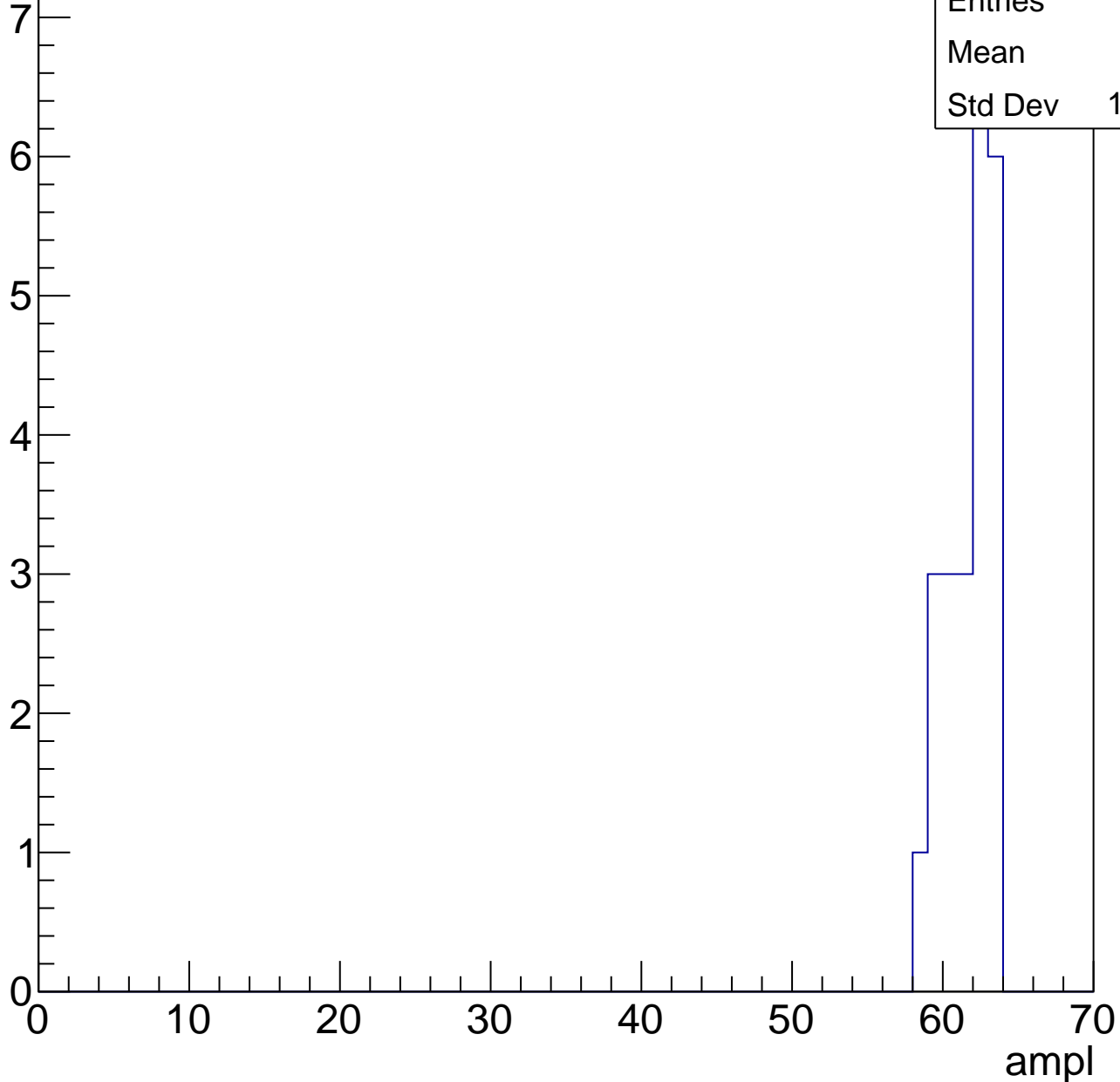
Entries	40
Mean	56.98
Std Dev	9.559



B1L101S, U26-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	23
Mean	61.3
Std Dev	1.516

B1L101S, U26-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch32, adc0

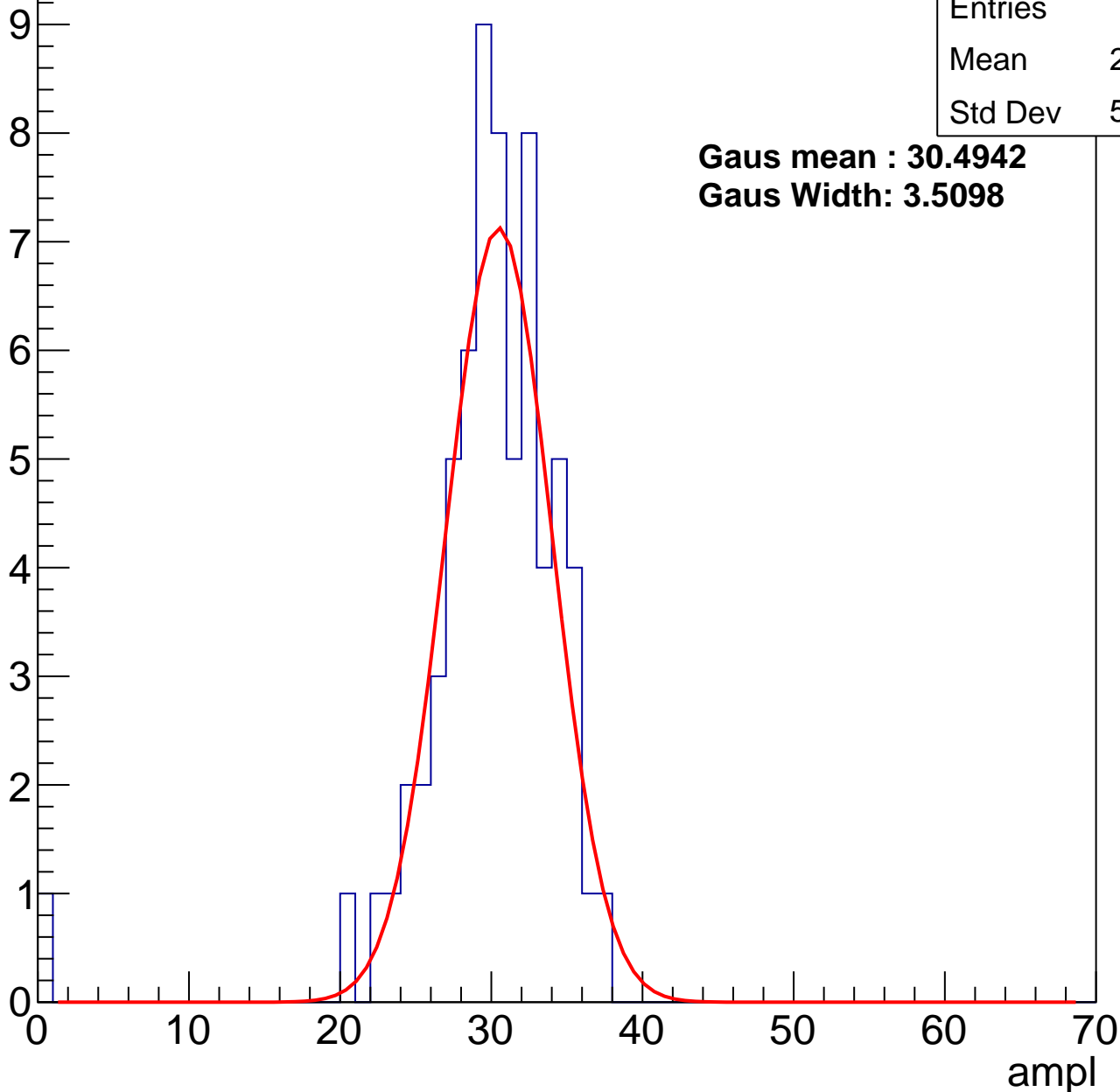
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.42
Std Dev	5.002

Gaus mean : 30.4942

Gaus Width: 3.5098



B1L101S, U26-ch32, adc1

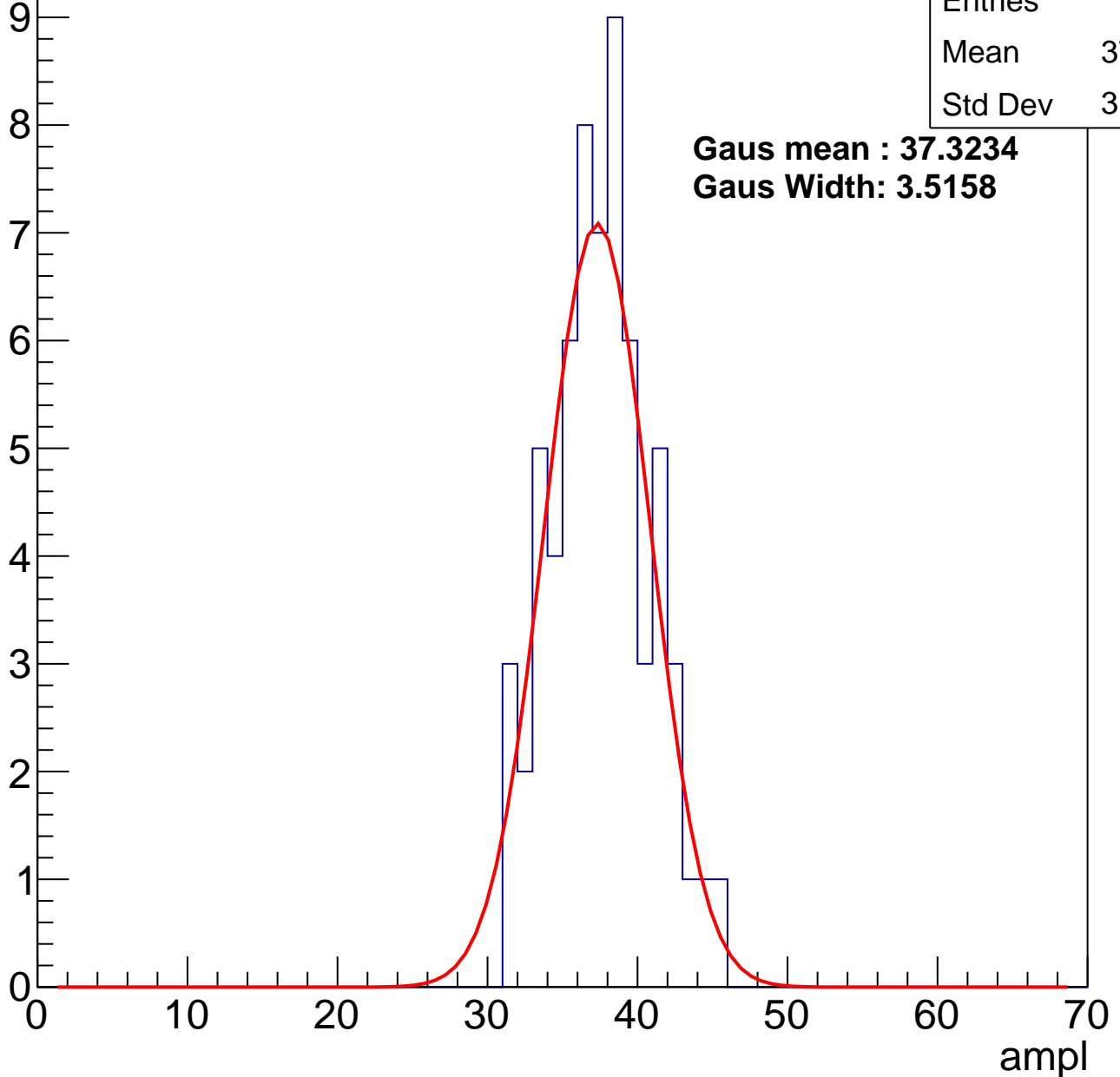
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.09
Std Dev	3.234

Gaus mean : 37.3234

Gaus Width: 3.5158



B1L101S, U26-ch32, adc2

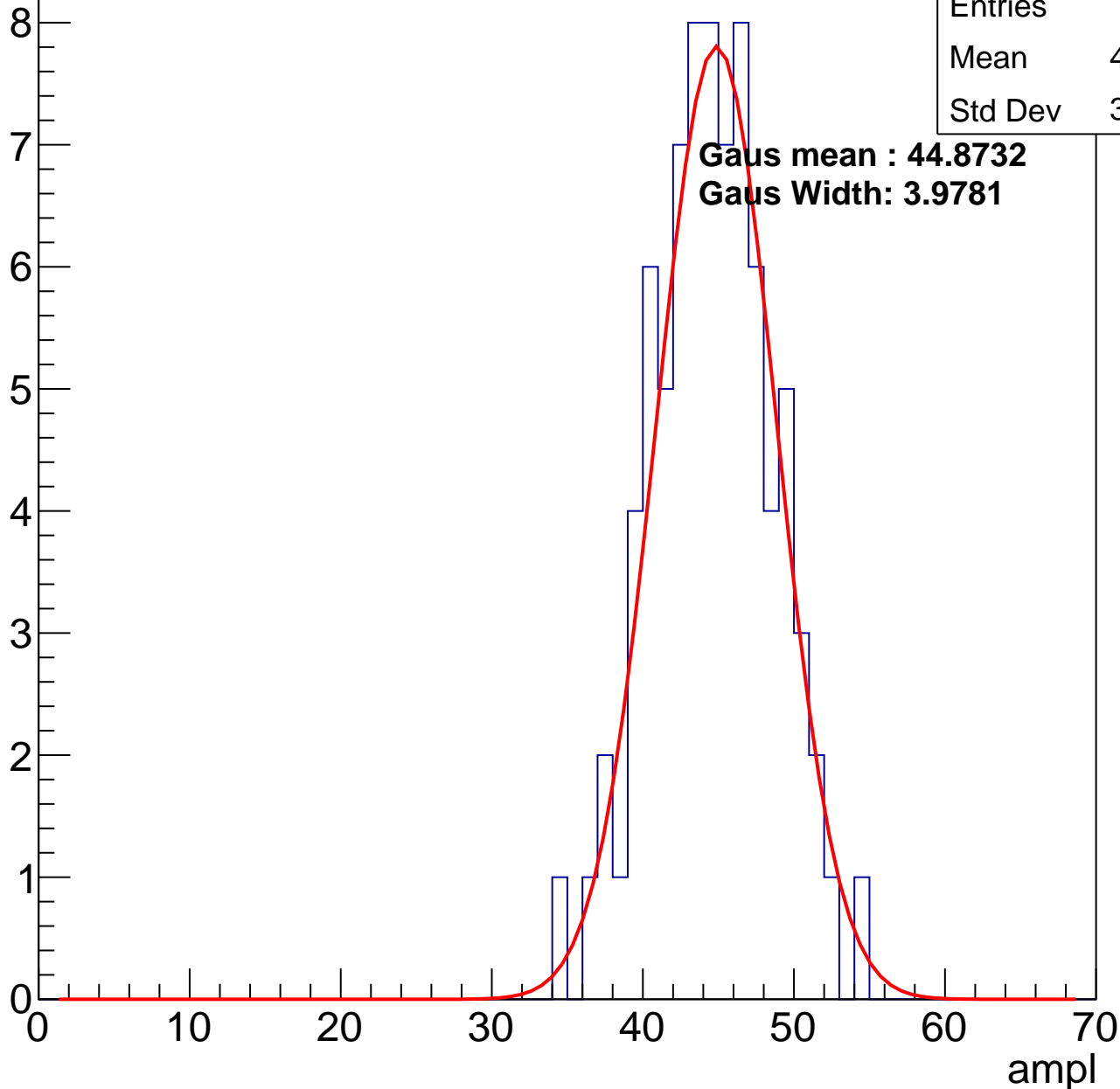
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	44.16
Std Dev	3.913

Gaus mean : 44.8732

Gaus Width: 3.9781

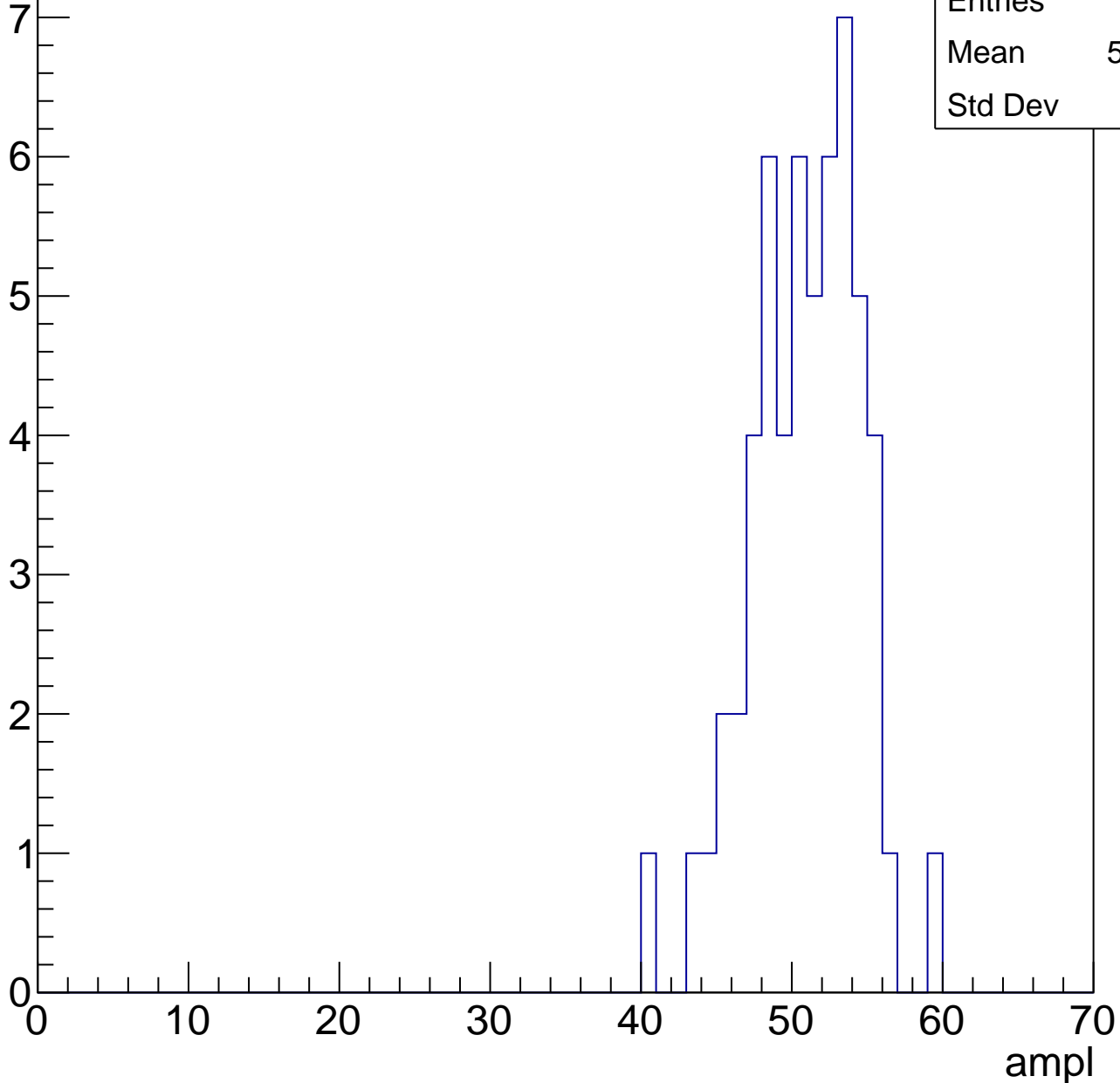


B1L101S, U26-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

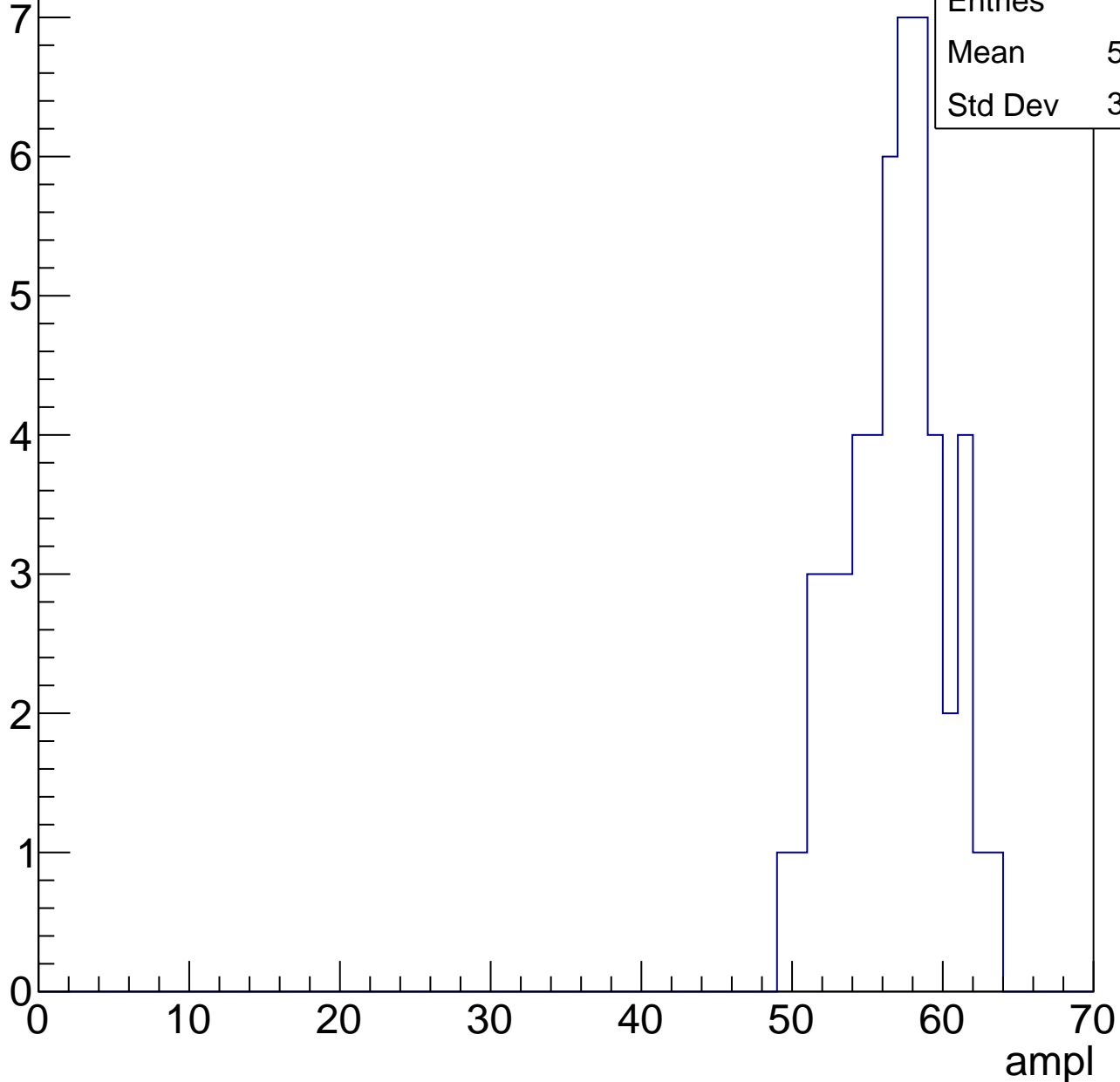
Entries	56
Mean	50.43
Std Dev	3.56



B1L101S, U26-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

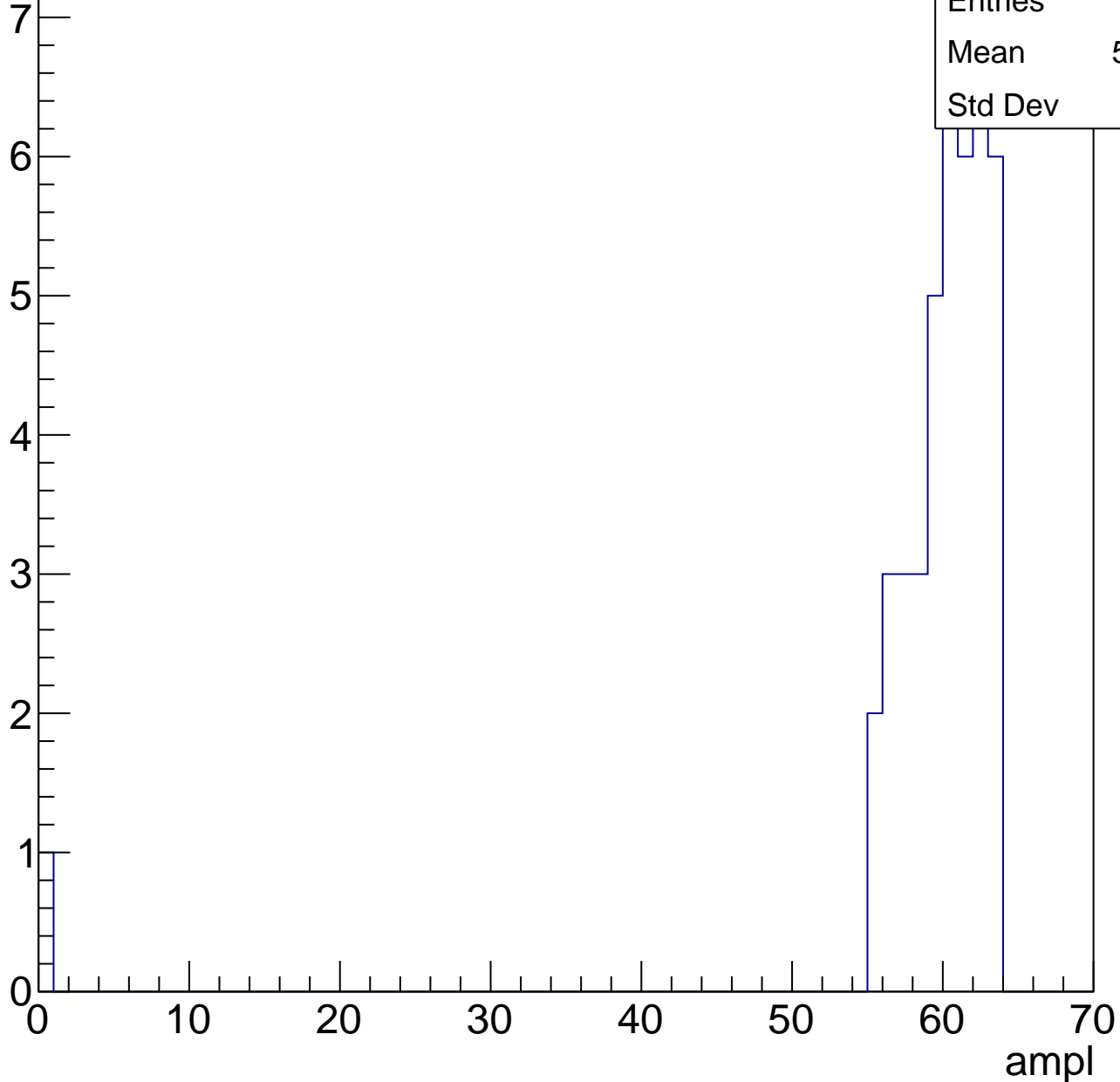


Entries	51
Mean	56.25
Std Dev	3.253

B1L101S, U26-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

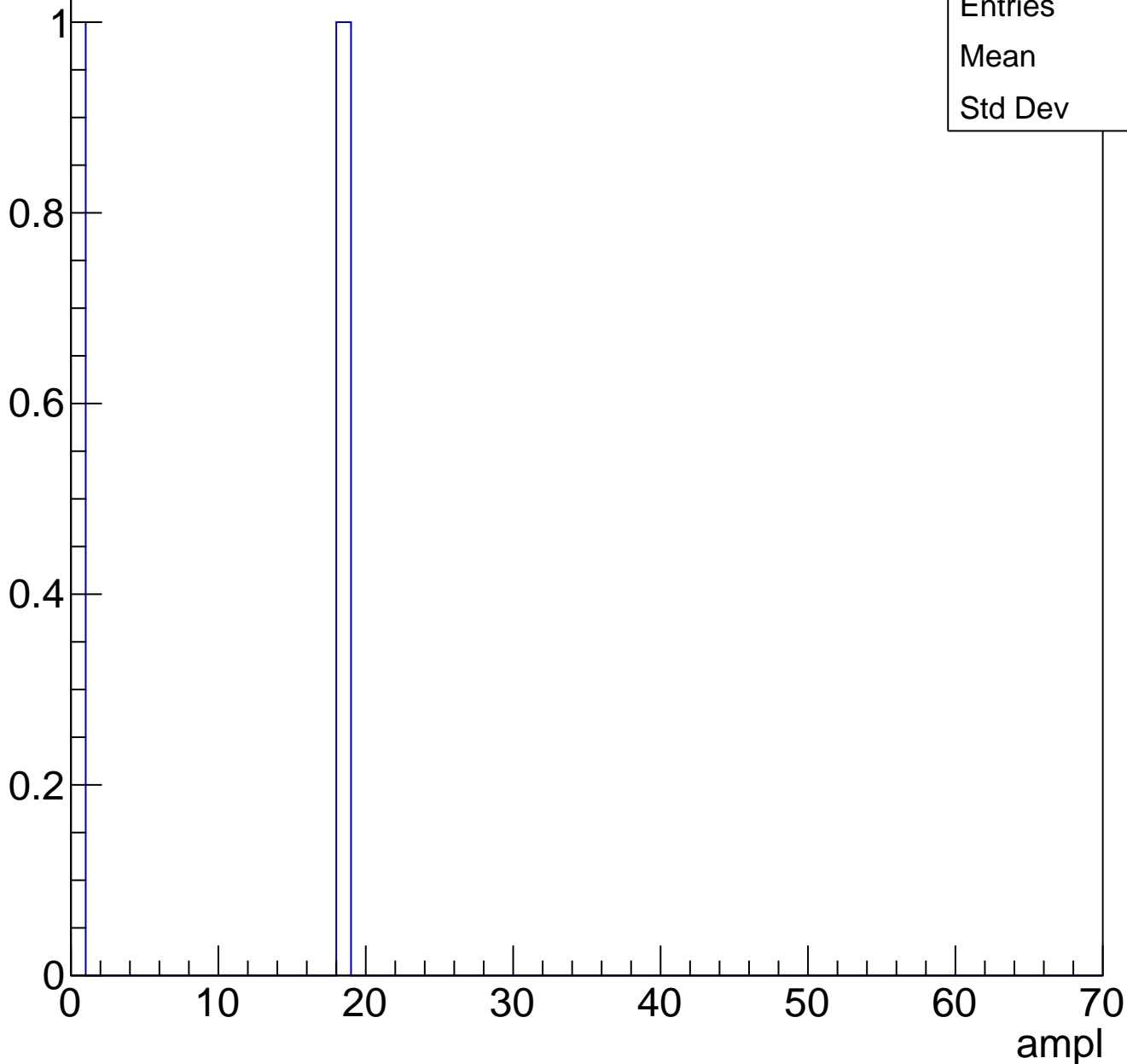
Entry



B1L101S, U26-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch33, adc0

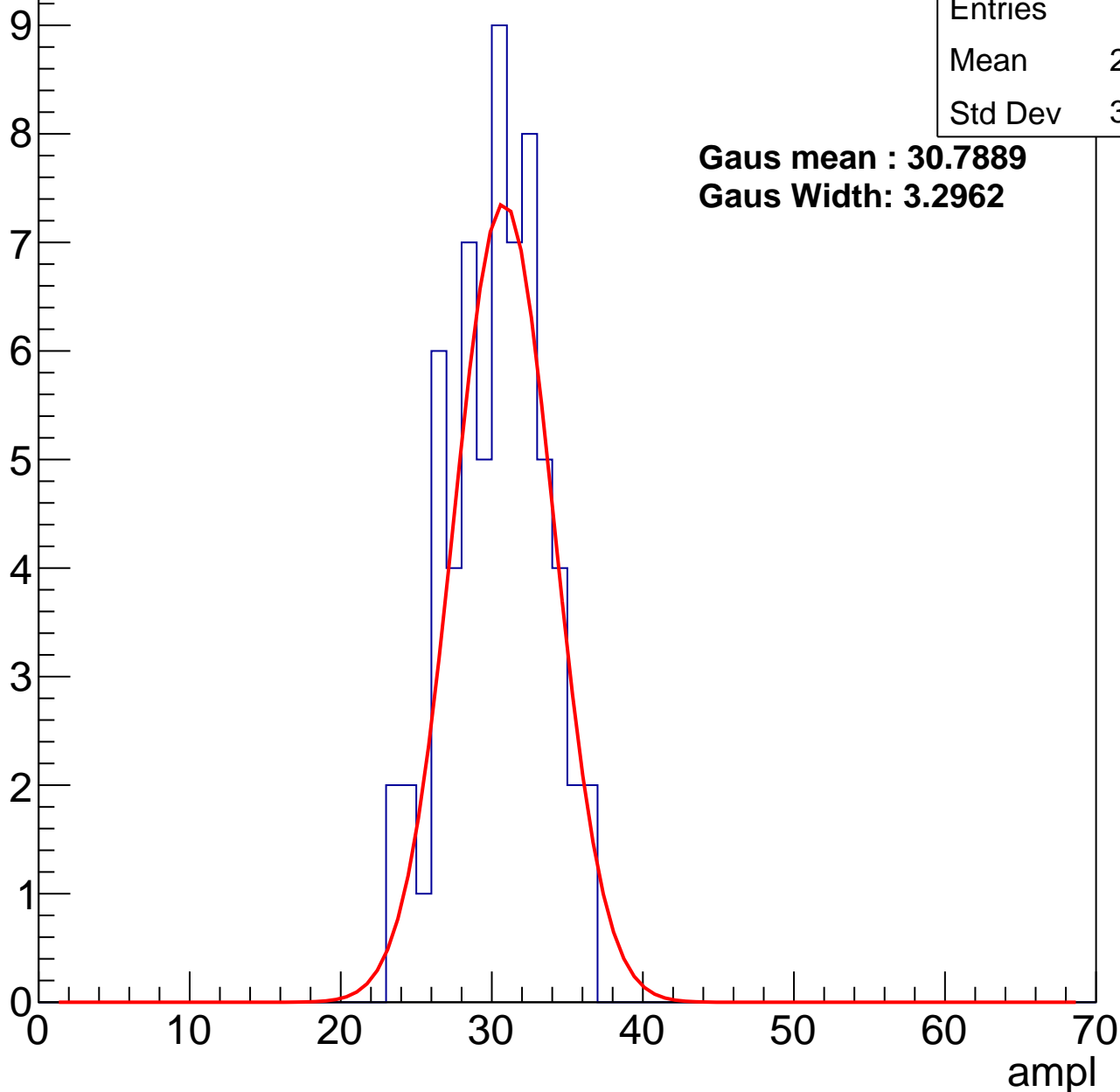
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.84
Std Dev	3.134

Gaus mean : 30.7889

Gaus Width: 3.2962



B1L101S, U26-ch33, adc1

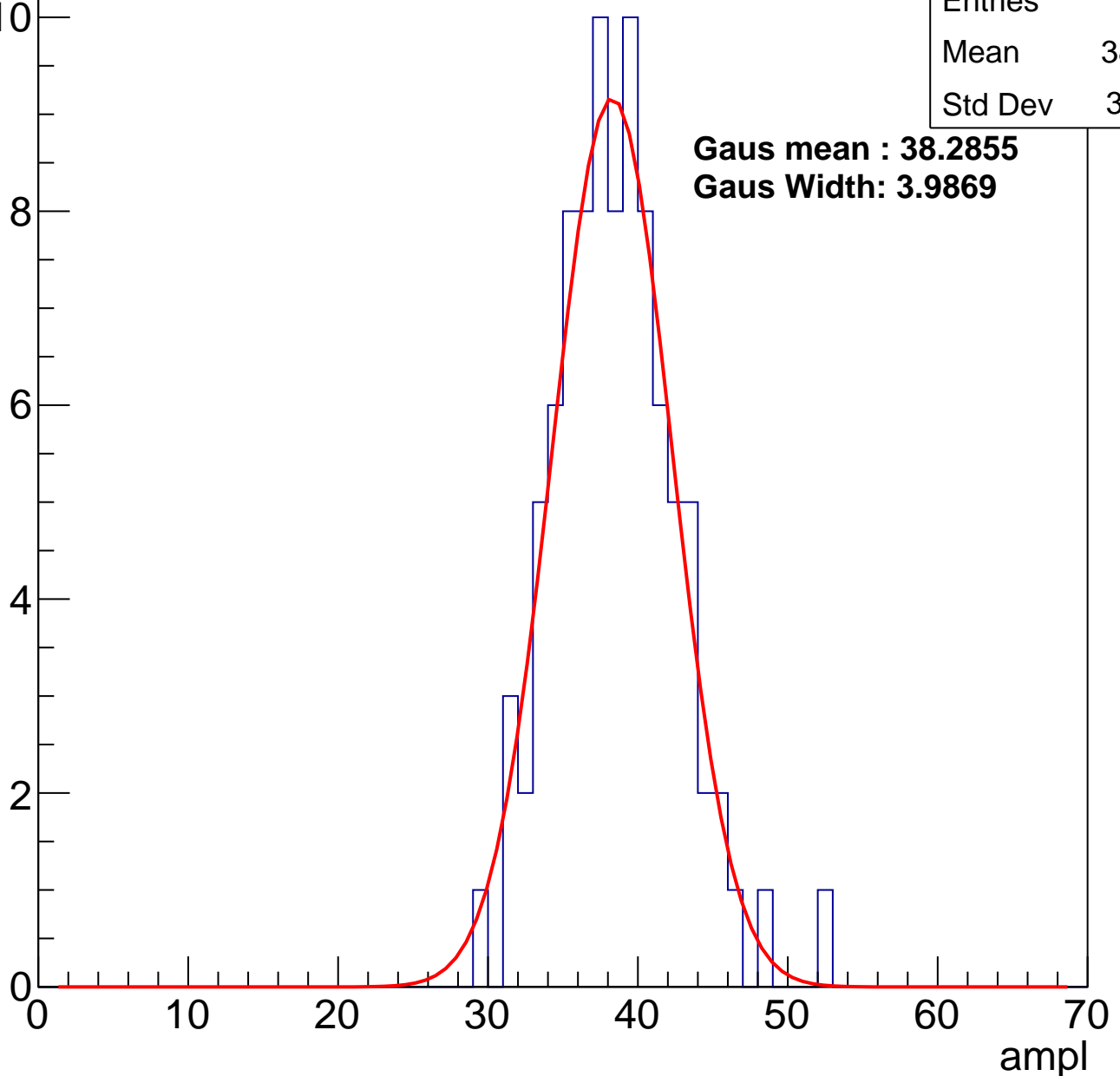
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	92
Mean	38.07
Std Dev	3.991

Gaus mean : 38.2855

Gaus Width: 3.9869



B1L101S, U26-ch33, adc2

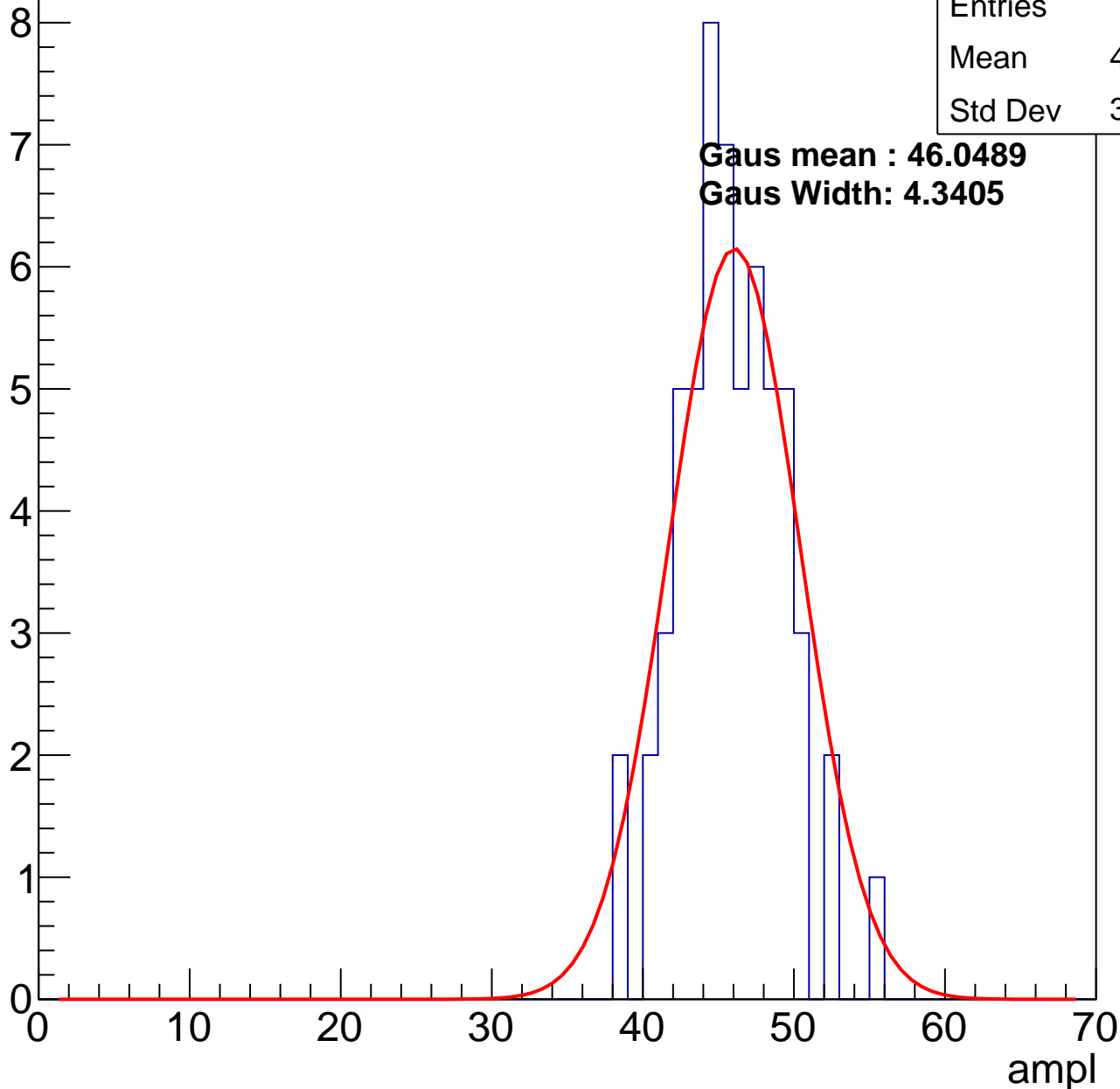
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	45.37
Std Dev	3.414

Gaus mean : 46.0489

Gaus Width: 4.3405

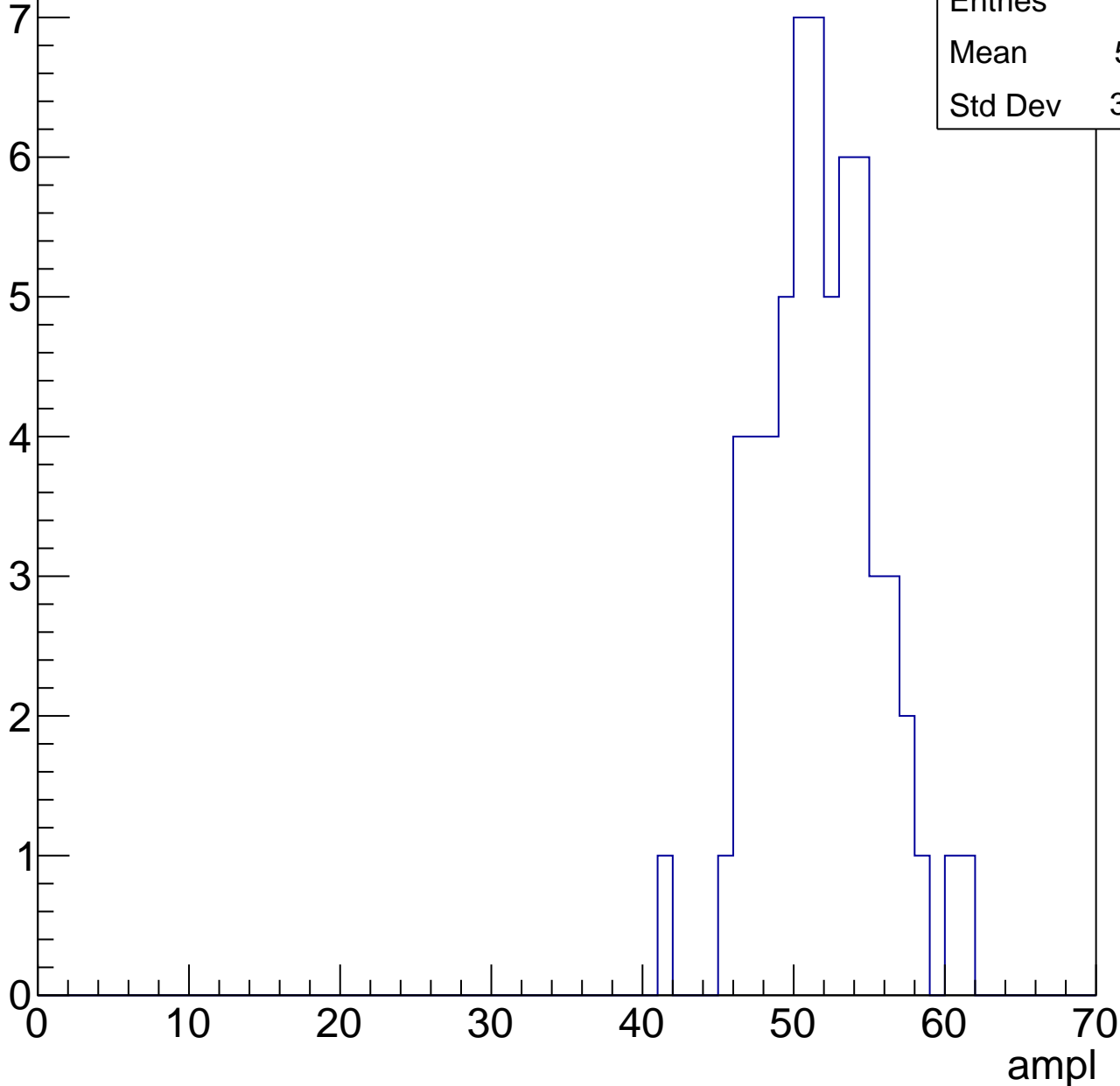


B1L101S, U26-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

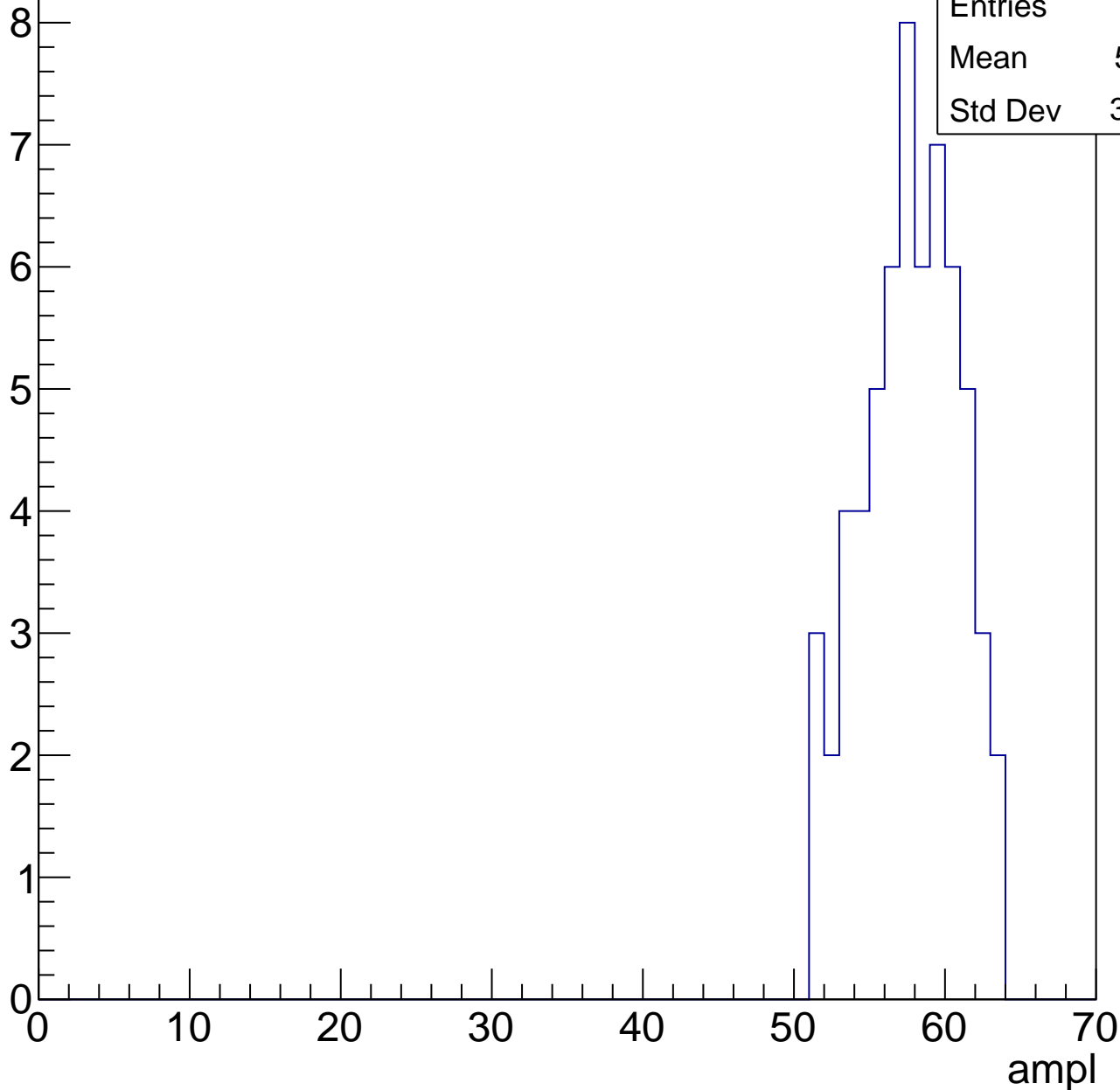
Entries	61
Mean	51.31
Std Dev	3.766



B1L101S, U26-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

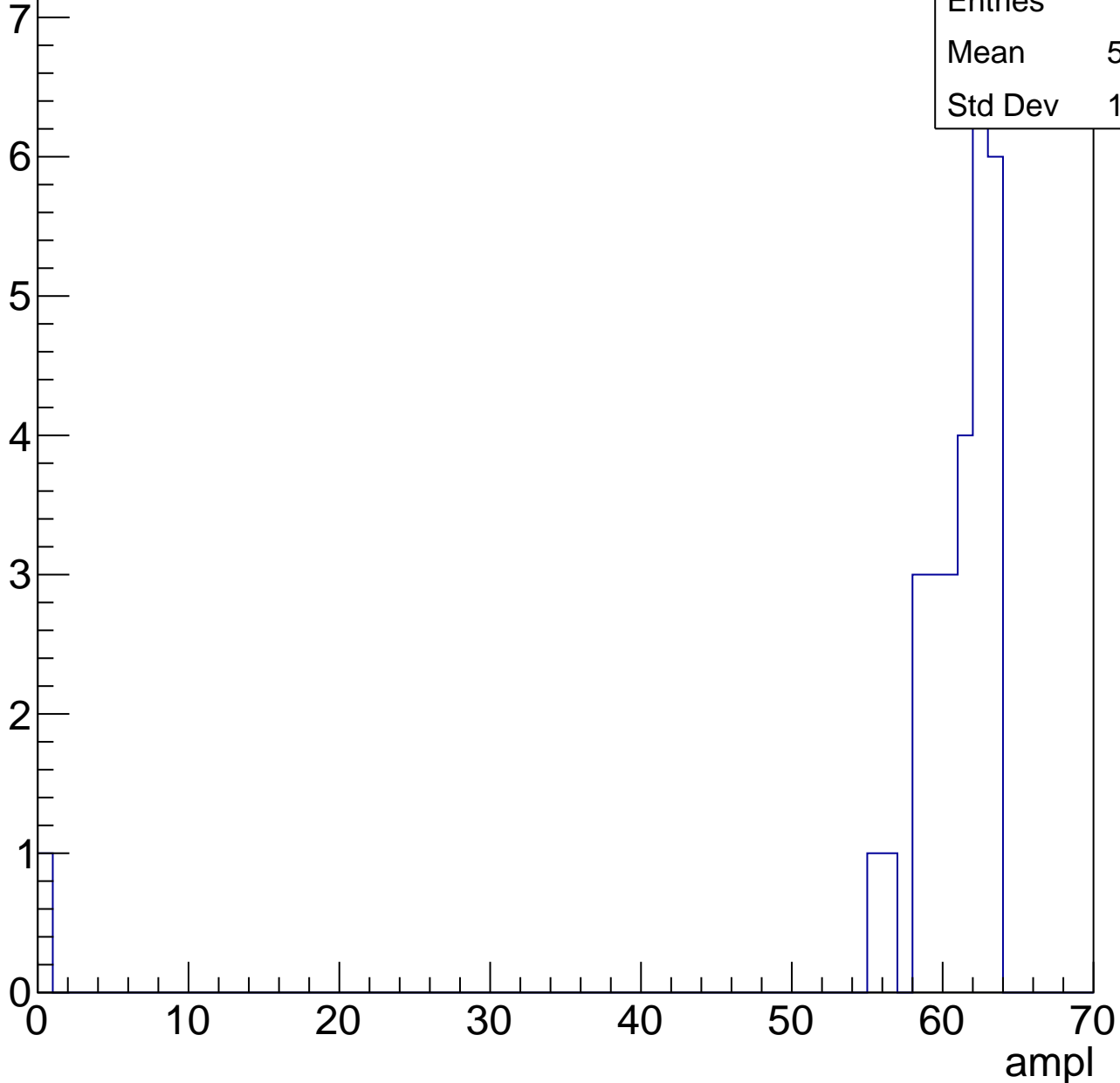


B1L101S, U26-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.55
Std Dev	11.27



B1L101S, U26-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch34, adc0

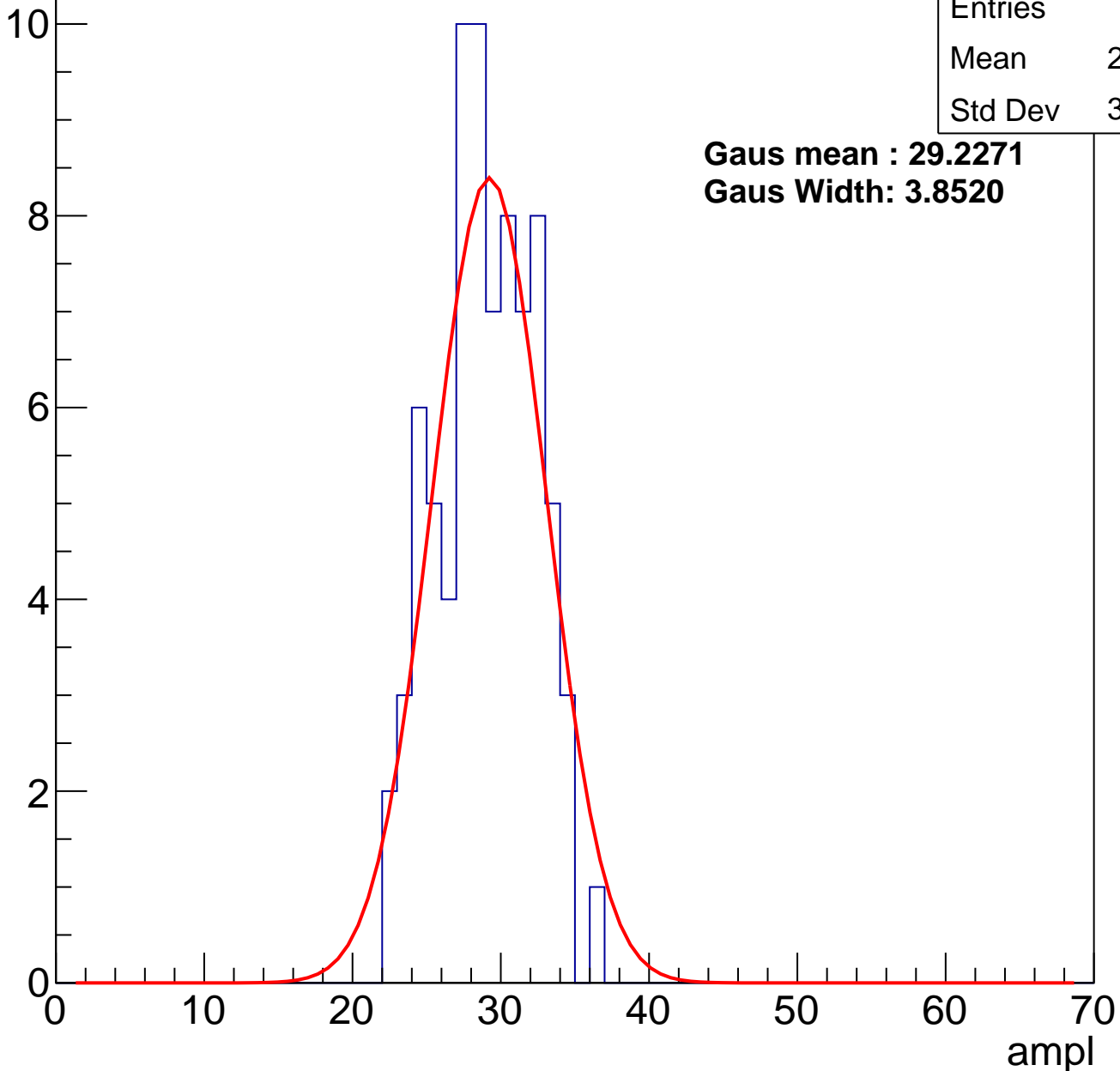
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	28.54
Std Dev	3.217

Gaus mean : 29.2271

Gaus Width: 3.8520

Entry



B1L101S, U26-ch34, adc1

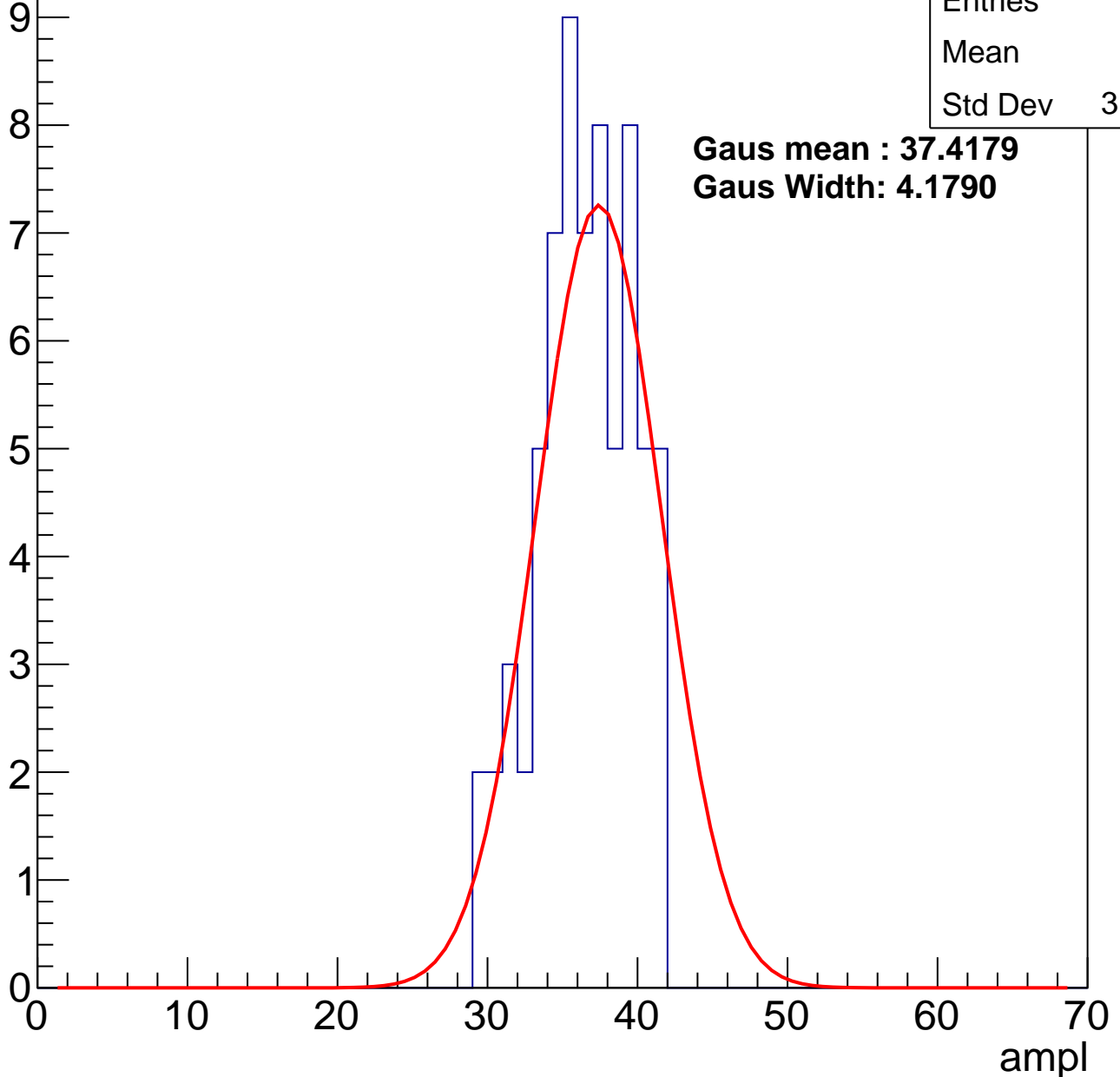
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36
Std Dev	3.125

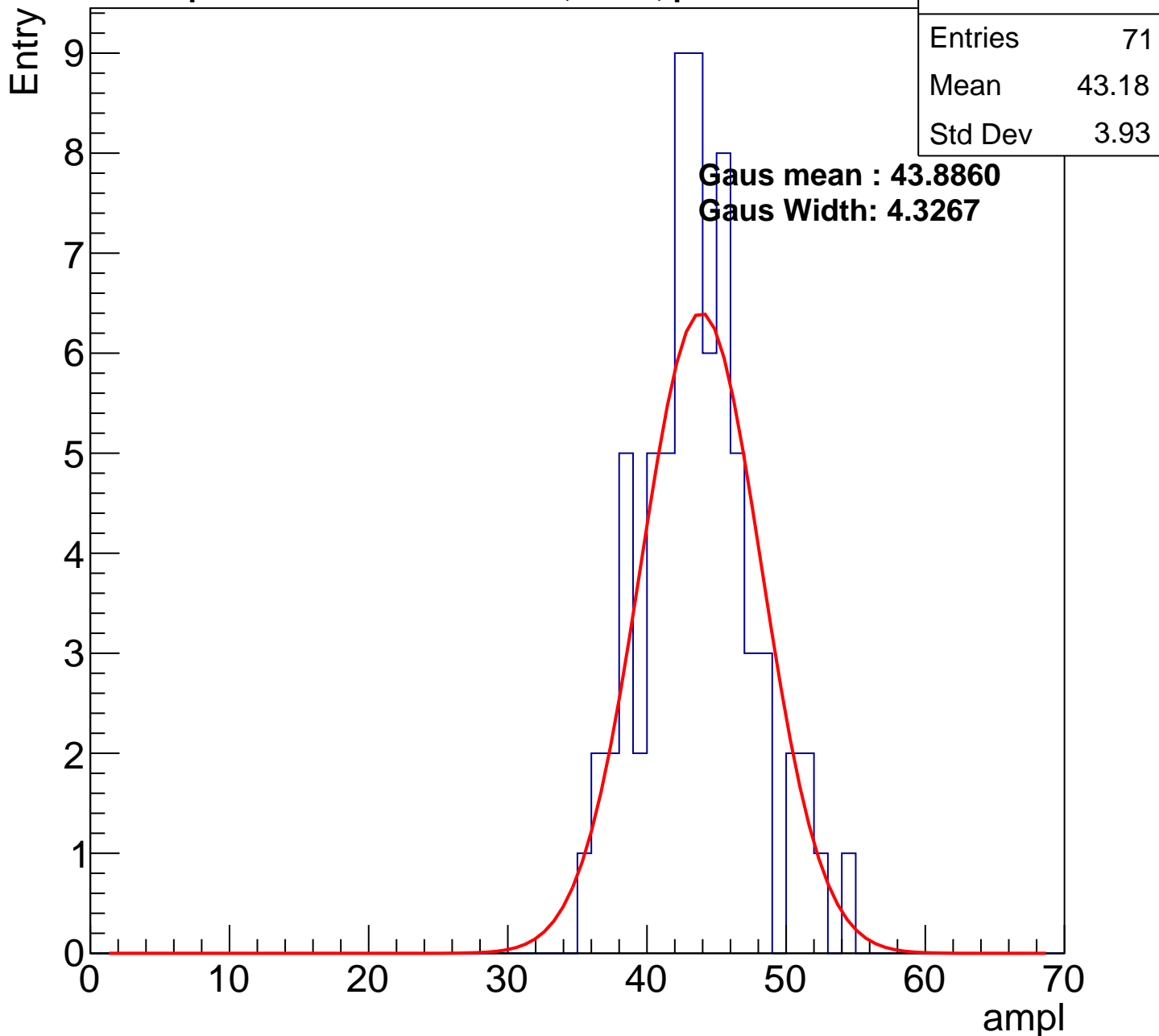
Gaus mean : 37.4179

Gaus Width: 4.1790



B1L101S, U26-ch34, adc2

calib_packv5_042523_0143.root, FC#0, port D2

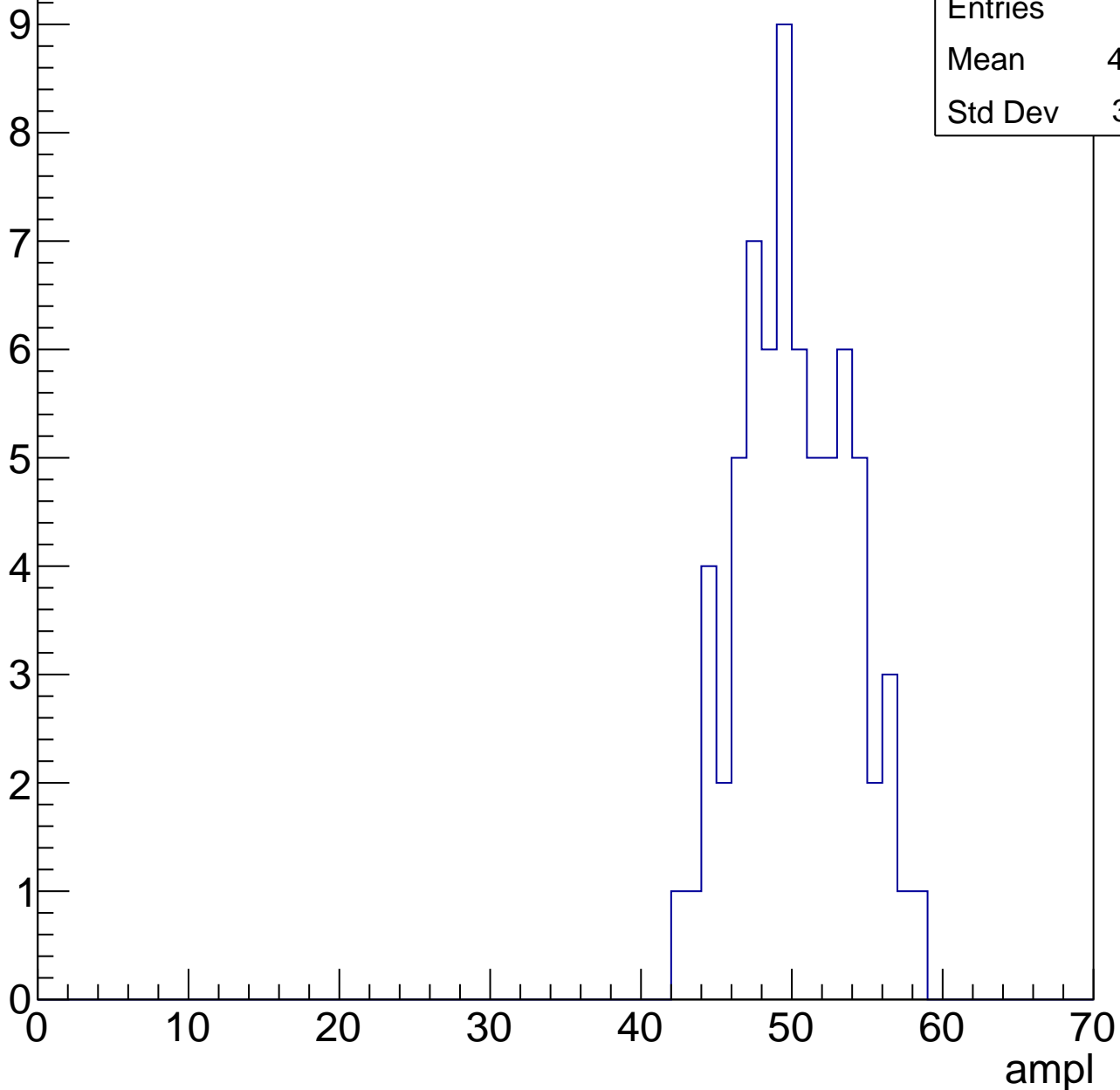


B1L101S, U26-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.78
Std Dev	3.631

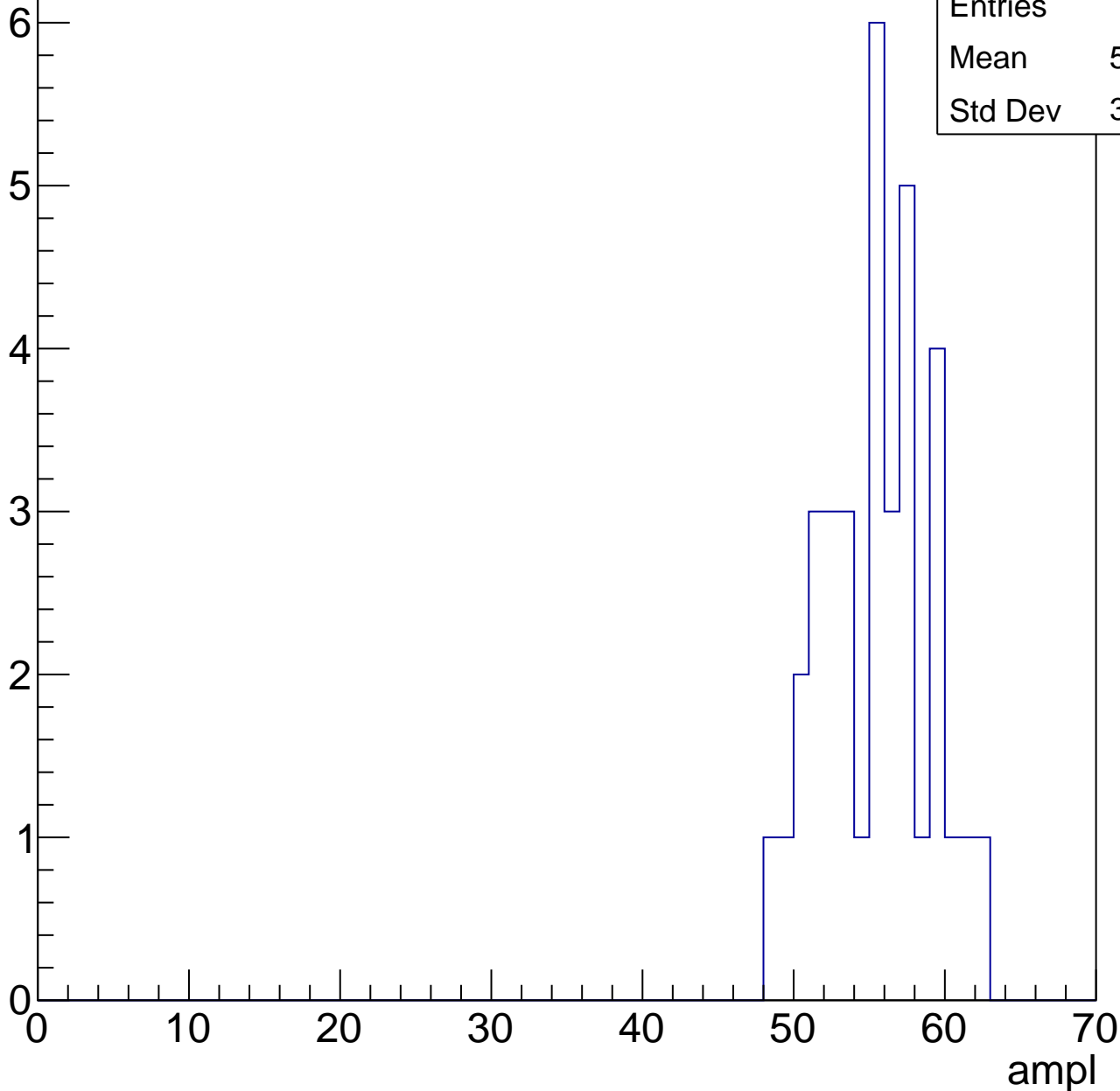


B1L101S, U26-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	54.97
Std Dev	3.452

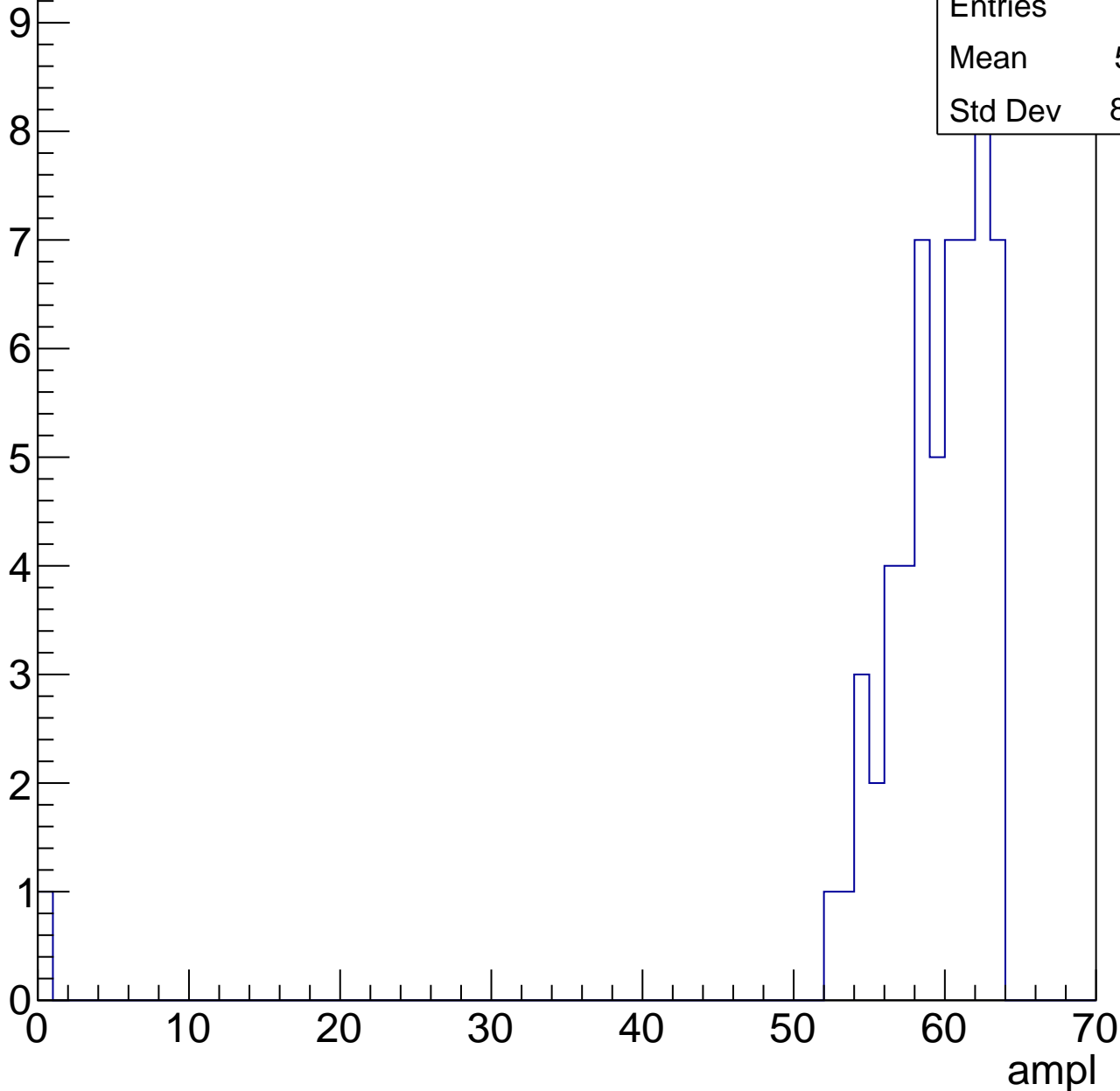


B1L101S, U26-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

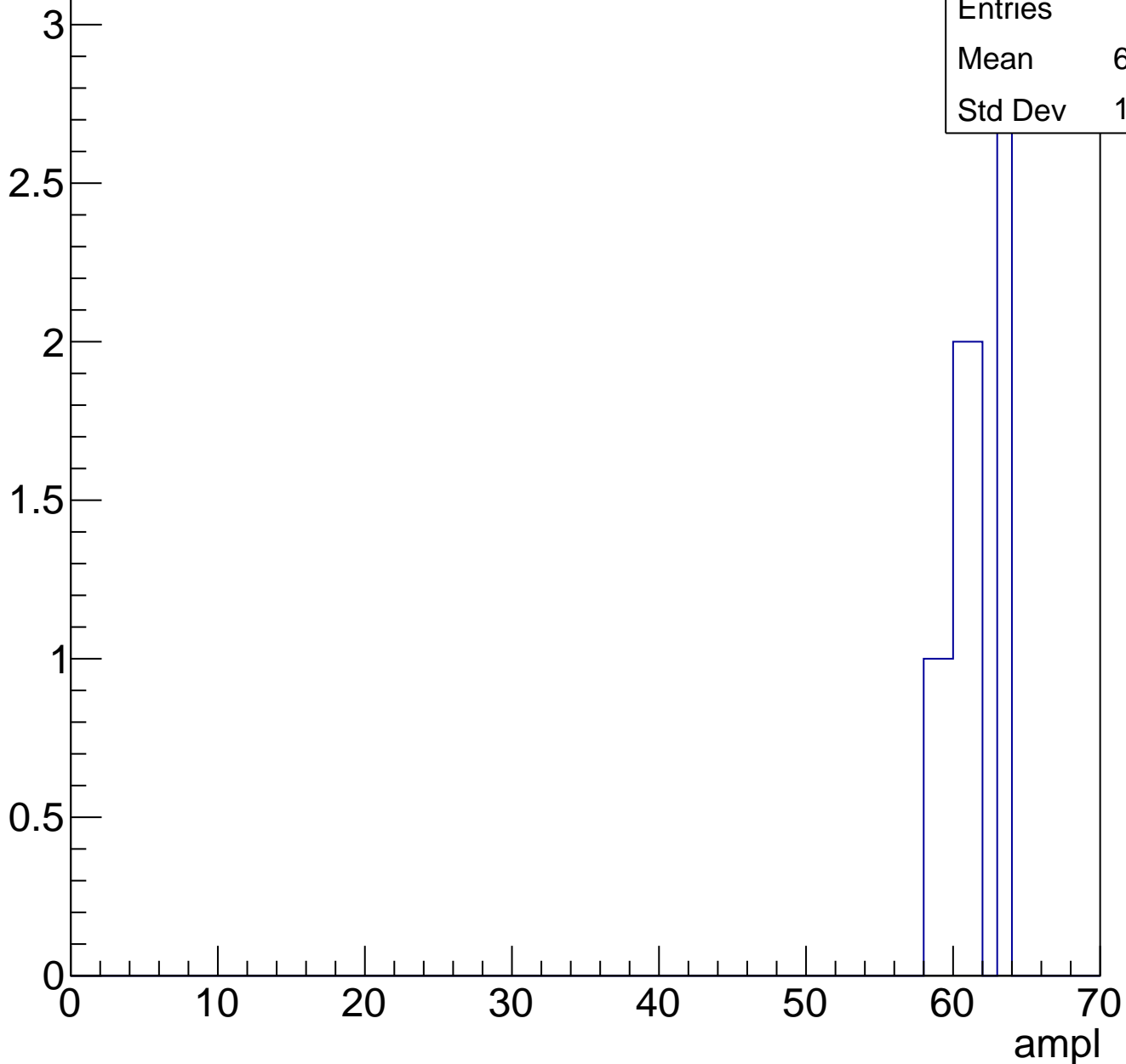
Entries	58
Mean	58.21
Std Dev	8.225



B1L101S, U26-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch35, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	29.21
Std Dev	5.011

Gaus mean : 29.7684

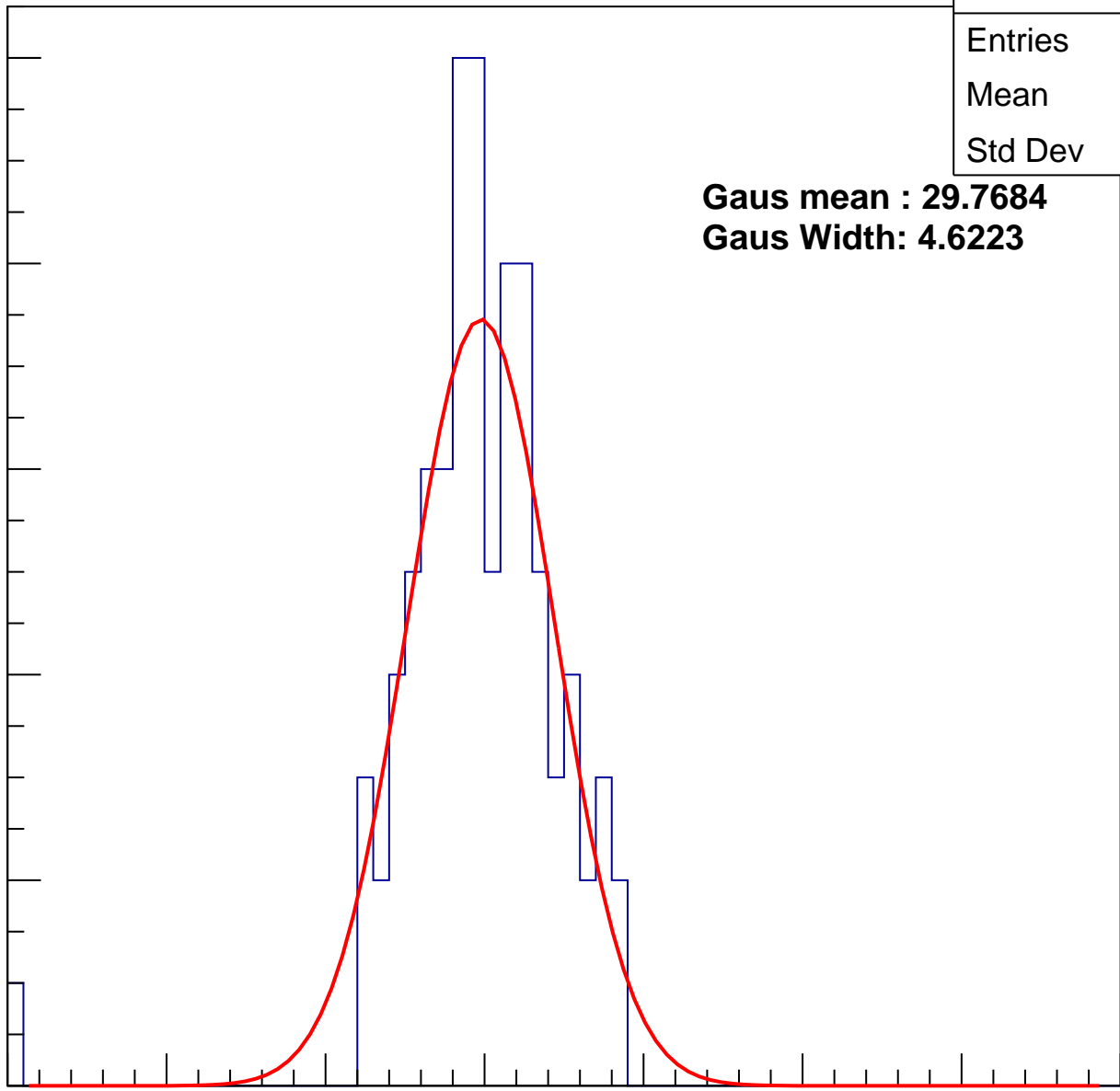
Gaus Width: 4.6223

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch35, adc1

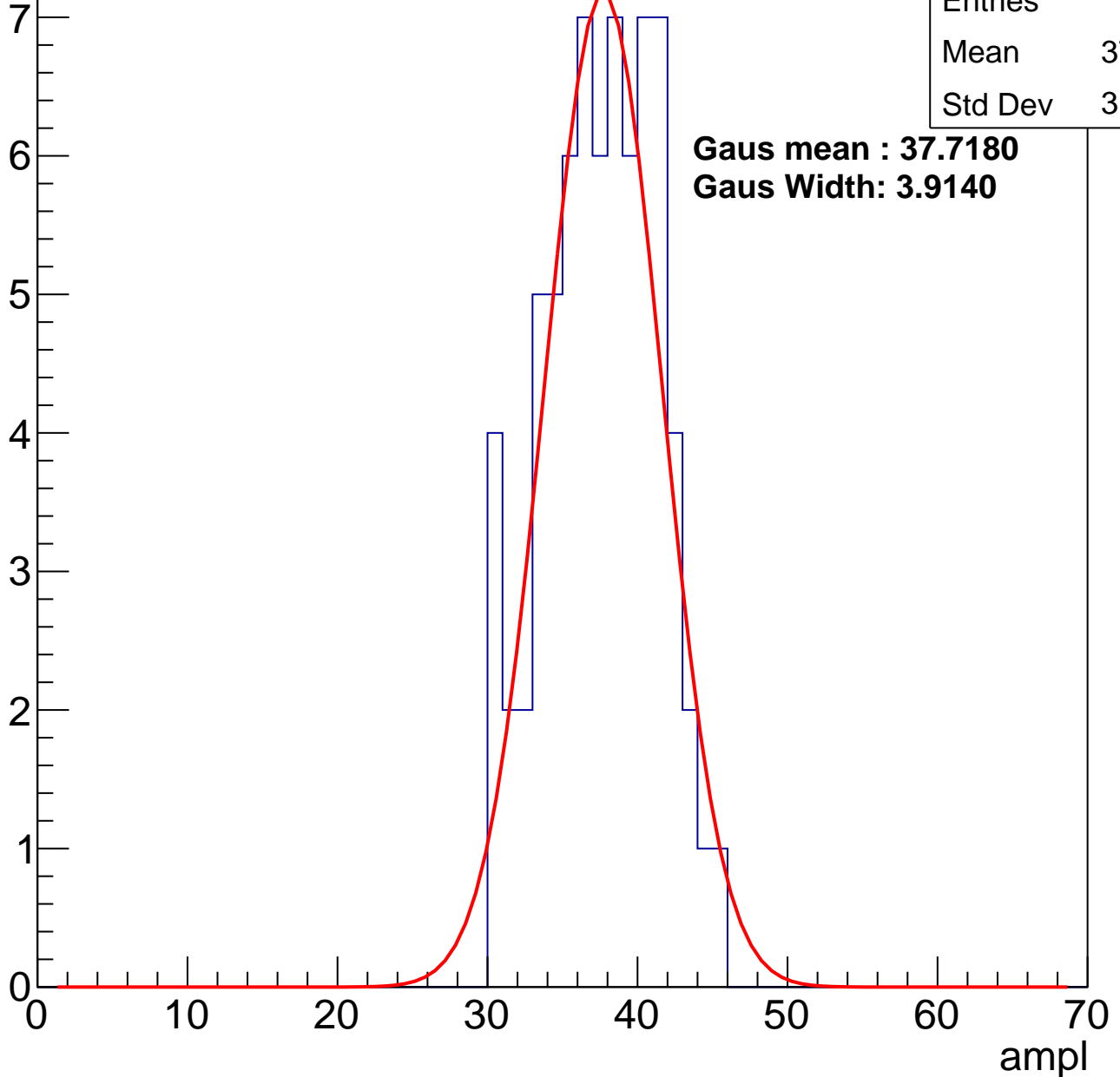
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.15
Std Dev	3.658

Gaus mean : 37.7180

Gaus Width: 3.9140



B1L101S, U26-ch35, adc2

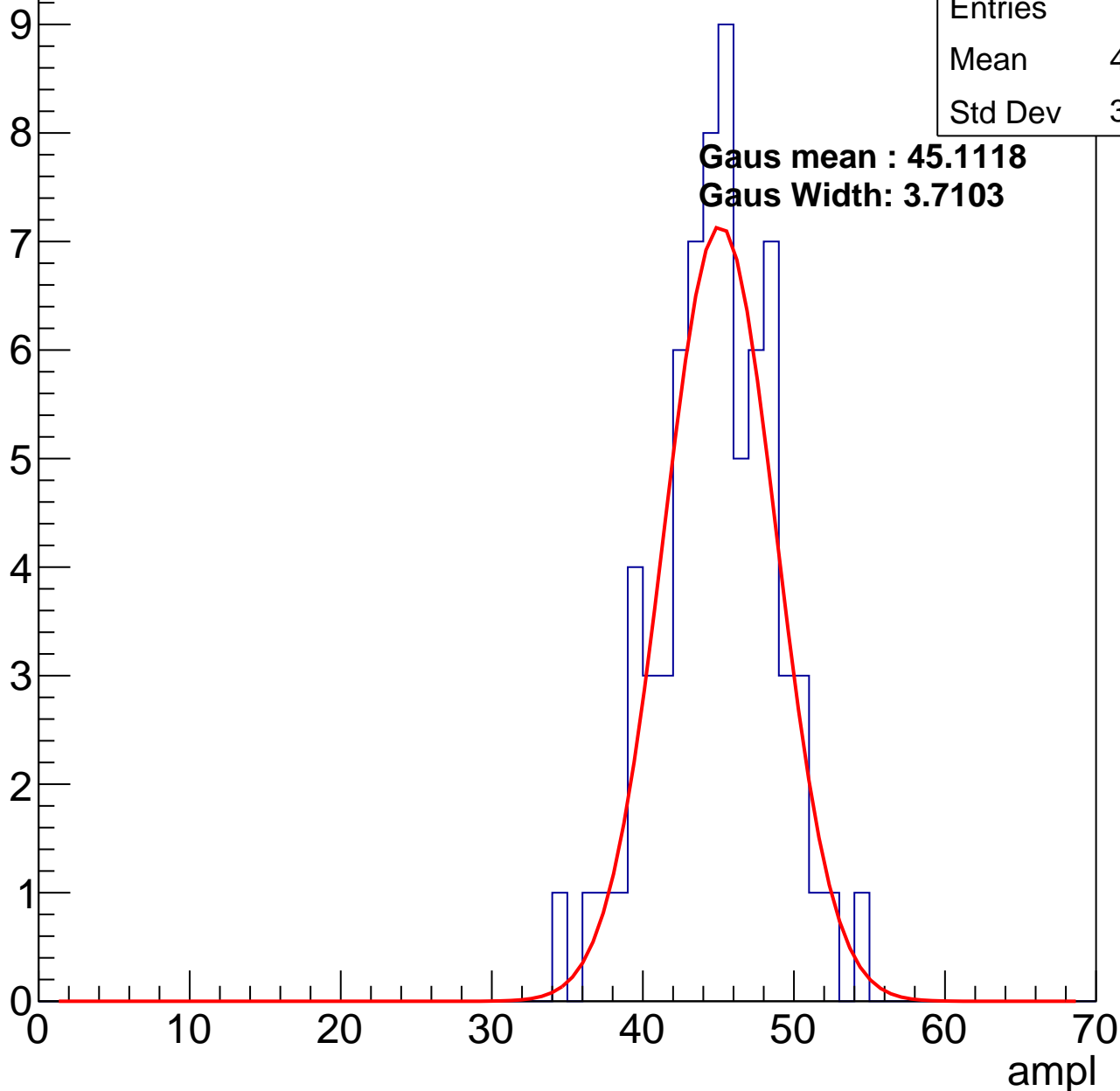
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.45
Std Dev	3.812

Gaus mean : 45.1118

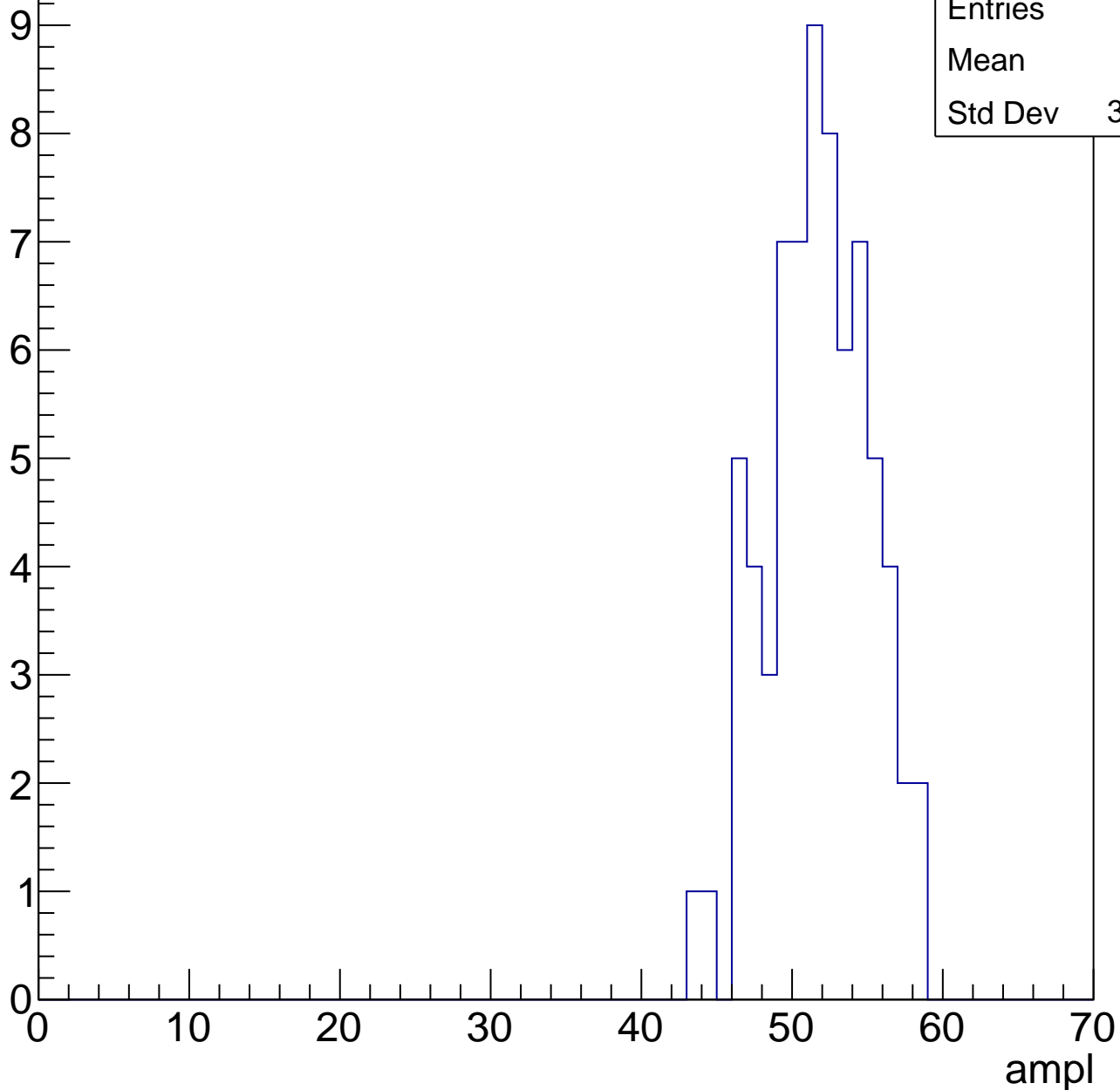
Gaus Width: 3.7103



B1L101S, U26-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch35, adc4

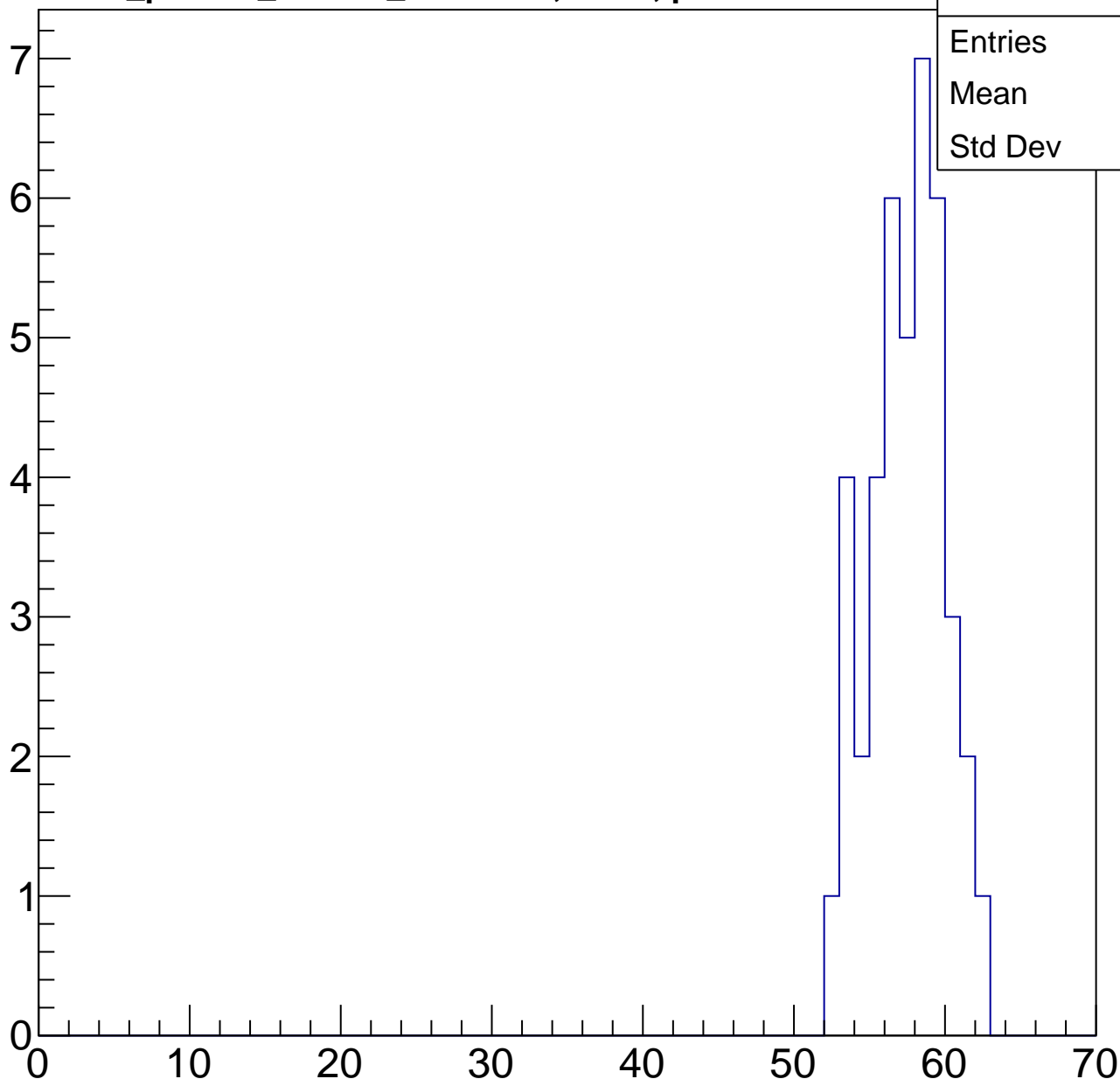
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	57
Std Dev	2.44

ampl



B1L101S, U26-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 42

Mean 59.38

Std Dev 9.479

8

6

4

2

0

0

10

20

30

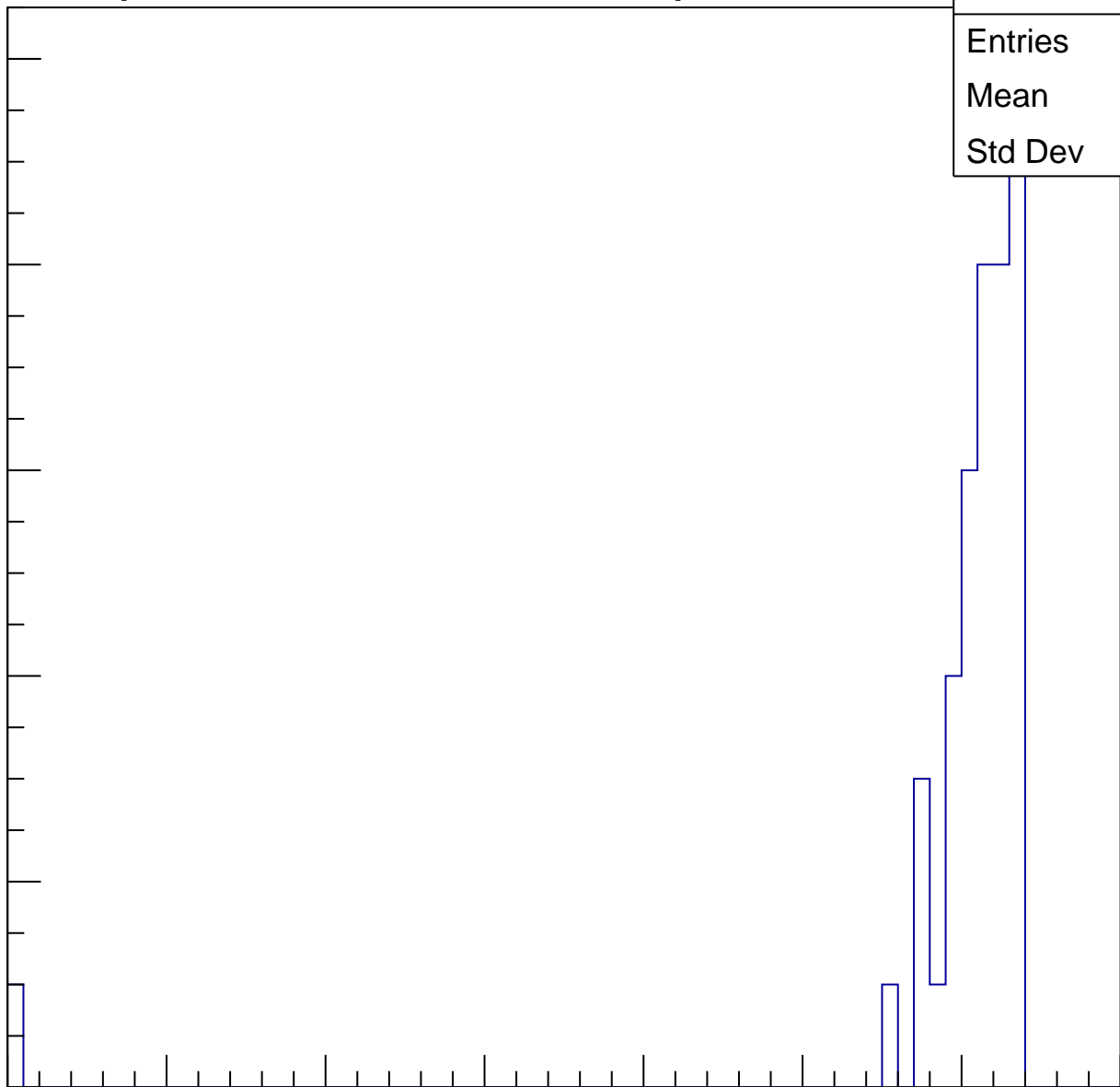
40

50

60

70

ampl



B1L101S, U26-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch36, adc0

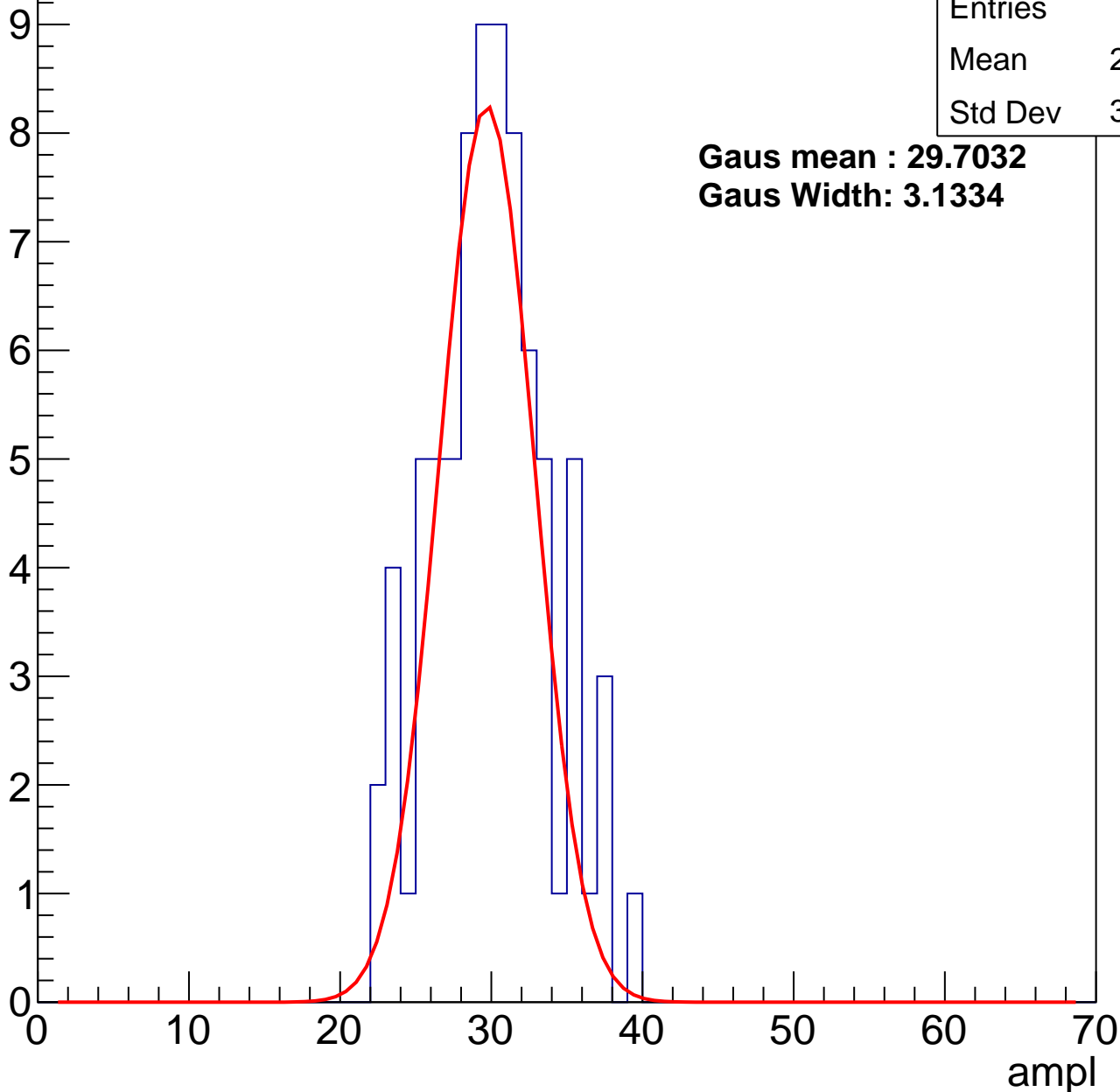
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.55
Std Dev	3.785

Gaus mean : 29.7032

Gaus Width: 3.1334



B1L101S, U26-ch36, adc1

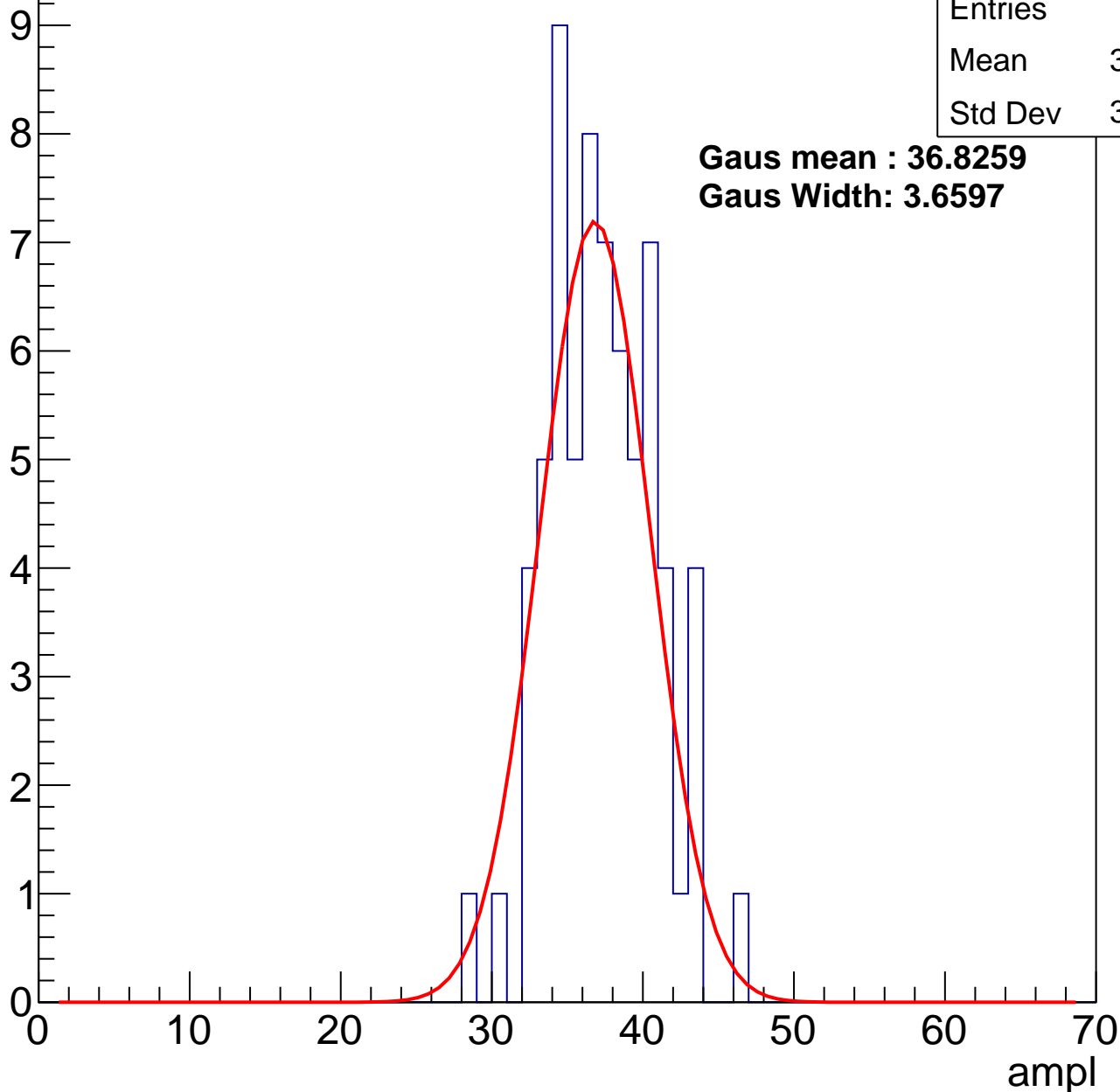
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.85
Std Dev	3.474

Gaus mean : 36.8259

Gaus Width: 3.6597



B1L101S, U26-ch36, adc2

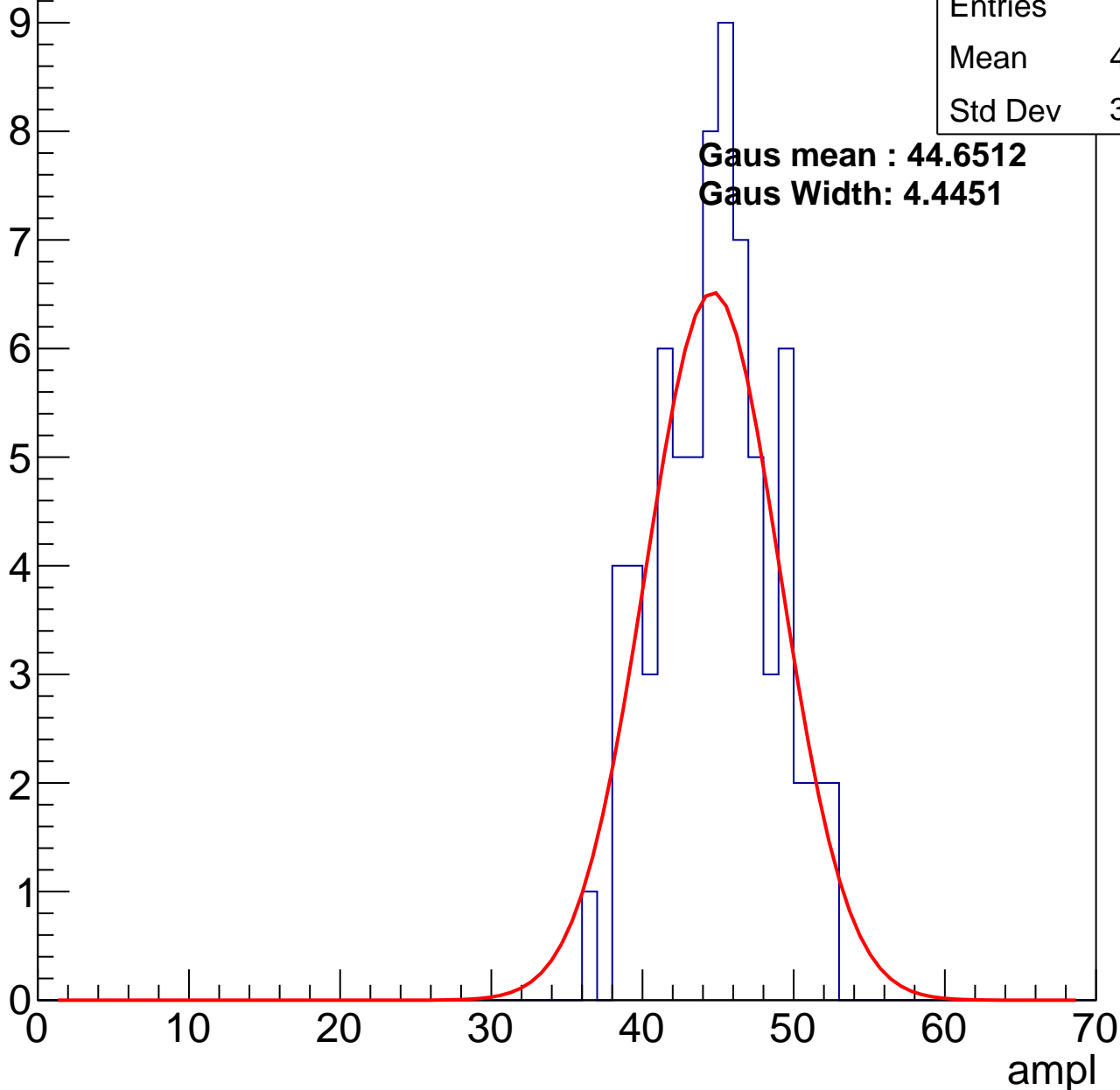
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	44.35
Std Dev	3.735

Gaus mean : 44.6512

Gaus Width: 4.4451

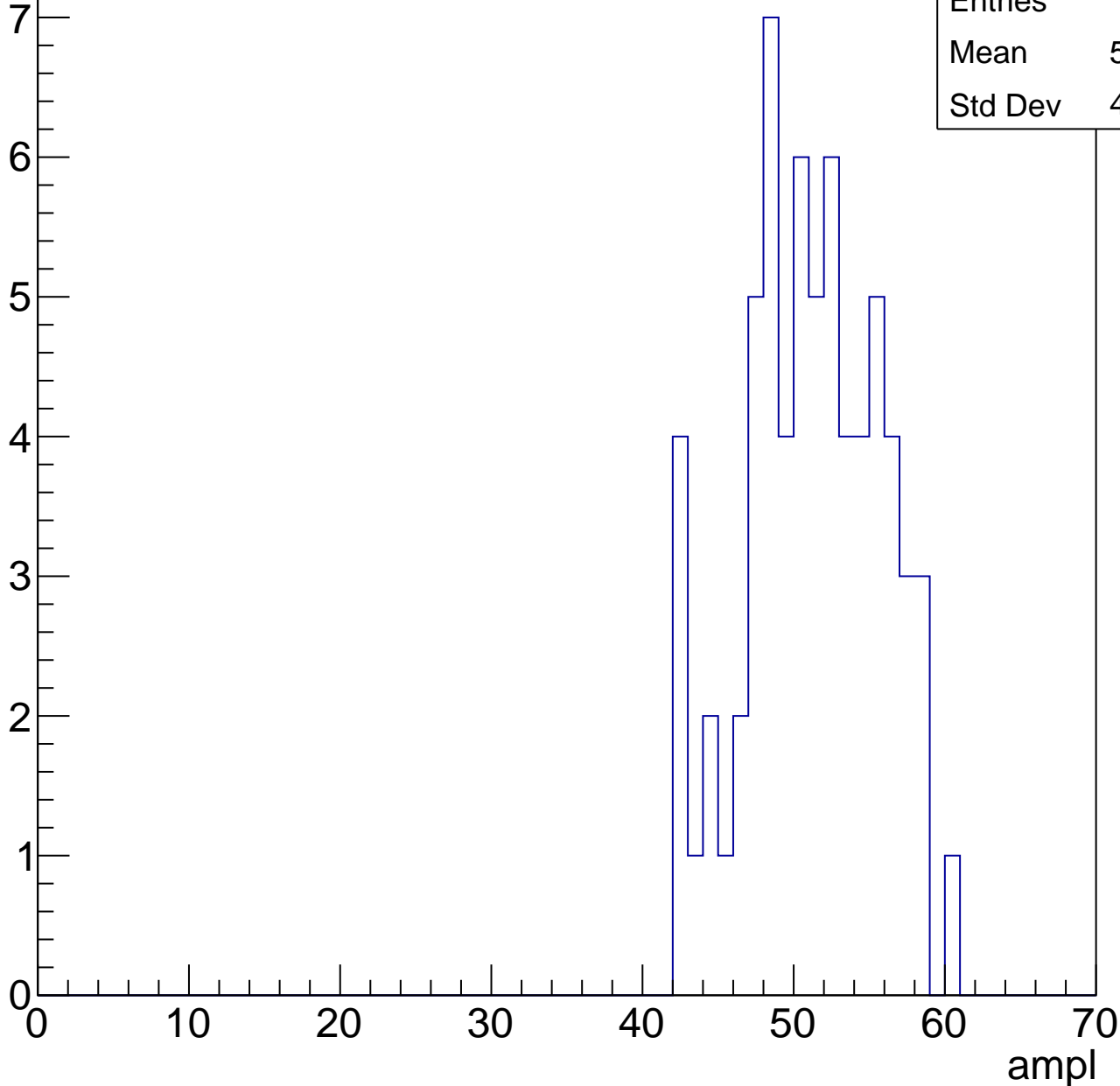


B1L101S, U26-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.78
Std Dev	4.418

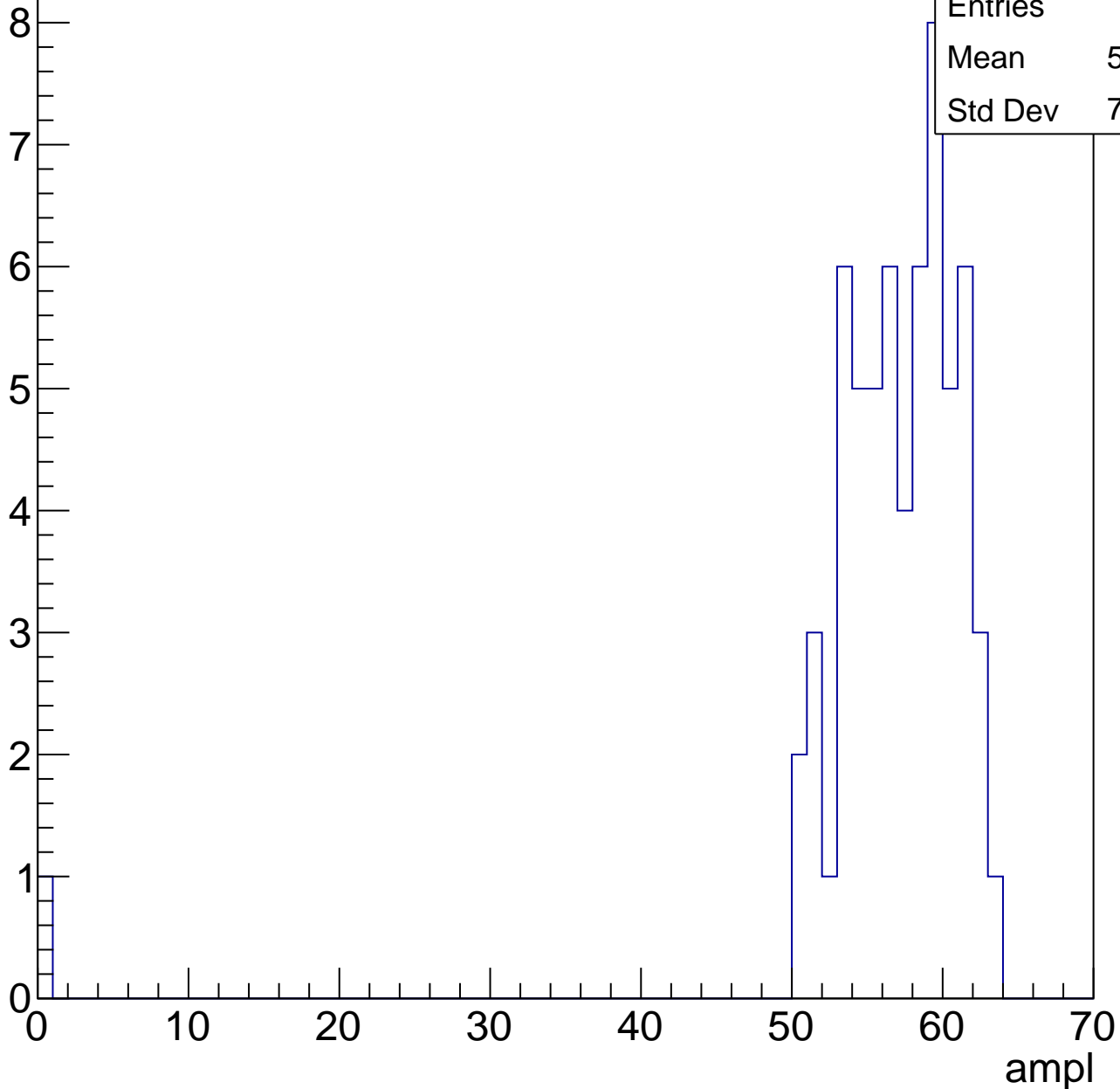


B1L101S, U26-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.92
Std Dev	7.895

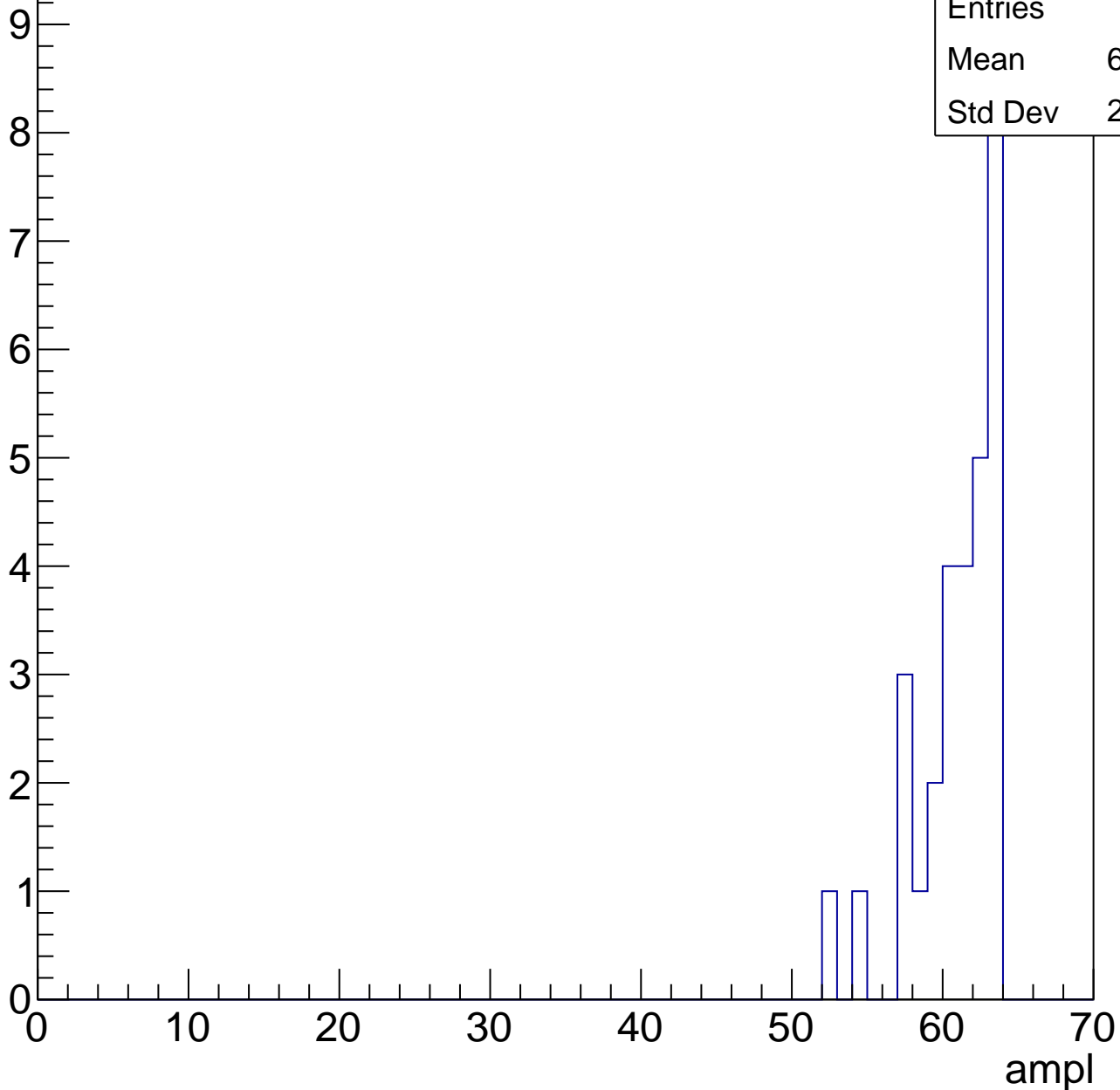


B1L101S, U26-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	60.47
Std Dev	2.778



B1L101S, U26-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L101S, U26-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch37, adc0

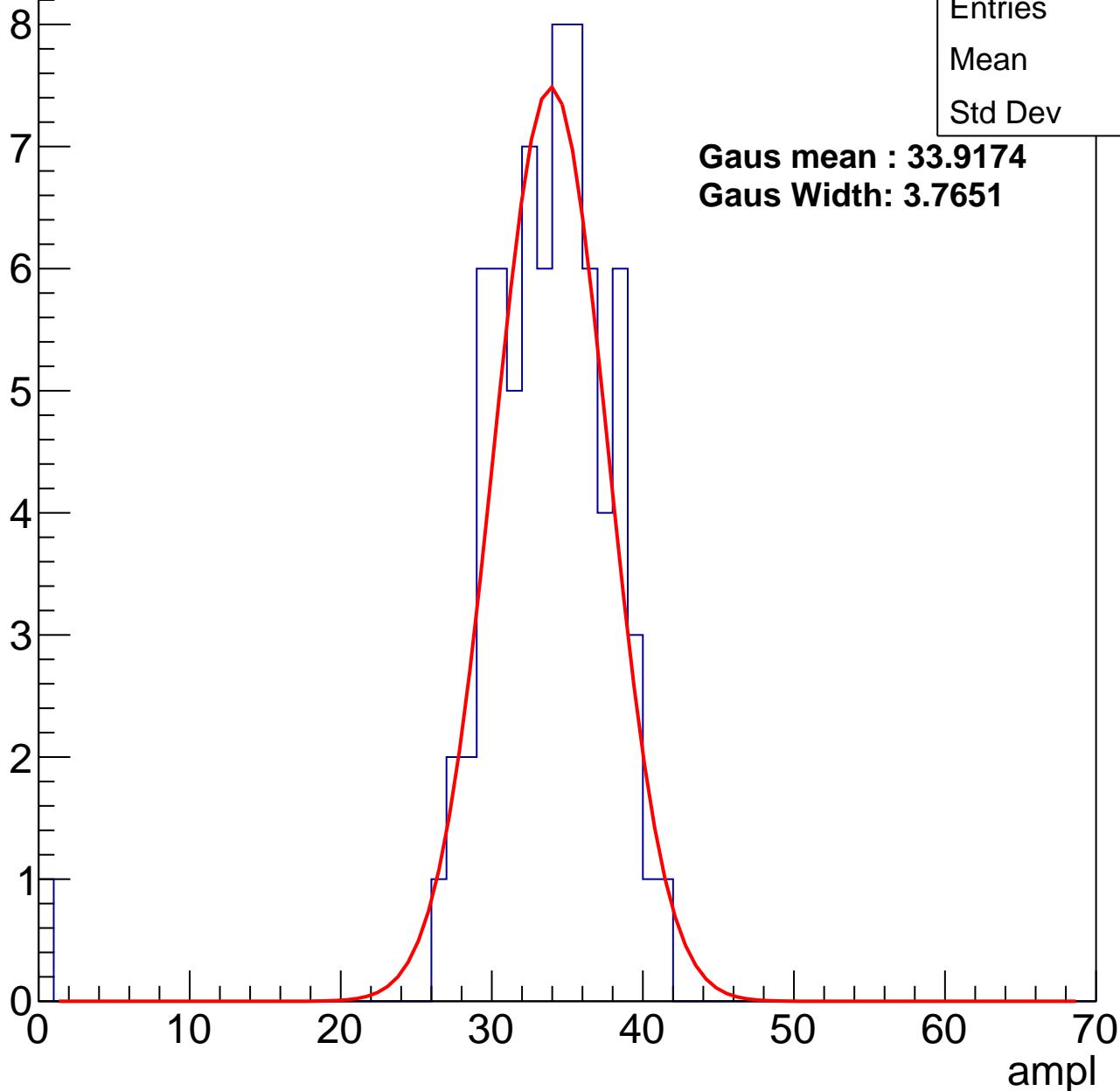
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	33
Std Dev	5.19

Gaus mean : 33.9174

Gaus Width: 3.7651



B1L101S, U26-ch37, adc1

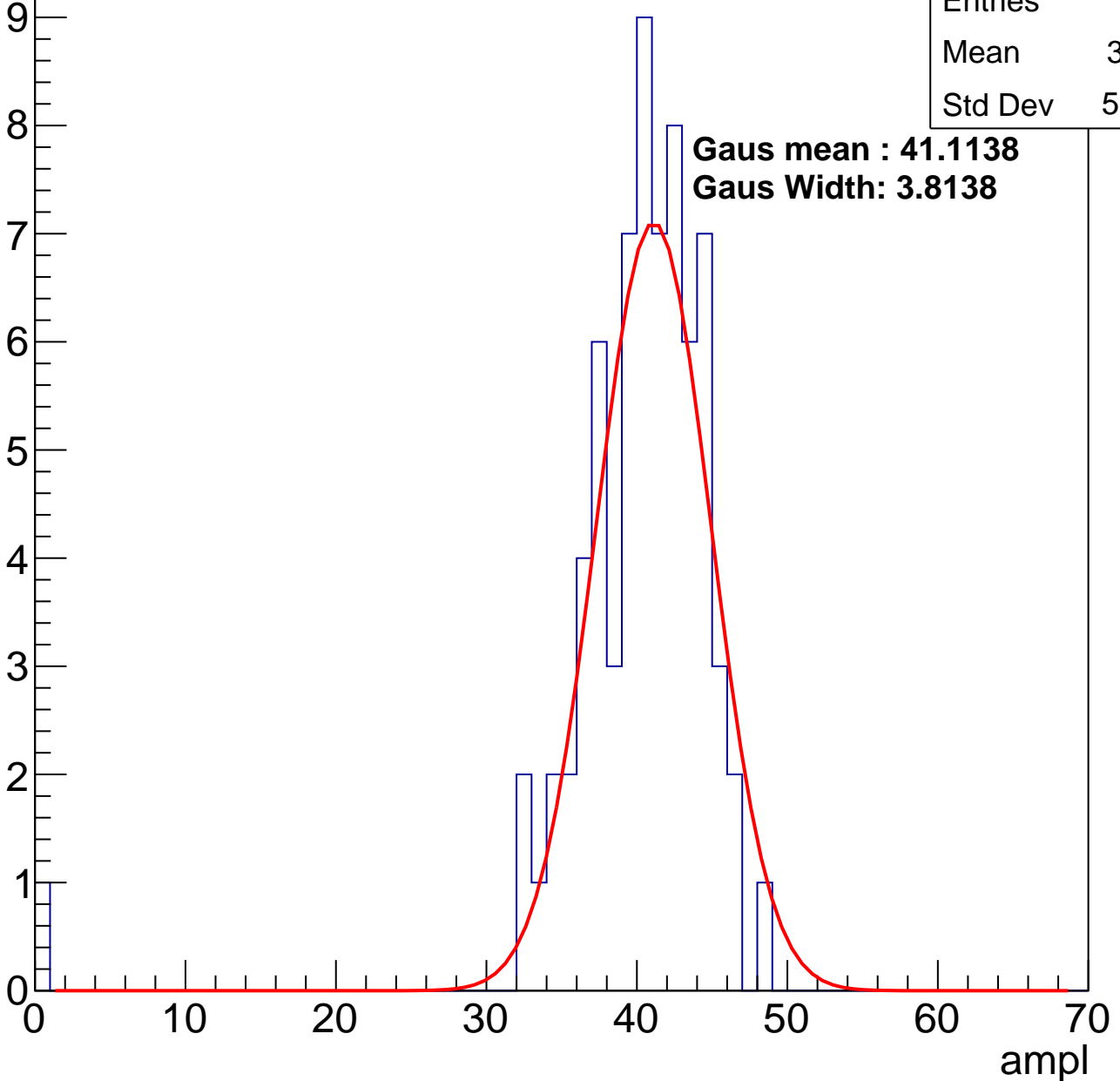
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	39.61
Std Dev	5.863

Gaus mean : 41.1138

Gaus Width: 3.8138



B1L101S, U26-ch37, adc2

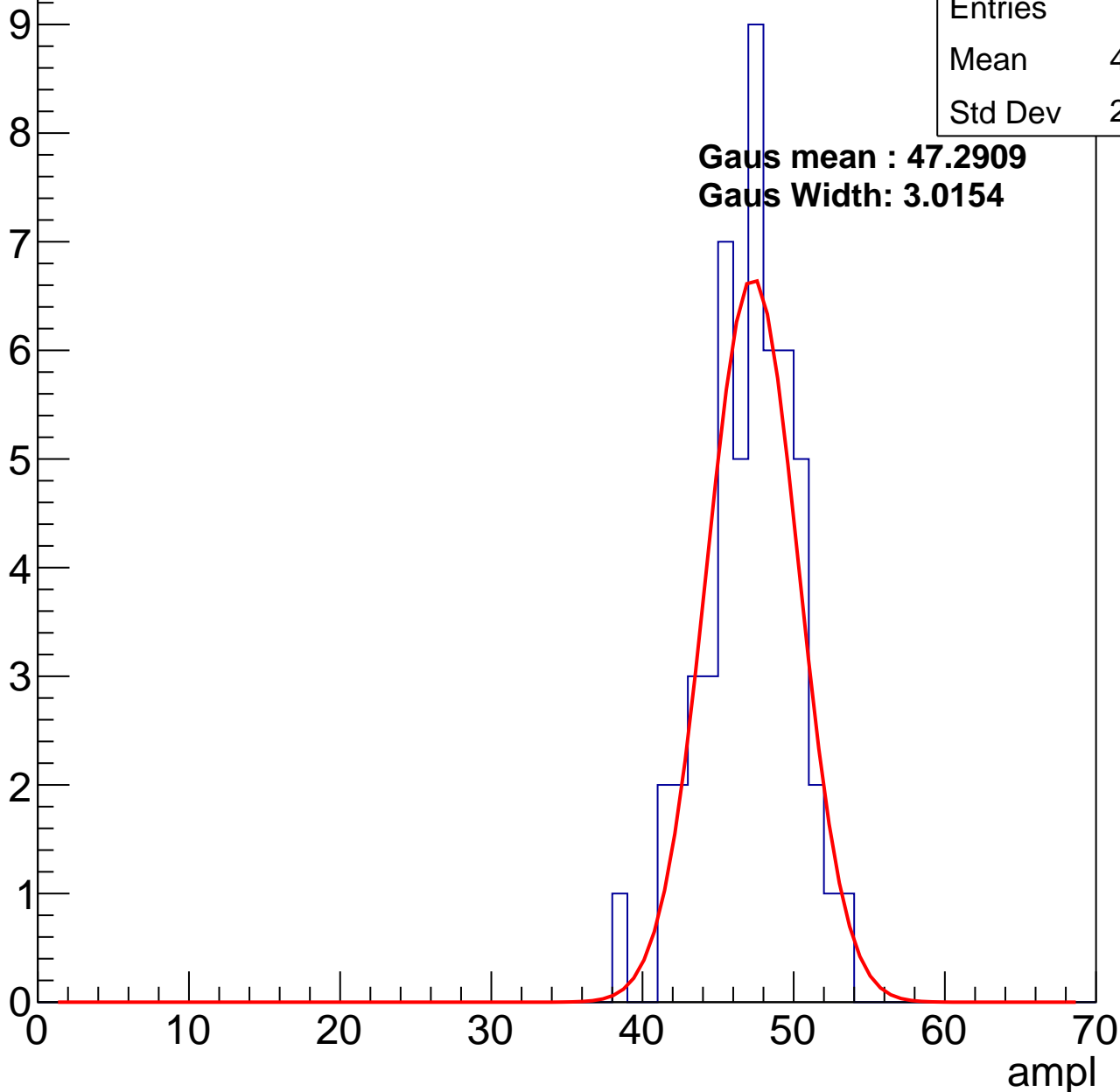
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	46.64
Std Dev	2.985

Gaus mean : 47.2909

Gaus Width: 3.0154

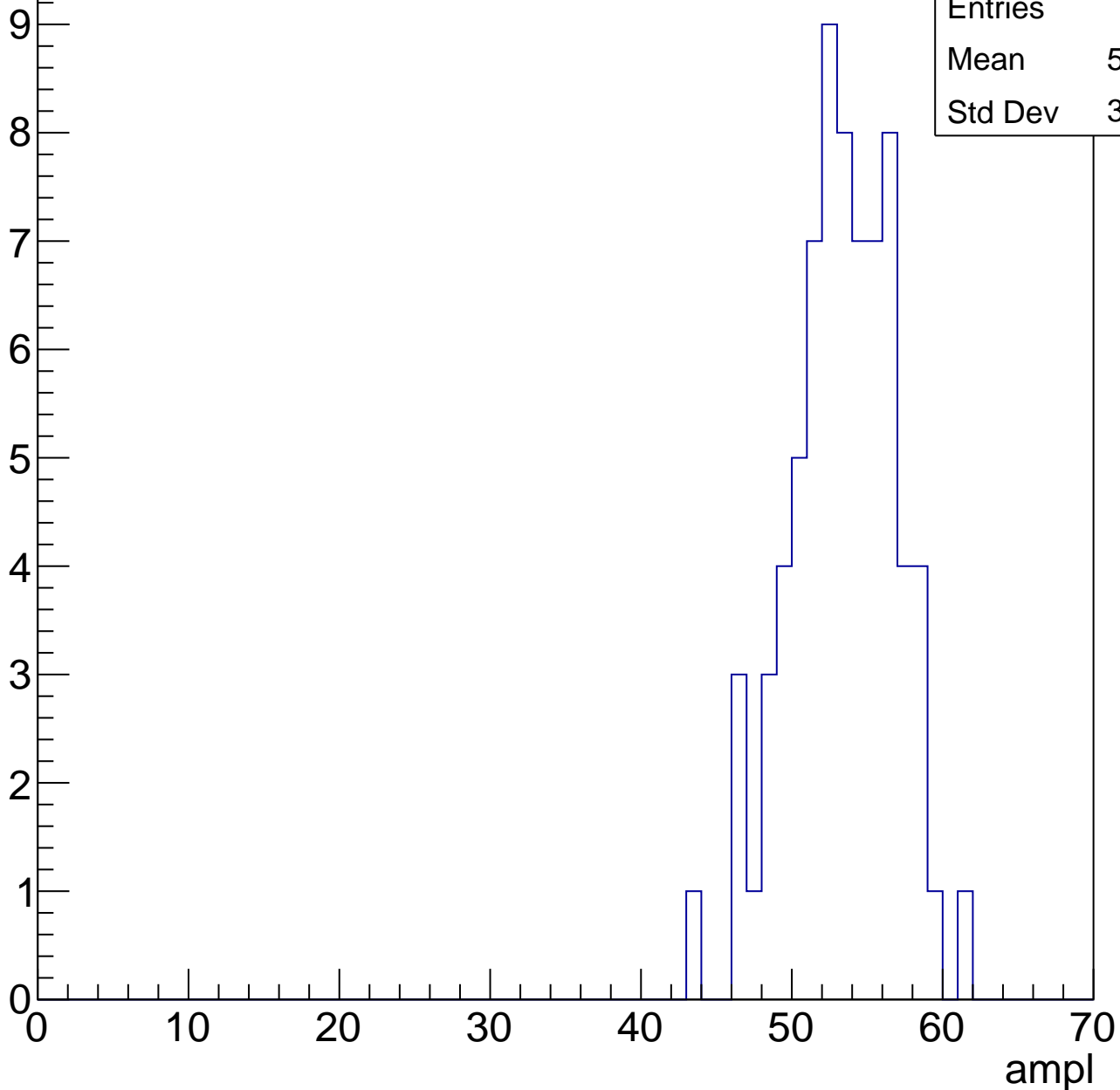


B1L101S, U26-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	52.85
Std Dev	3.459

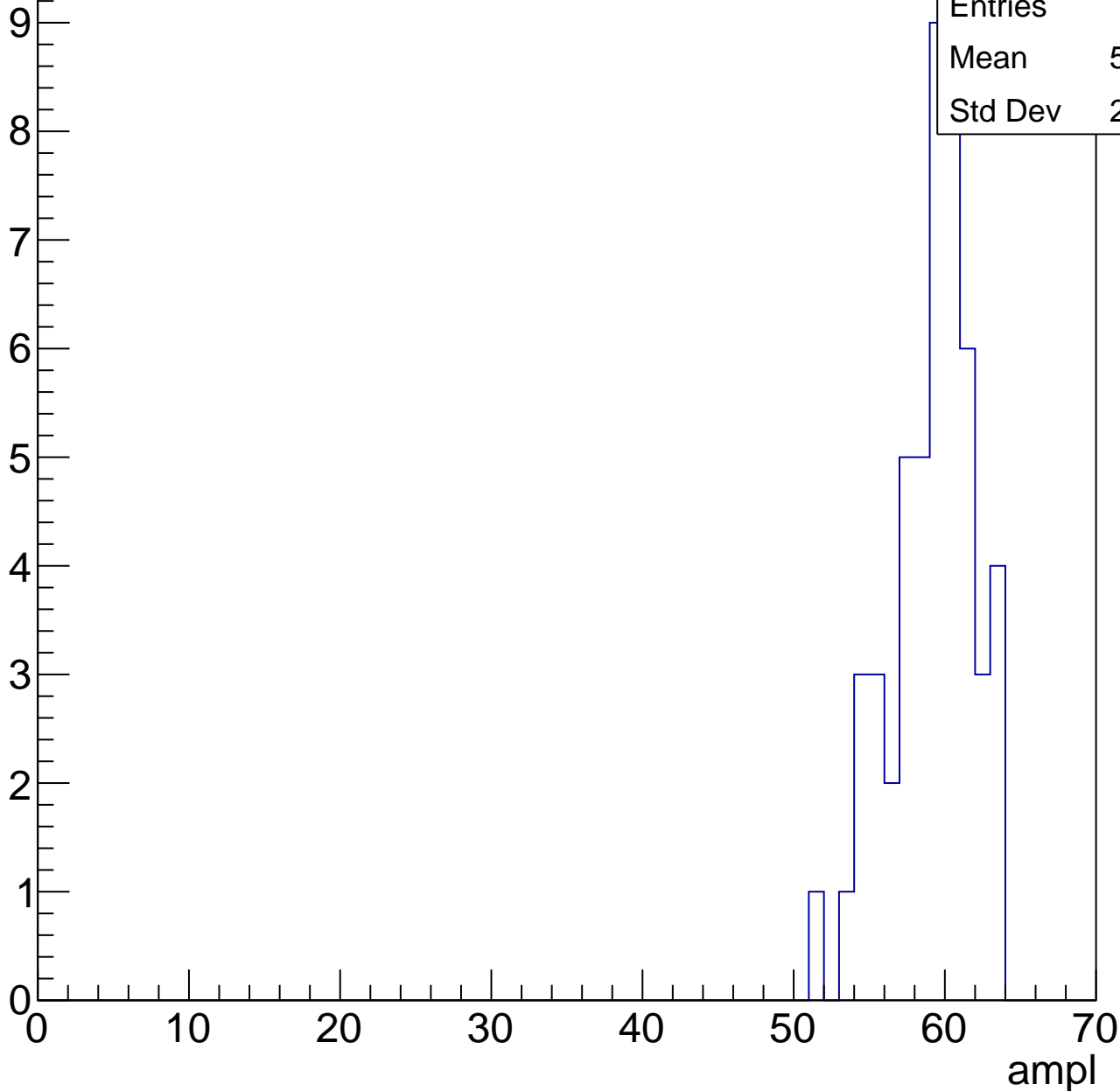


B1L101S, U26-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.69
Std Dev	2.747

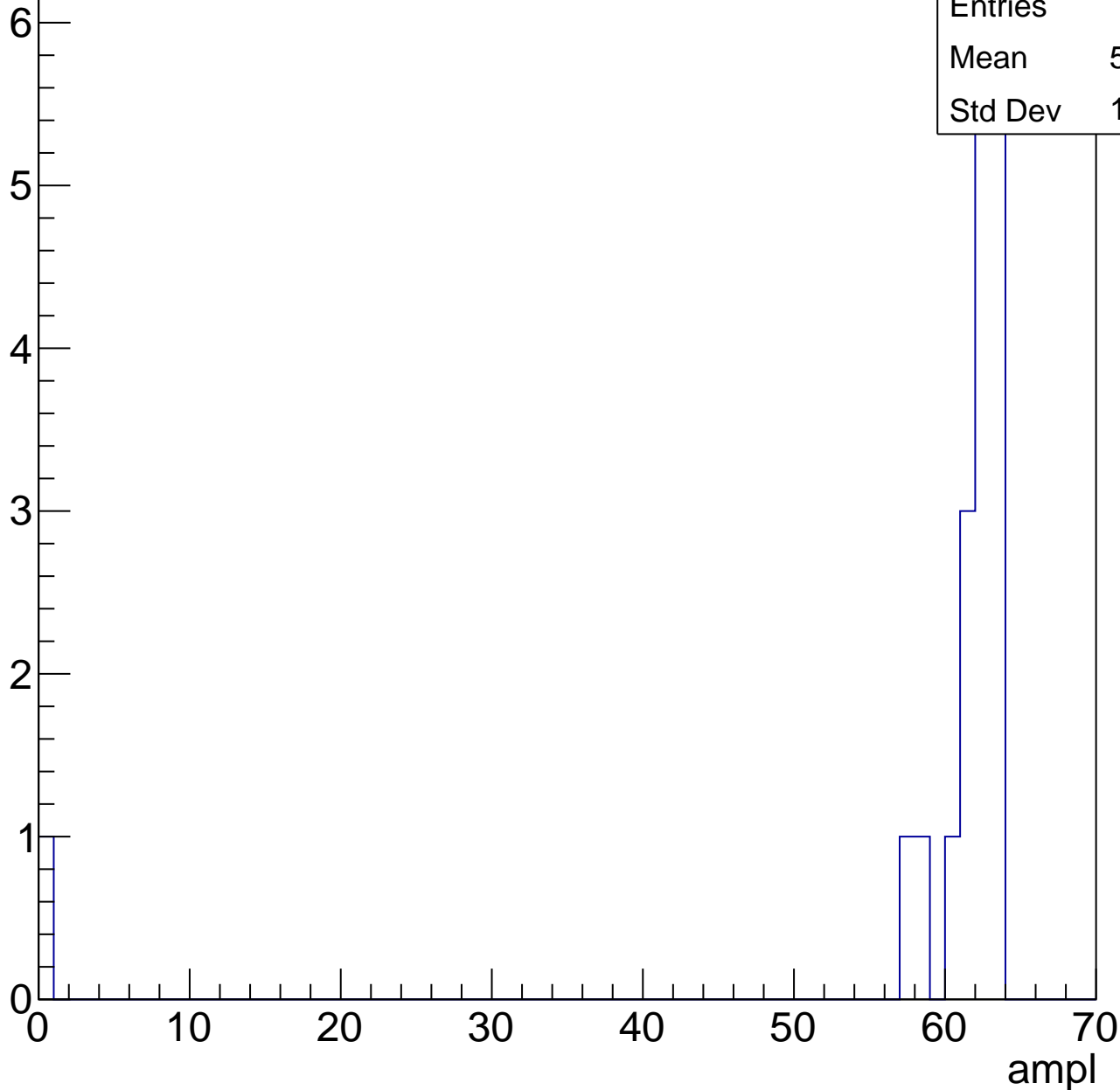


B1L101S, U26-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	58.32
Std Dev	13.84



B1L101S, U26-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U26-ch38, adc0

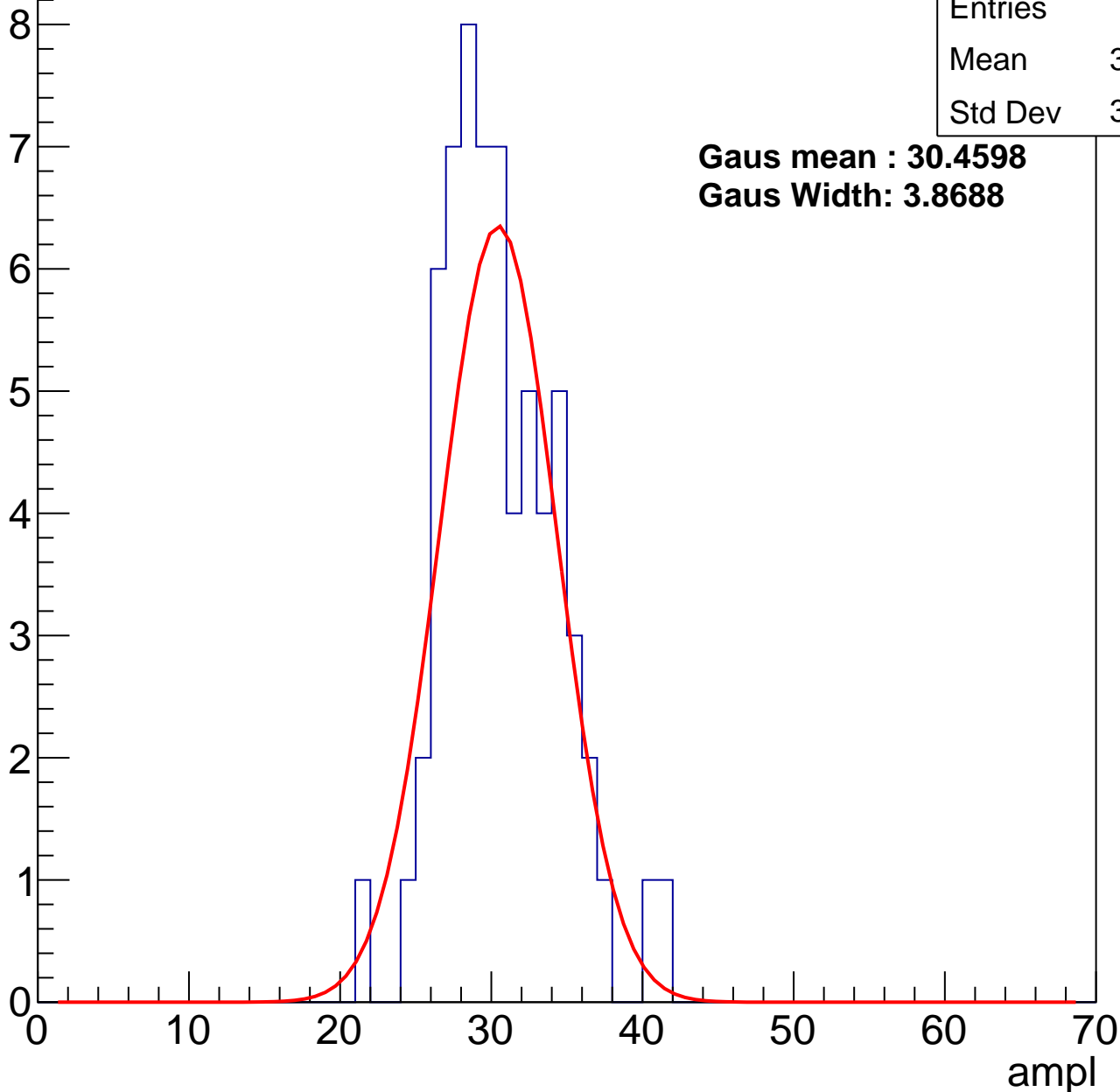
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.12
Std Dev	3.764

Gaus mean : 30.4598

Gaus Width: 3.8688



B1L101S, U26-ch38, adc1

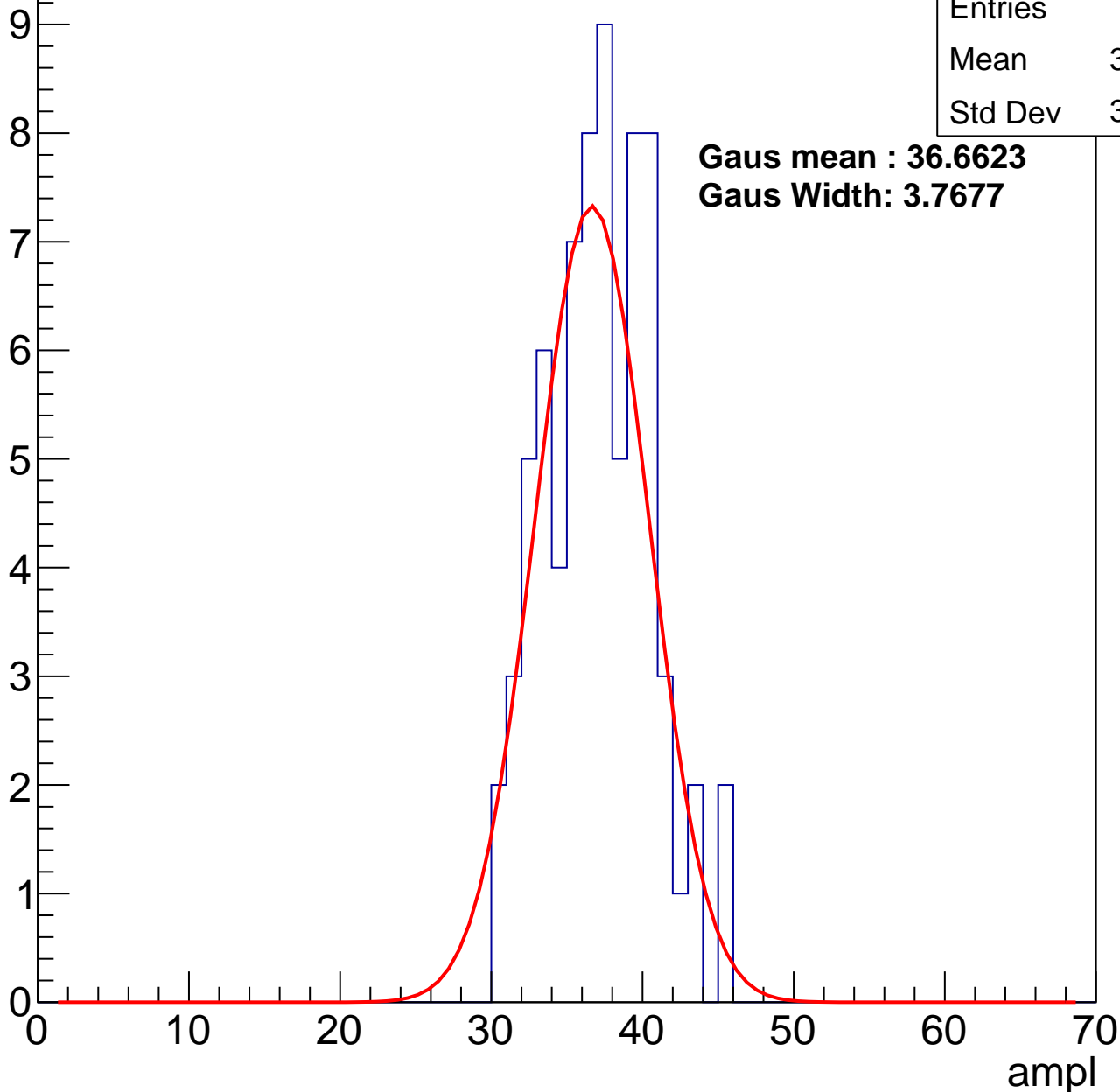
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.66
Std Dev	3.457

Gaus mean : 36.6623

Gaus Width: 3.7677



B1L101S, U26-ch38, adc2

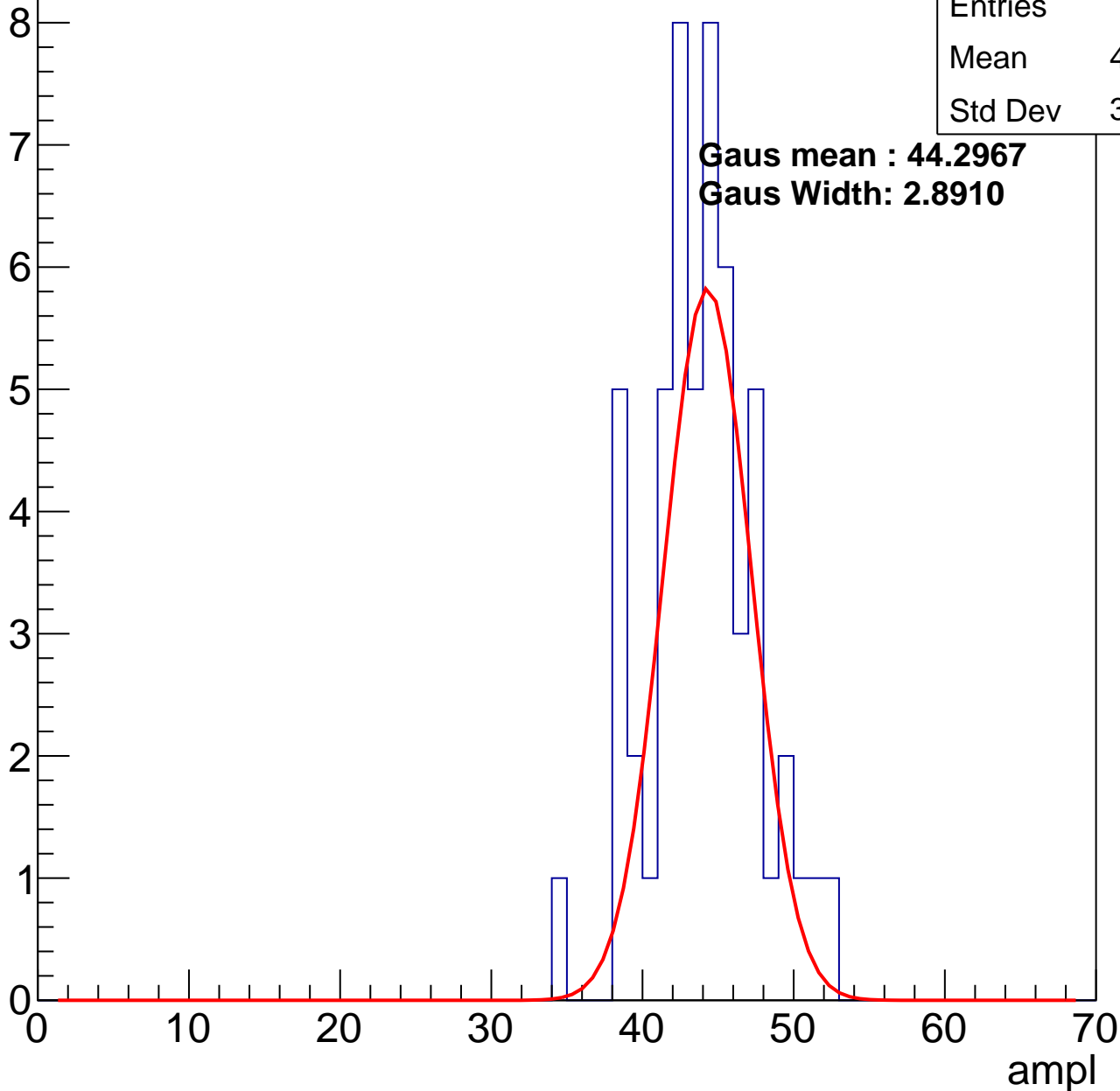
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.49
Std Dev	3.546

Gaus mean : 44.2967

Gaus Width: 2.8910

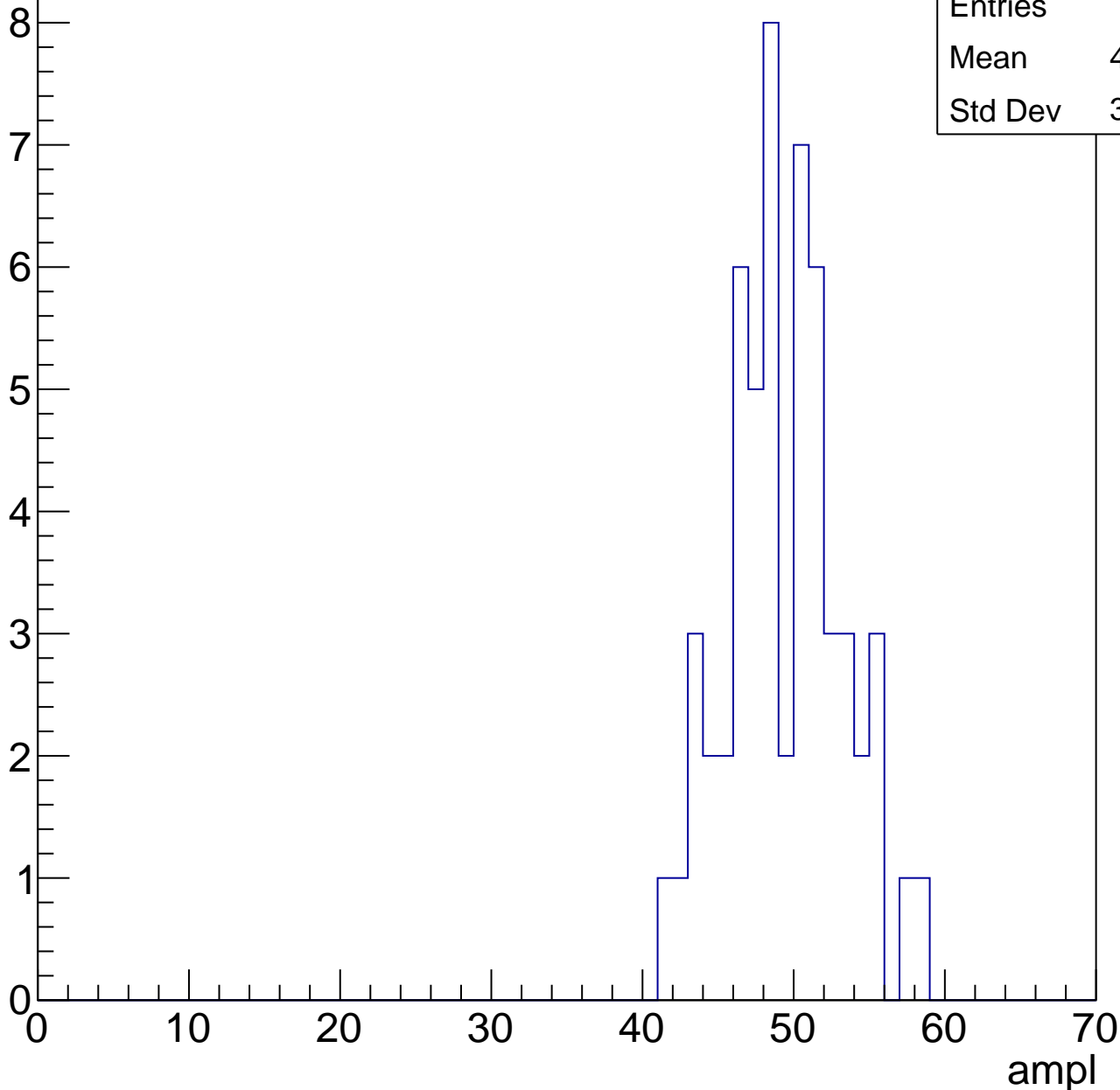


B1L101S, U26-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

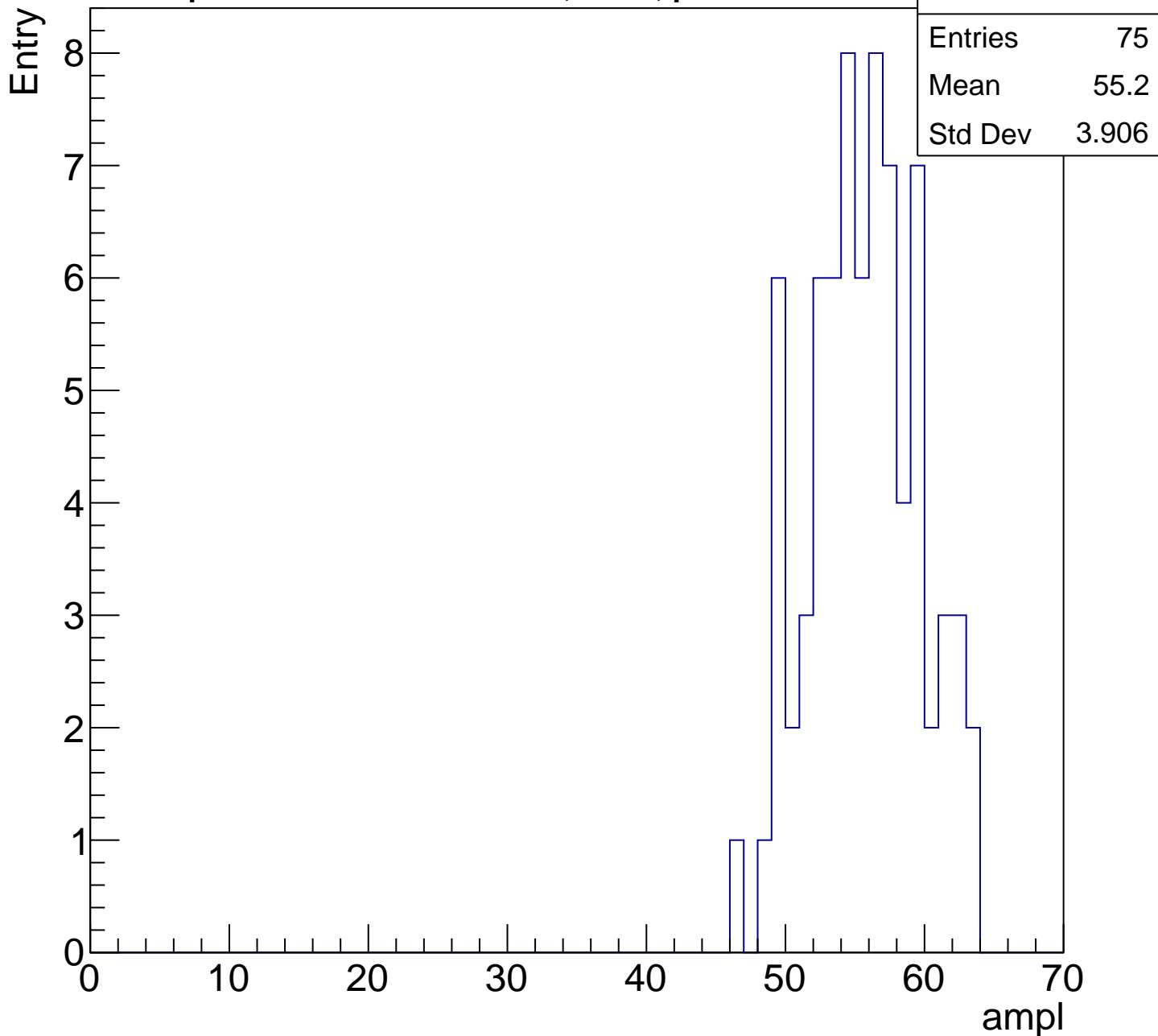
Entry

Entries	56
Mean	48.96
Std Dev	3.765



B1L101S, U26-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch38, adc5

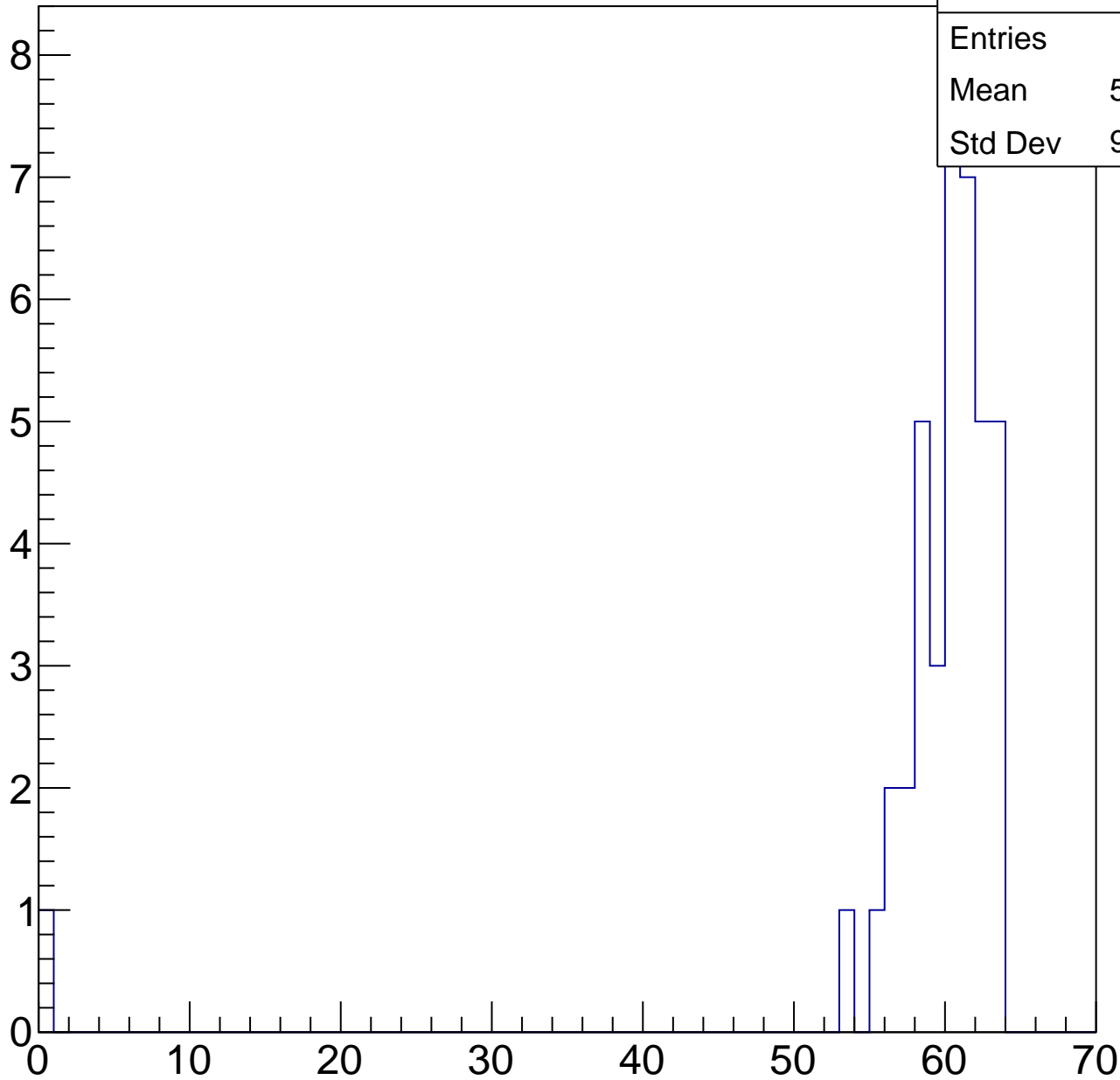
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.33
Std Dev	9.626

ampl



B1L101S, U26-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U26-ch39, adc0

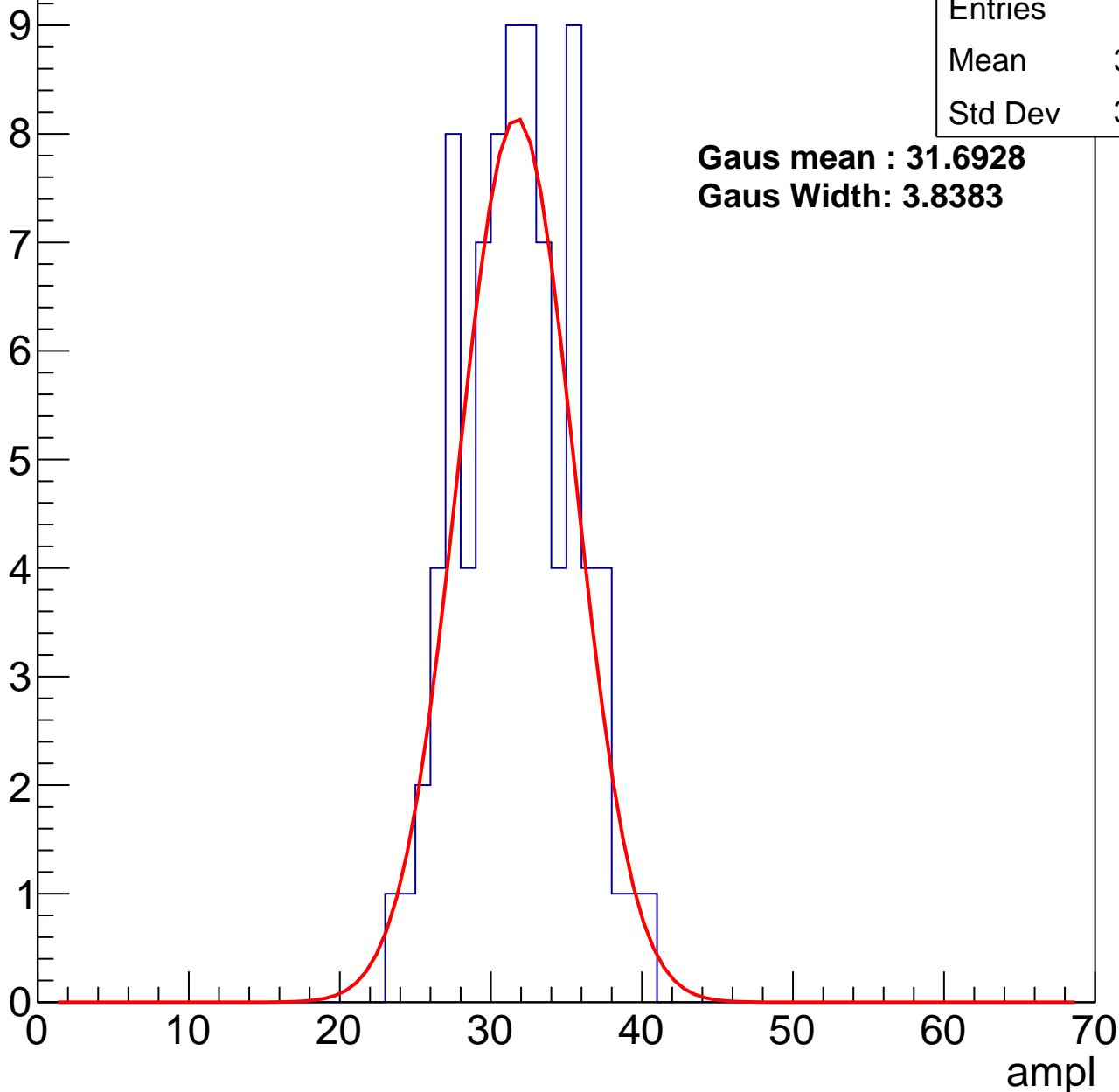
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	31.31
Std Dev	3.671

Gaus mean : 31.6928

Gaus Width: 3.8383



B1L101S, U26-ch39, adc1

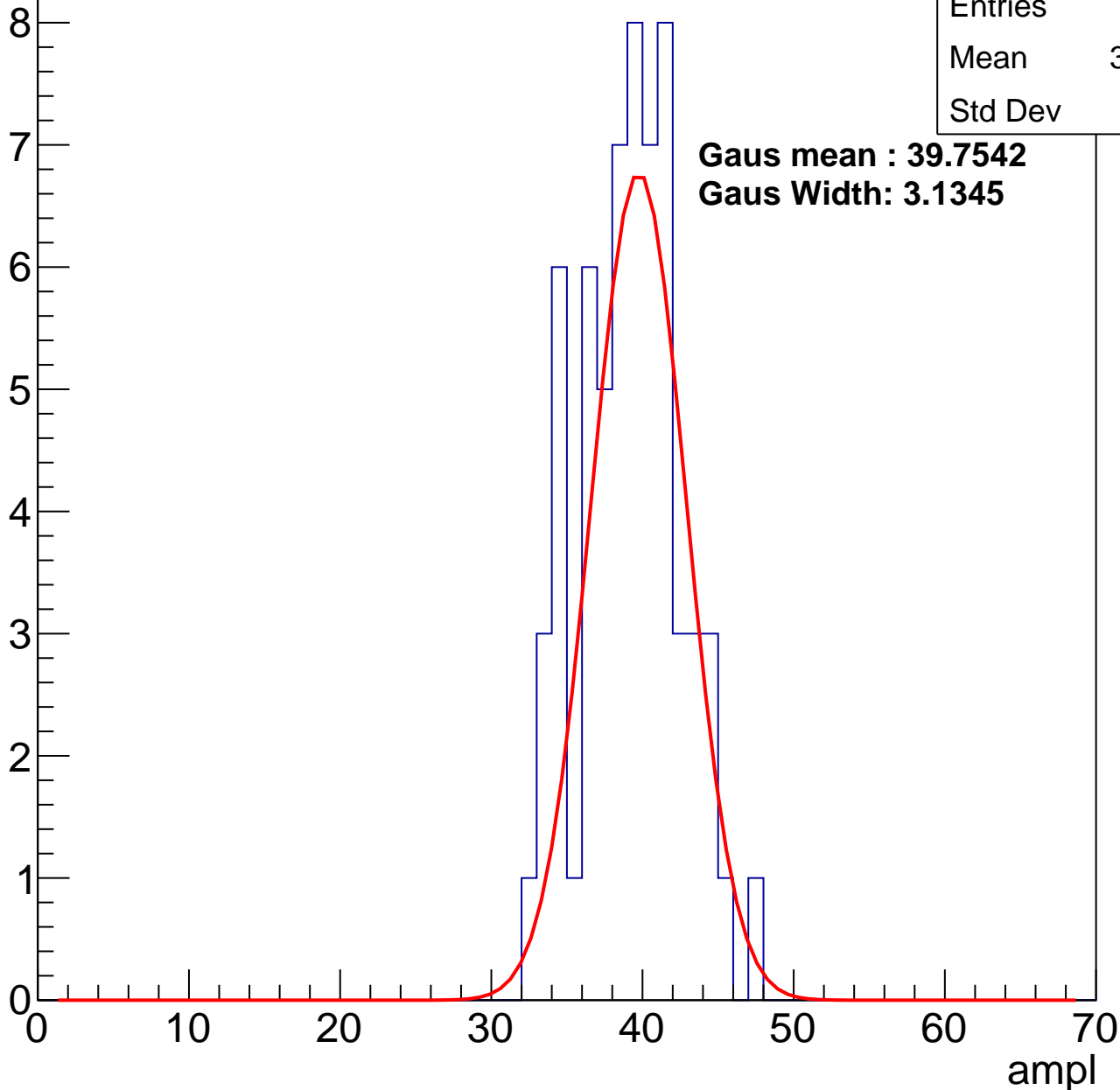
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.67
Std Dev	3.3

Gaus mean : 39.7542

Gaus Width: 3.1345



B1L101S, U26-ch39, adc2

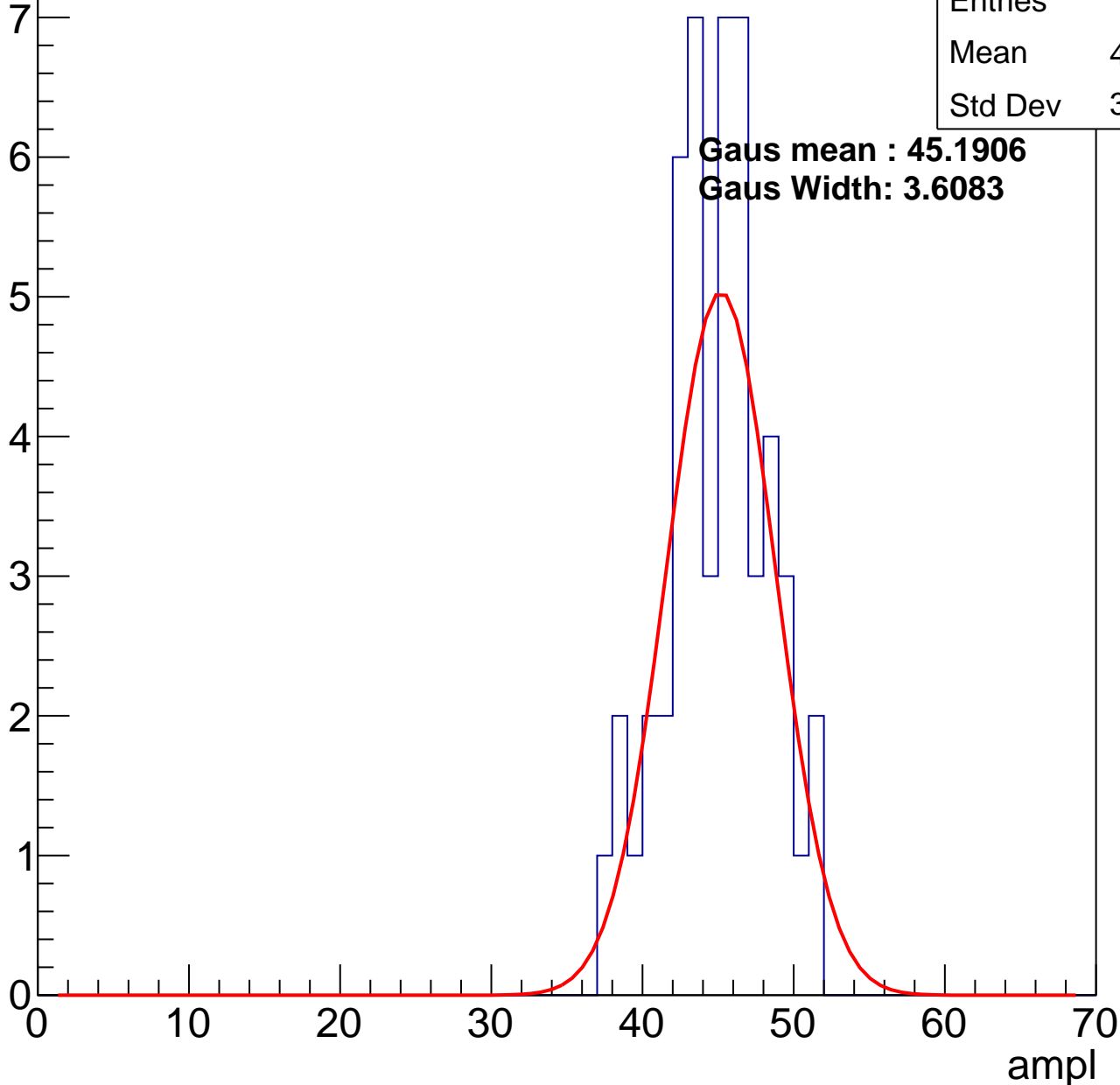
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	44.47
Std Dev	3.286

Gaus mean : 45.1906

Gaus Width: 3.6083

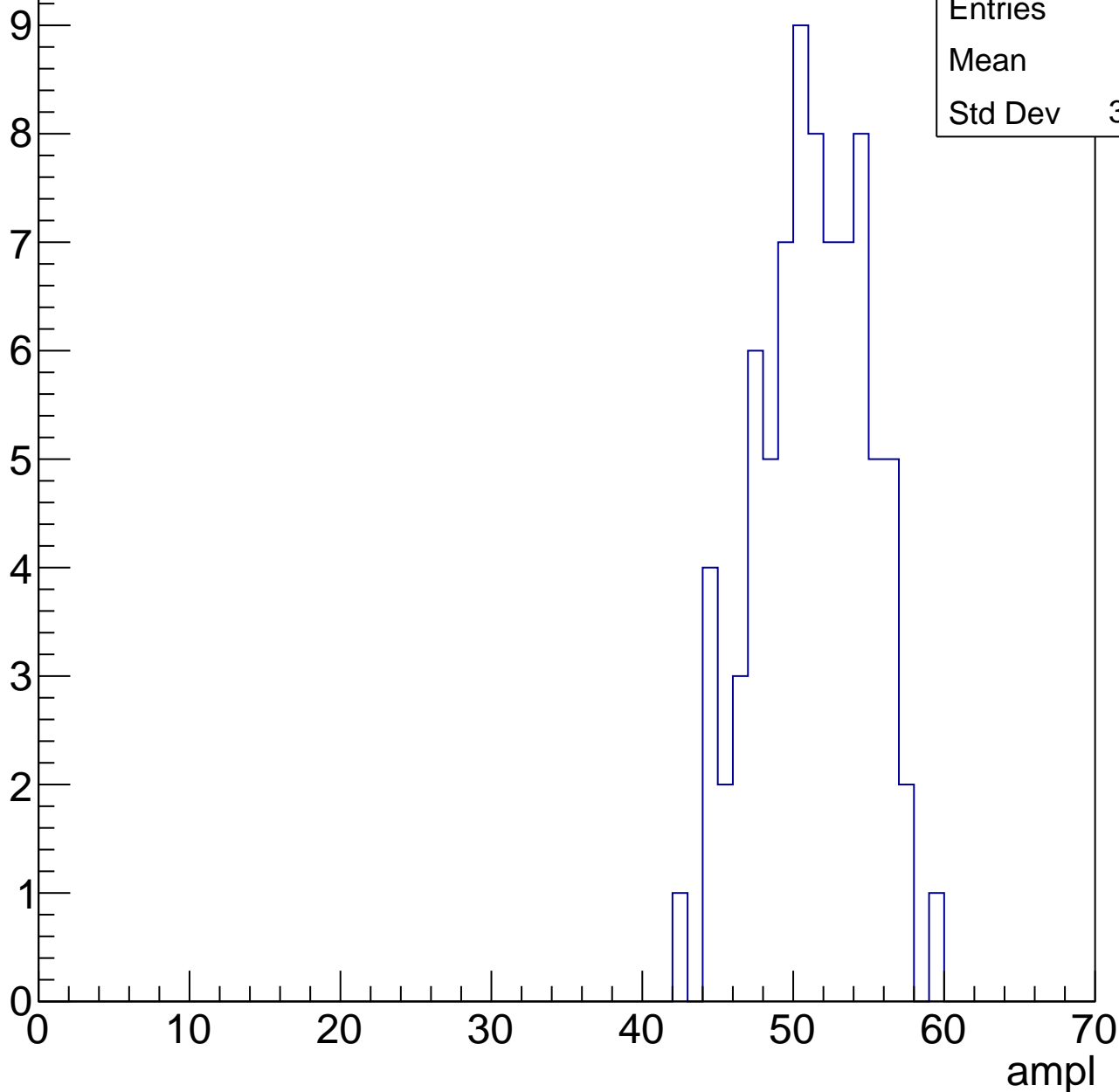


B1L101S, U26-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	50.8
Std Dev	3.617

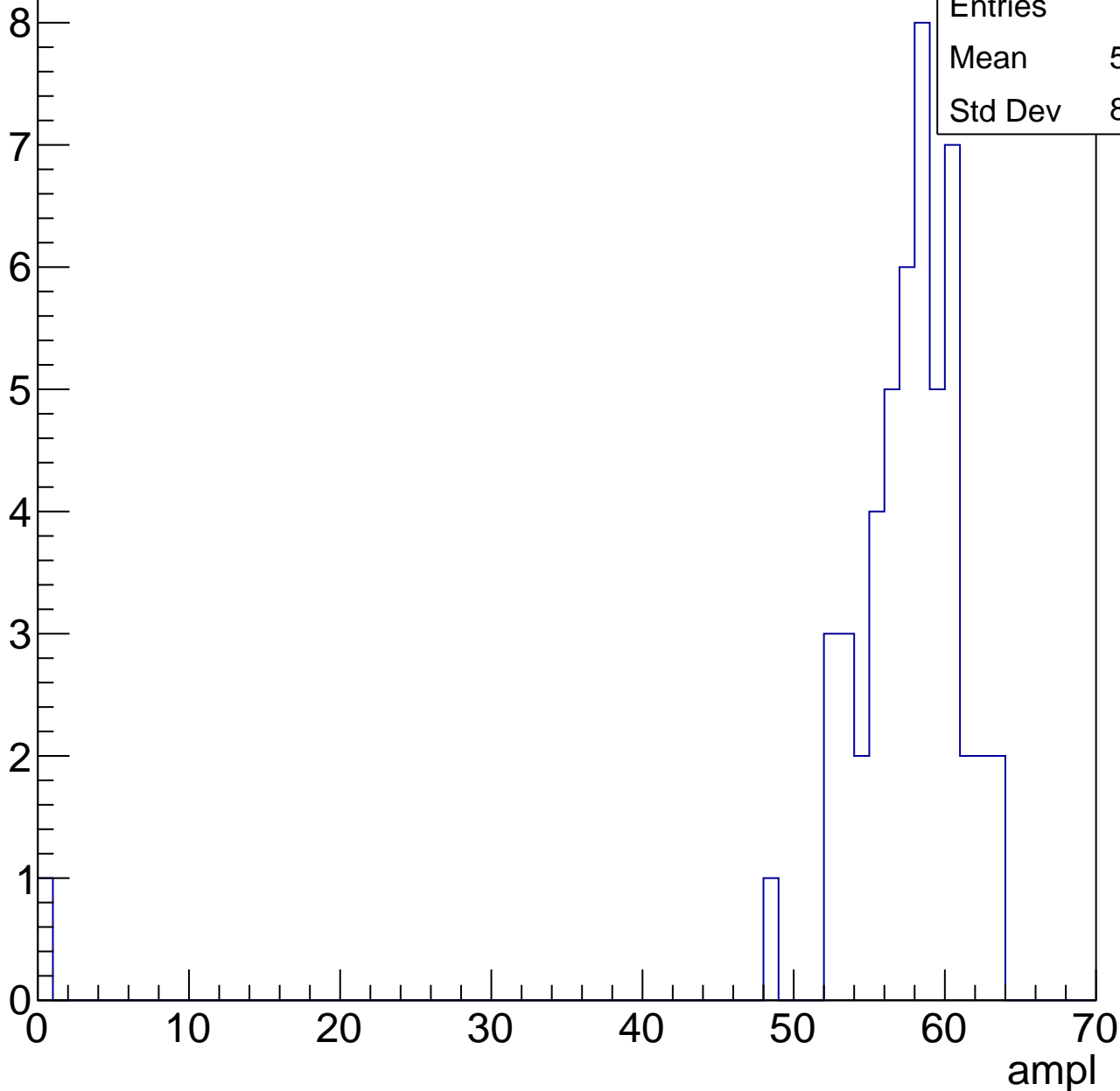


B1L101S, U26-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.16
Std Dev	8.518

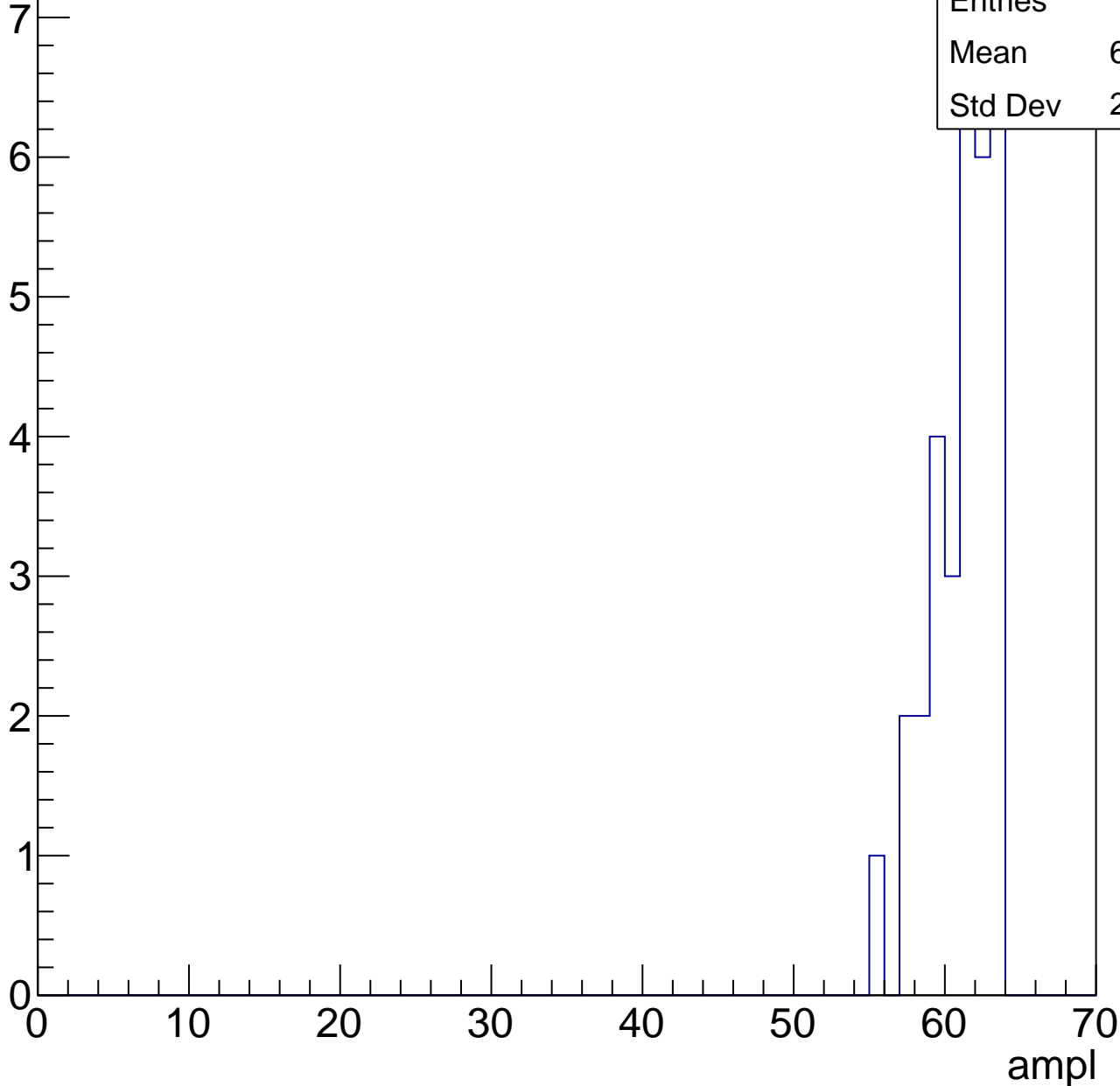


B1L101S, U26-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	60.66
Std Dev	2.056



B1L101S, U26-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

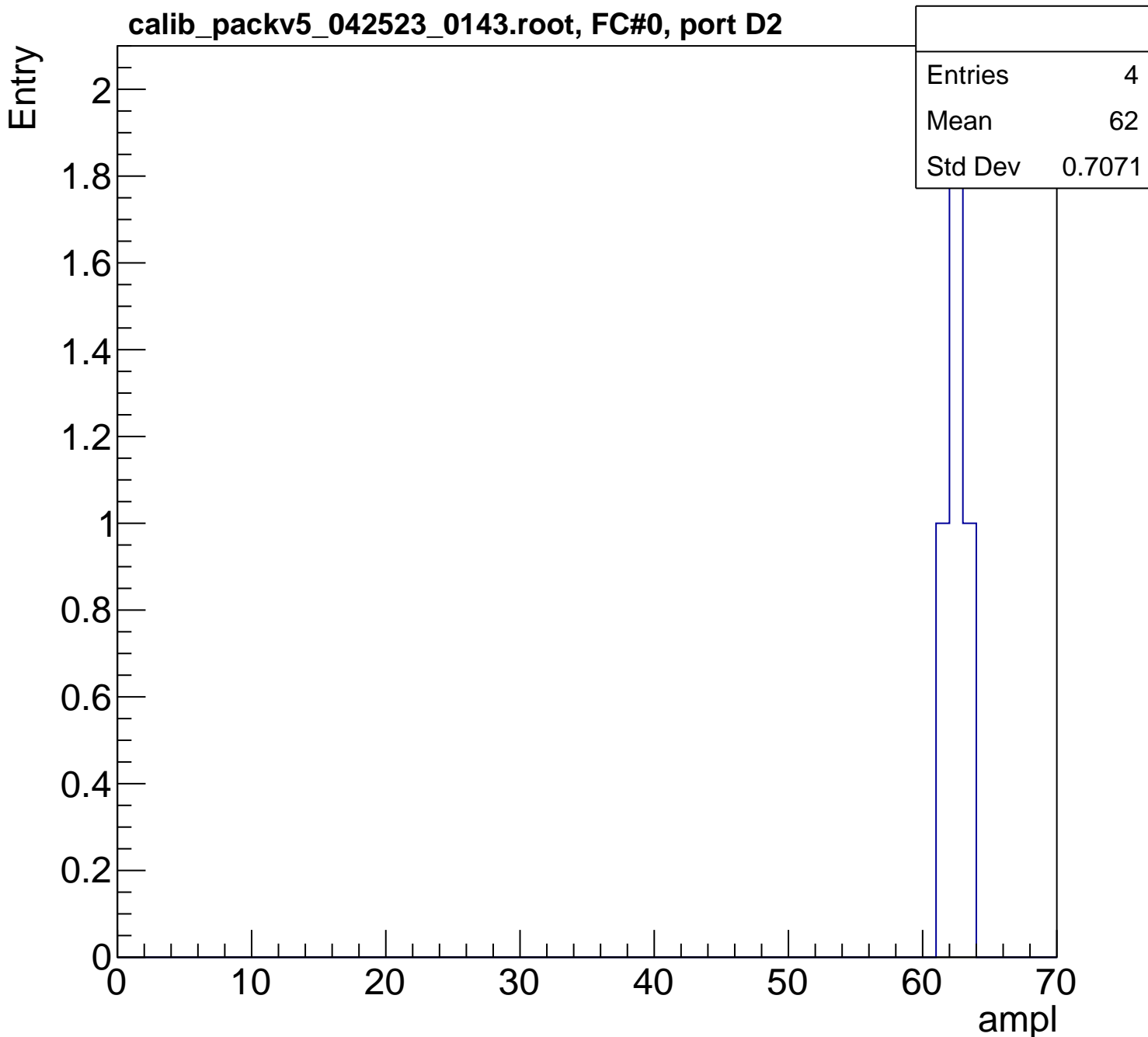
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

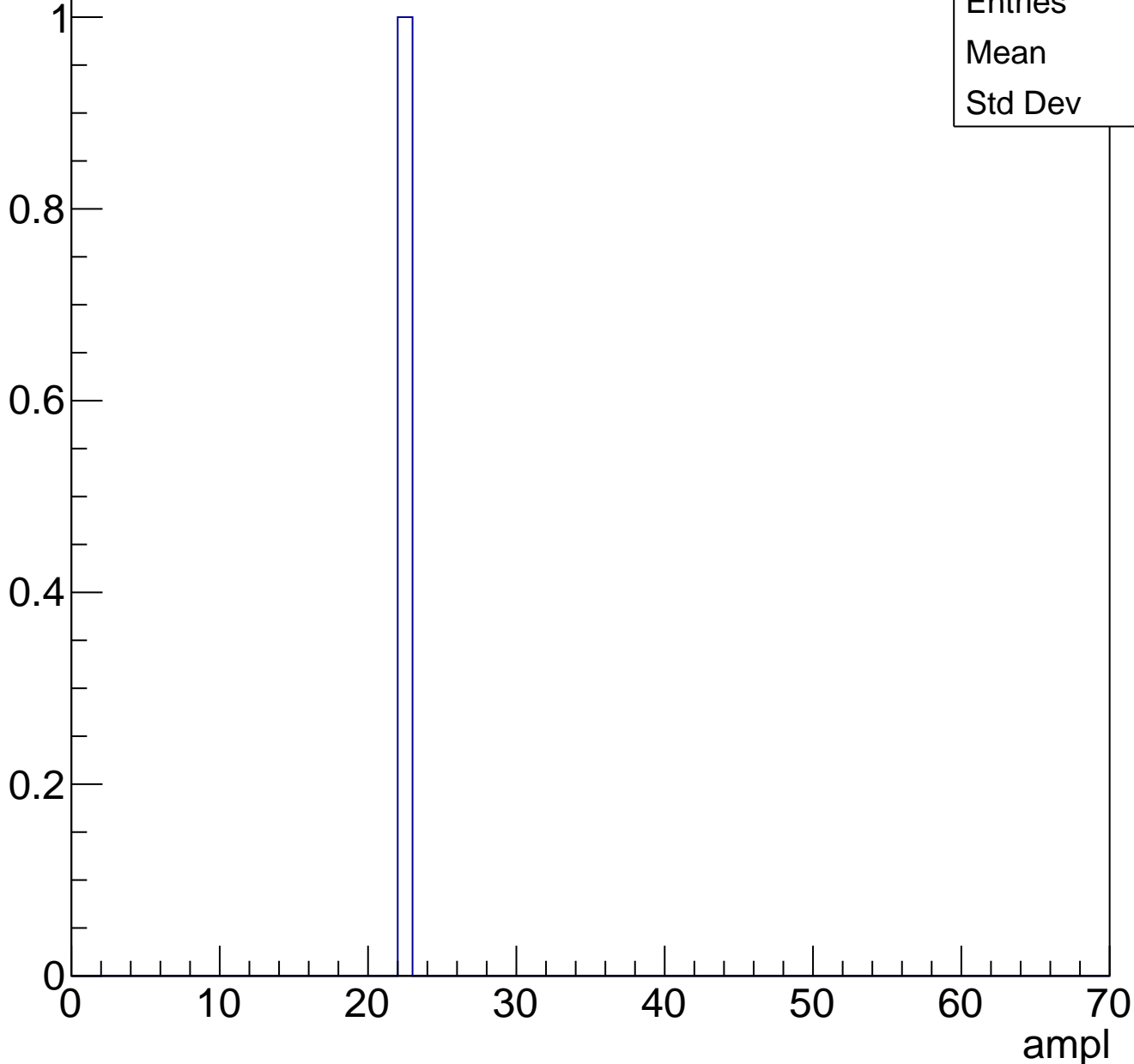
0 10 20 30 40 50 60 70



B1L101S, U26-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U26-ch40, adc0

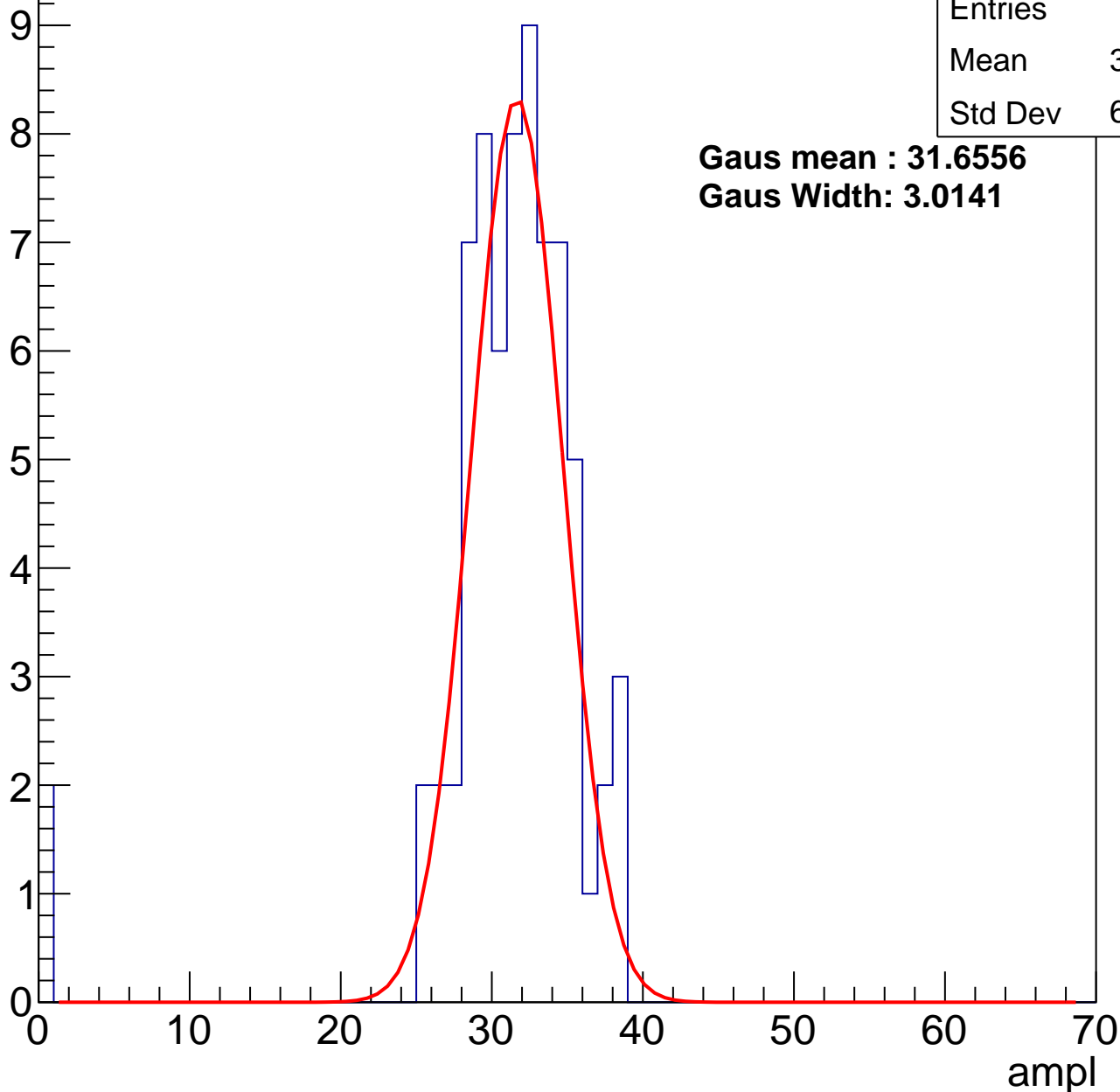
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.54
Std Dev	6.035

Gaus mean : 31.6556

Gaus Width: 3.0141



B1L101S, U26-ch40, adc1

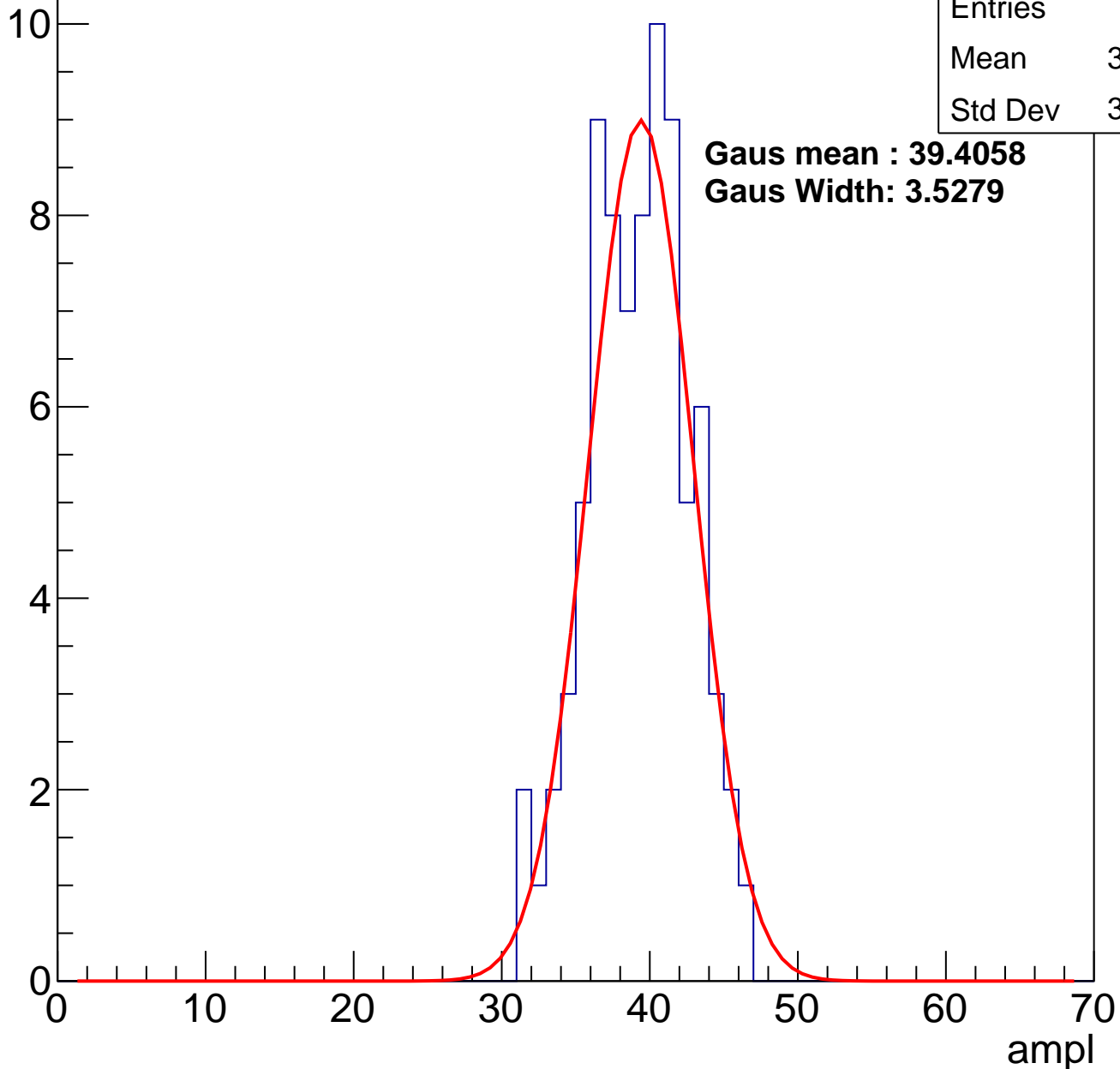
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	38.77
Std Dev	3.338

Gaus mean : 39.4058

Gaus Width: 3.5279

Entry



B1L101S, U26-ch40, adc2

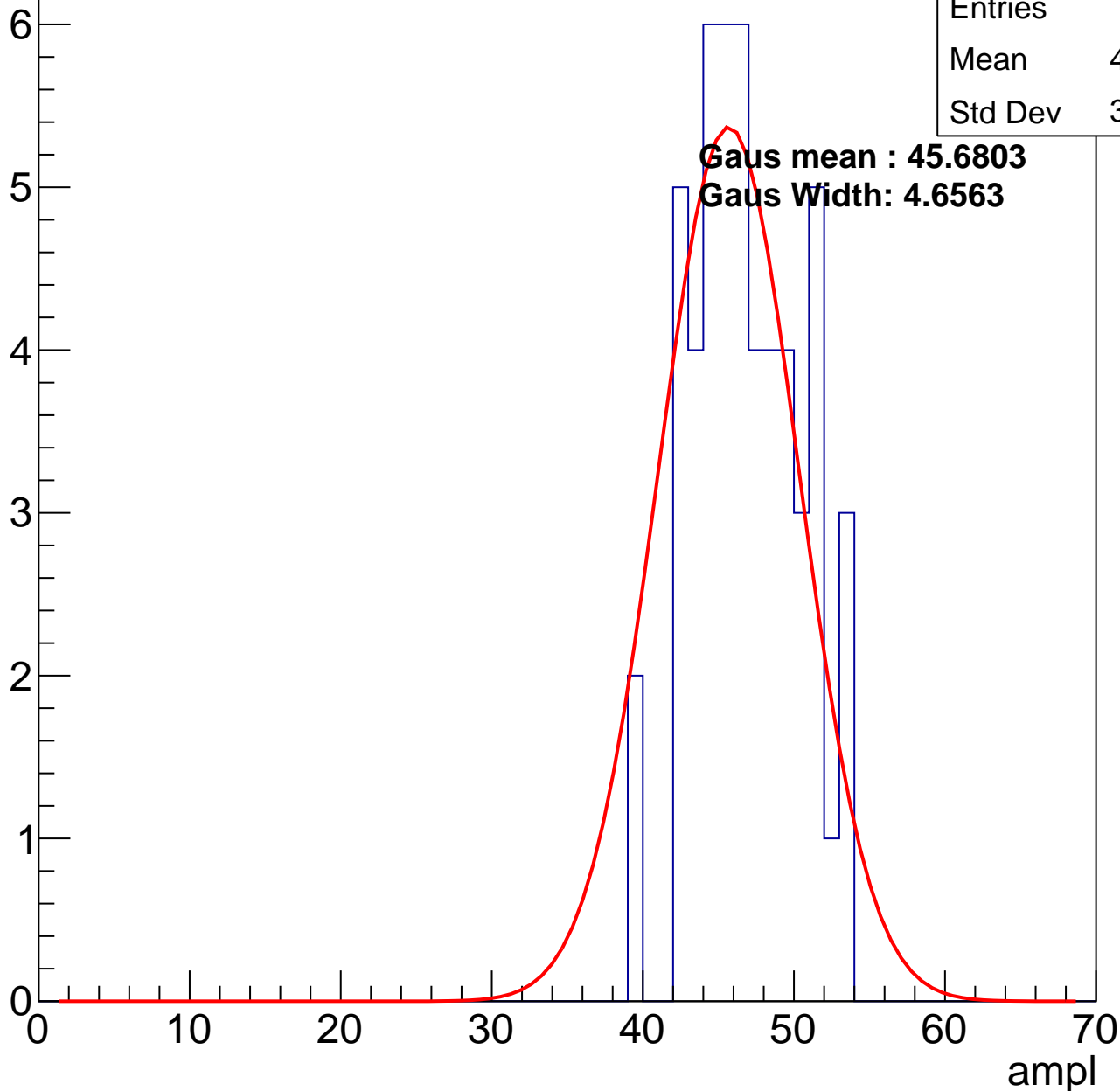
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	46.45
Std Dev	3.489

Gaus mean : 45.6803

Gaus Width: 4.6563

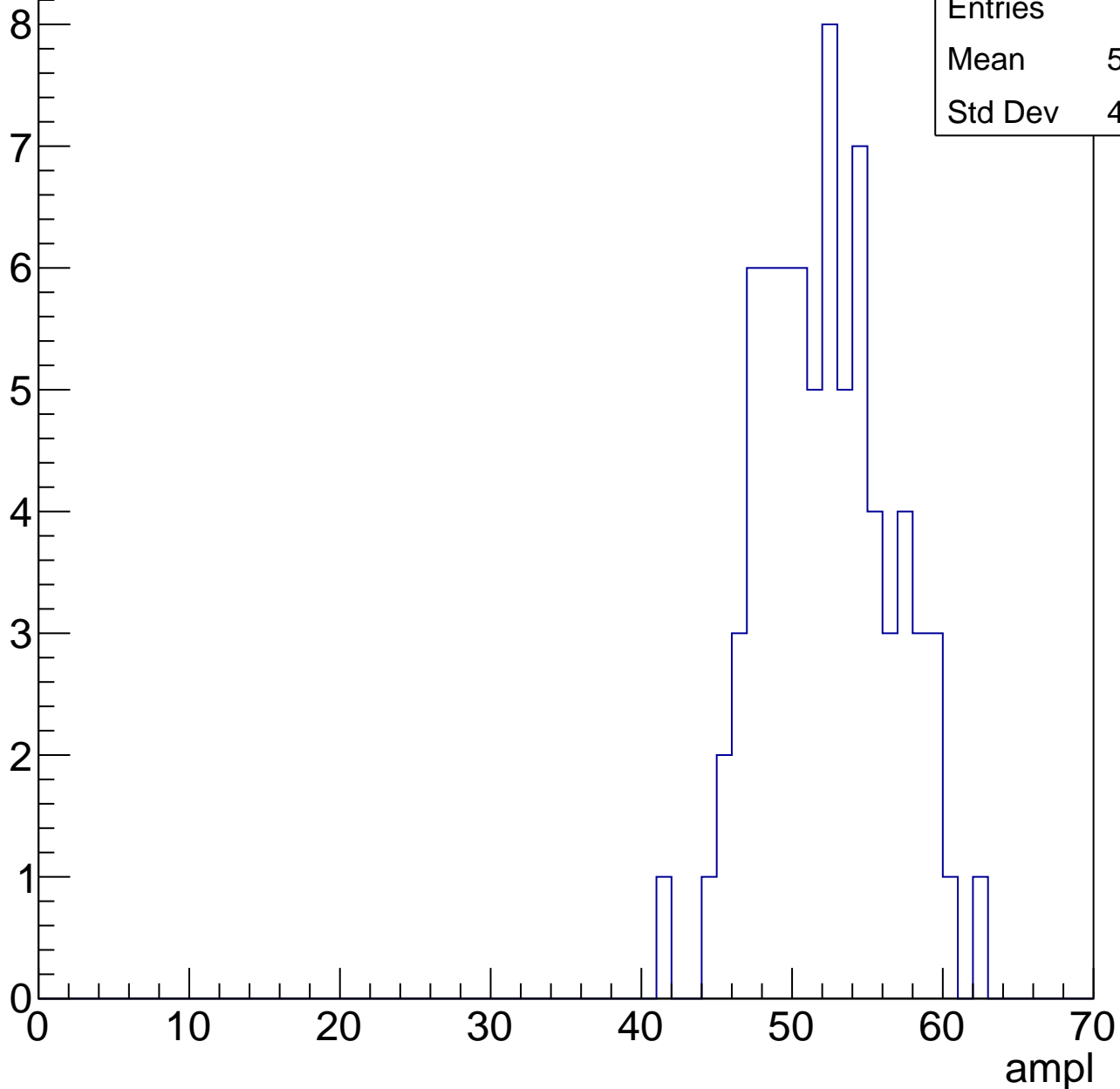


B1L101S, U26-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	51.73
Std Dev	4.244



B1L101S, U26-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries 39

Mean 56.95

Std Dev 2.791

0

10

20

30

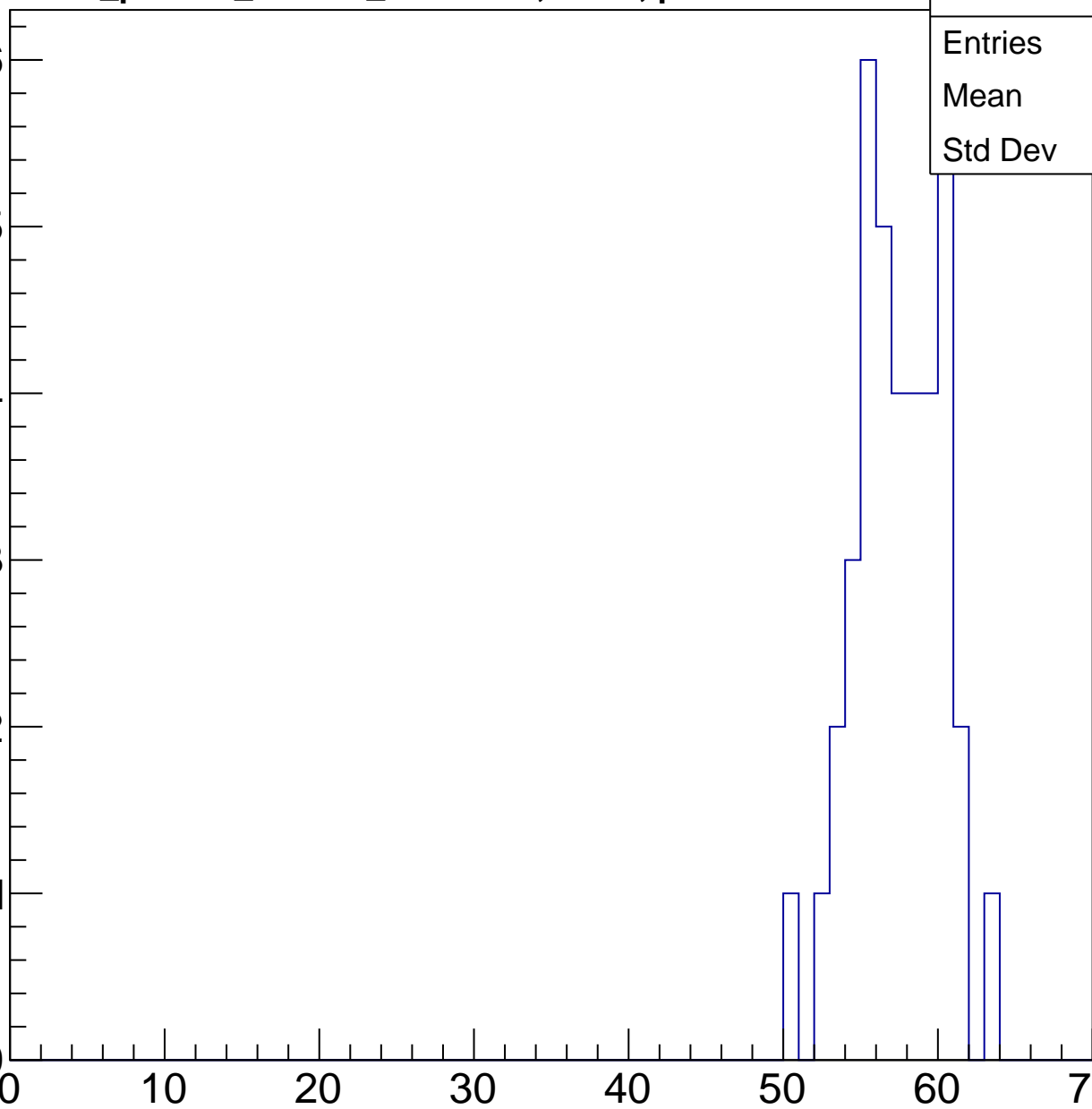
40

50

60

70

ampl

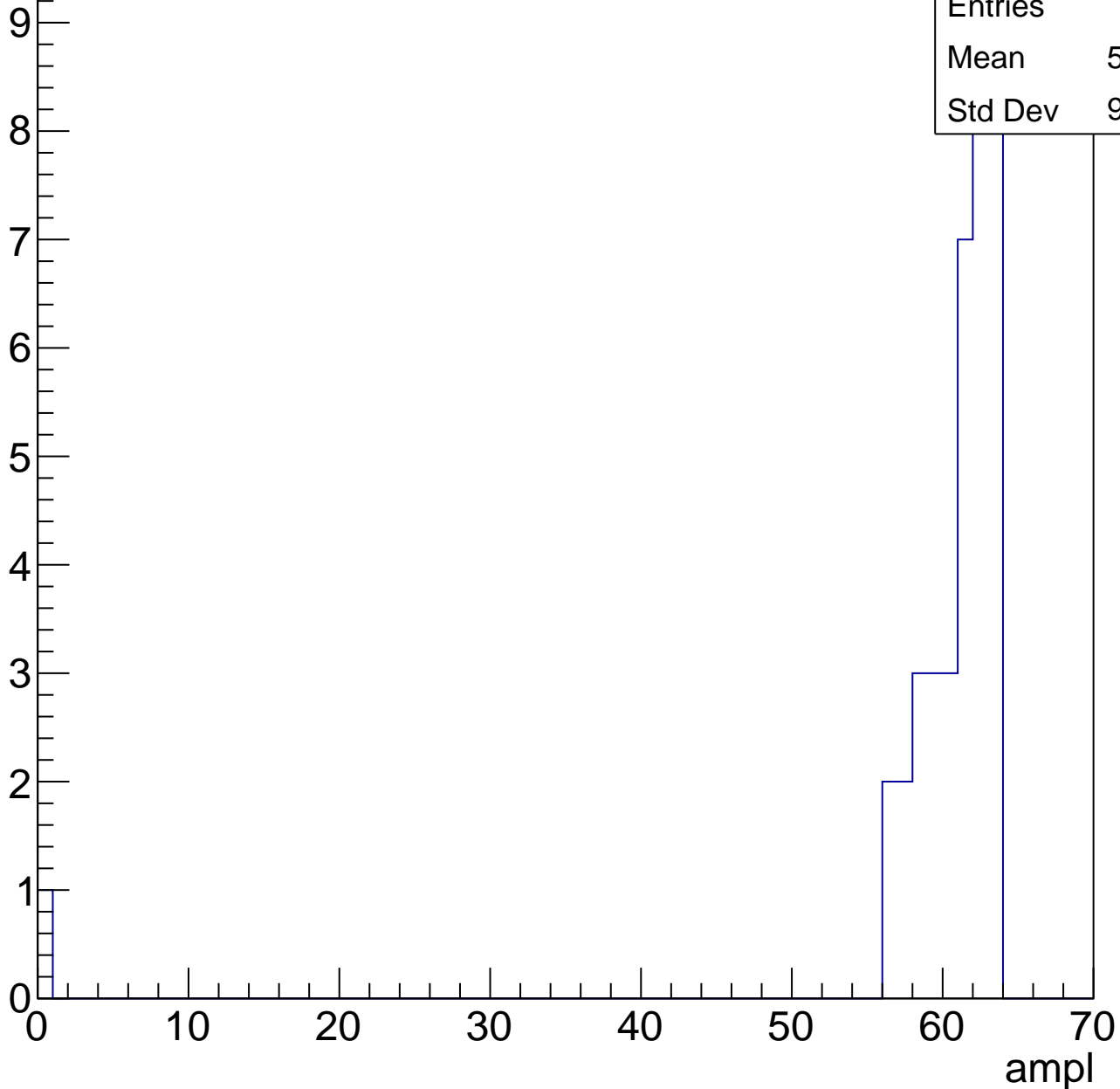


B1L101S, U26-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59.13
Std Dev	9.942



B1L101S, U26-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U26-ch41, adc0

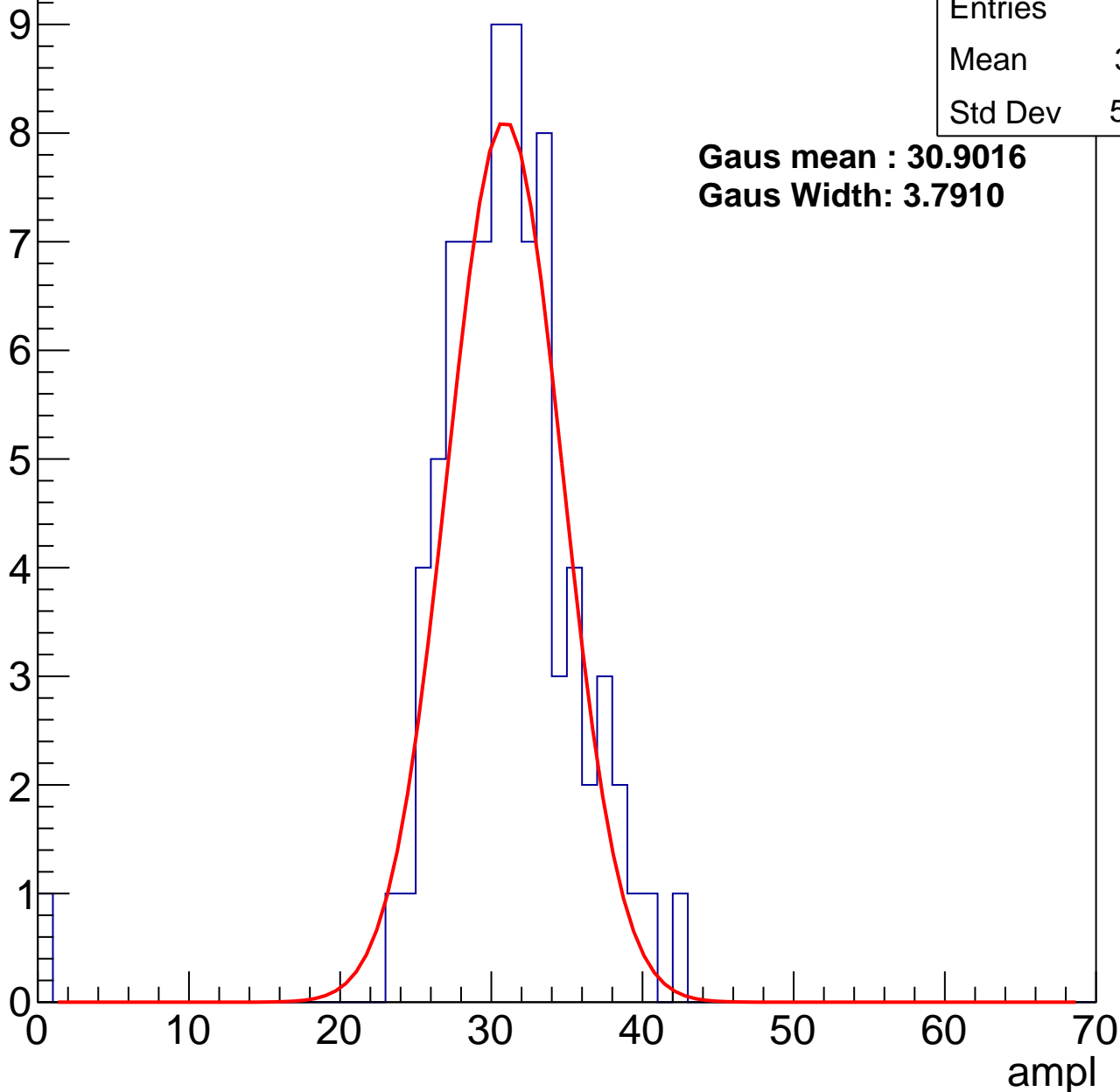
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	30.41
Std Dev	5.123

Gaus mean : 30.9016

Gaus Width: 3.7910



B1L101S, U26-ch41, adc1

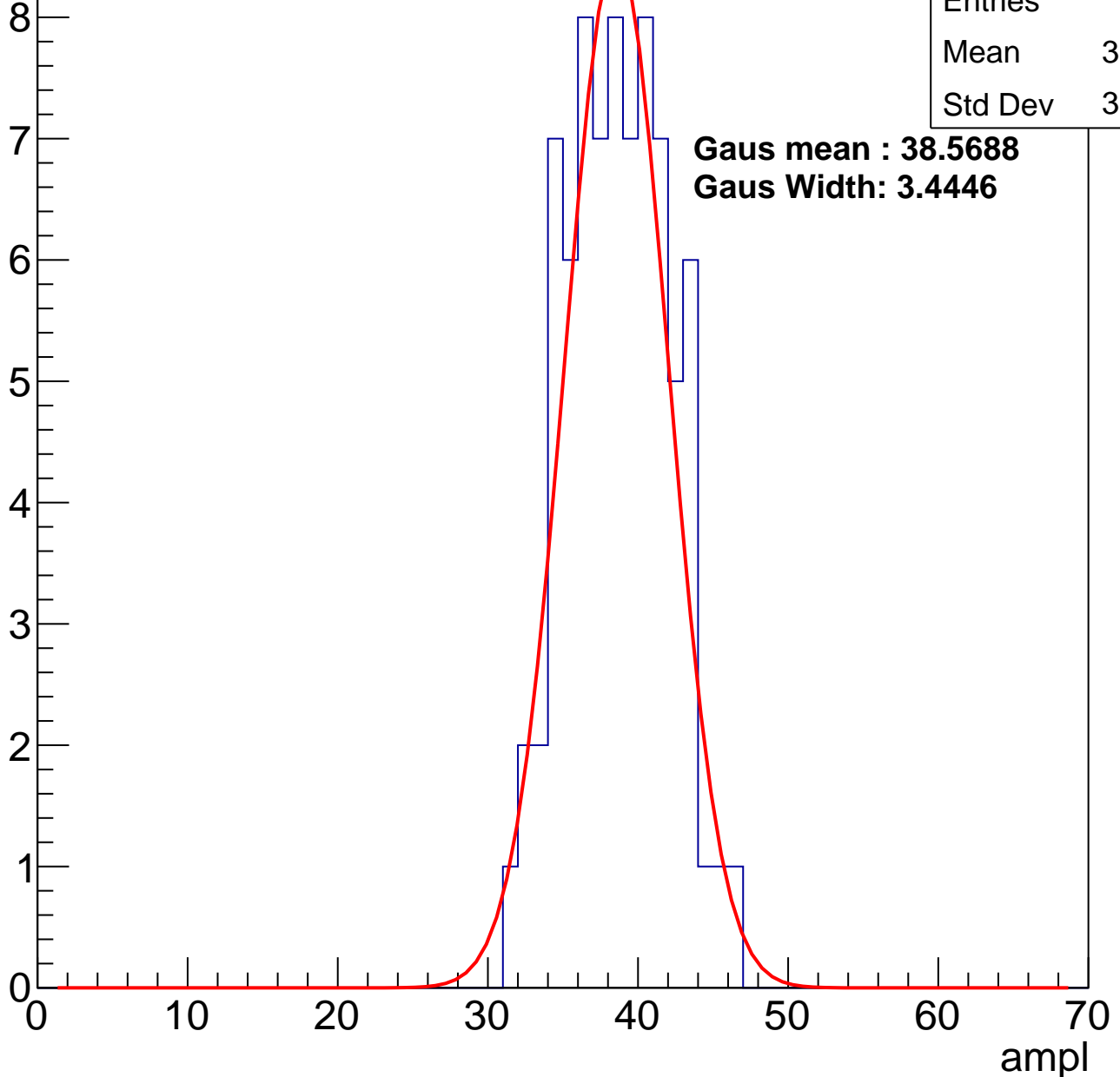
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	38.22
Std Dev	3.329

Gaus mean : 38.5688

Gaus Width: 3.4446



B1L101S, U26-ch41, adc2

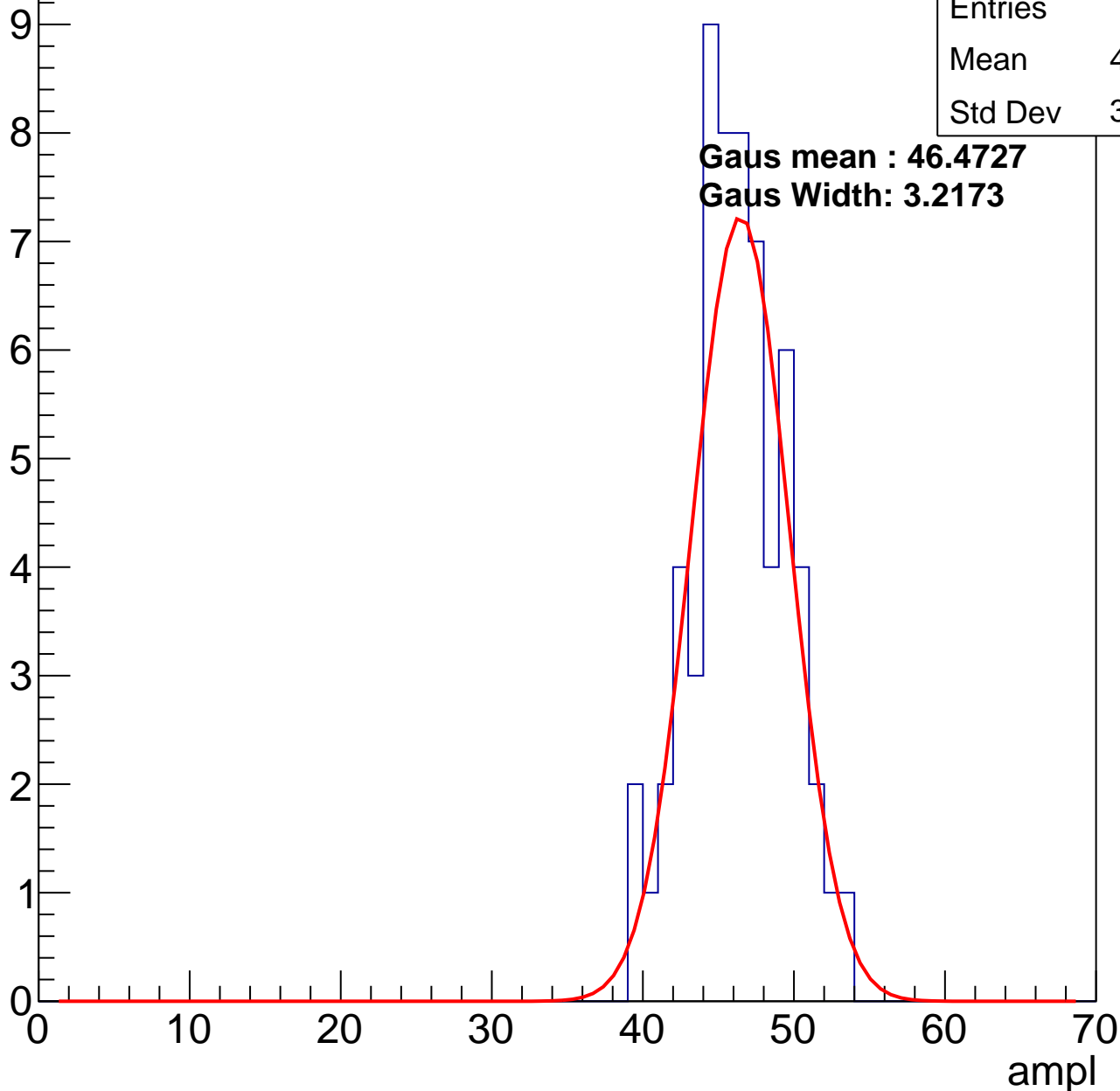
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	45.85
Std Dev	3.094

Gaus mean : 46.4727

Gaus Width: 3.2173

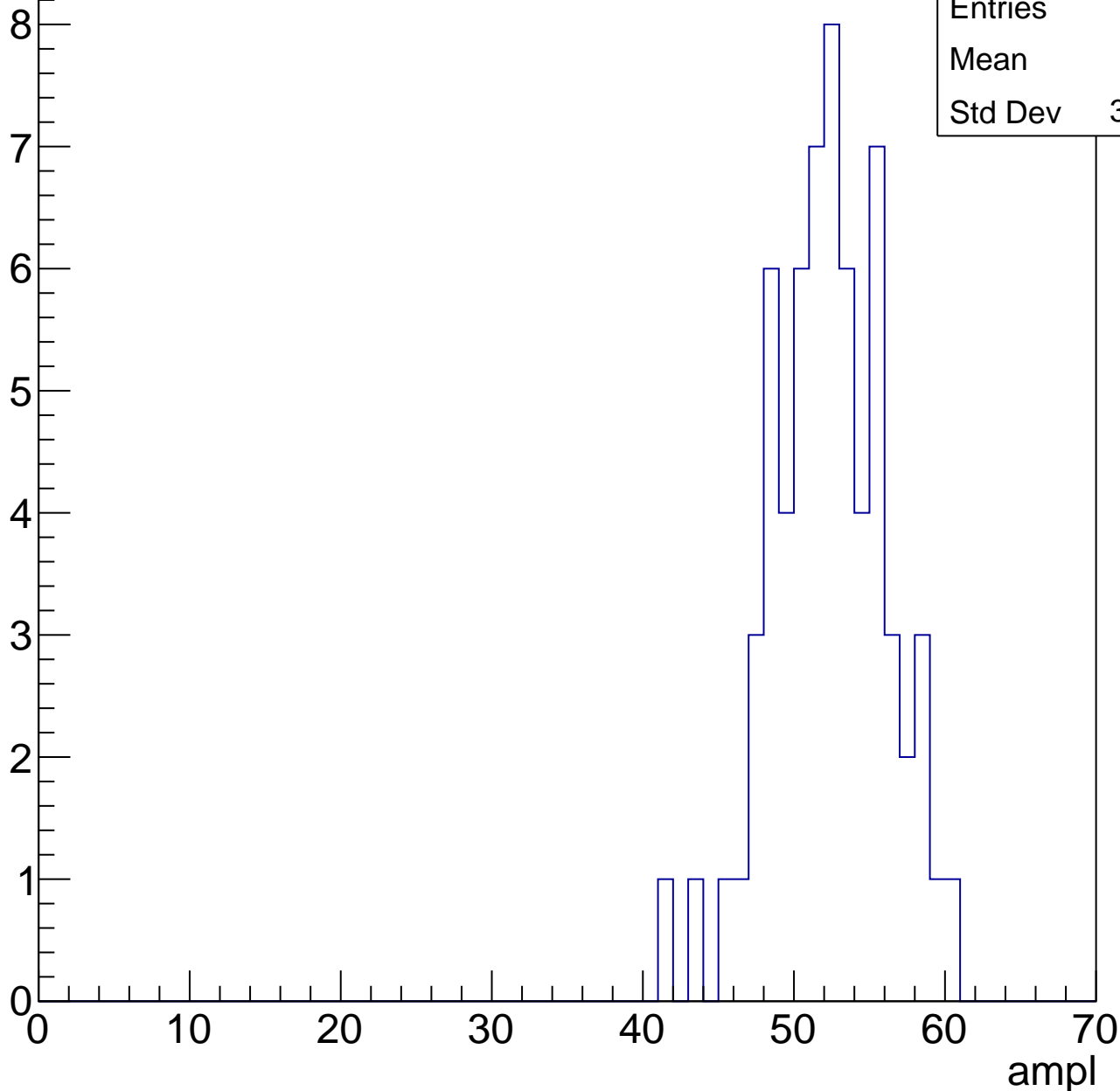


B1L101S, U26-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

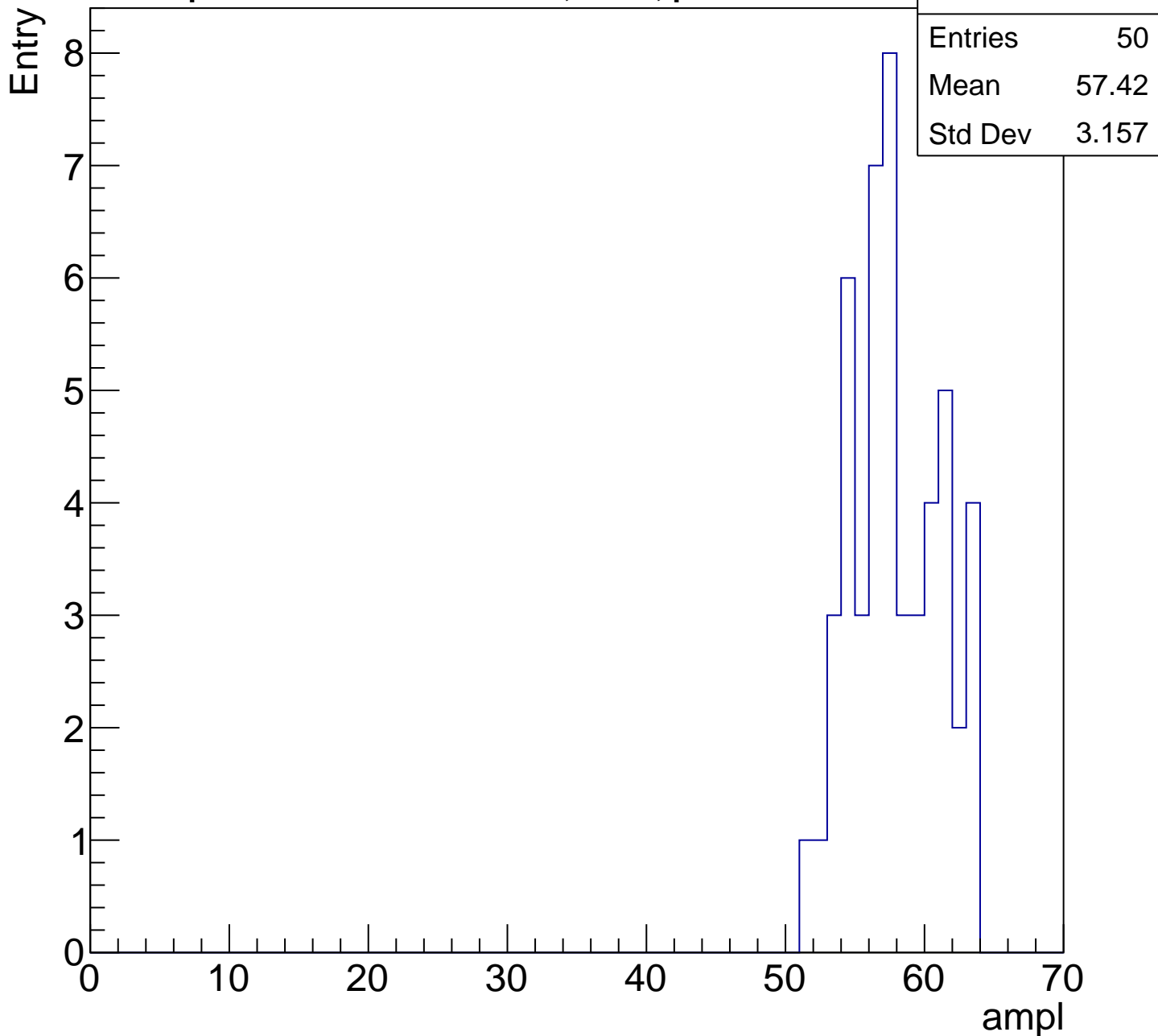
Entry

Entries	65
Mean	51.8
Std Dev	3.775



B1L101S, U26-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

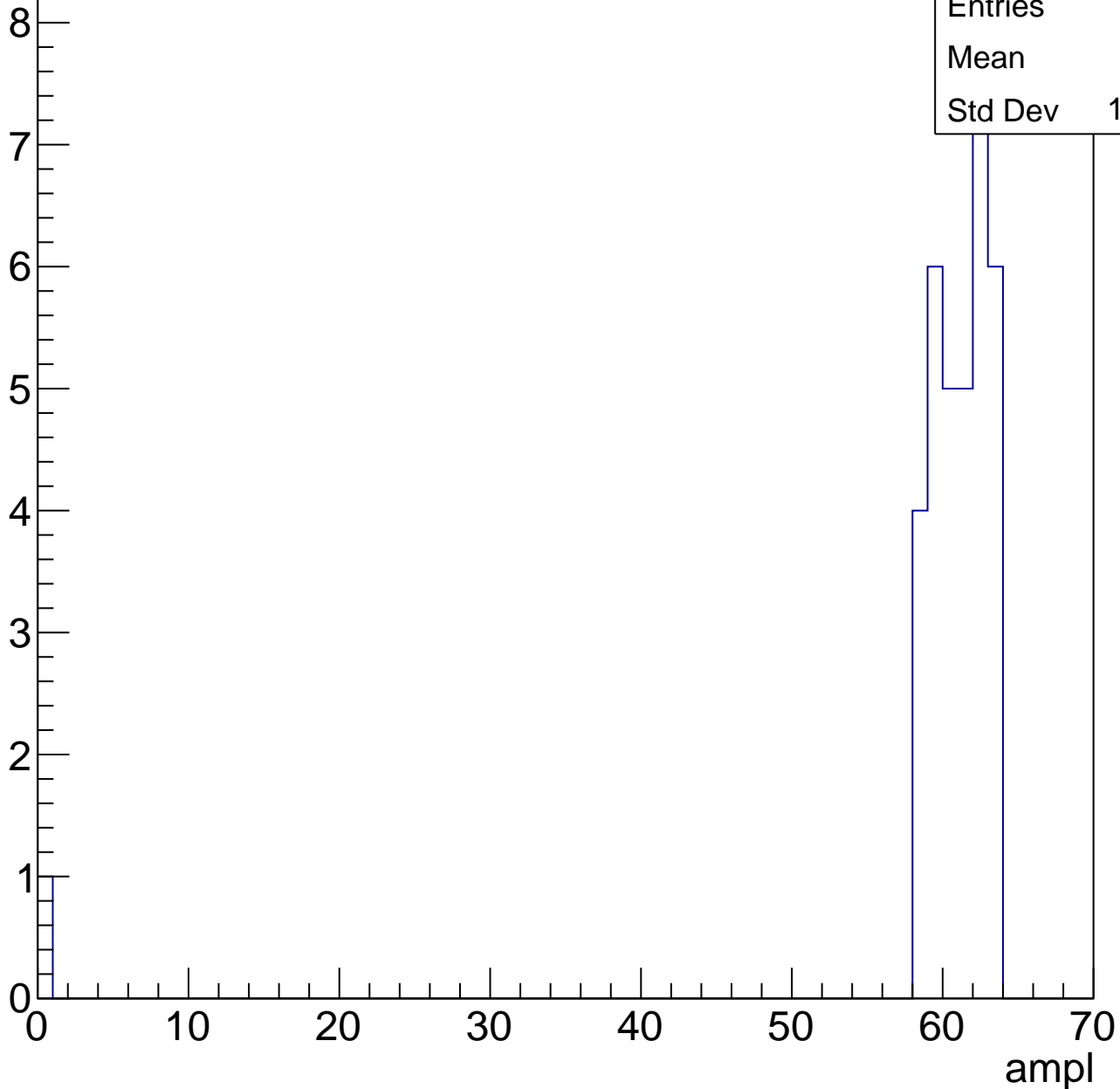


B1L101S, U26-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	59
Std Dev	10.25



B1L101S, U26-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch42, adc0

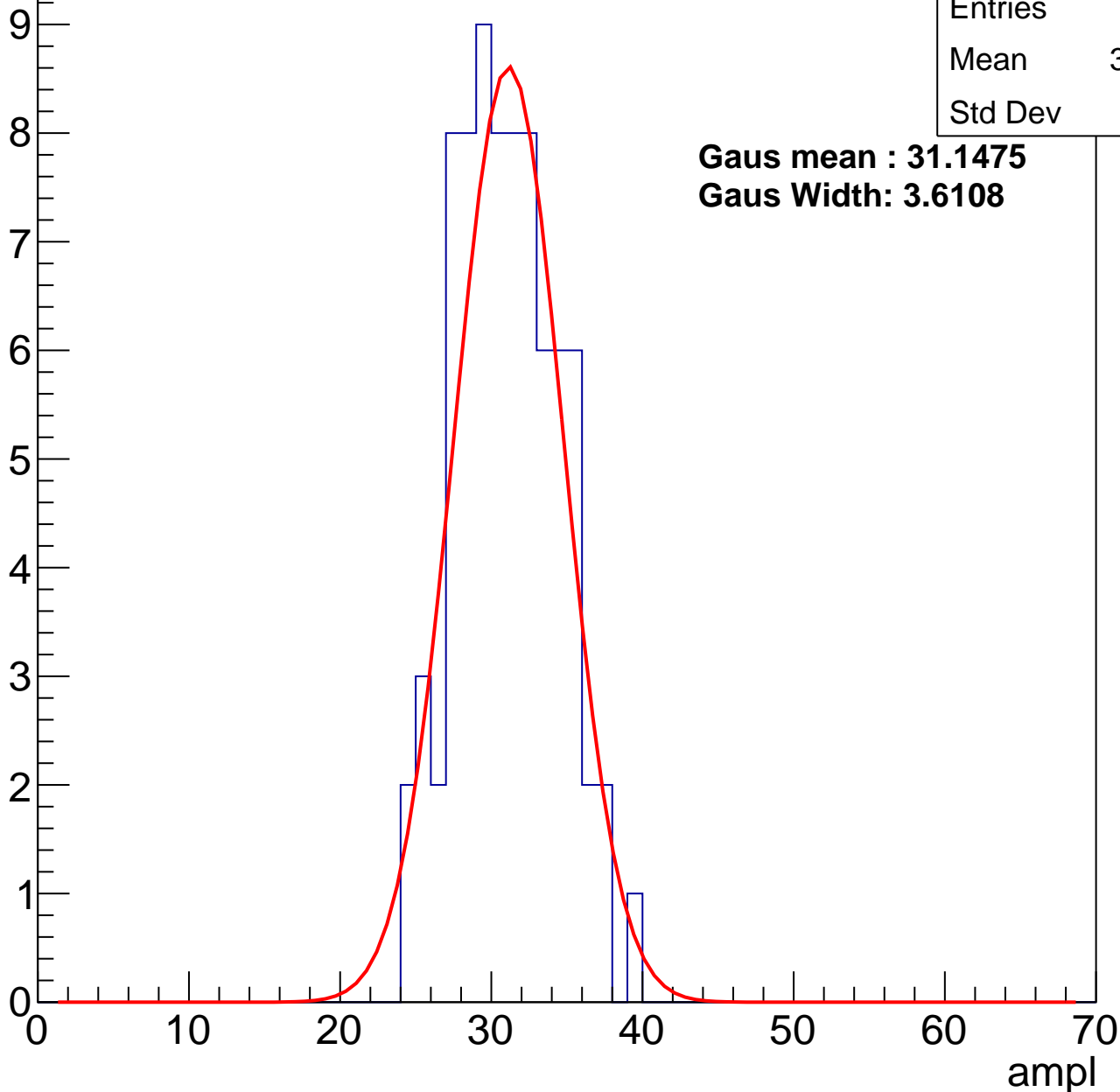
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	30.59
Std Dev	3.29

Gaus mean : 31.1475

Gaus Width: 3.6108



B1L101S, U26-ch42, adc1

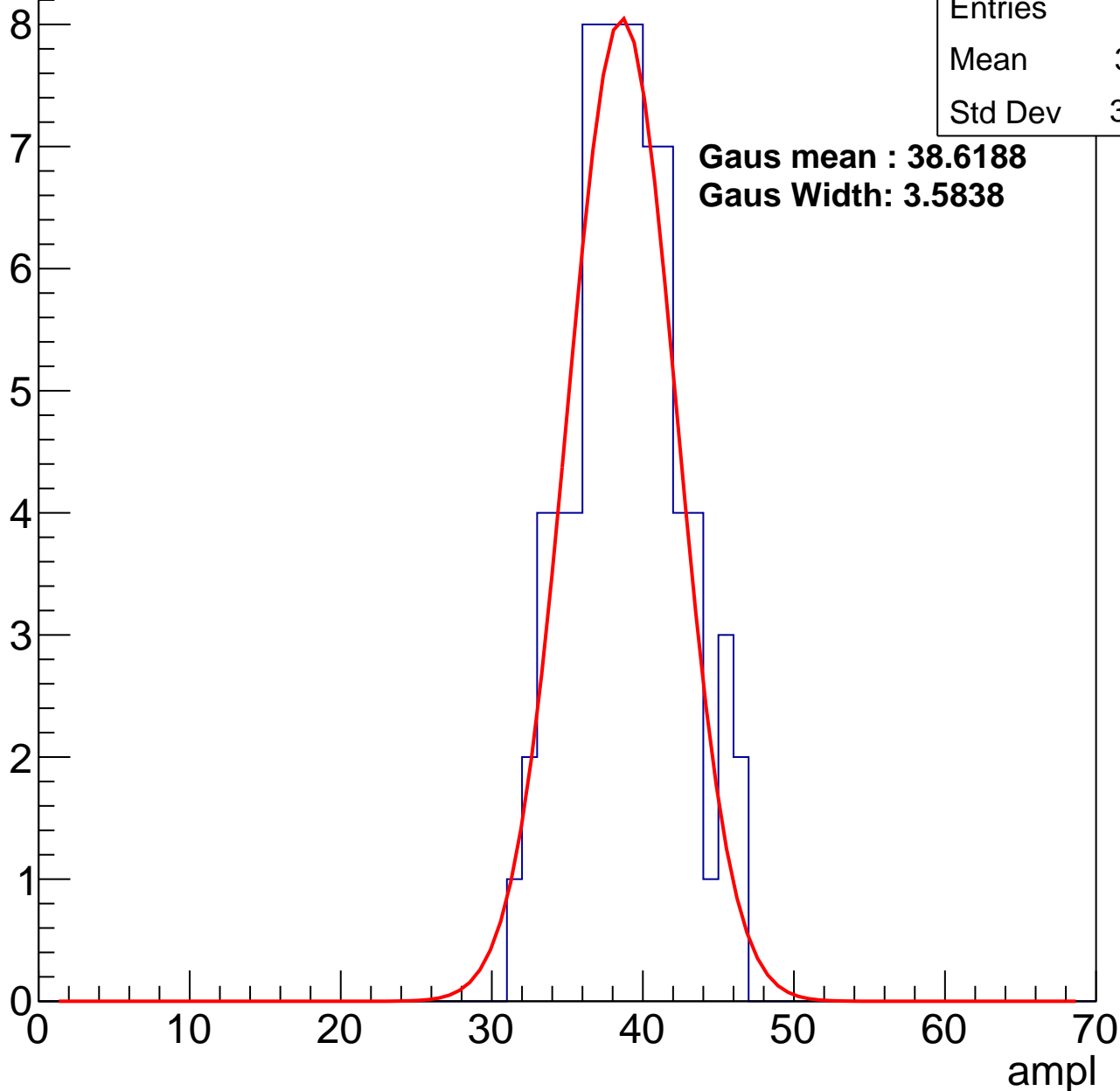
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	38.41
Std Dev	3.507

Gaus mean : 38.6188

Gaus Width: 3.5838



B1L101S, U26-ch42, adc2

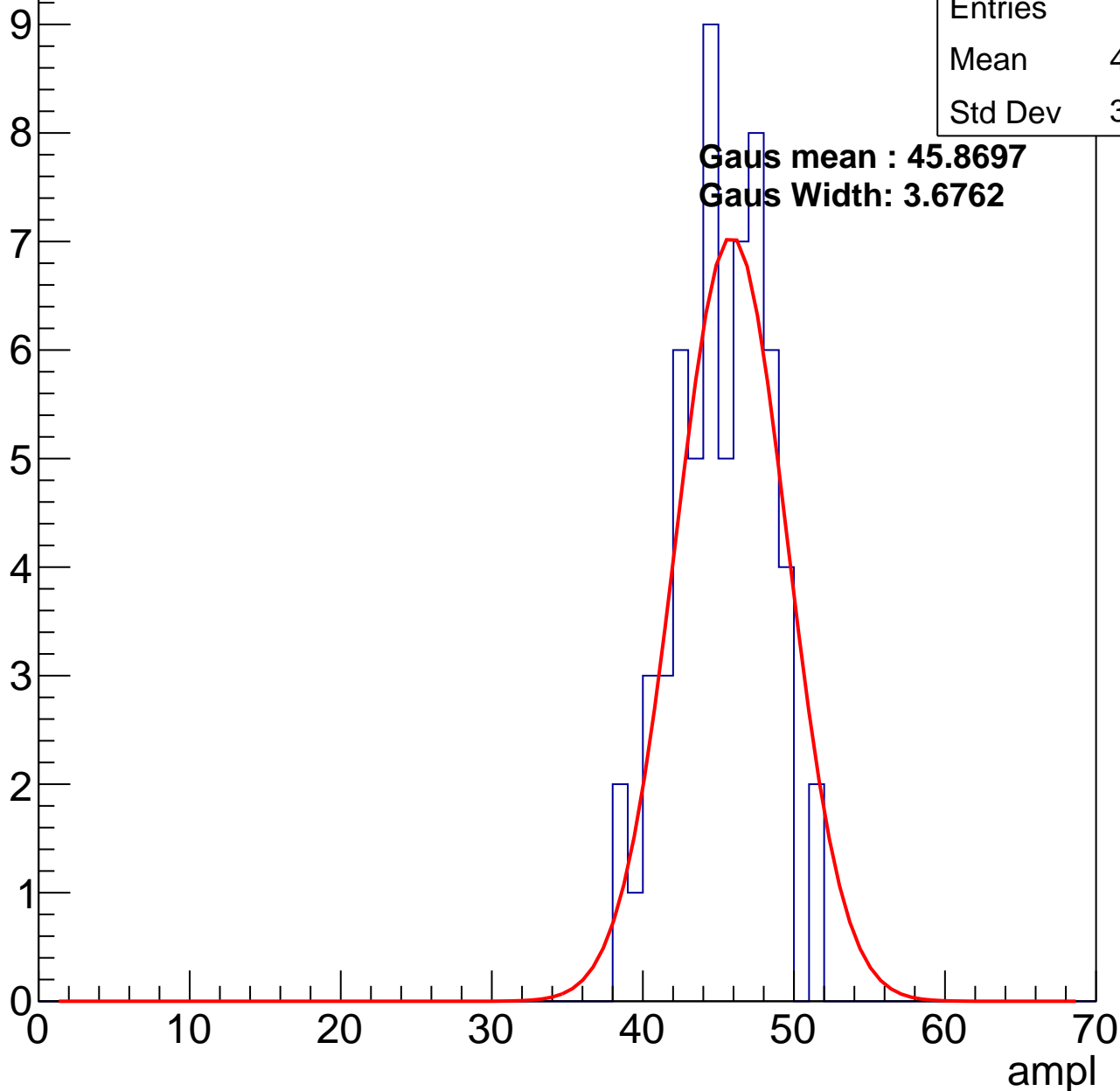
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	44.75
Std Dev	3.044

Gaus mean : 45.8697

Gaus Width: 3.6762

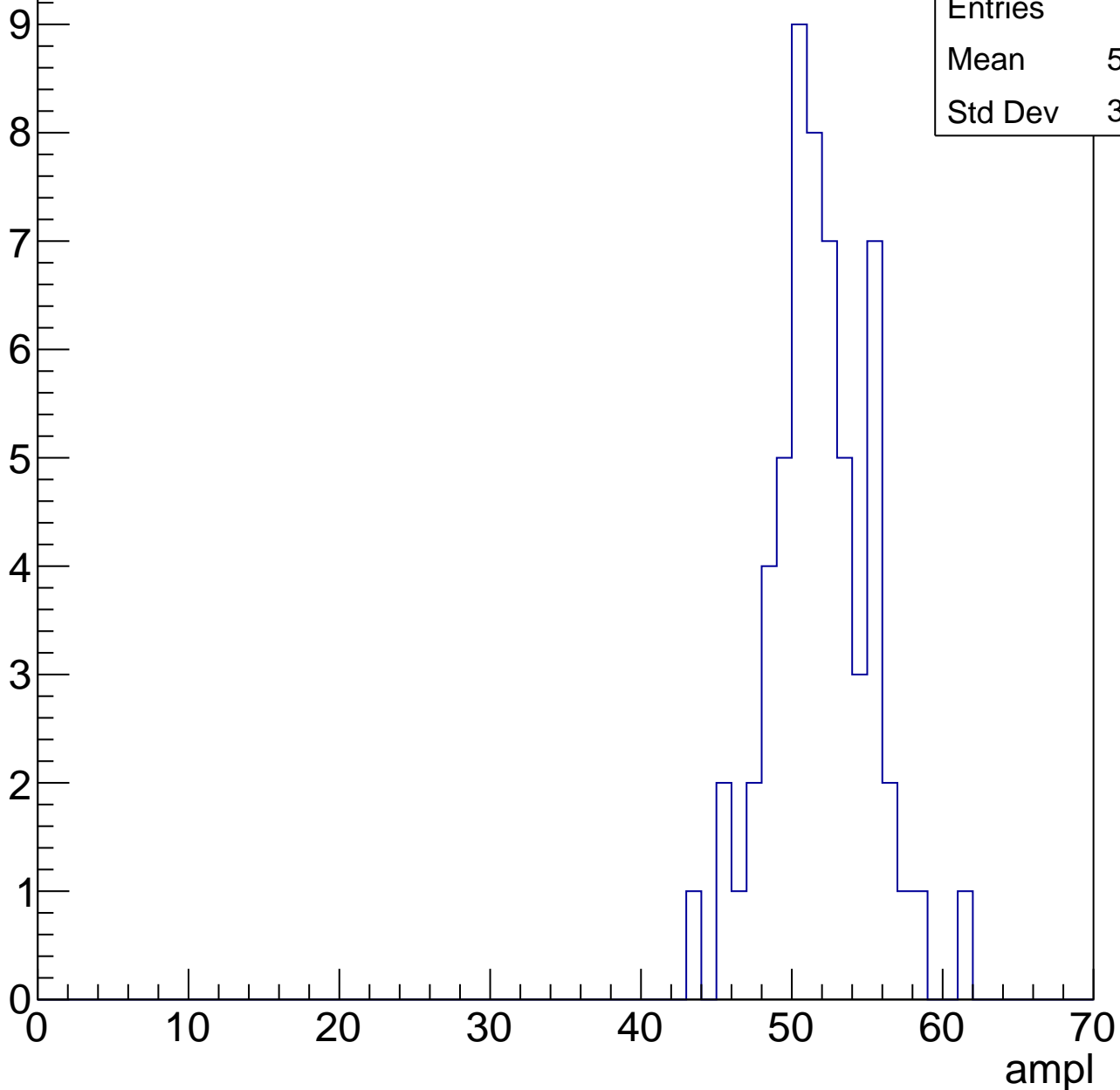


B1L101S, U26-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	51.39
Std Dev	3.319

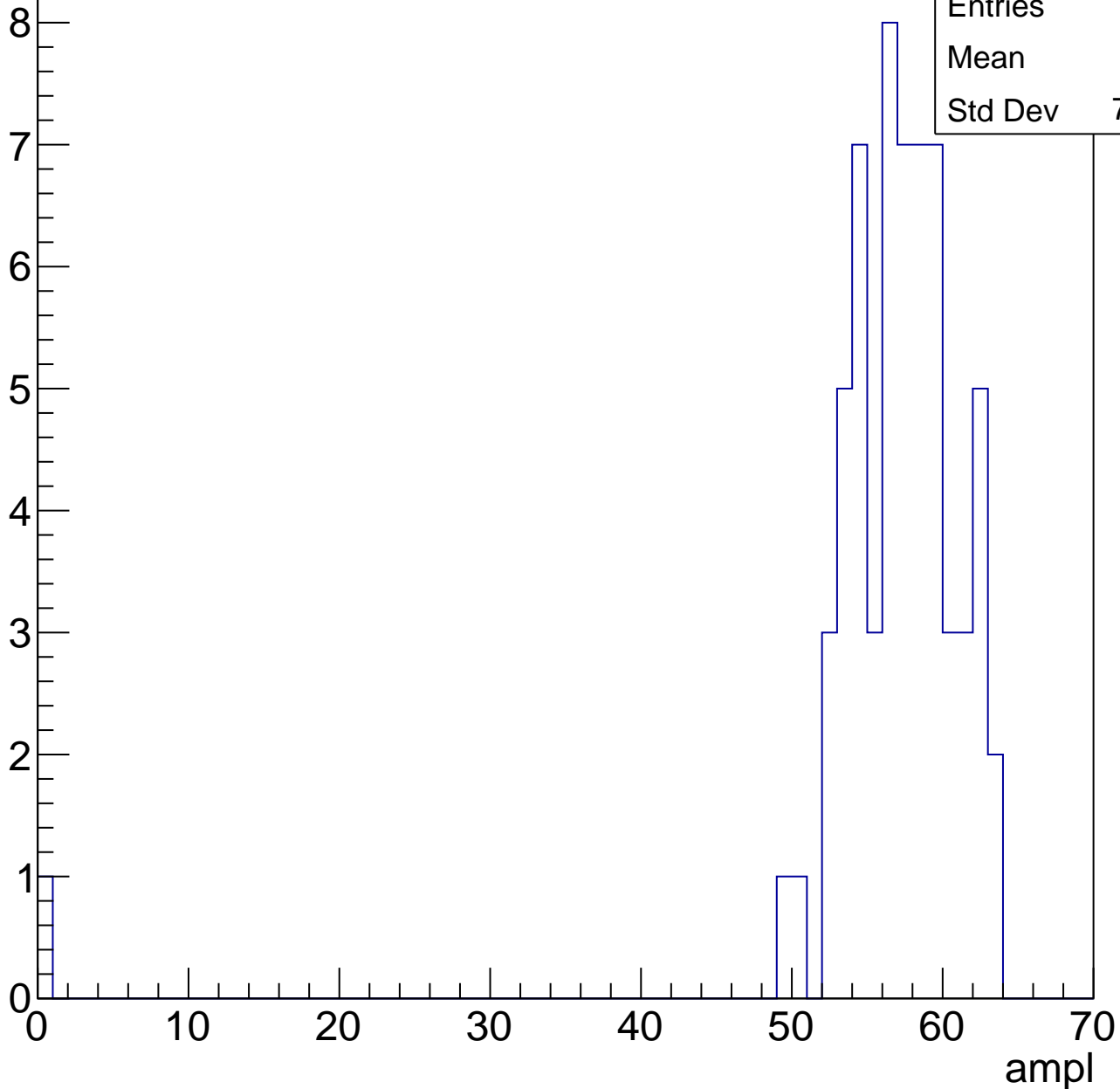


B1L101S, U26-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	56
Std Dev	7.811



B1L101S, U26-ch42, adc5

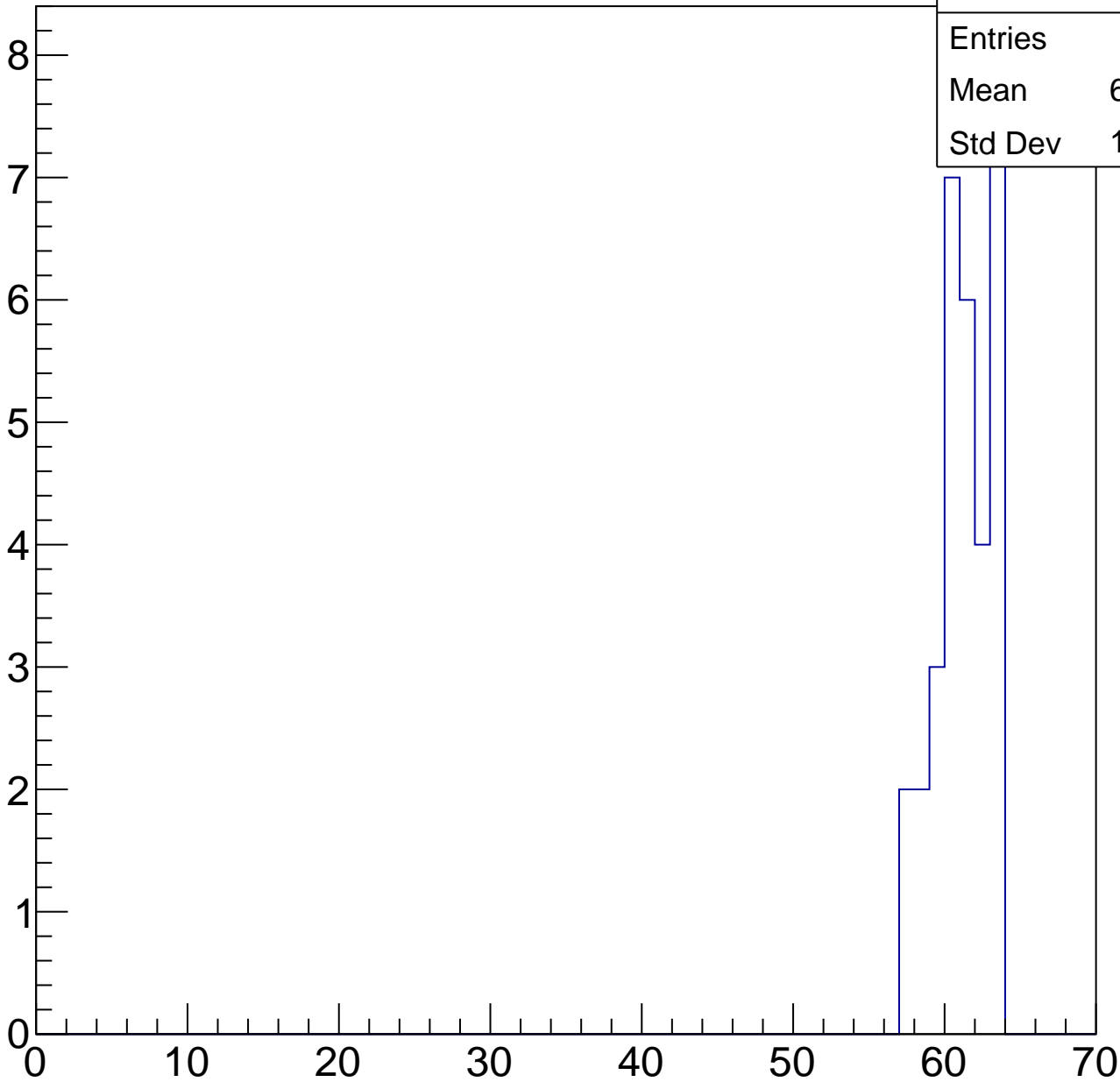
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.78
Std Dev	1.798

ampl



B1L101S, U26-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch43, adc0

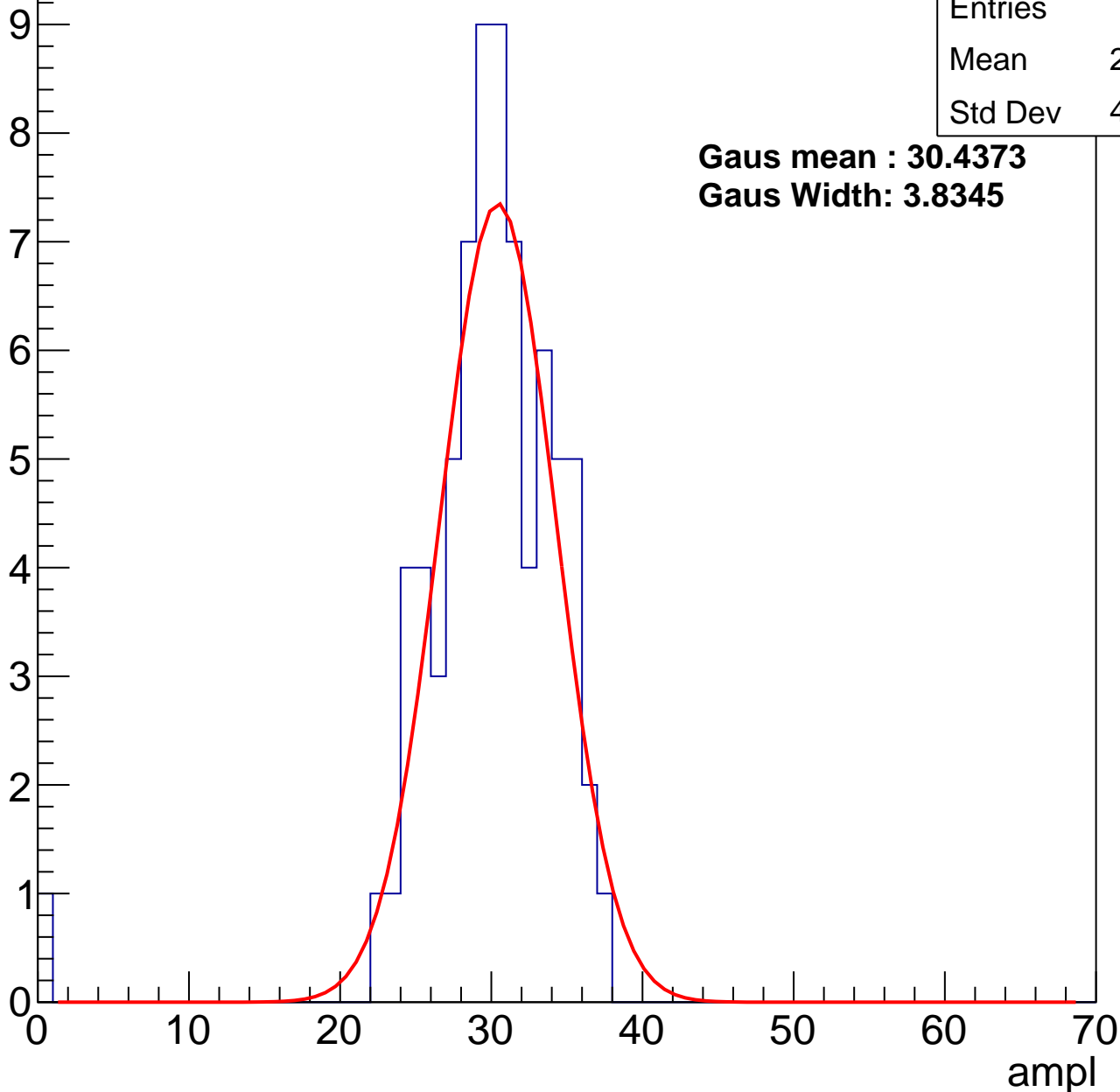
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.43
Std Dev	4.885

Gaus mean : 30.4373

Gaus Width: 3.8345



B1L101S, U26-ch43, adc1

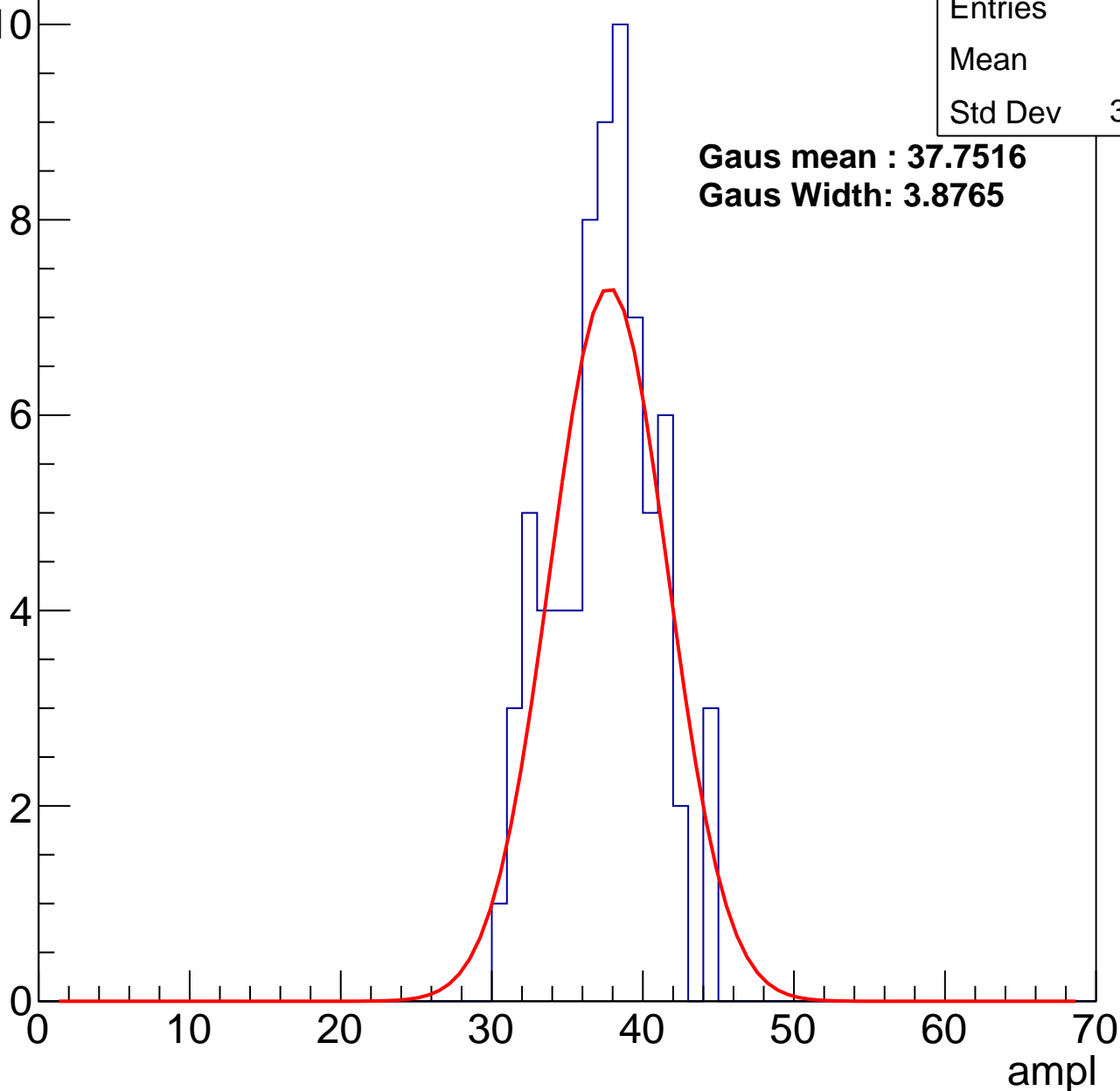
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37
Std Dev	3.319

Gaus mean : 37.7516

Gaus Width: 3.8765



B1L101S, U26-ch43, adc2

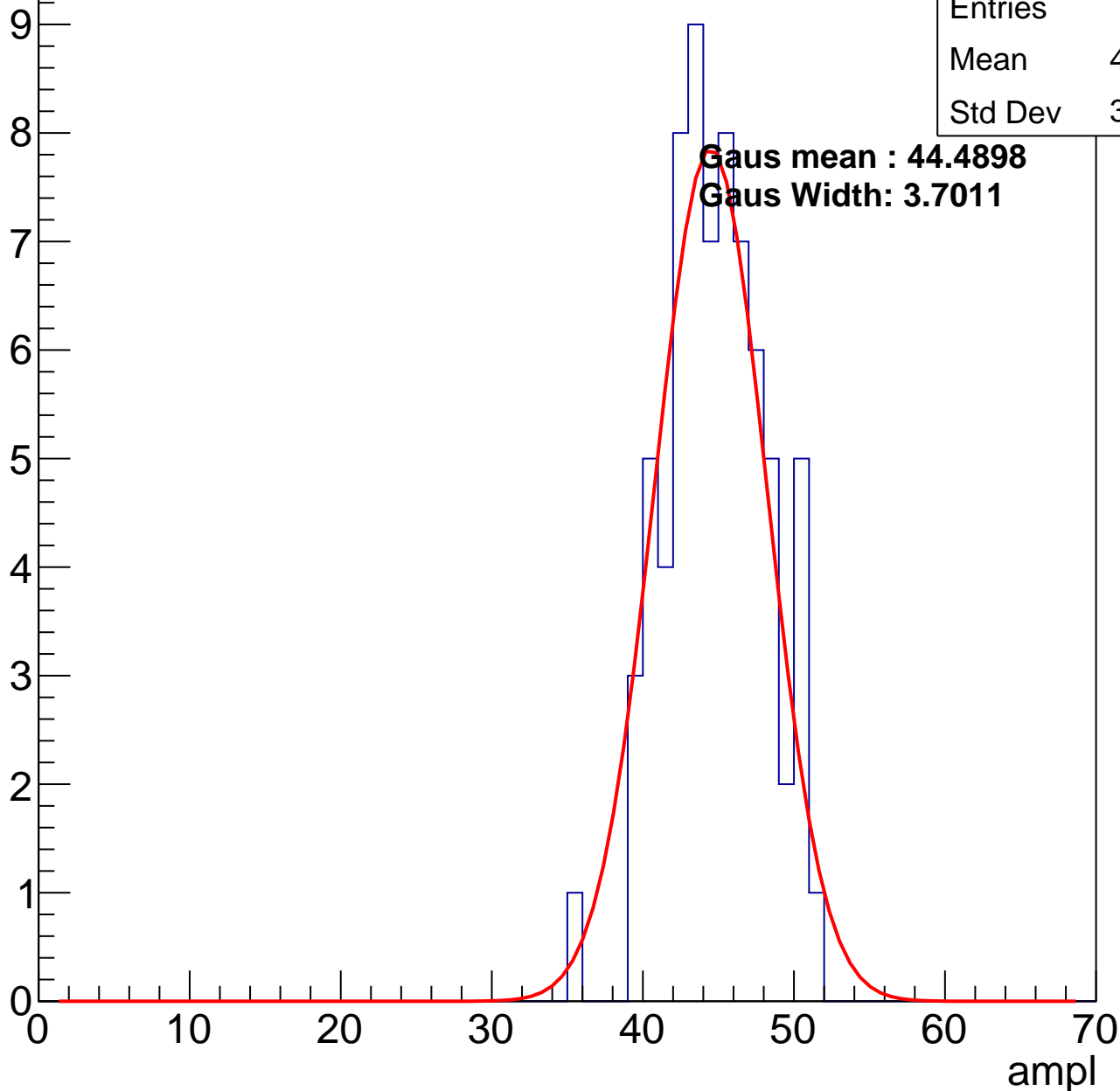
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.37
Std Dev	3.256

Gaus mean : 44.4898

Gaus Width: 3.7011



B1L101S, U26-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	51.49
Std Dev	3.358

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

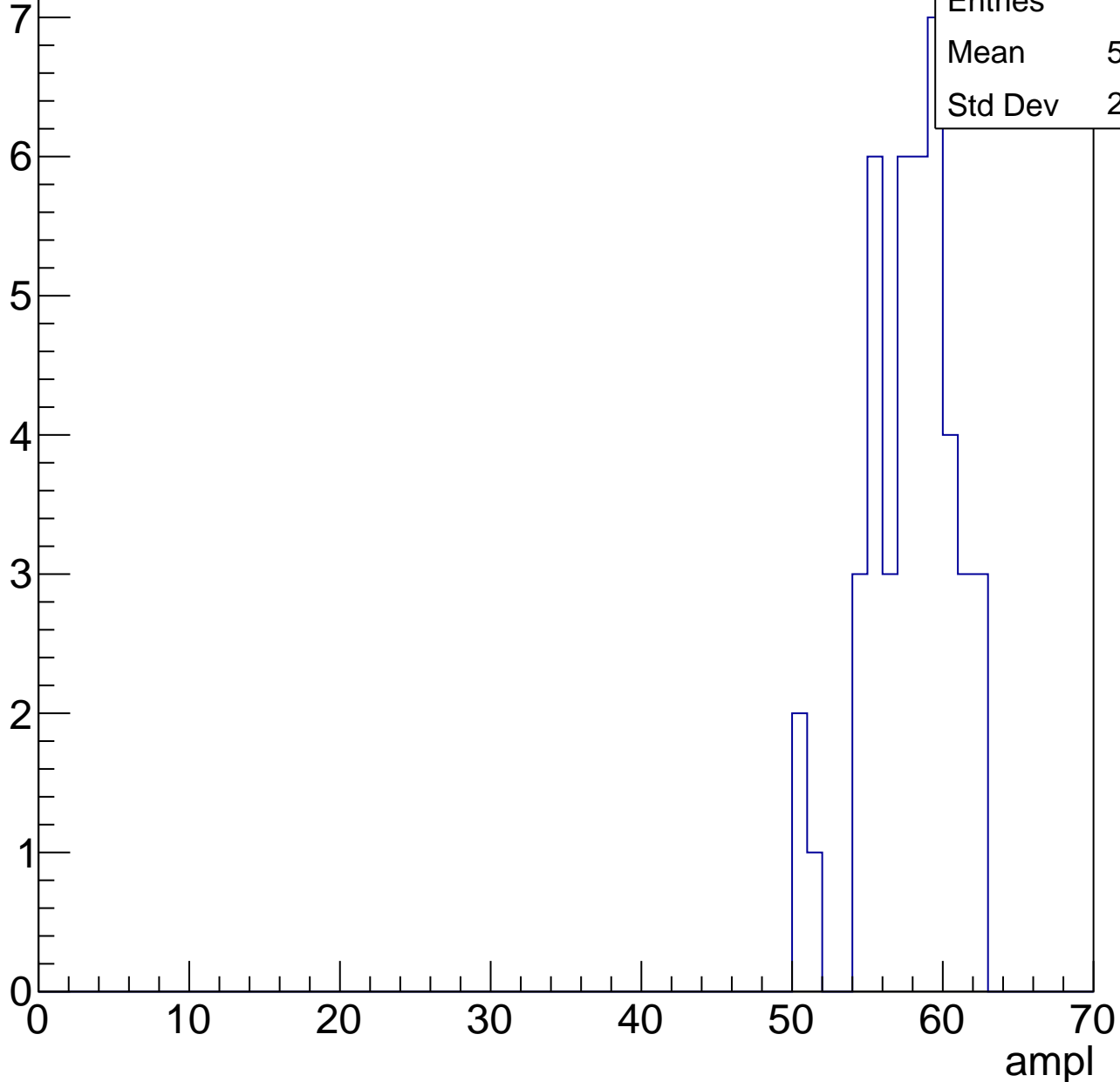
60

70

B1L101S, U26-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

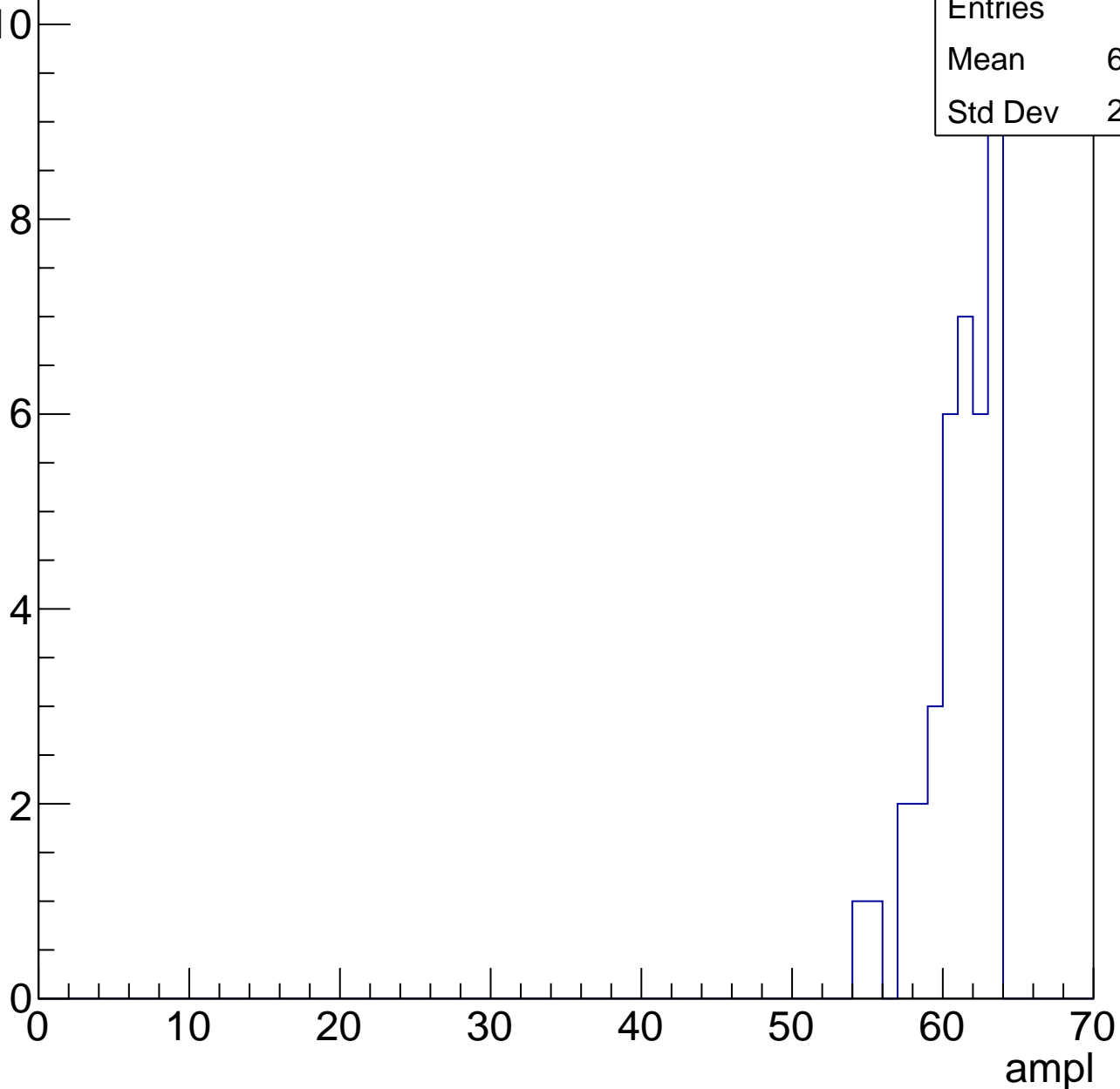


B1L101S, U26-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	60.66
Std Dev	2.263



B1L101S, U26-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

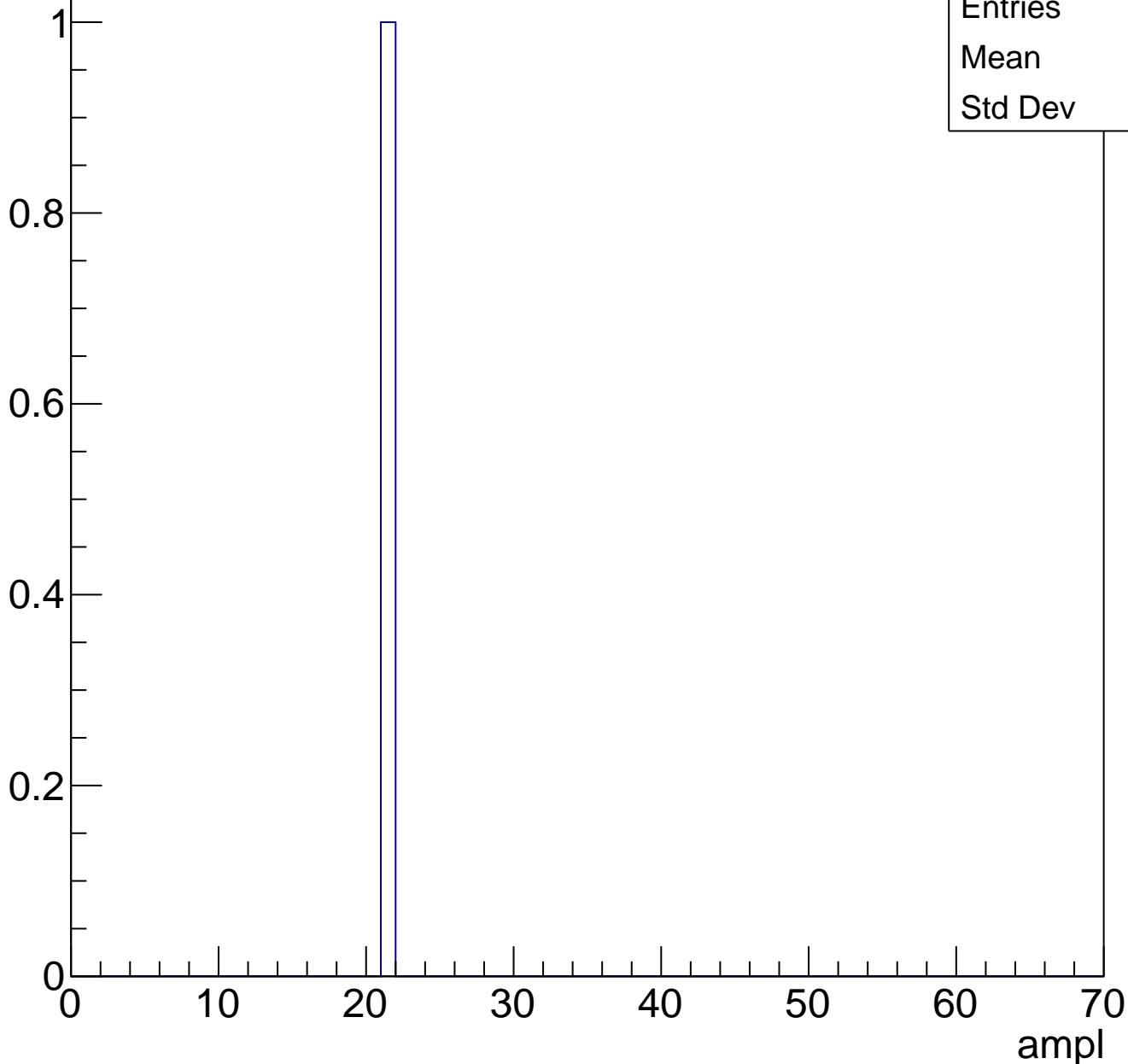
Entry



B1L101S, U26-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch44, adc0

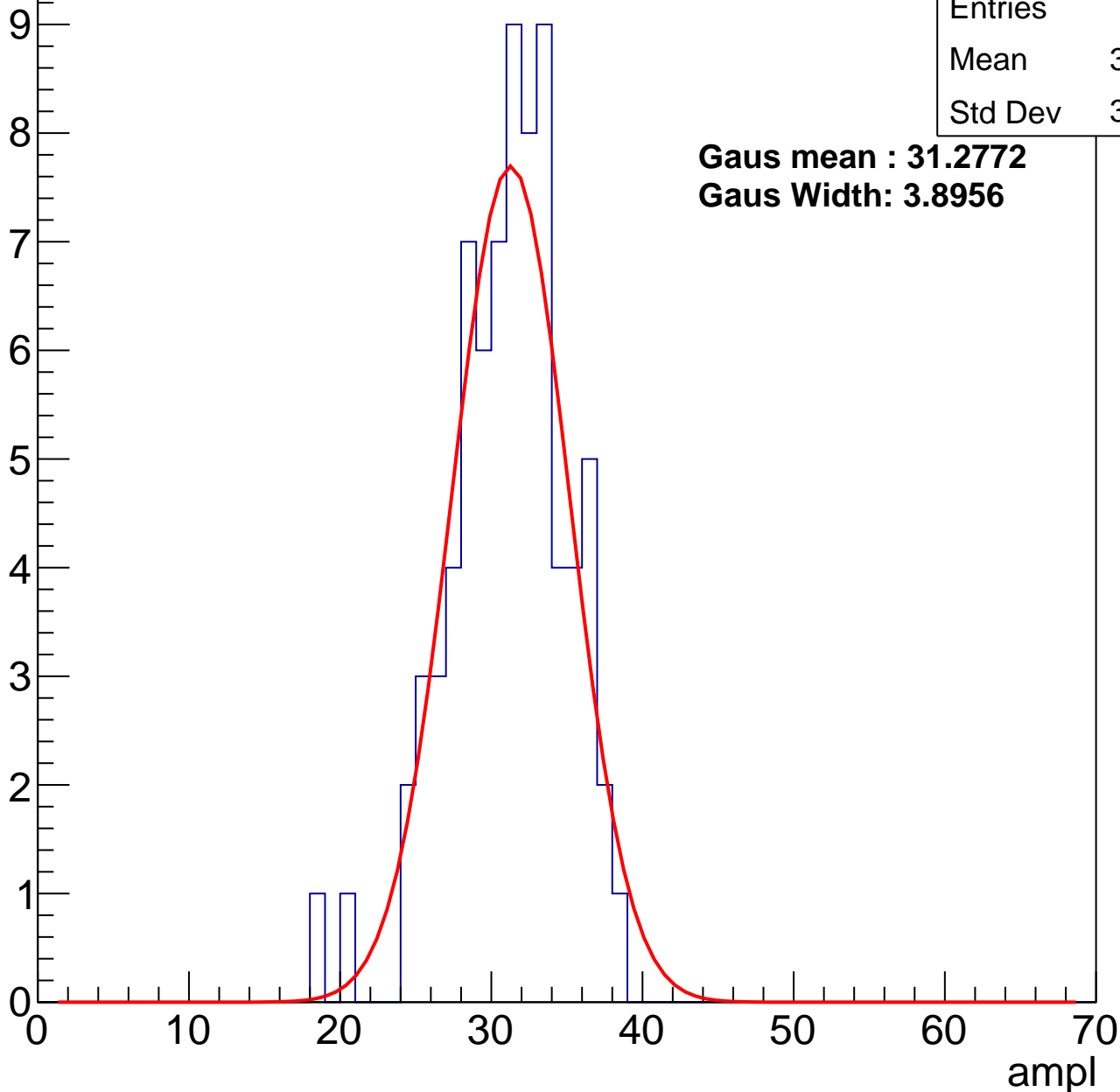
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.62
Std Dev	3.818

Gaus mean : 31.2772

Gaus Width: 3.8956



B1L101S, U26-ch44, adc1

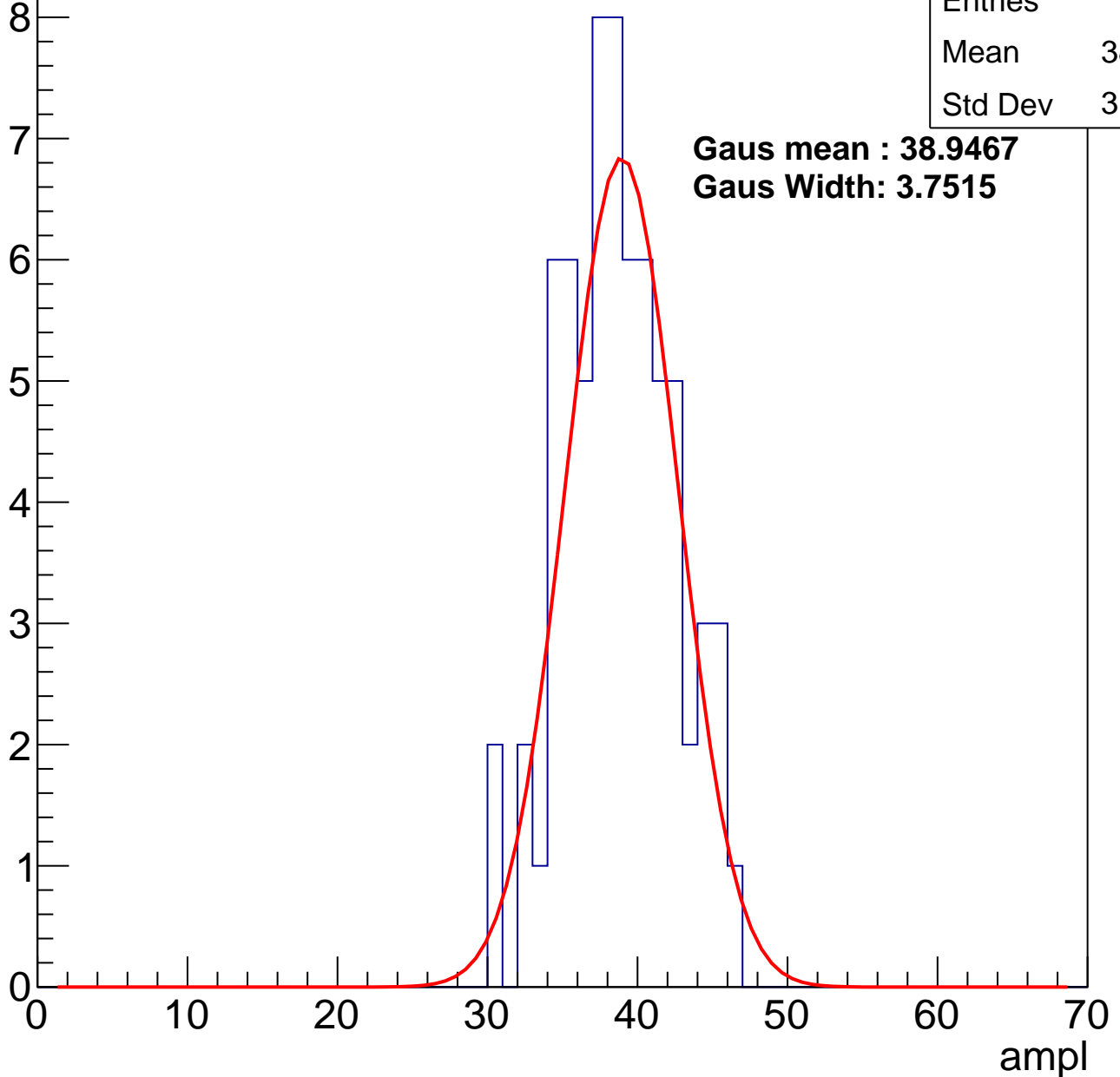
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.25
Std Dev	3.657

Gaus mean : 38.9467

Gaus Width: 3.7515



B1L101S, U26-ch44, adc2

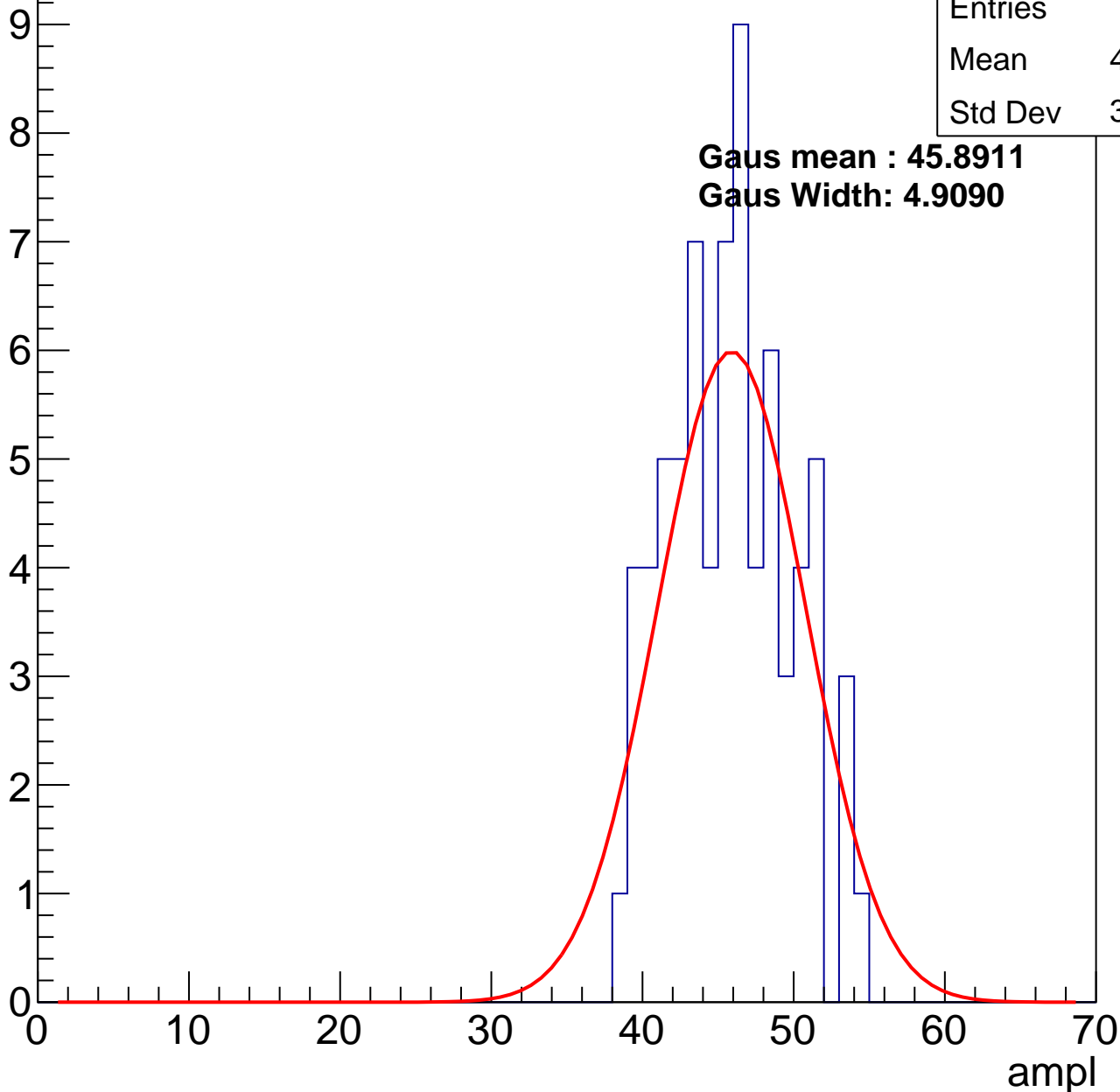
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	45.36
Std Dev	3.945

Gaus mean : 45.8911

Gaus Width: 4.9090

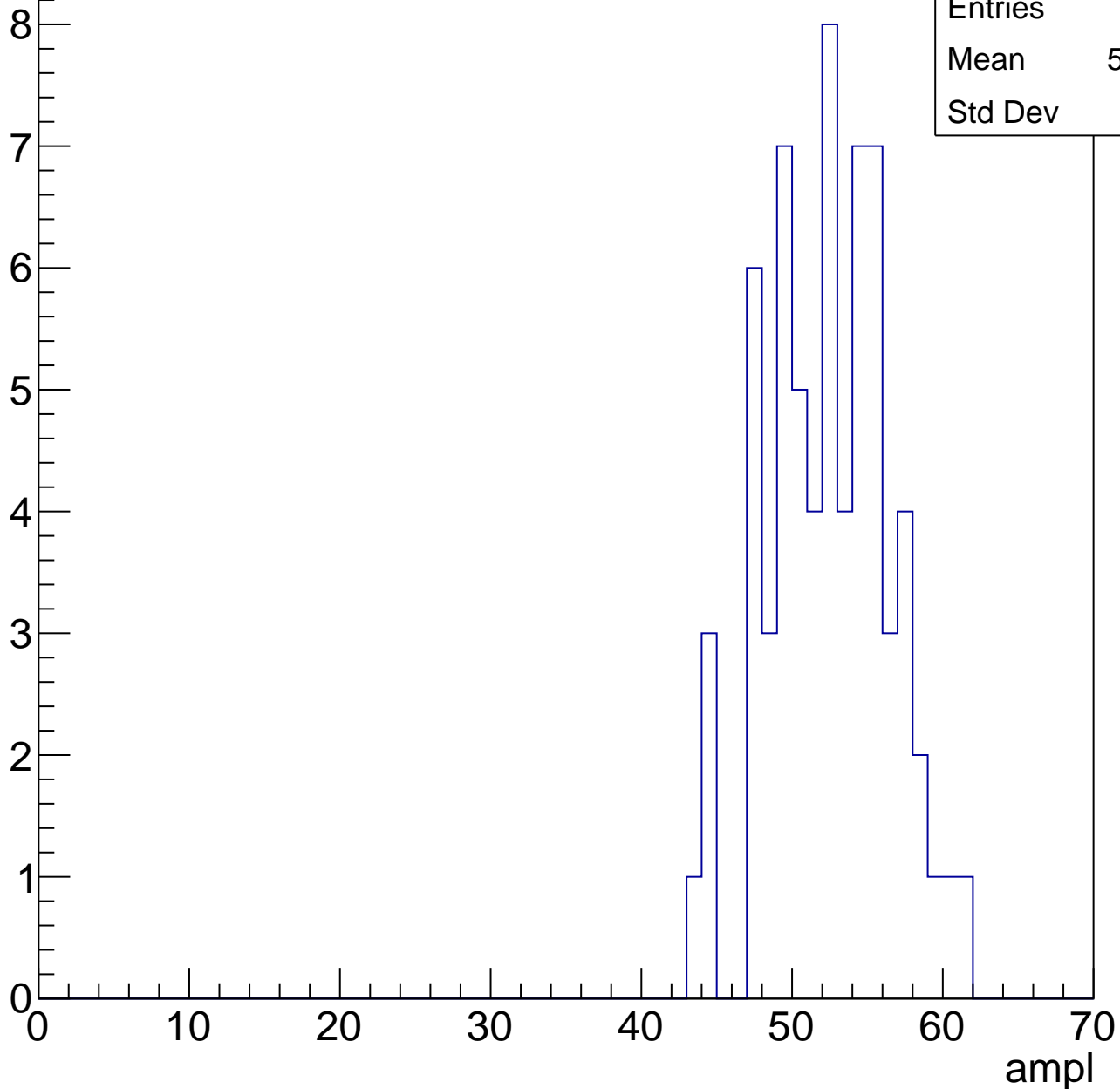


B1L101S, U26-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

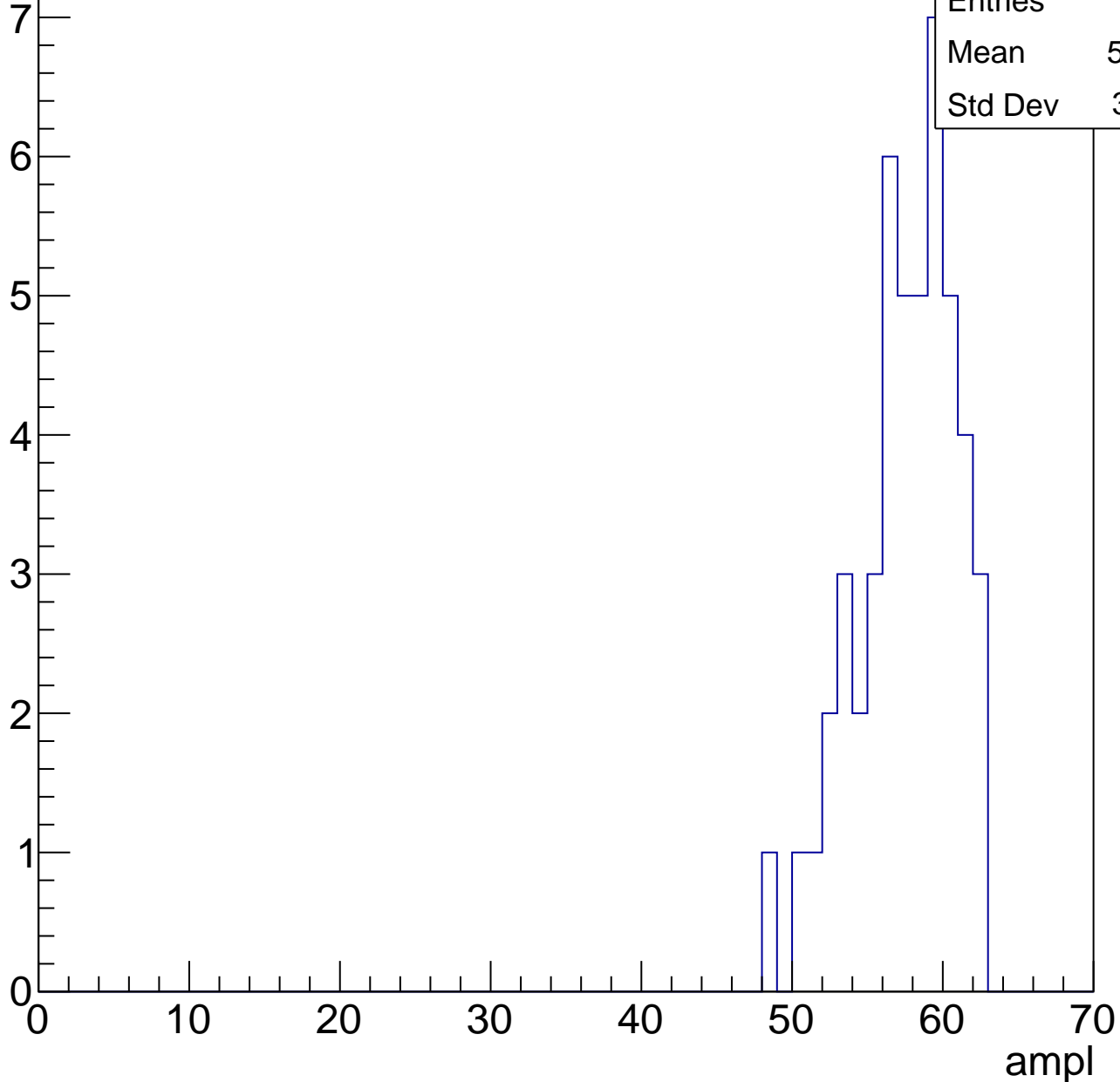
Entries	67
Mean	51.96
Std Dev	3.99



B1L101S, U26-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

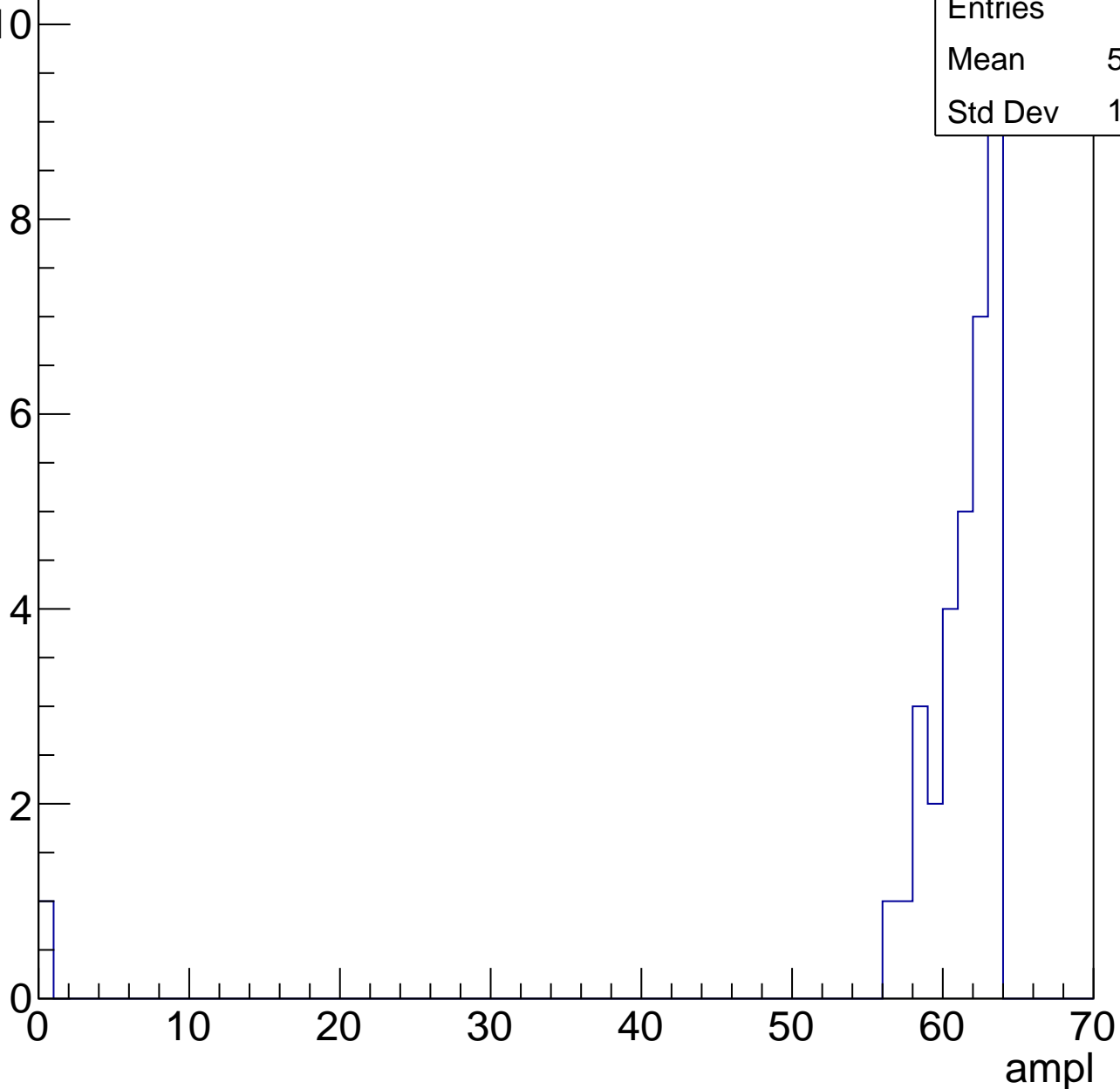


B1L101S, U26-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.24
Std Dev	10.49



B1L101S, U26-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch45, adc0

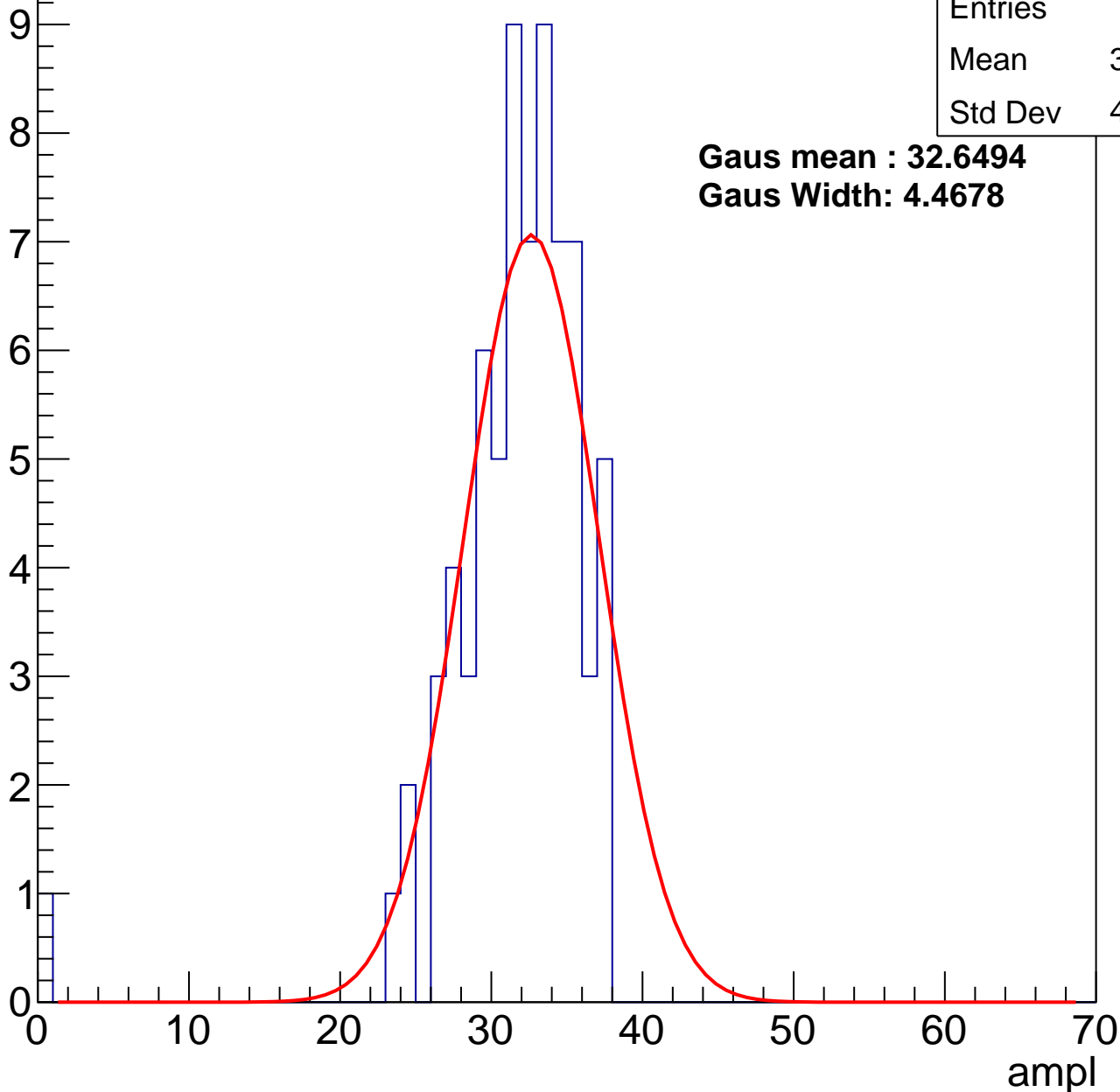
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	31.12
Std Dev	4.986

Gaus mean : 32.6494

Gaus Width: 4.4678



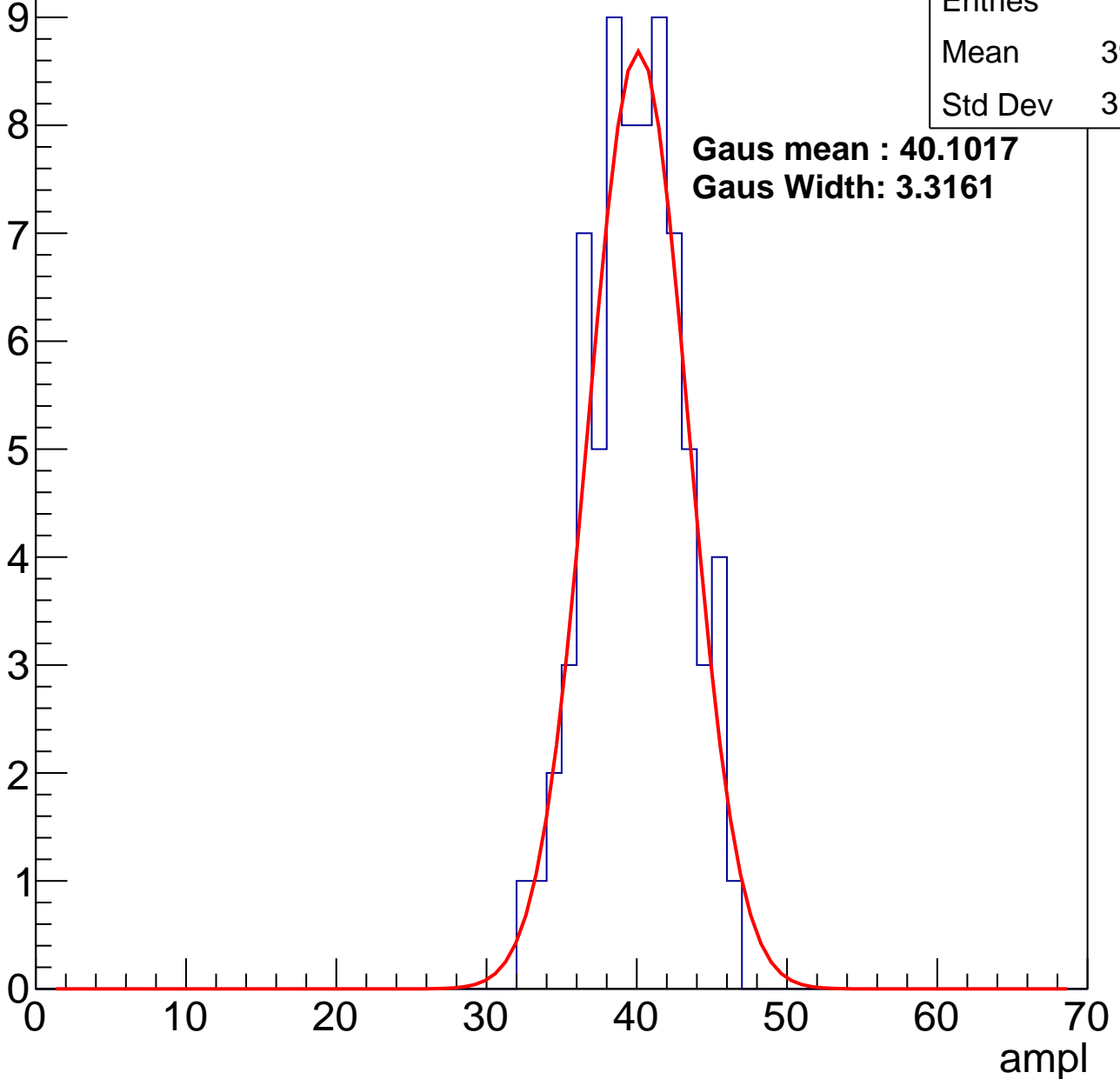
B1L101S, U26-ch45, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	39.52
Std Dev	3.119

Gaus mean : 40.1017
Gaus Width: 3.3161



B1L101S, U26-ch45, adc2

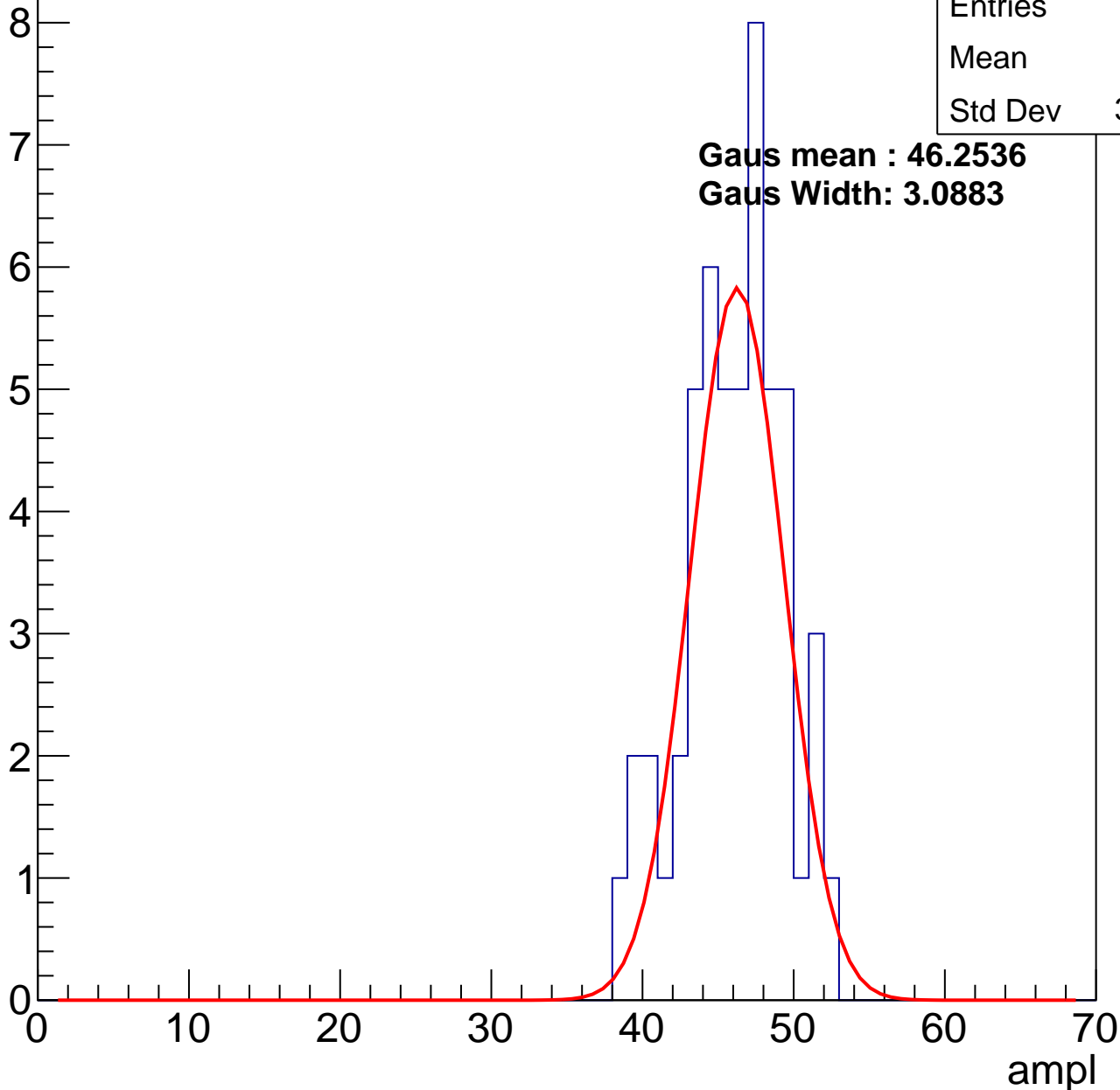
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	45.6
Std Dev	3.271

Gaus mean : 46.2536

Gaus Width: 3.0883

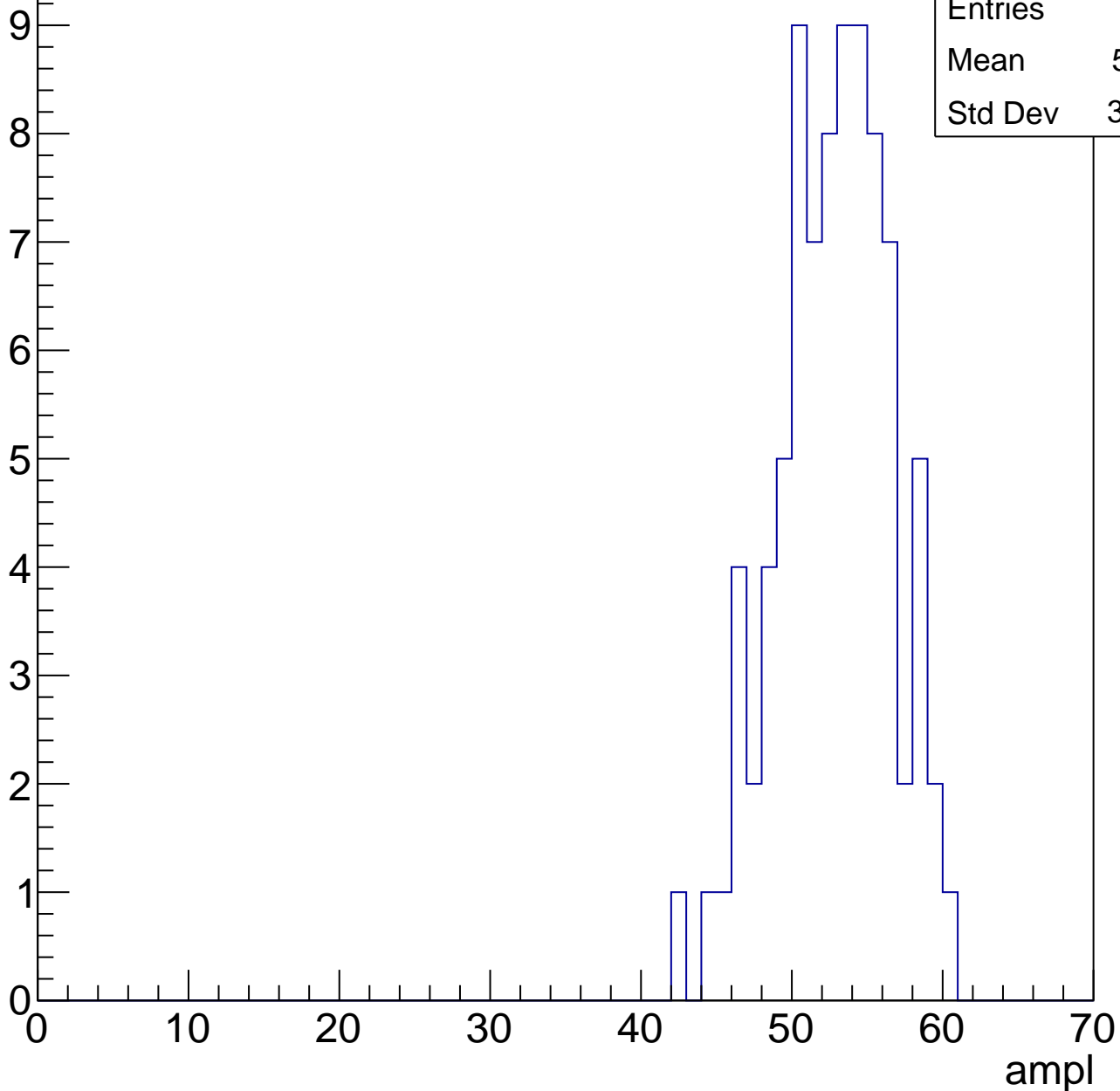


B1L101S, U26-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

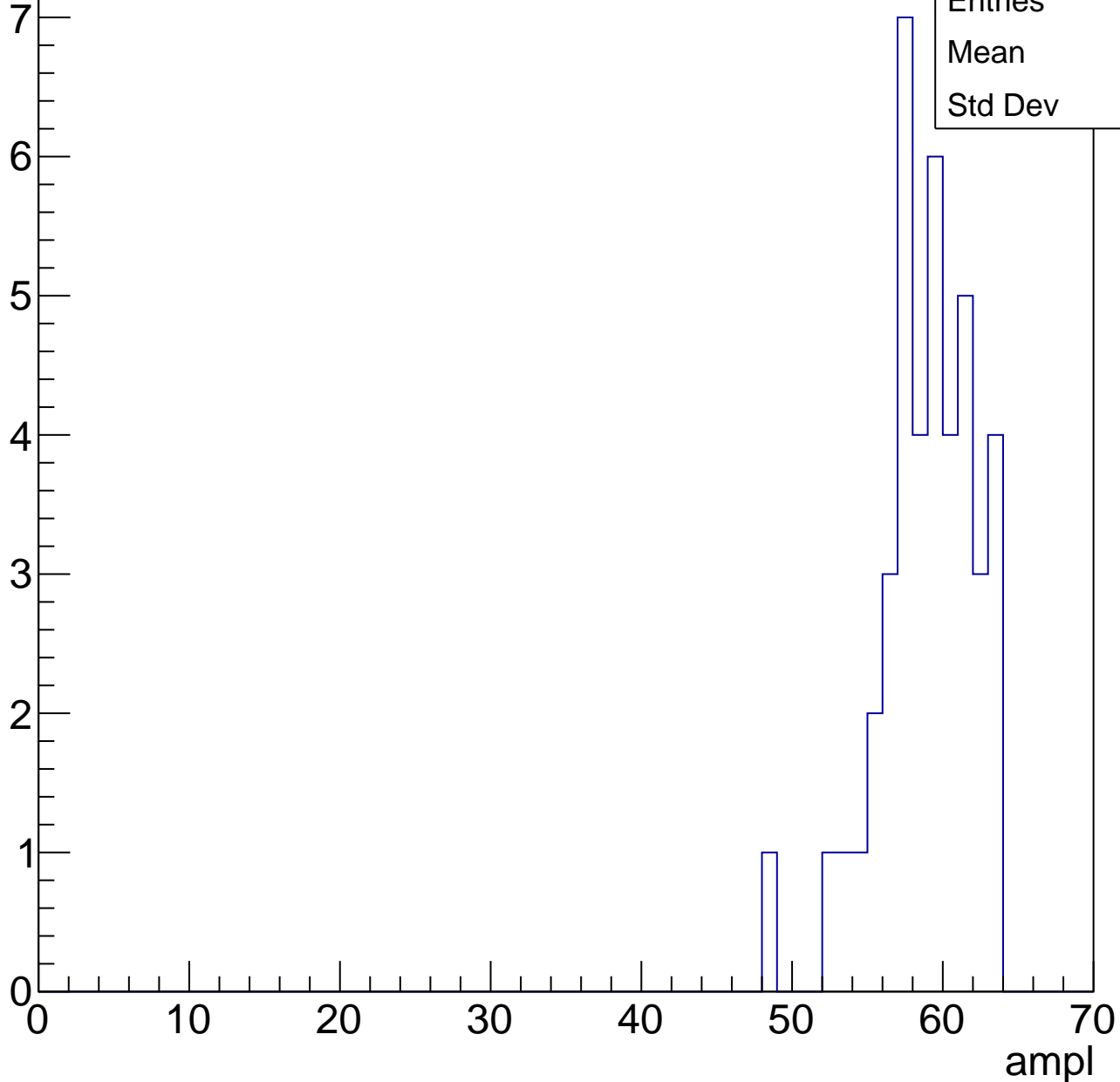
Entries	85
Mean	52.31
Std Dev	3.713



B1L101S, U26-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

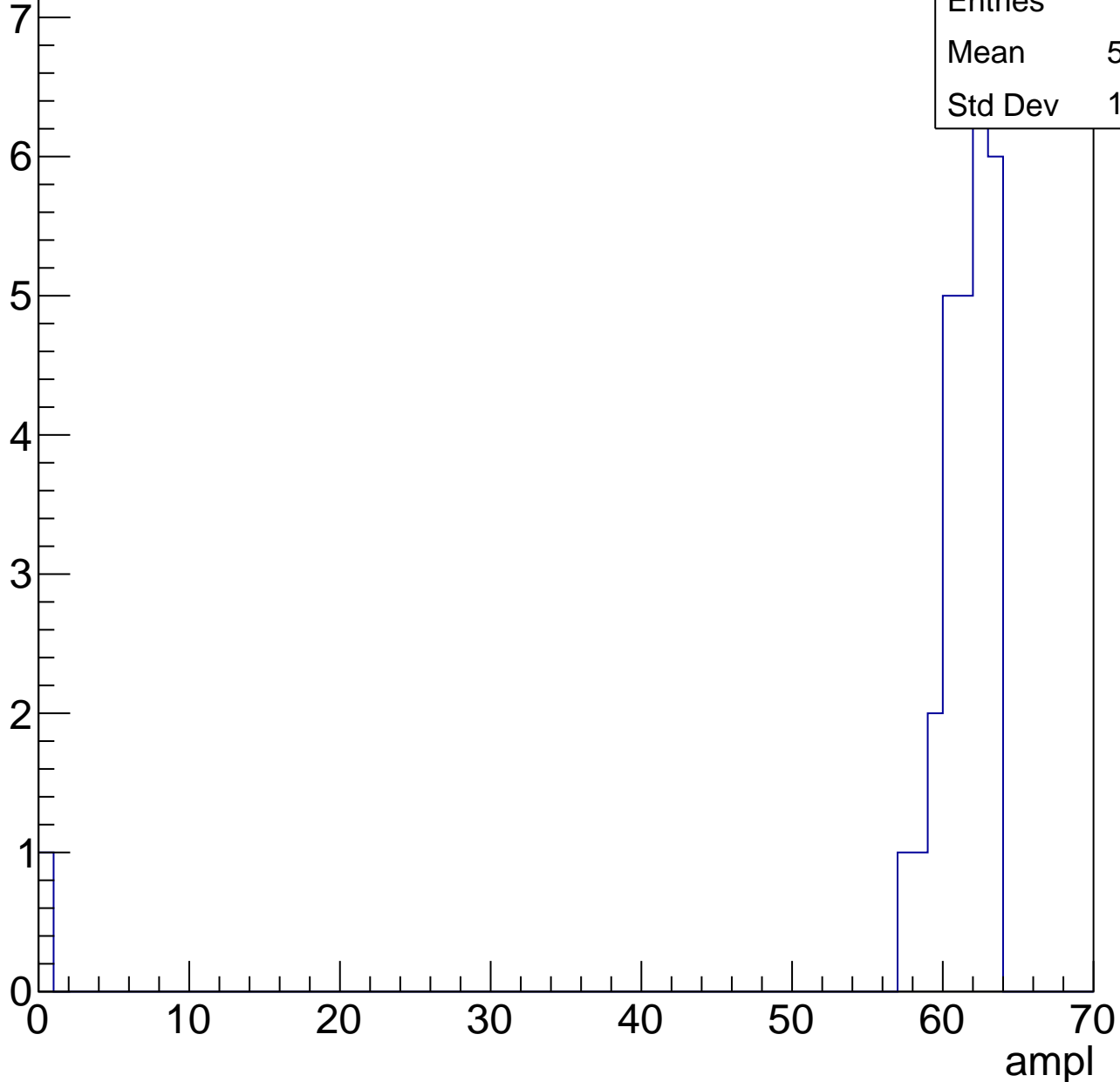
Entry



B1L101S, U26-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	28
Mean	58.93
Std Dev	11.45

B1L101S, U26-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch46, adc0

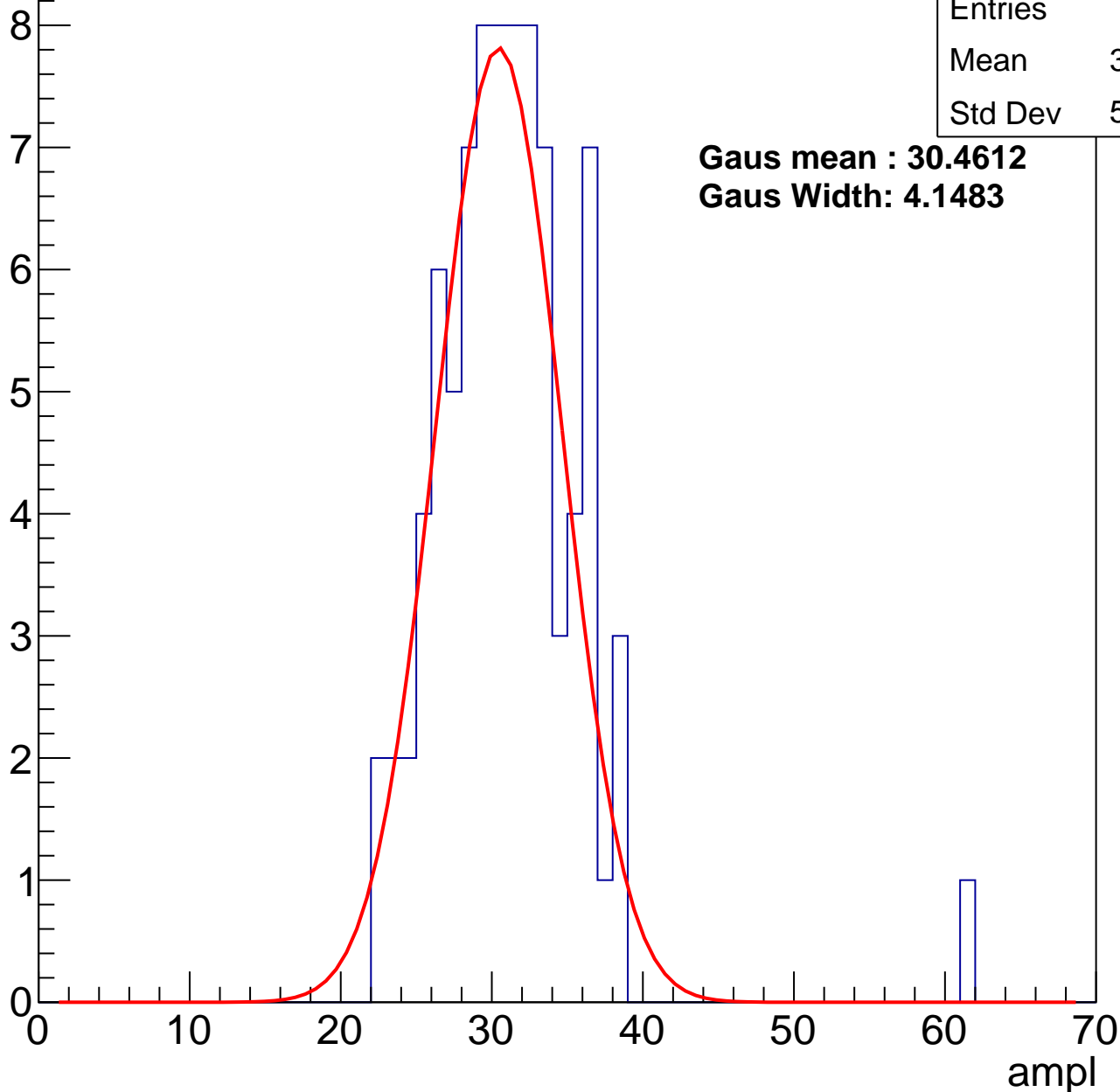
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	30.67
Std Dev	5.105

Gaus mean : 30.4612

Gaus Width: 4.1483

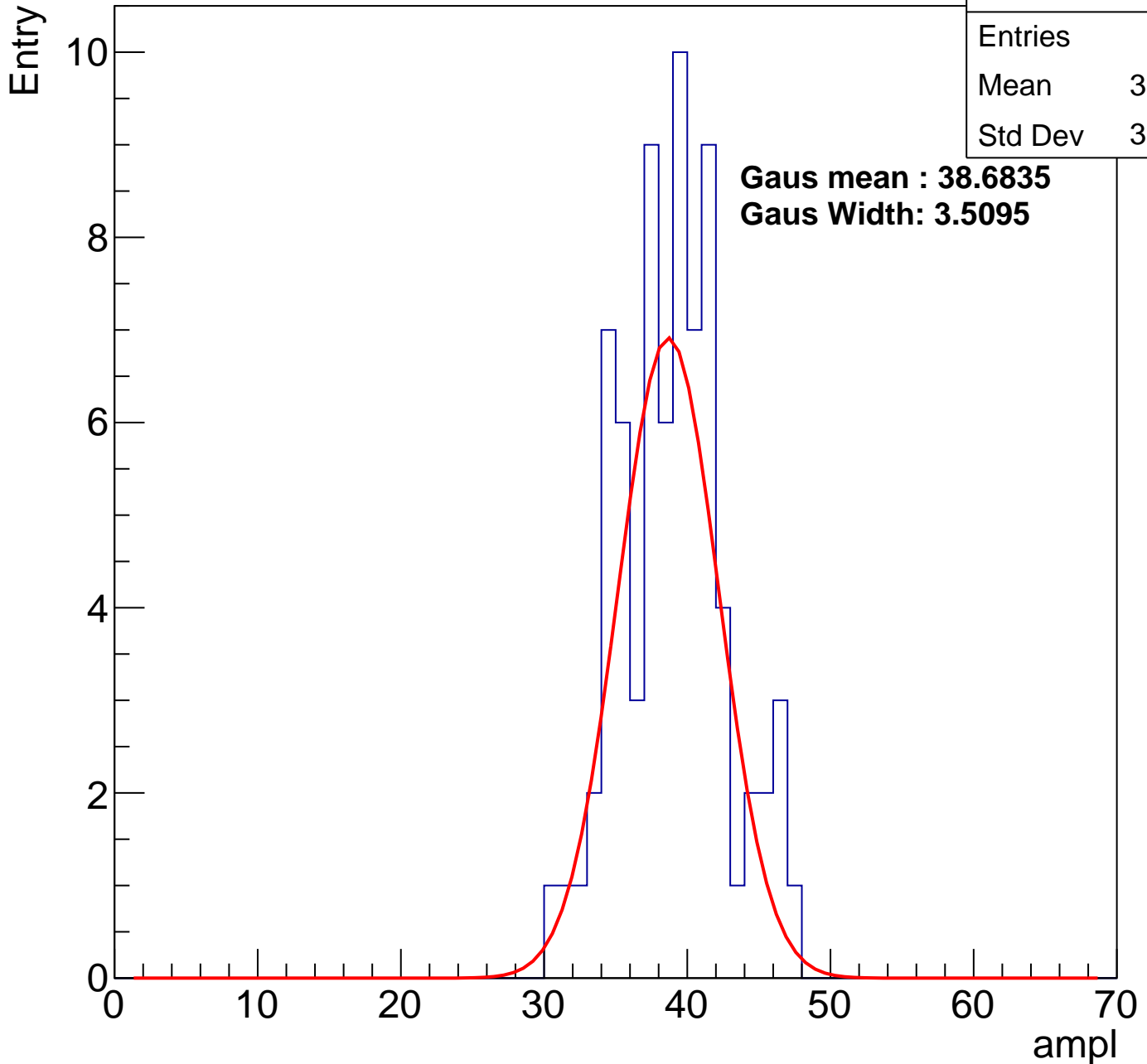


B1L101S, U26-ch46, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	38.52
Std Dev	3.682

Gaus mean : 38.6835
Gaus Width: 3.5095



B1L101S, U26-ch46, adc2

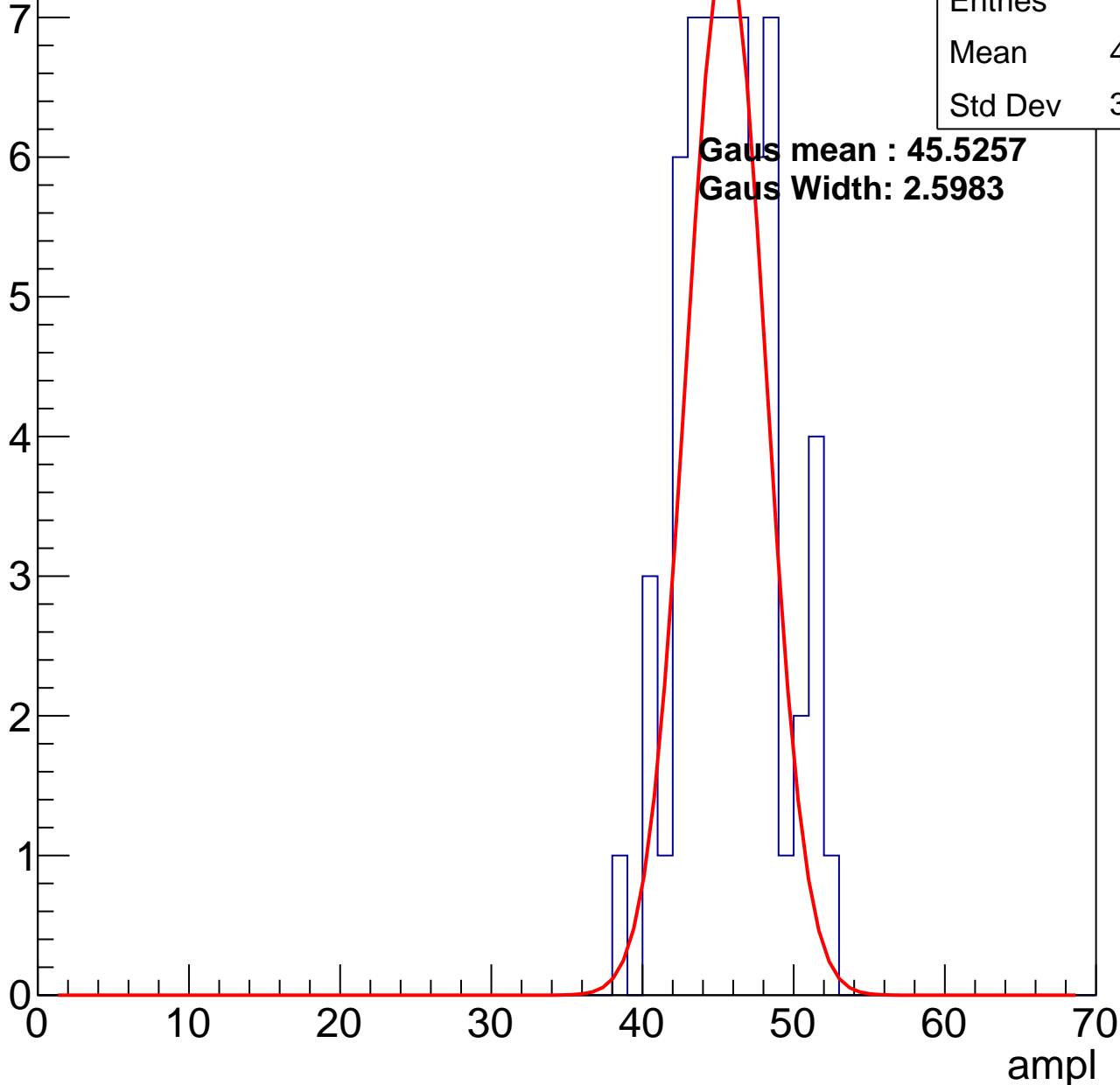
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	45.33
Std Dev	3.097

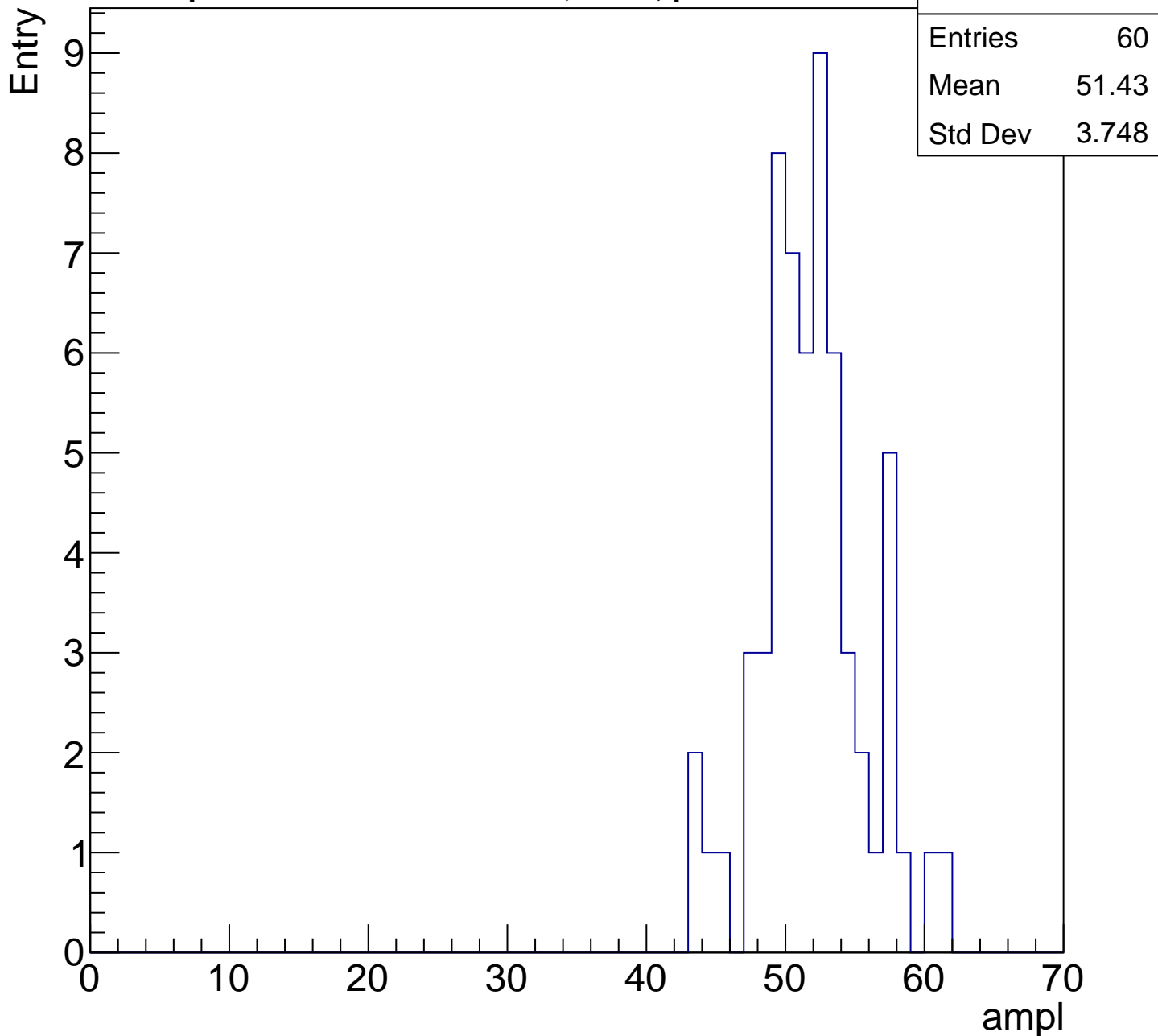
Gaus mean : 45.5257

Gaus Width: 2.5983



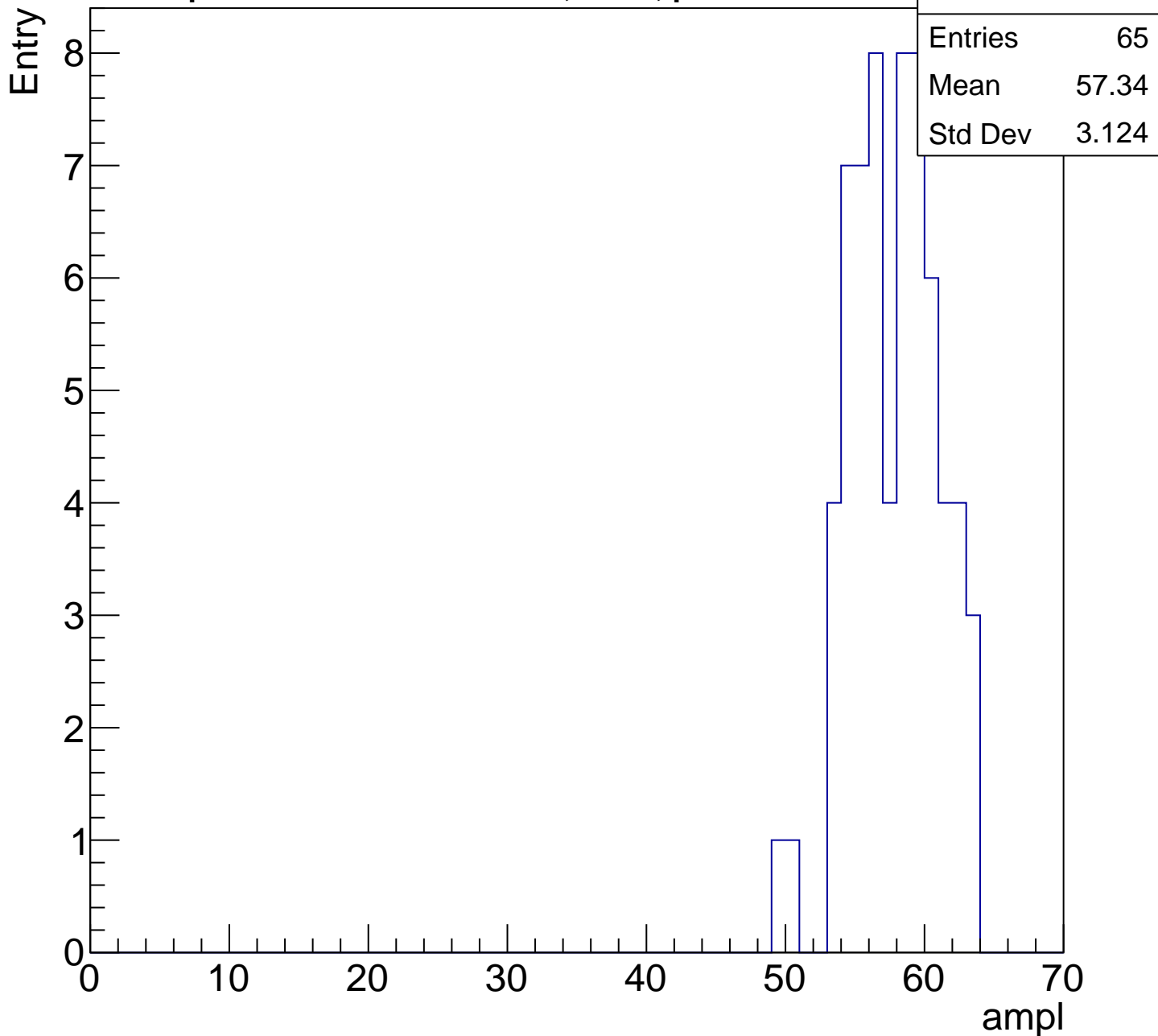
B1L101S, U26-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

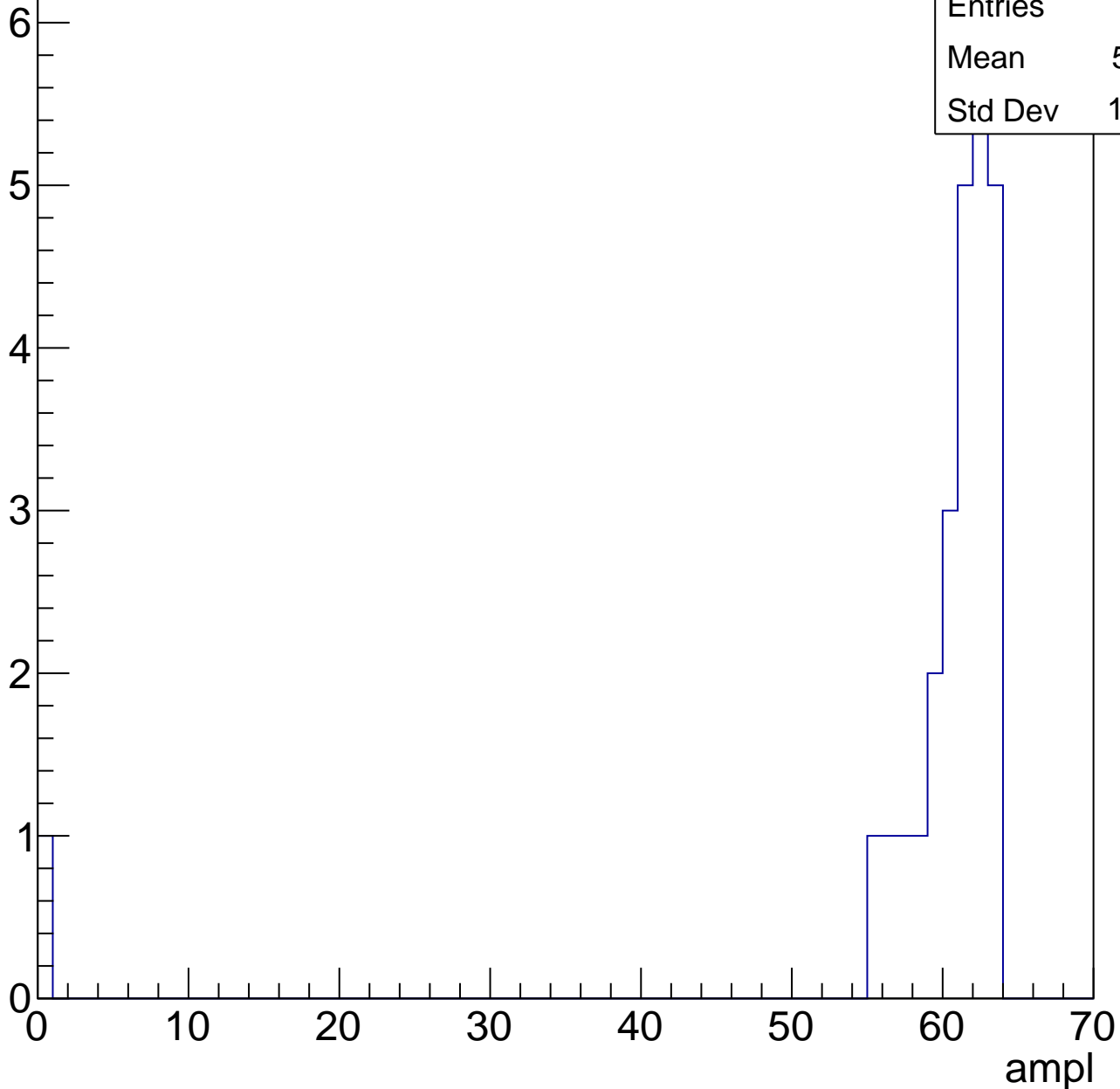


B1L101S, U26-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.31
Std Dev	11.86



B1L101S, U26-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch47, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	30.28
Std Dev	4.745

Gaus mean : 31.1692

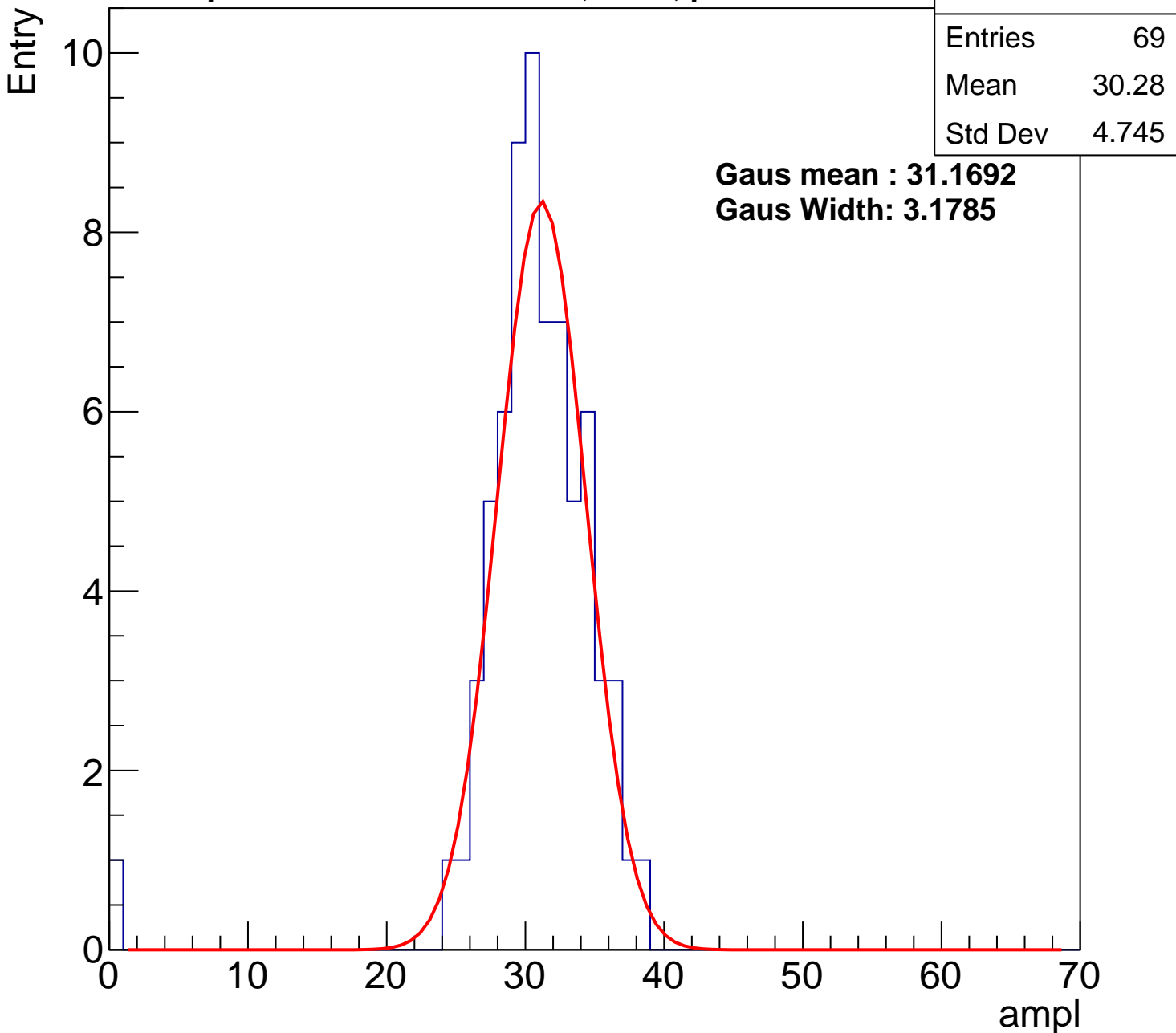
Gaus Width: 3.1785

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch47, adc1

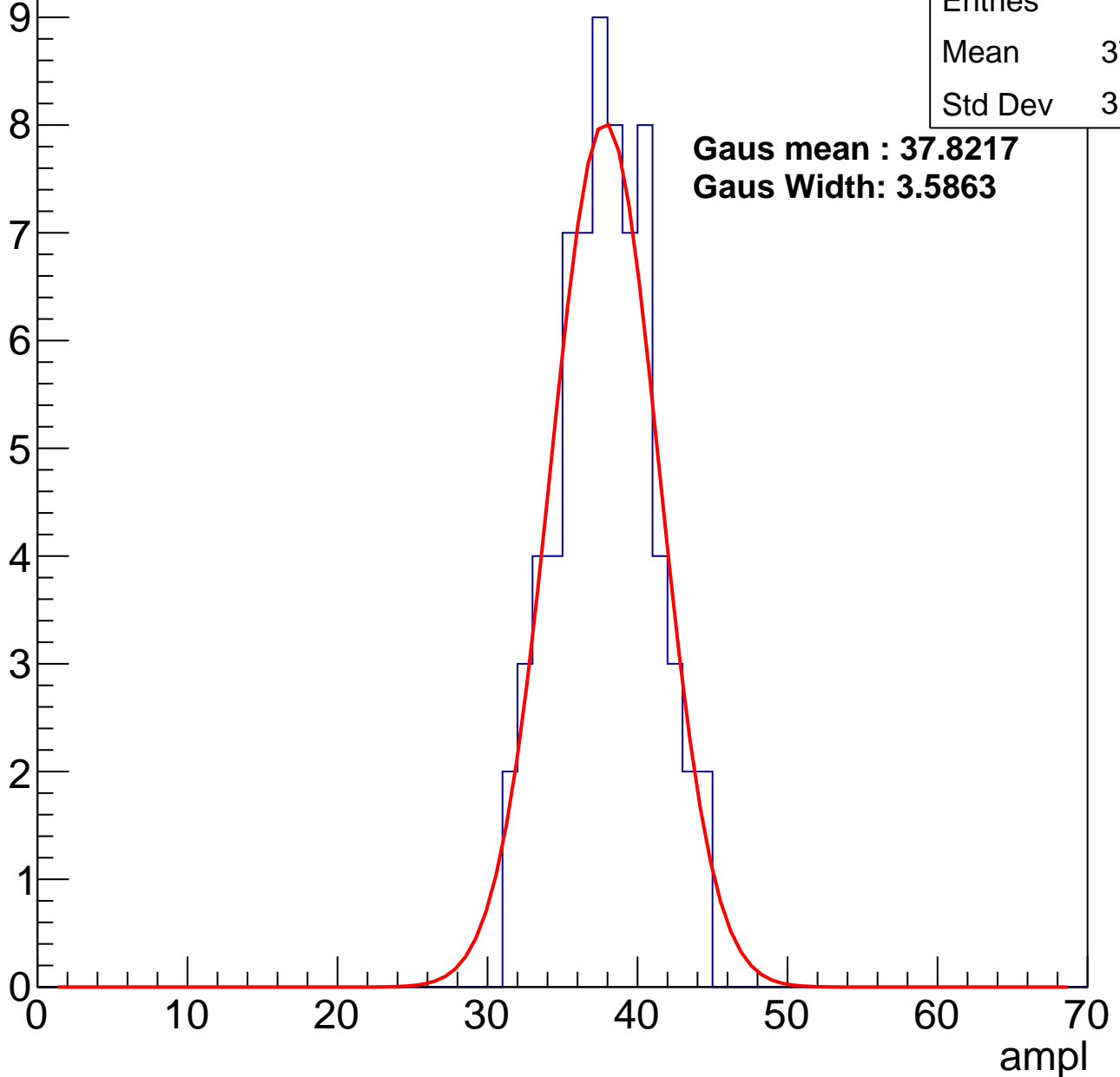
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.39
Std Dev	3.136

Gaus mean : 37.8217

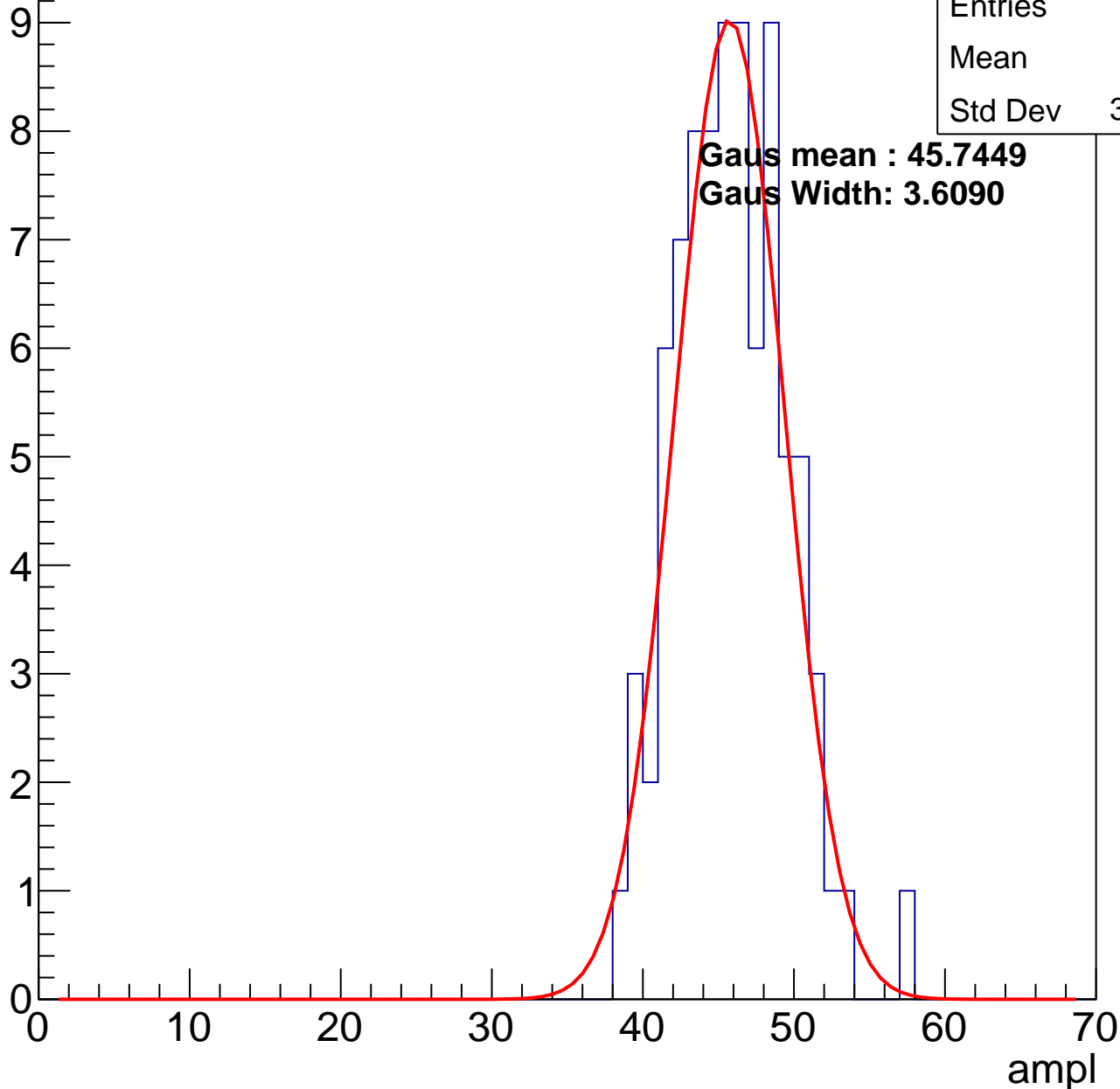
Gaus Width: 3.5863



B1L101S, U26-ch47, adc2

calib_packv5_042523_0143.root, FC#0, port D2

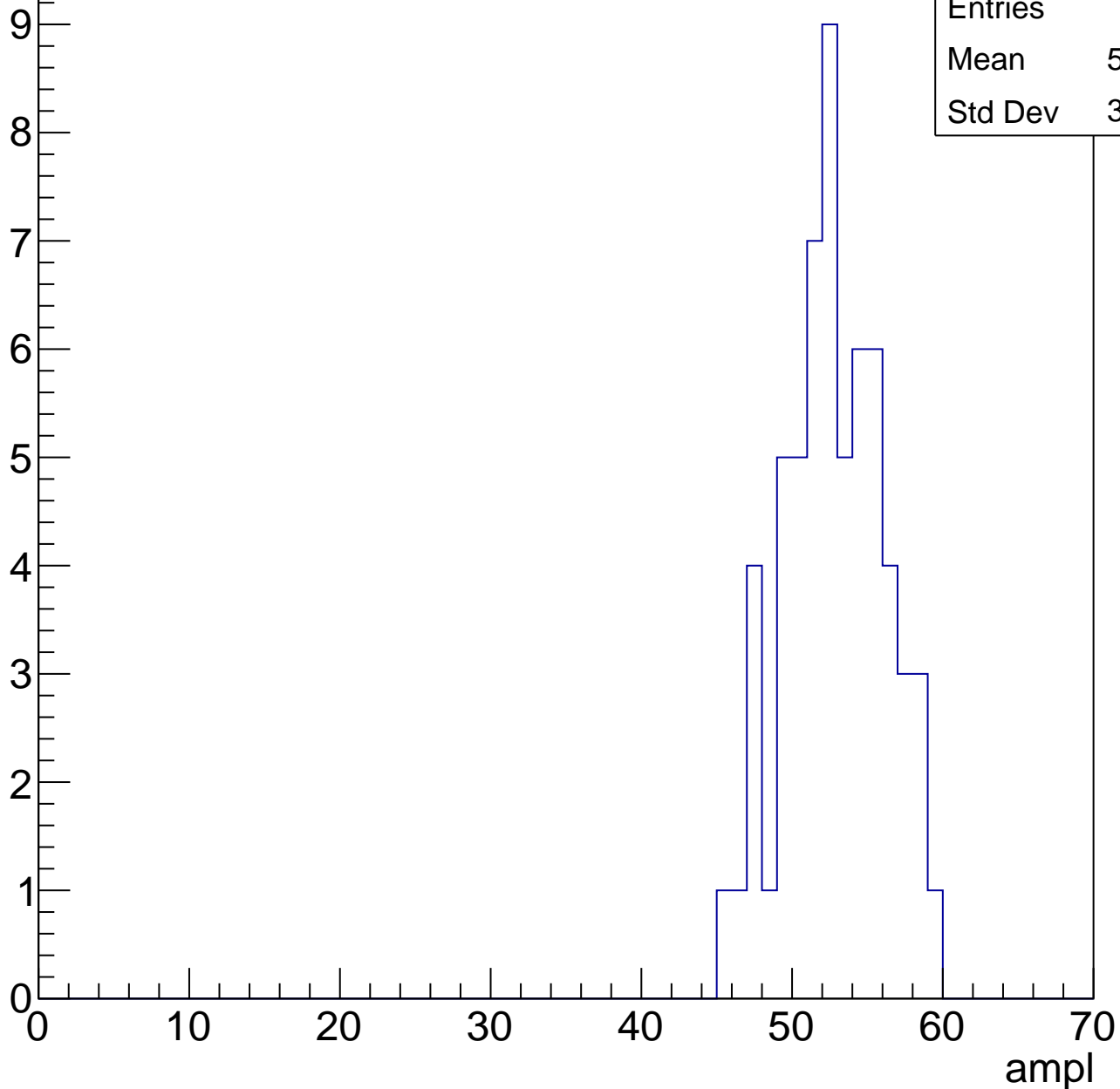
Entry



B1L101S, U26-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

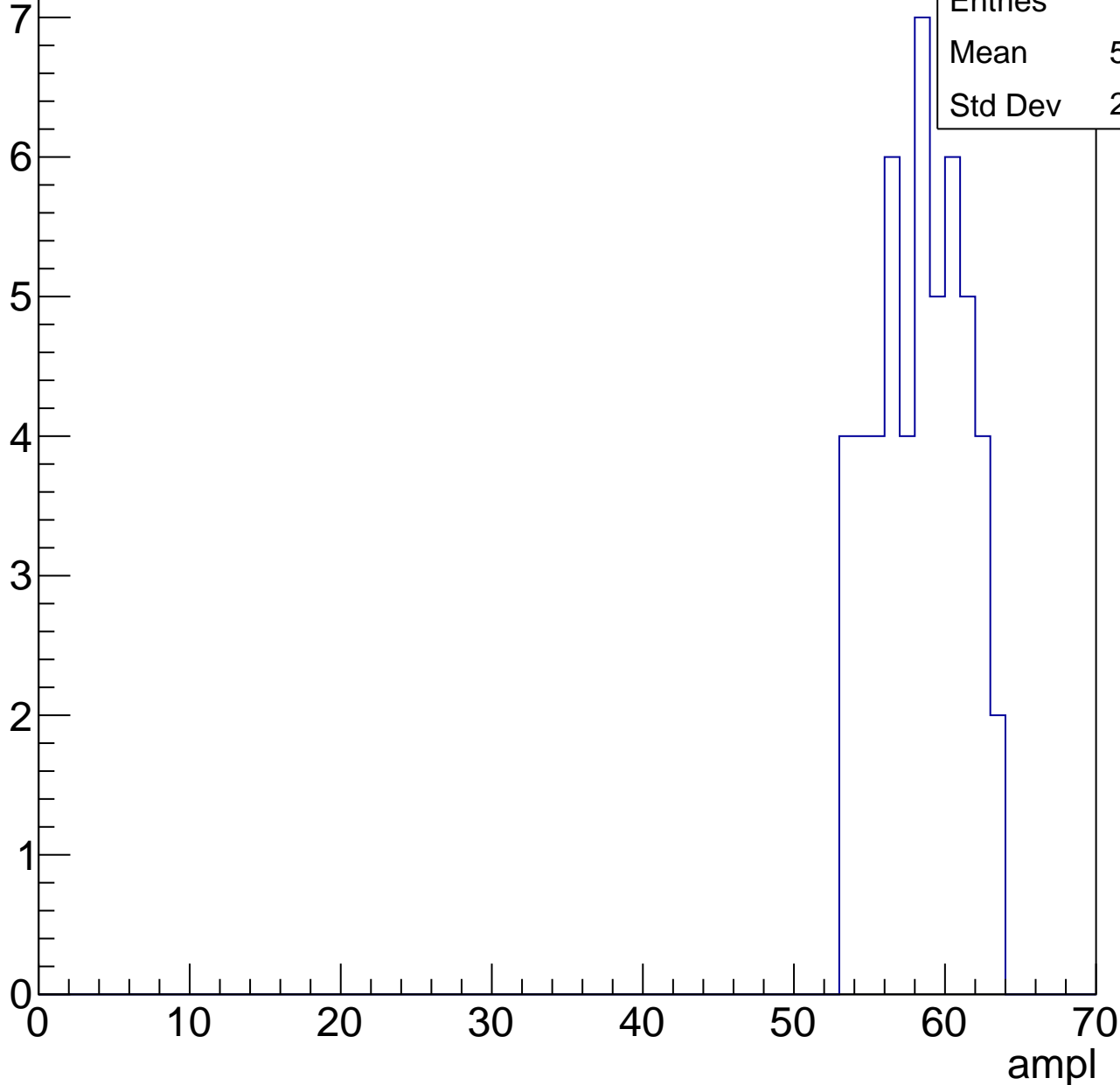
Entry



B1L101S, U26-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

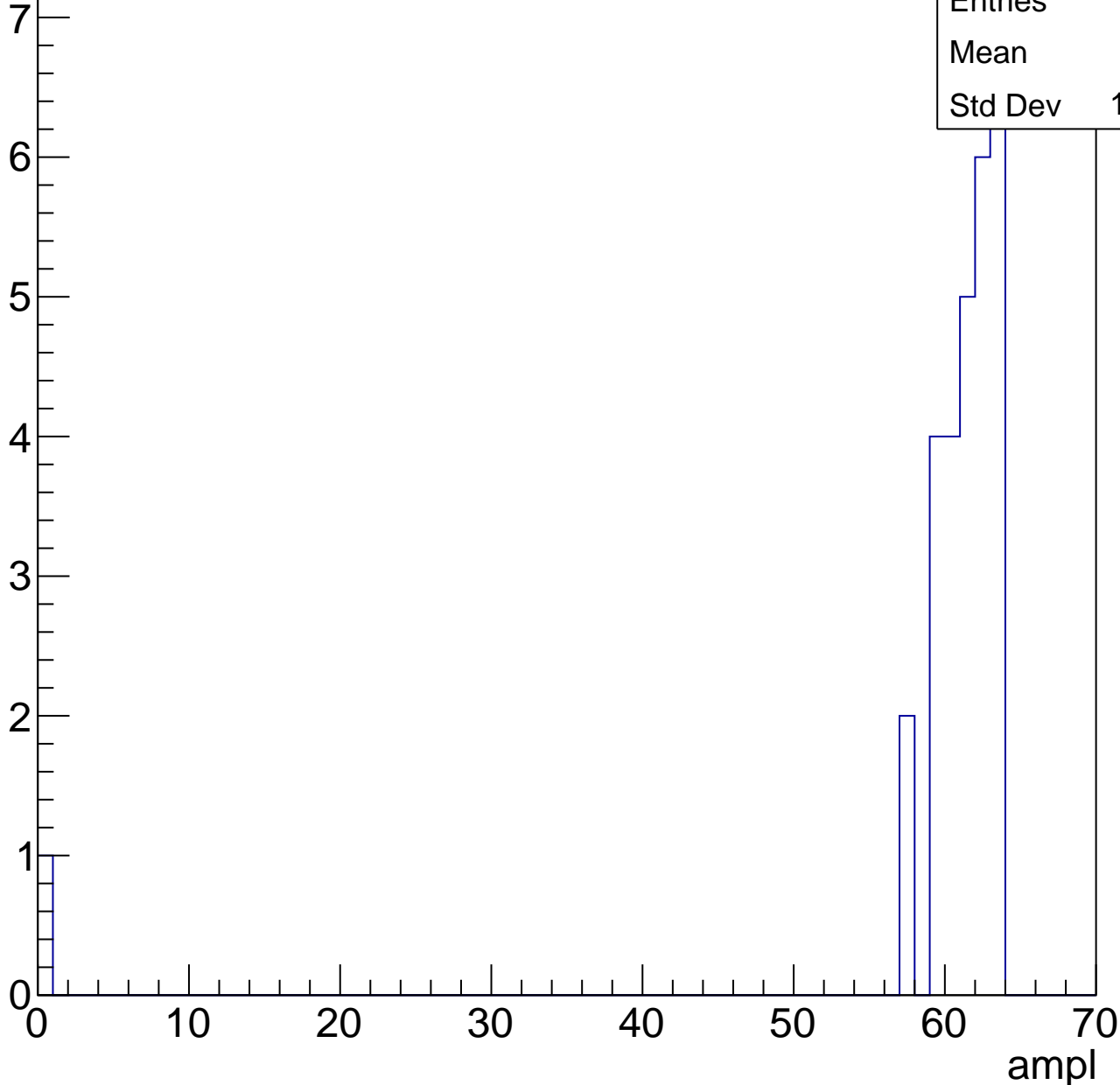


B1L101S, U26-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.9
Std Dev	11.26



B1L101S, U26-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch48, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	30.38
Std Dev	3.167

Gaus mean : 31.0826

Gaus Width: 4.1616

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L101S, U26-ch48, adc1

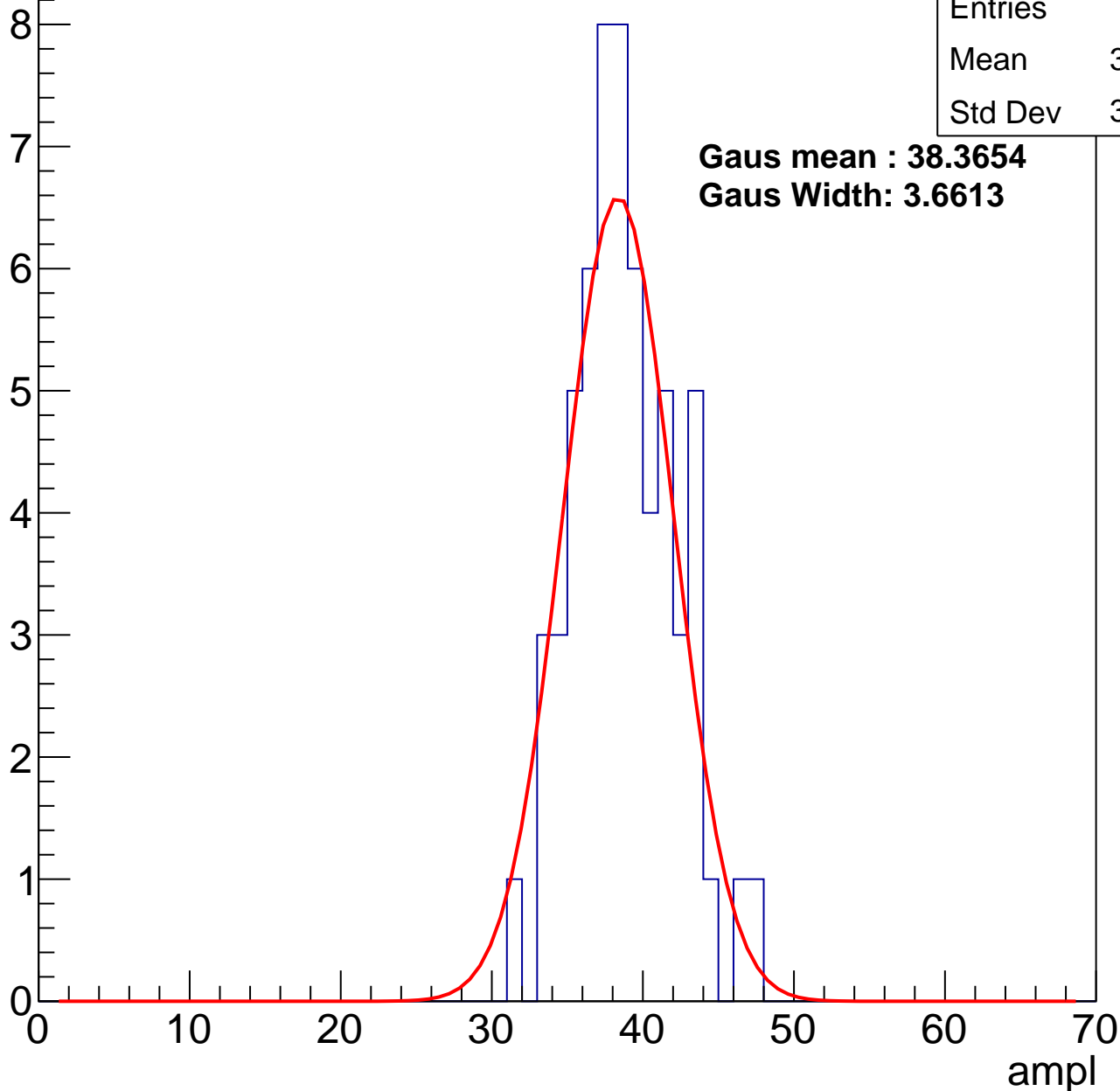
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.33
Std Dev	3.325

Gaus mean : 38.3654

Gaus Width: 3.6613



B1L101S, U26-ch48, adc2

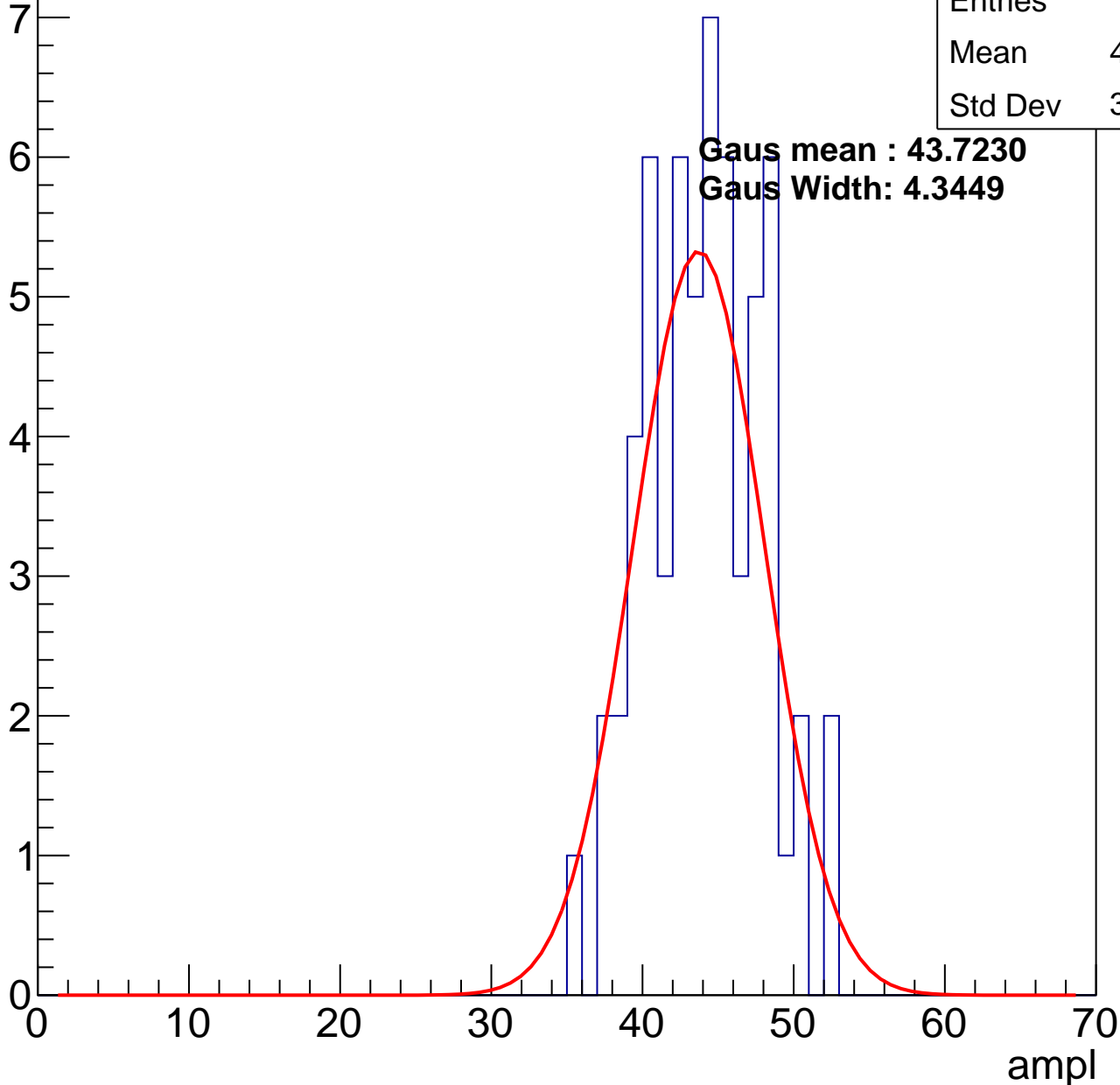
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.66
Std Dev	3.798

Gaus mean : 43.7230

Gaus Width: 4.3449

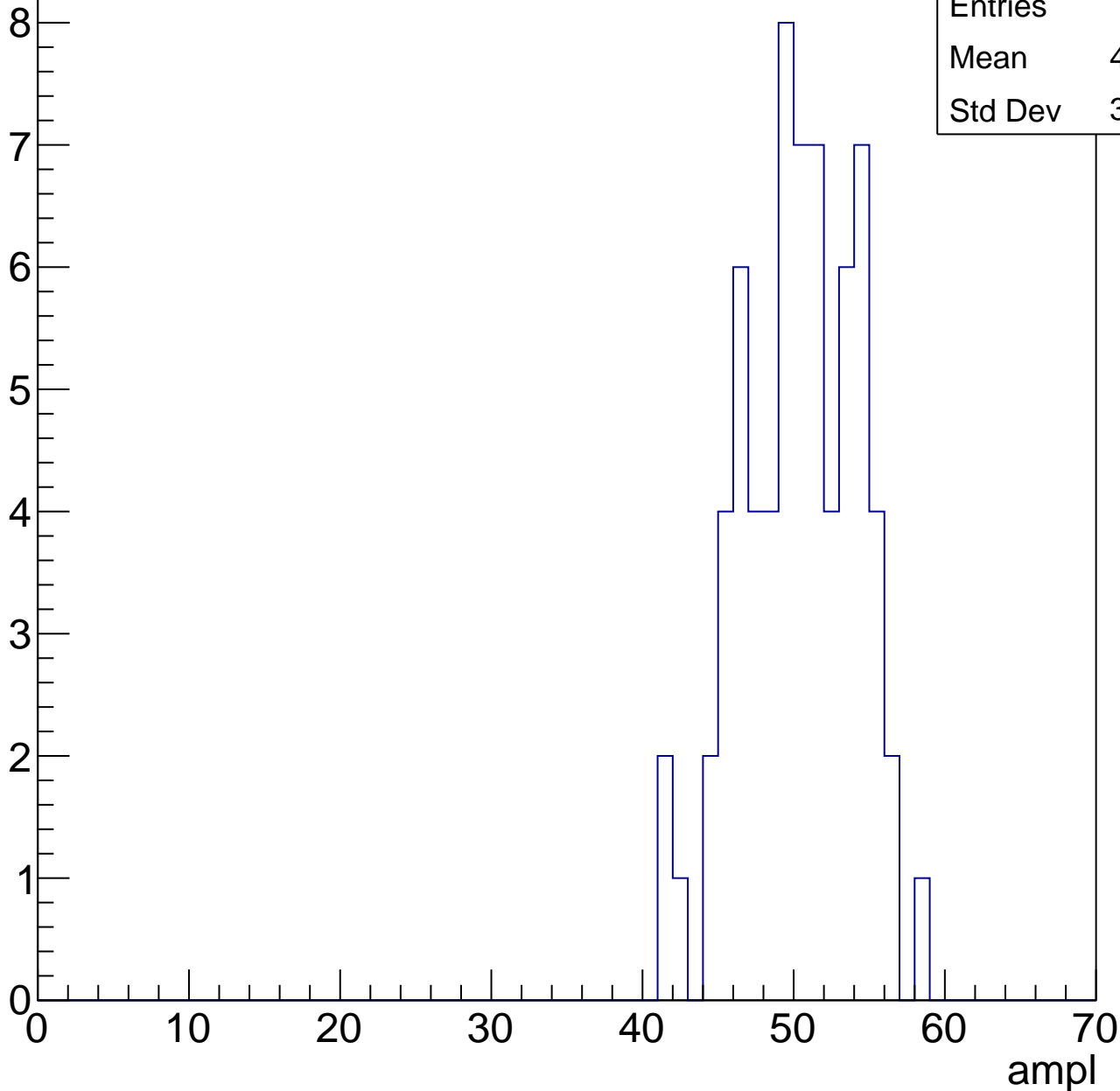


B1L101S, U26-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.87
Std Dev	3.764

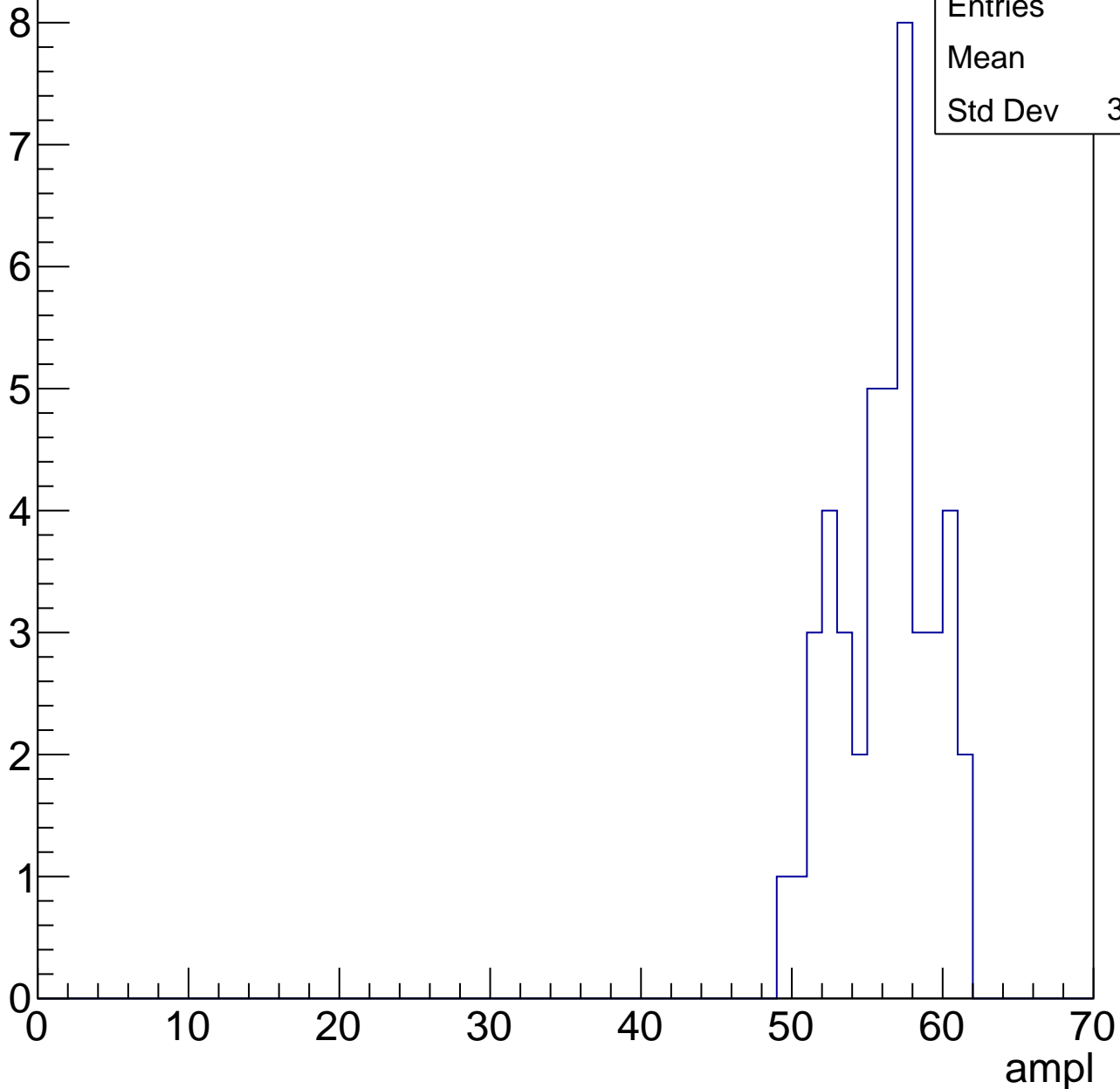


B1L101S, U26-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	55.7
Std Dev	3.094

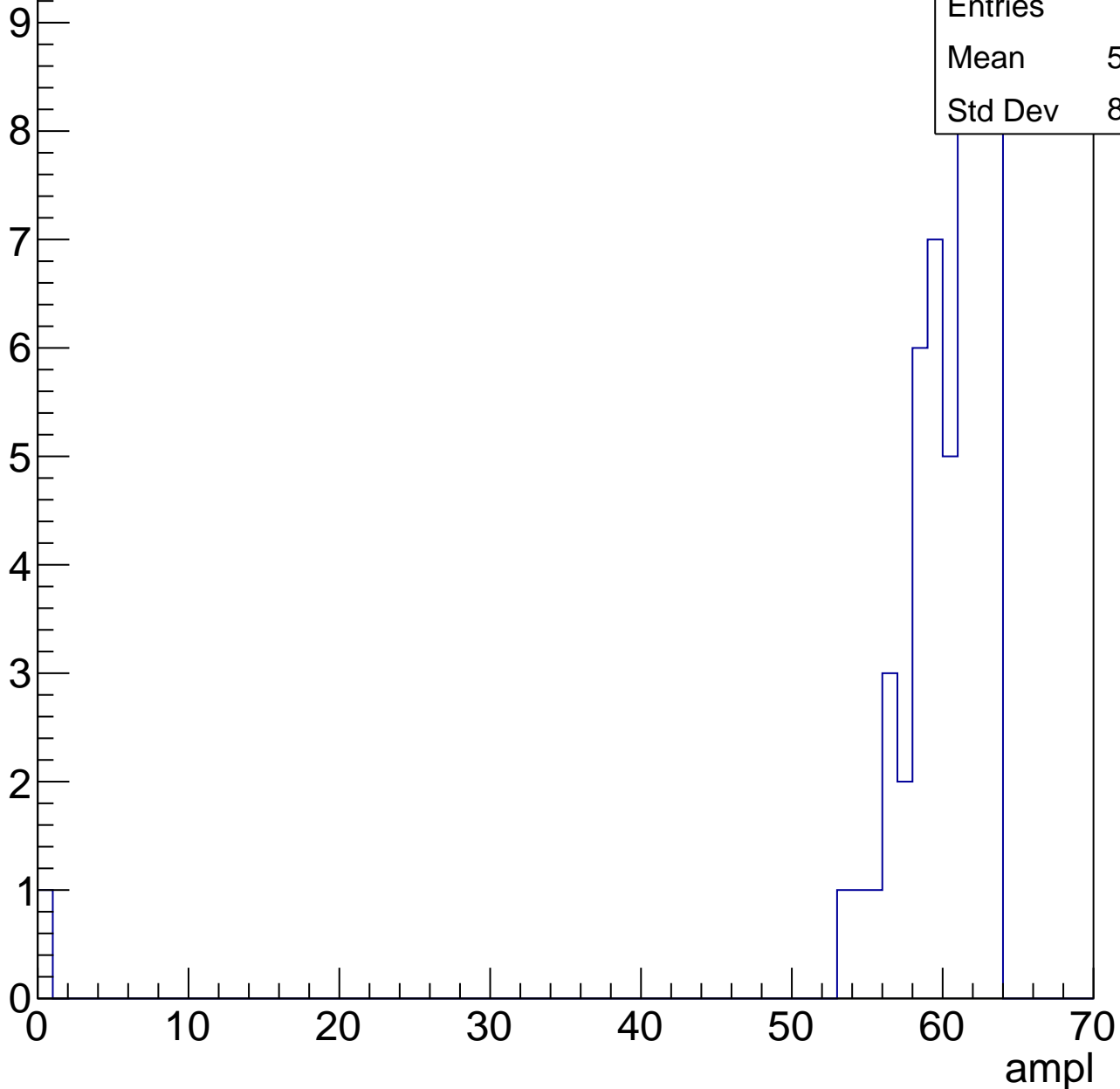


B1L101S, U26-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	58.75
Std Dev	8.595



B1L101S, U26-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch49, adc0

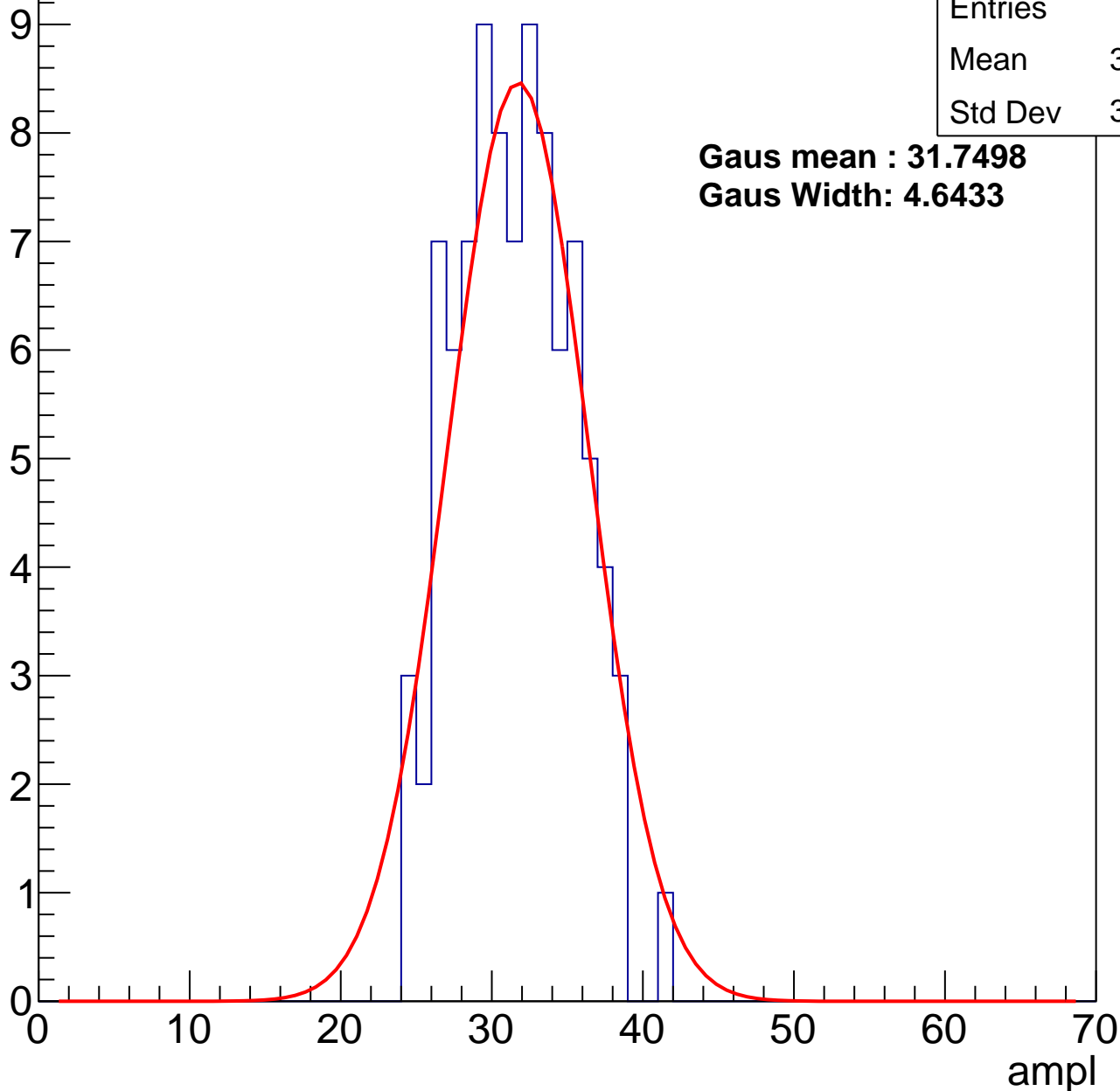
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	92
Mean	31.13
Std Dev	3.786

Gaus mean : 31.7498

Gaus Width: 4.6433



B1L101S, U26-ch49, adc1

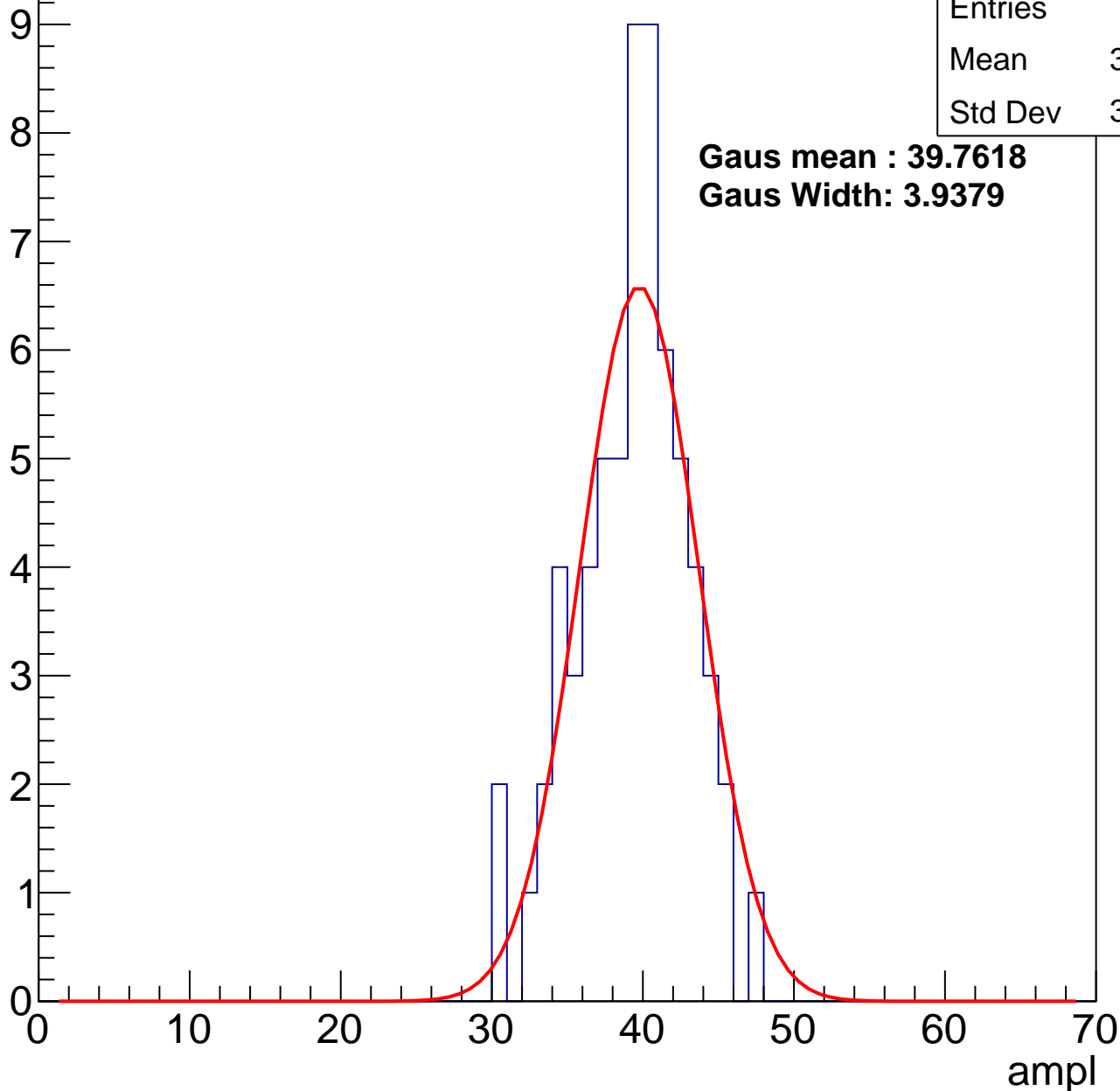
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	38.86
Std Dev	3.603

Gaus mean : 39.7618

Gaus Width: 3.9379



B1L101S, U26-ch49, adc2

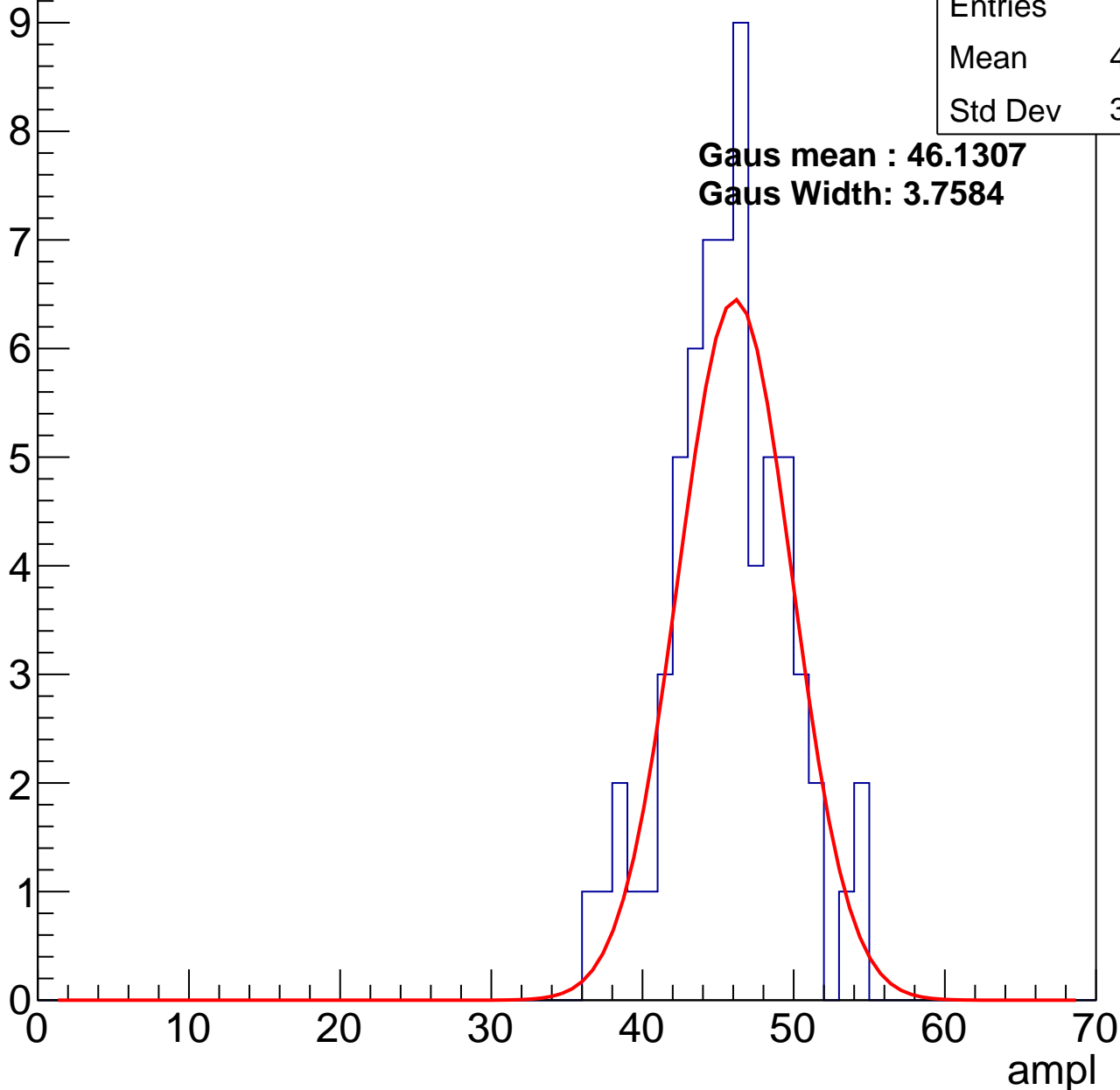
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.26
Std Dev	3.816

Gaus mean : 46.1307

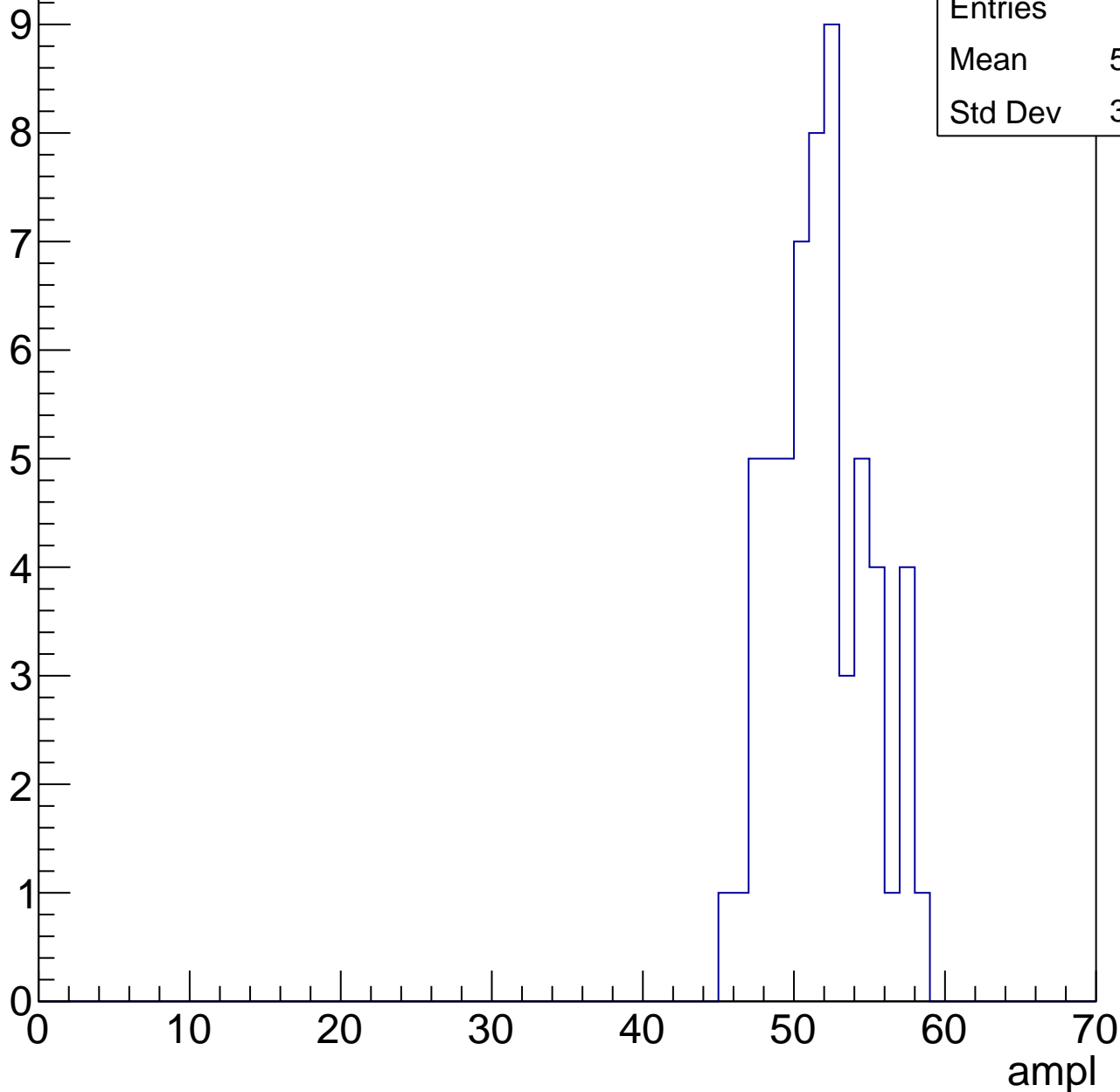
Gaus Width: 3.7584



B1L101S, U26-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

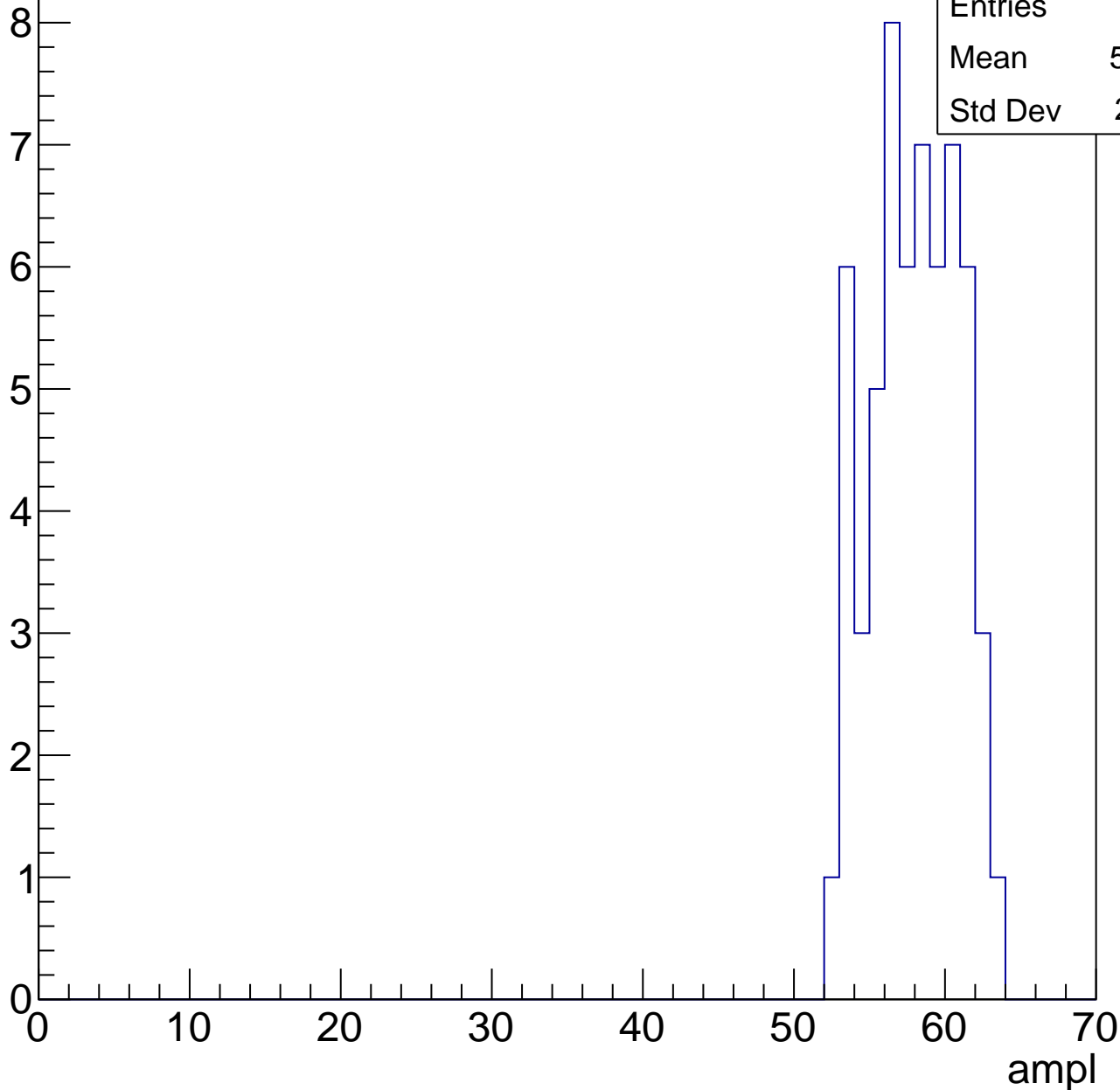


B1L101S, U26-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	57.49
Std Dev	2.801

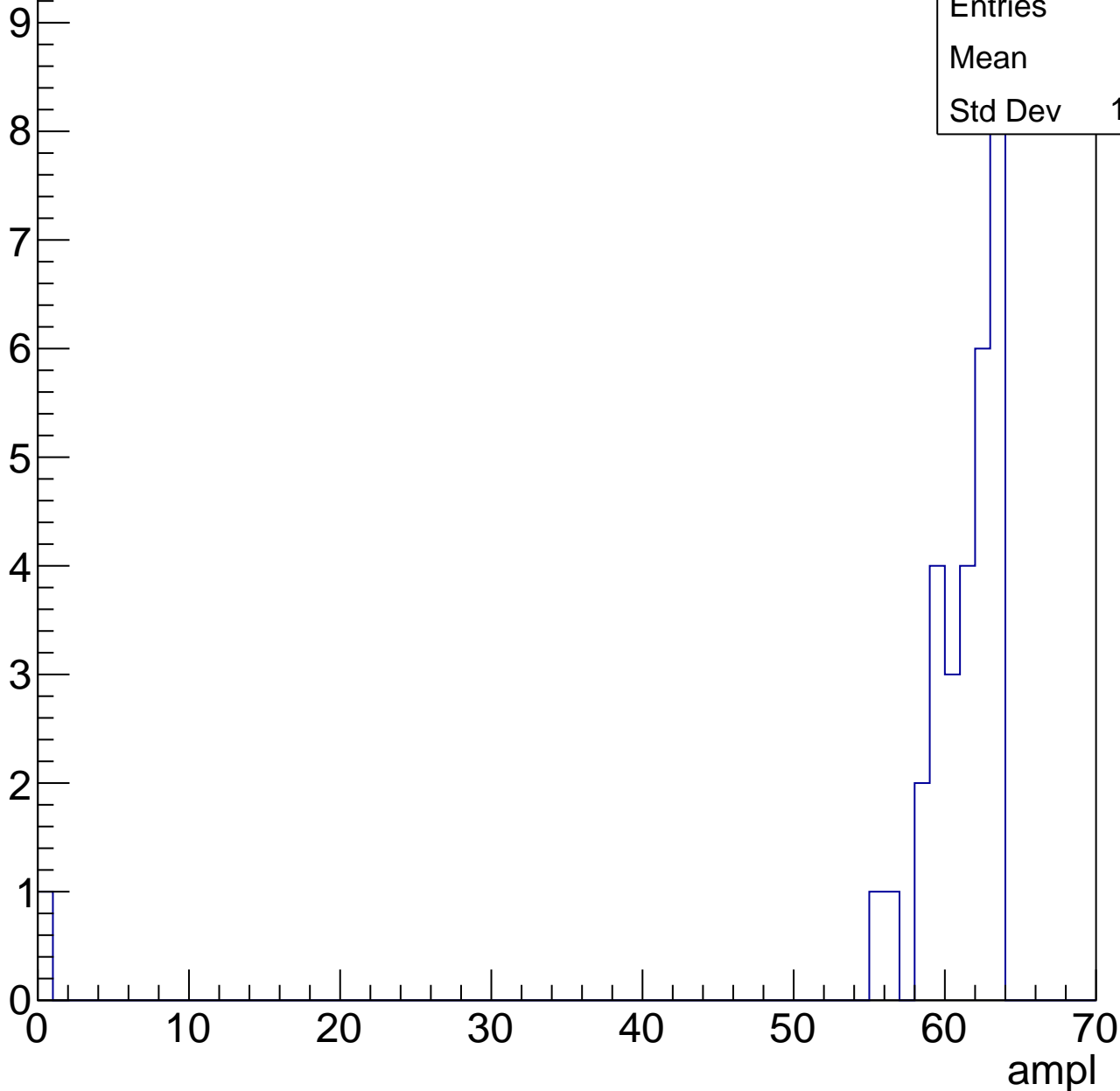


B1L101S, U26-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.9
Std Dev	10.96



B1L101S, U26-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U26-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L101S, U26-ch50, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	30.47
Std Dev	5.059

Gaus mean : 31.2860

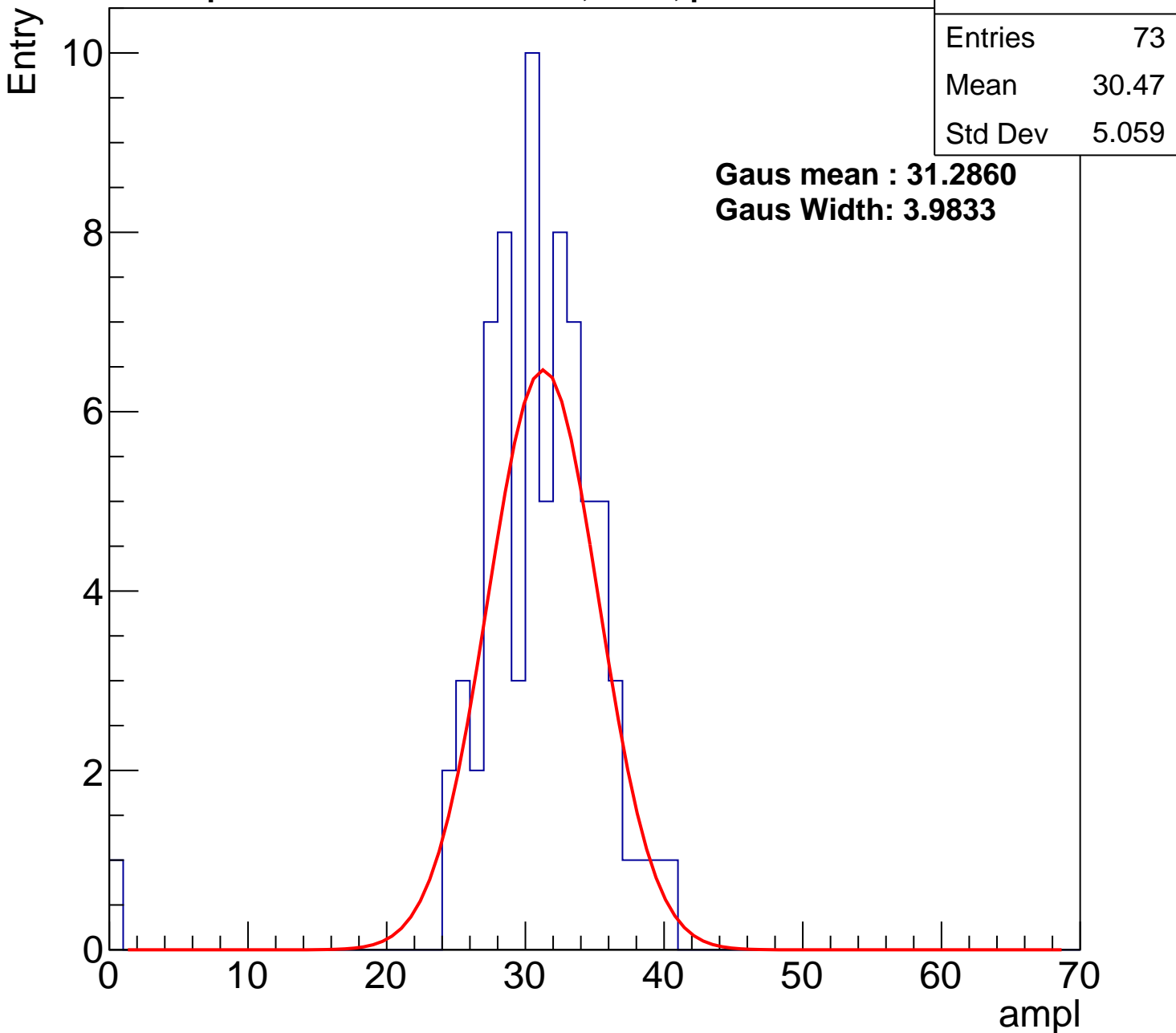
Gaus Width: 3.9833

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch50, adc1

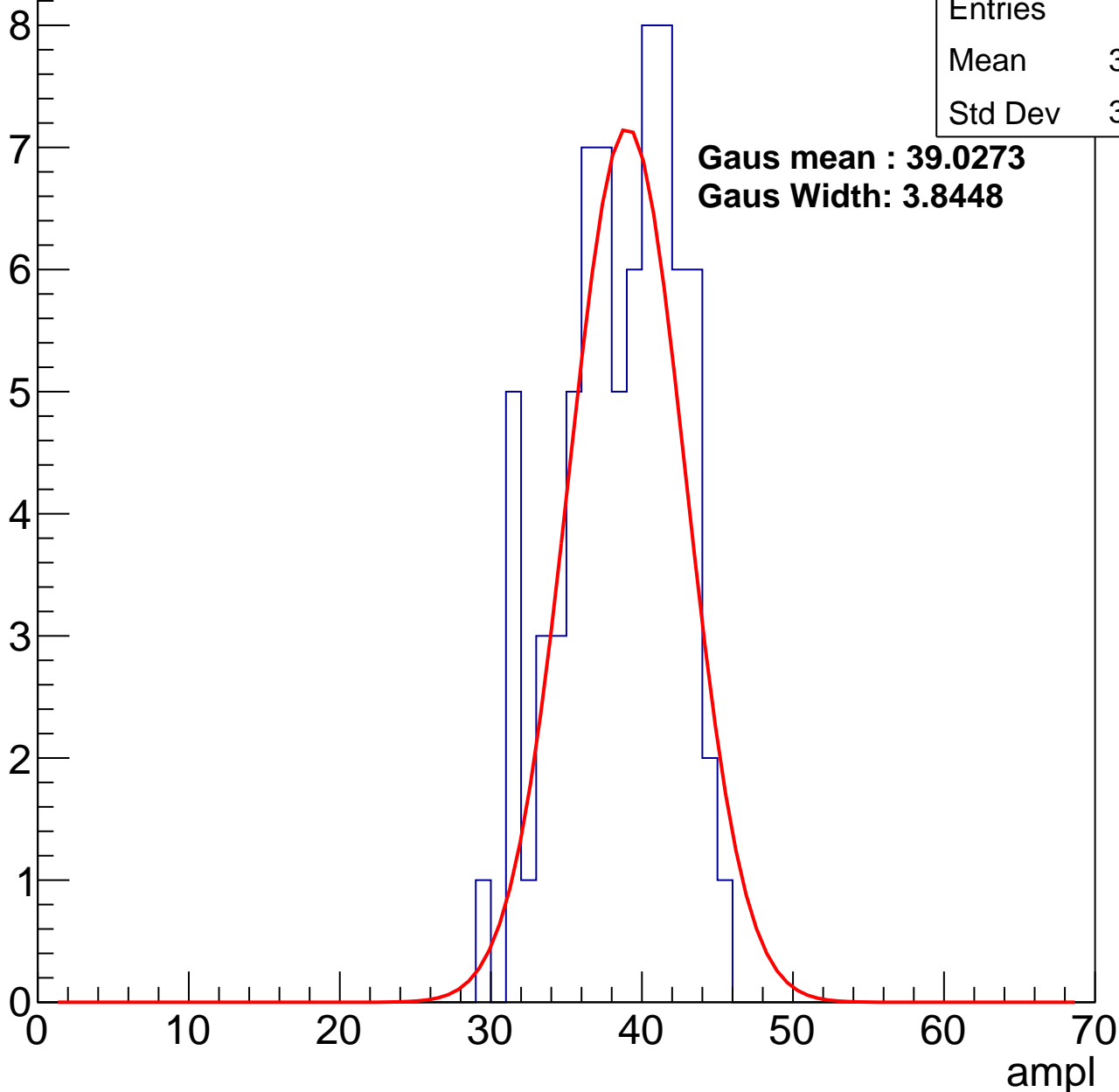
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	38.08
Std Dev	3.752

Gaus mean : 39.0273

Gaus Width: 3.8448



B1L101S, U26-ch50, adc2

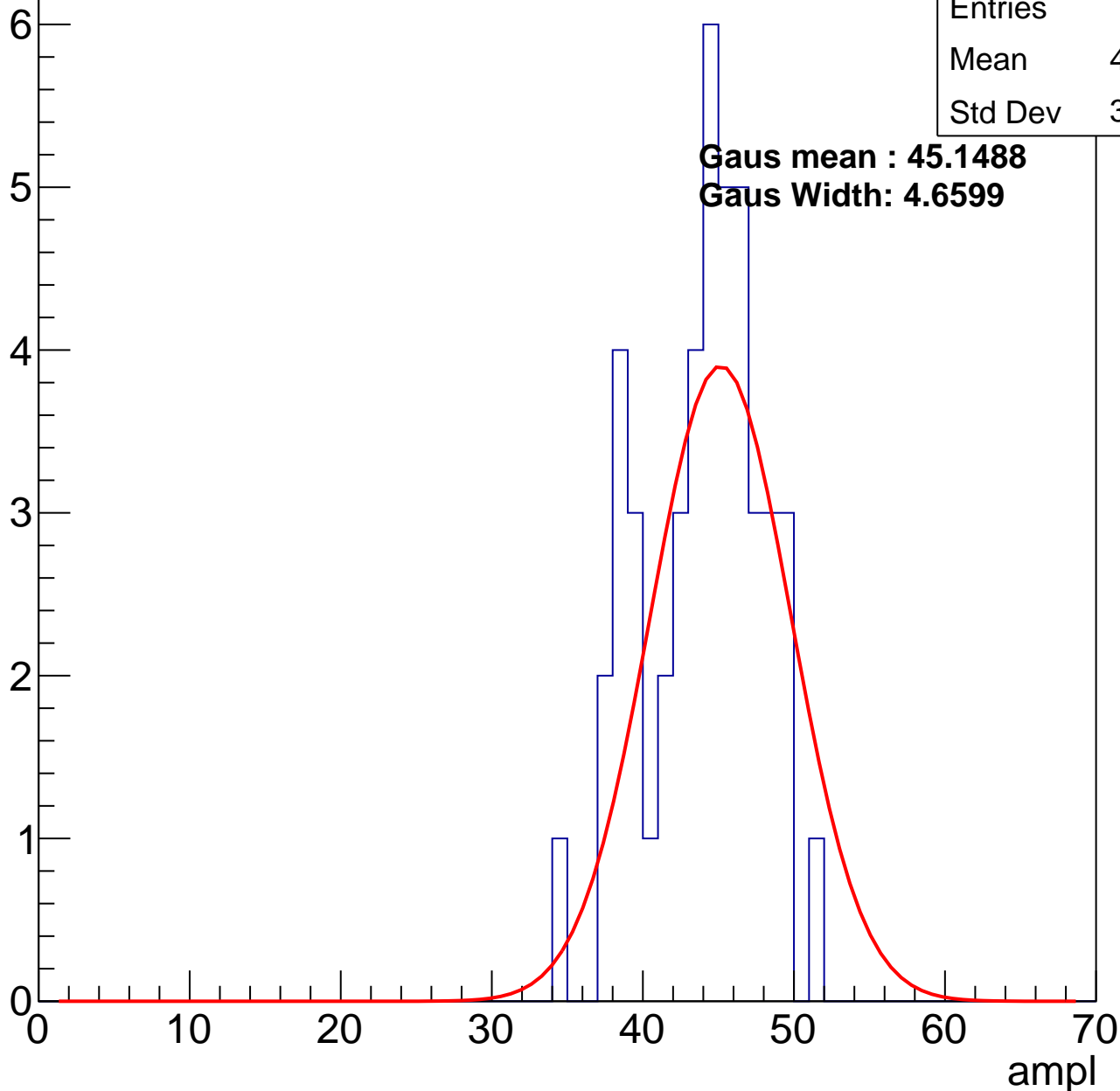
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	43.46
Std Dev	3.837

Gaus mean : 45.1488

Gaus Width: 4.6599

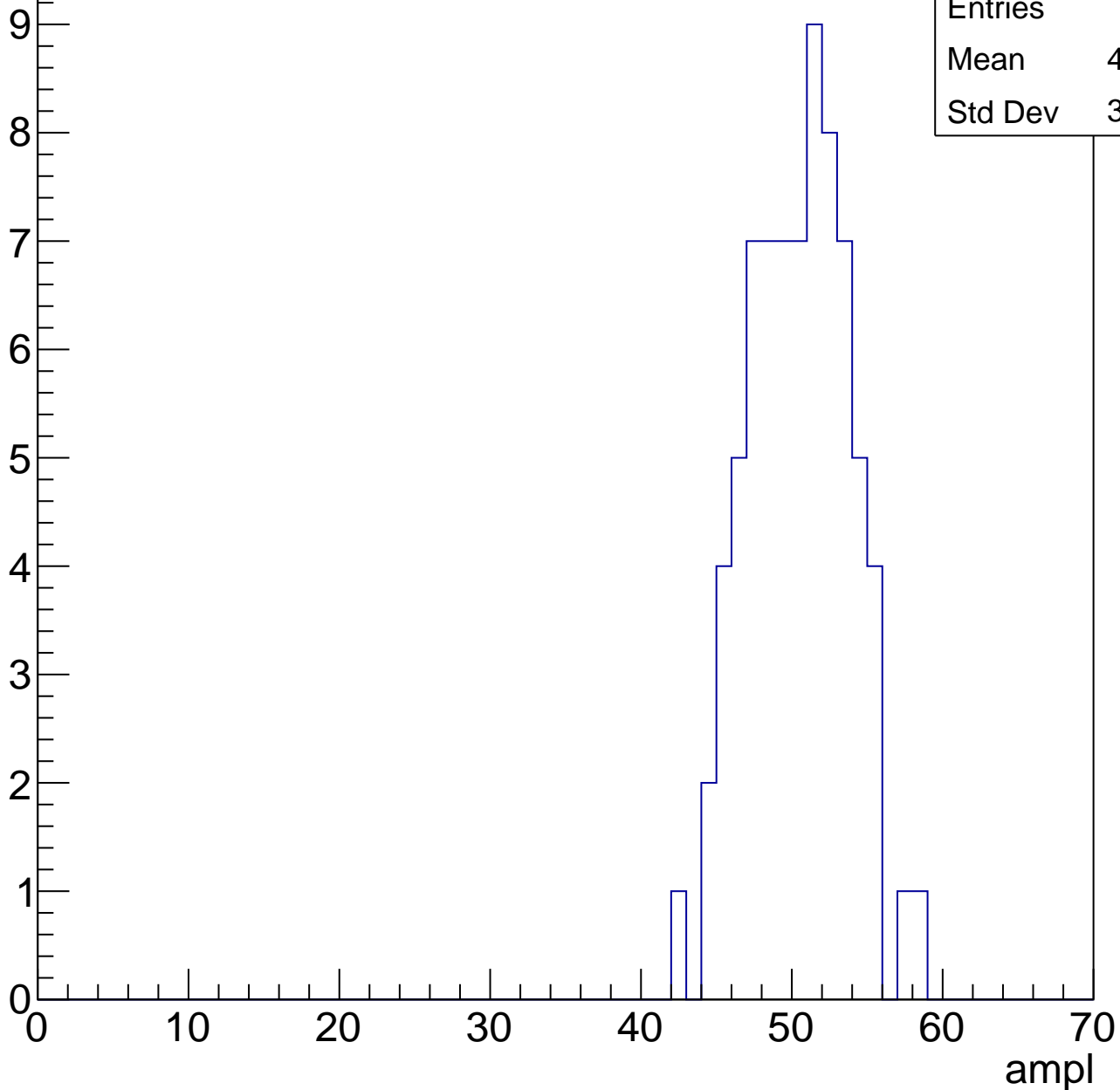


B1L101S, U26-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

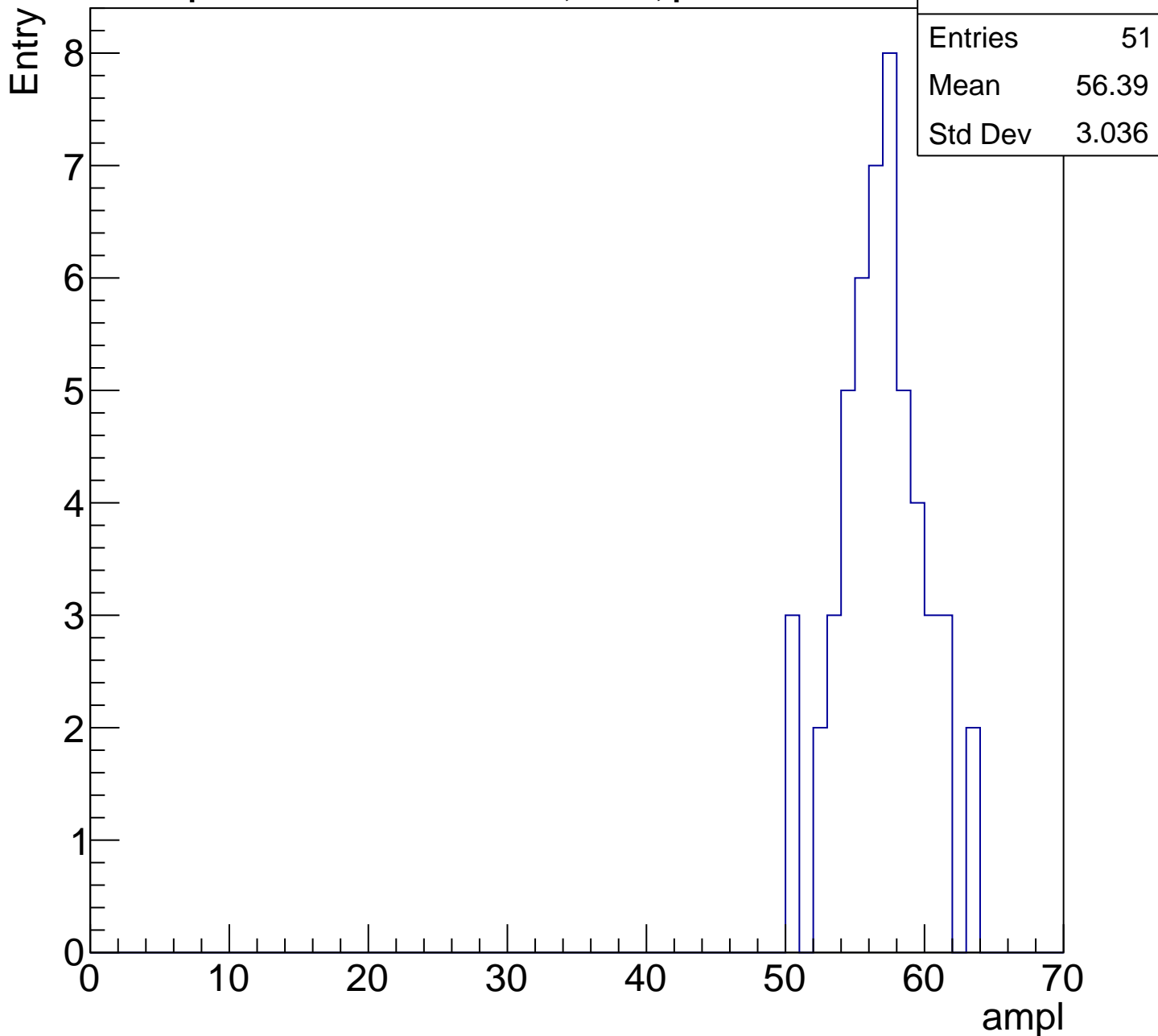
Entry

Entries	75
Mean	49.99
Std Dev	3.288



B1L101S, U26-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

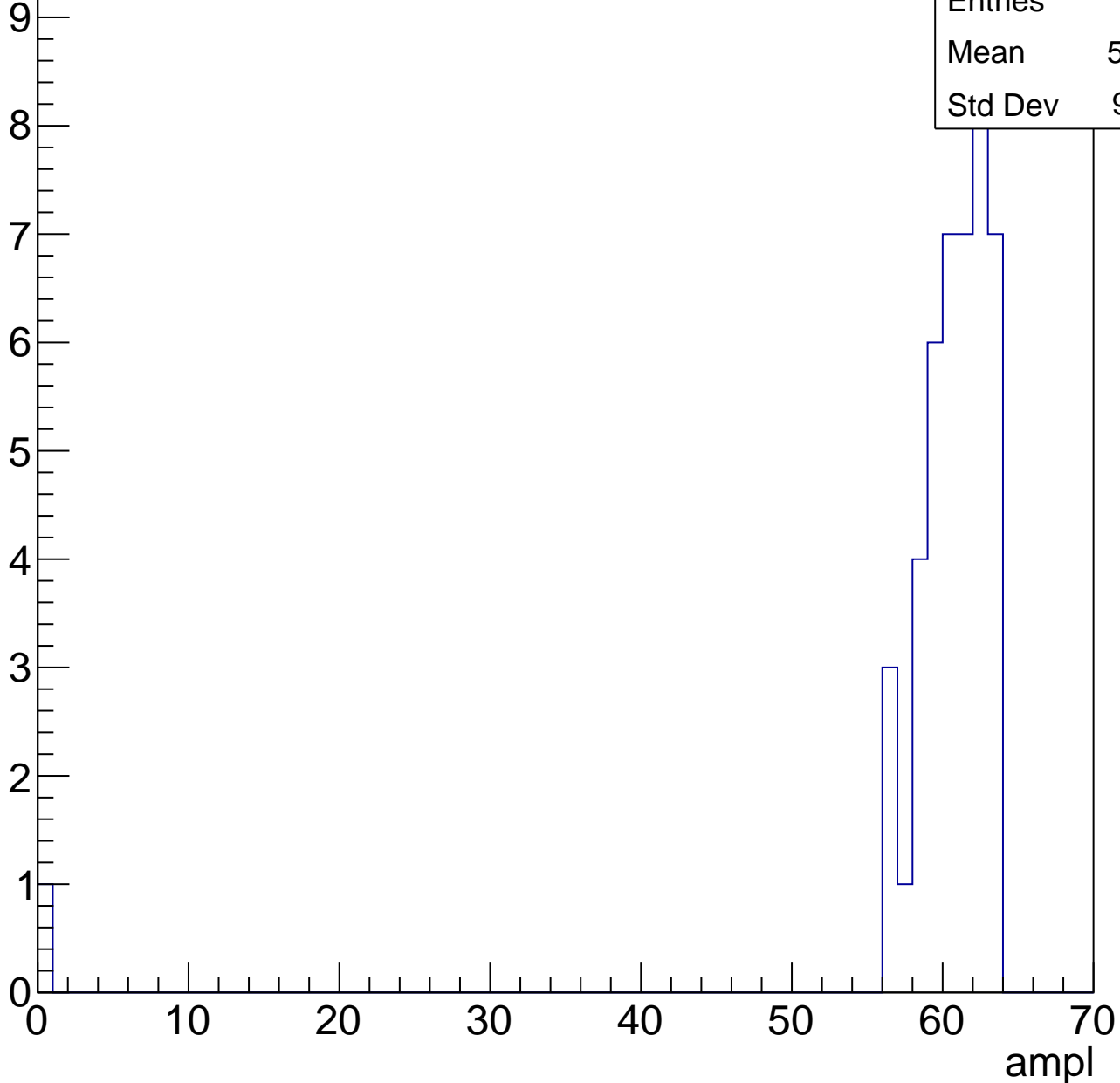


B1L101S, U26-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.04
Std Dev	9.121



B1L101S, U26-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch51, adc0

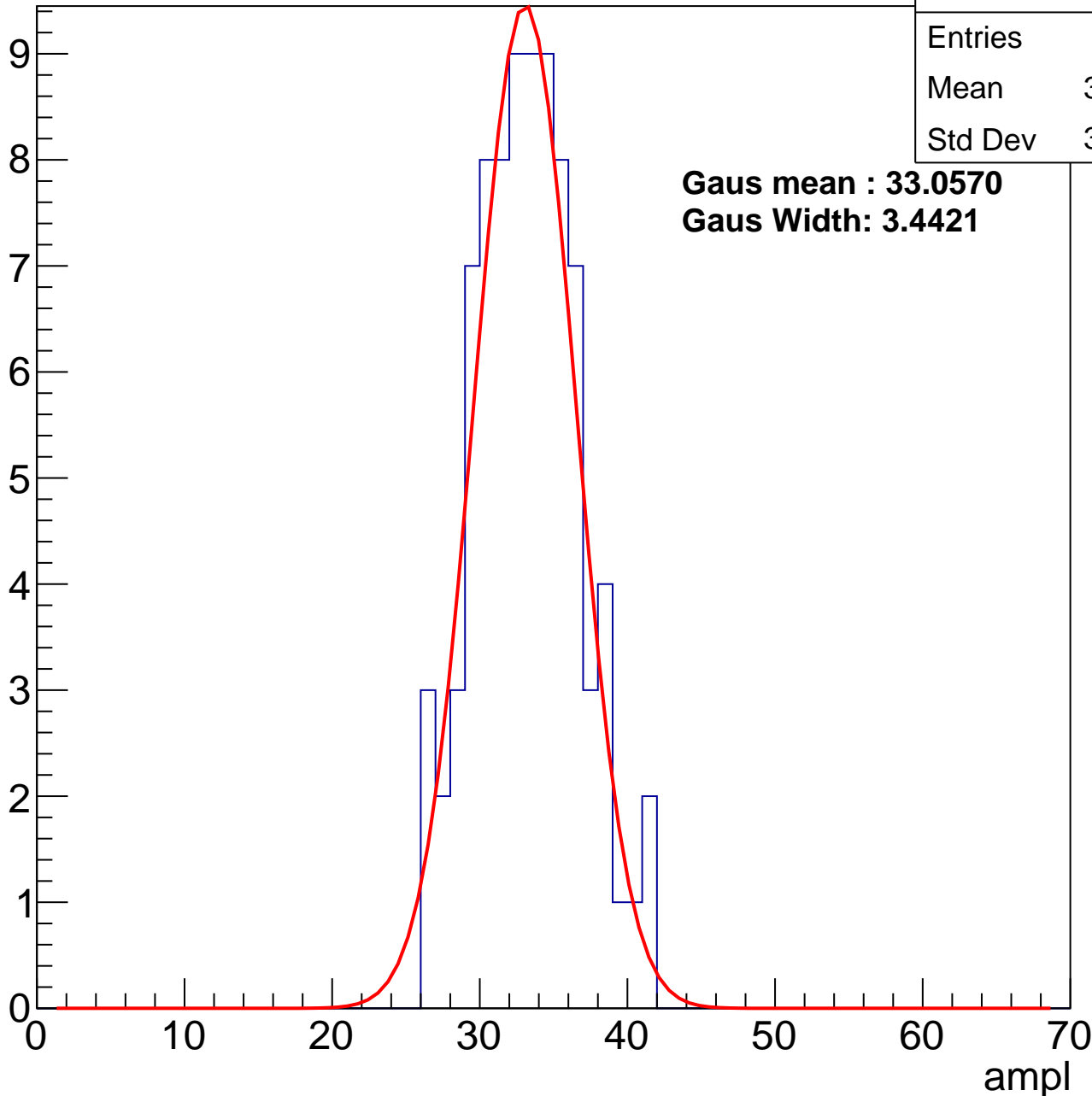
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	32.79
Std Dev	3.416

Gaus mean : 33.0570

Gaus Width: 3.4421

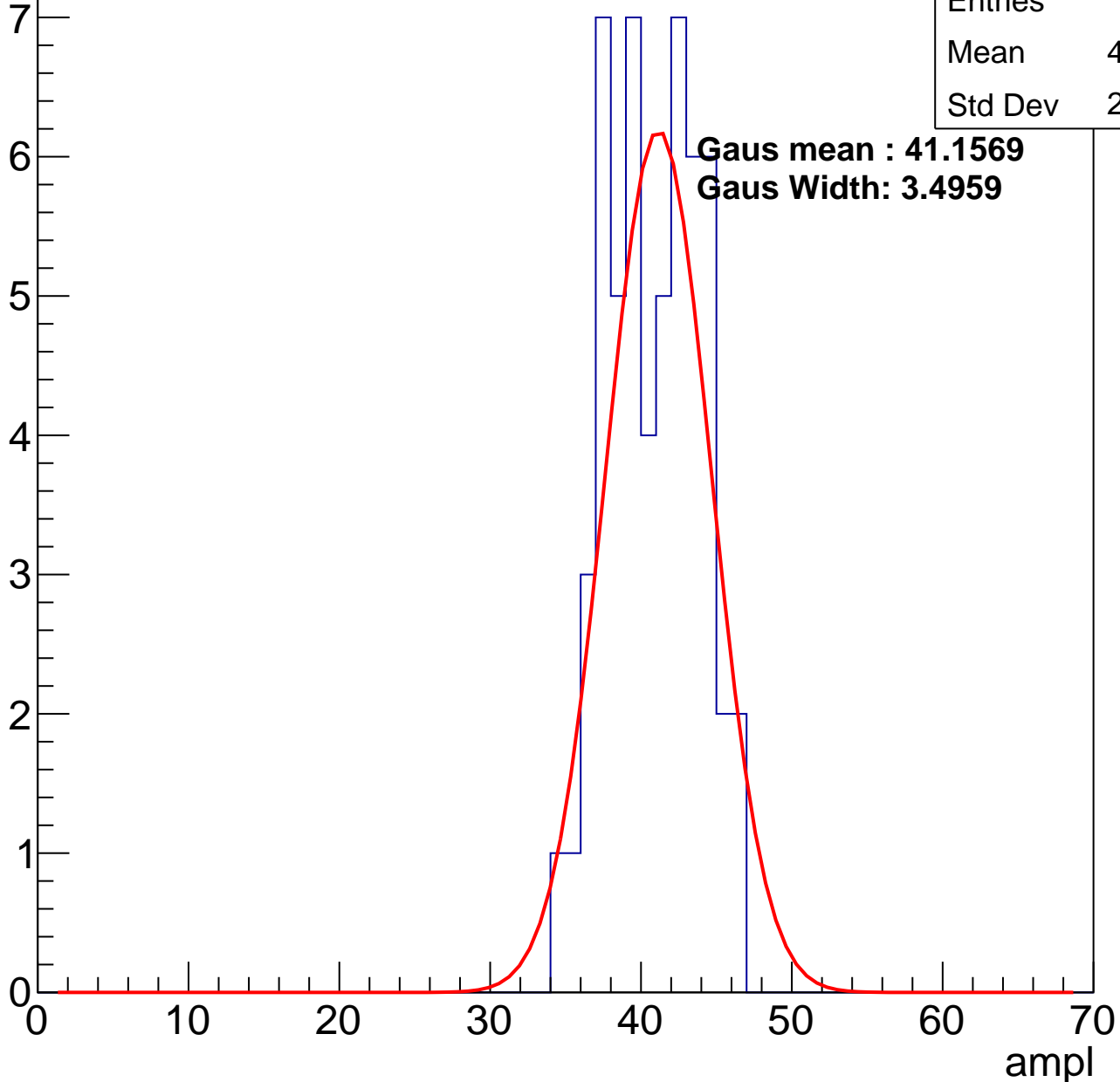


B1L101S, U26-ch51, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	40.39
Std Dev	2.974



B1L101S, U26-ch51, adc2

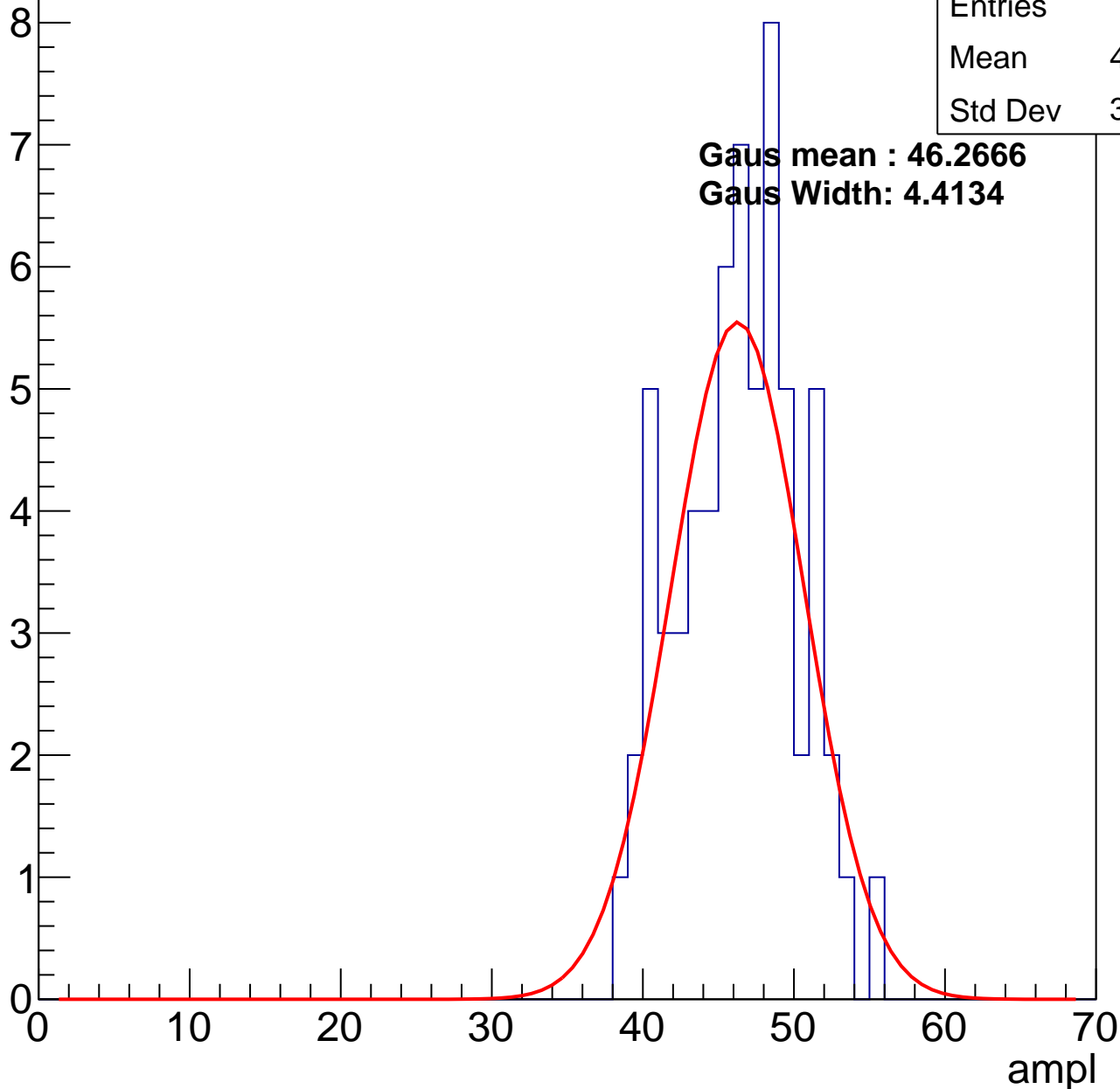
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.88
Std Dev	3.879

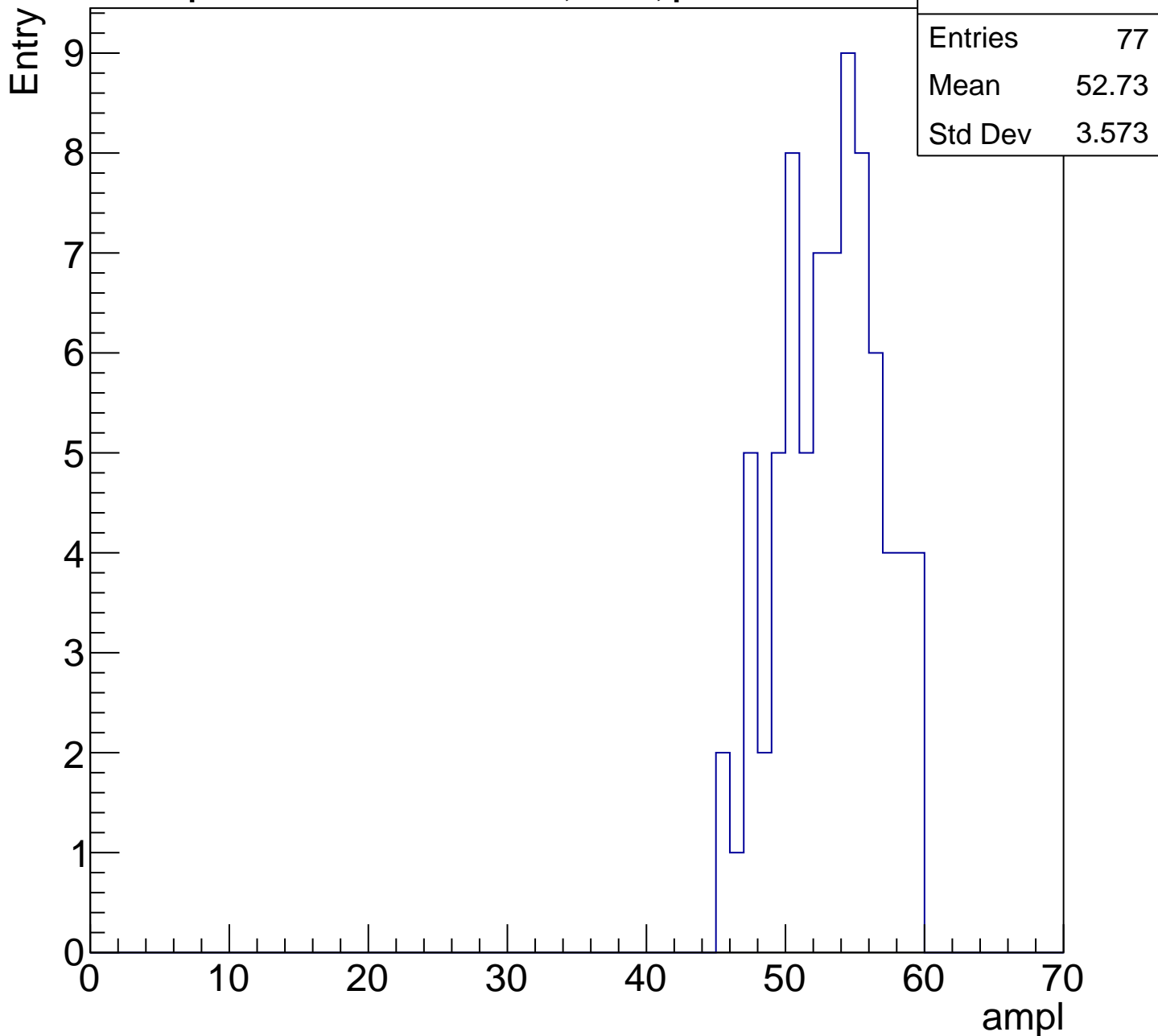
Gaus mean : 46.2666

Gaus Width: 4.4134



B1L101S, U26-ch51, adc3

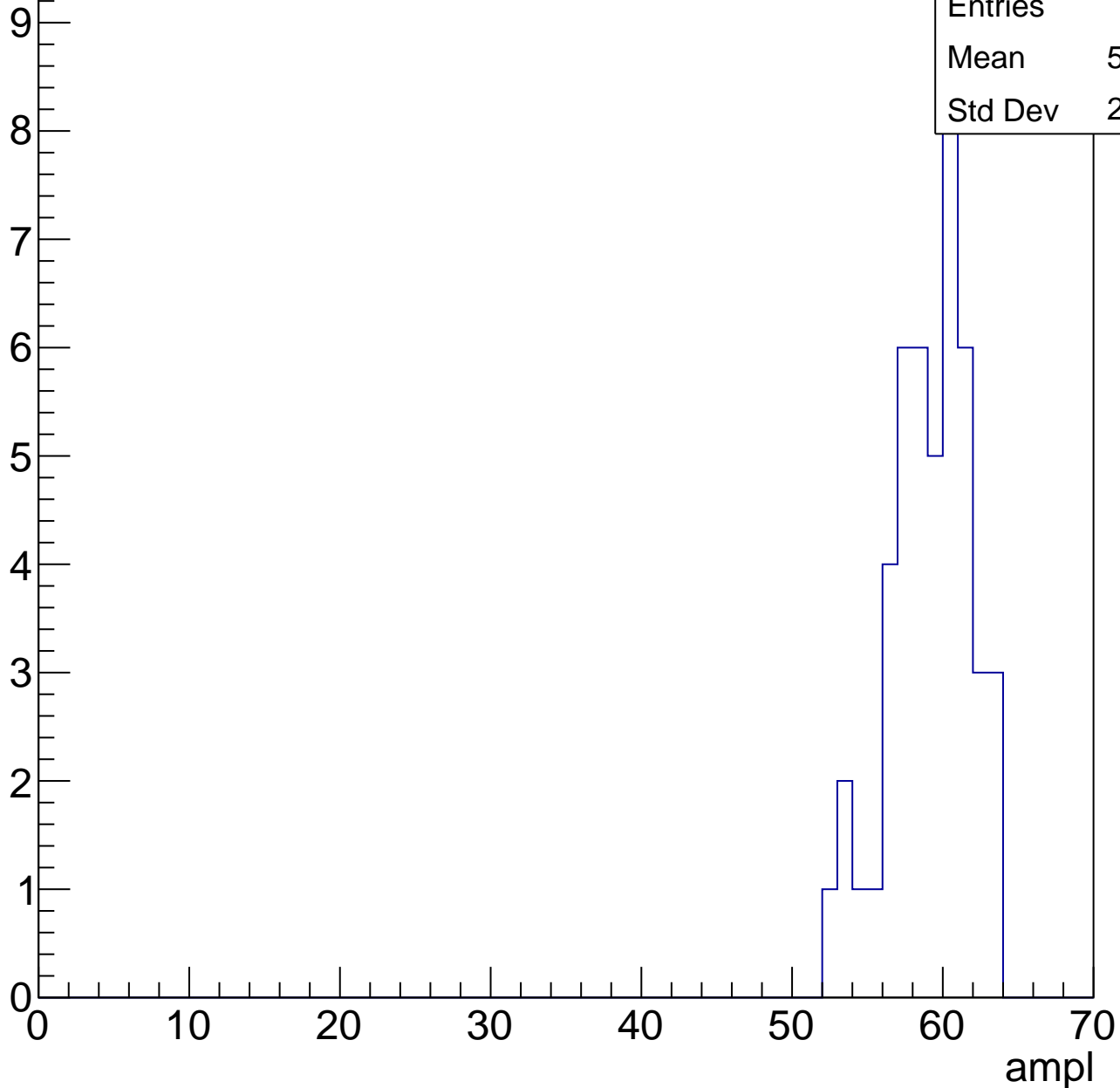
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

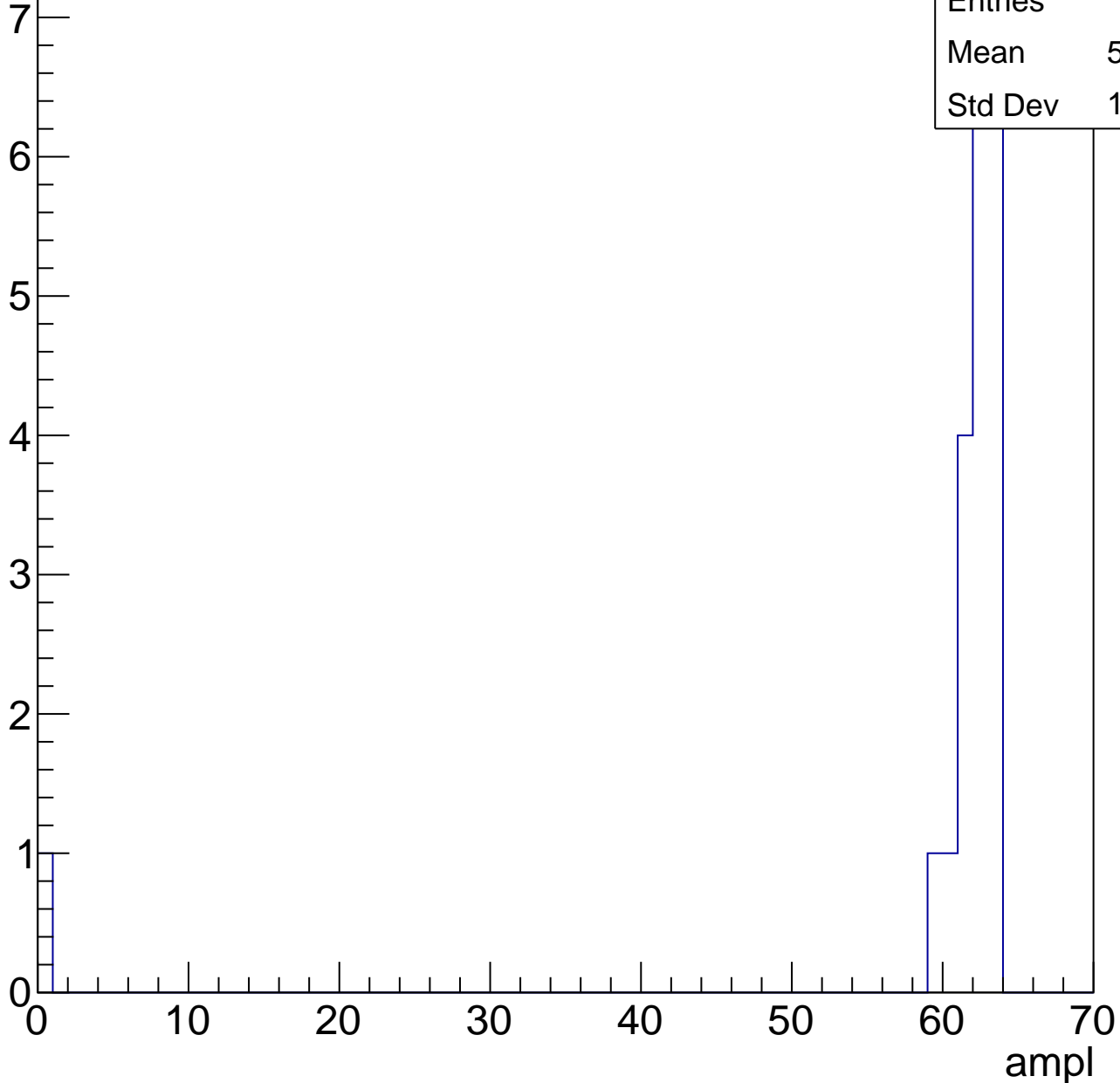


B1L101S, U26-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.95
Std Dev	13.23



B1L101S, U26-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L101S, U26-ch52, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.89
Std Dev	2.889

Gaus mean : 30.2414

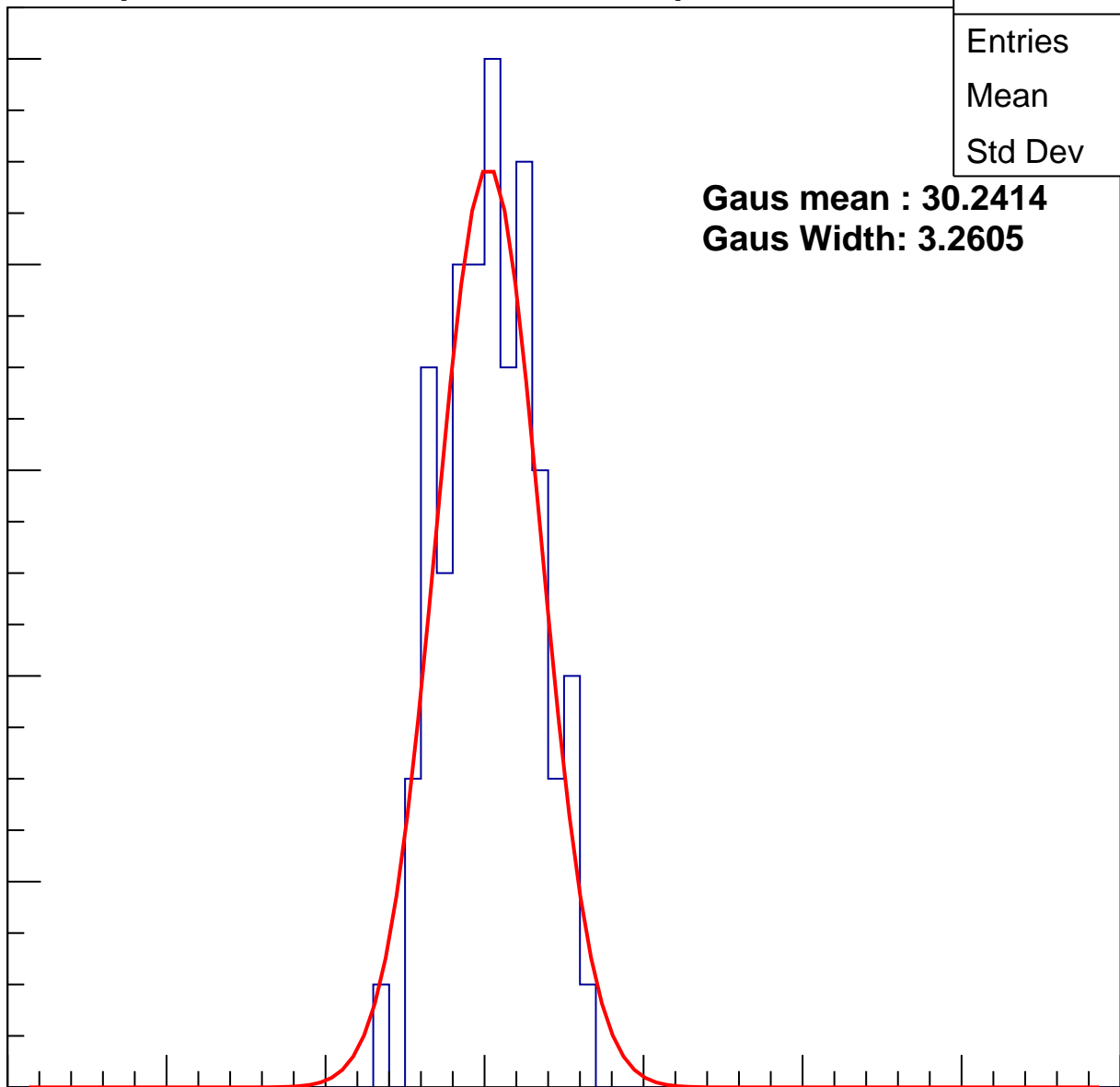
Gaus Width: 3.2605

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch52, adc1

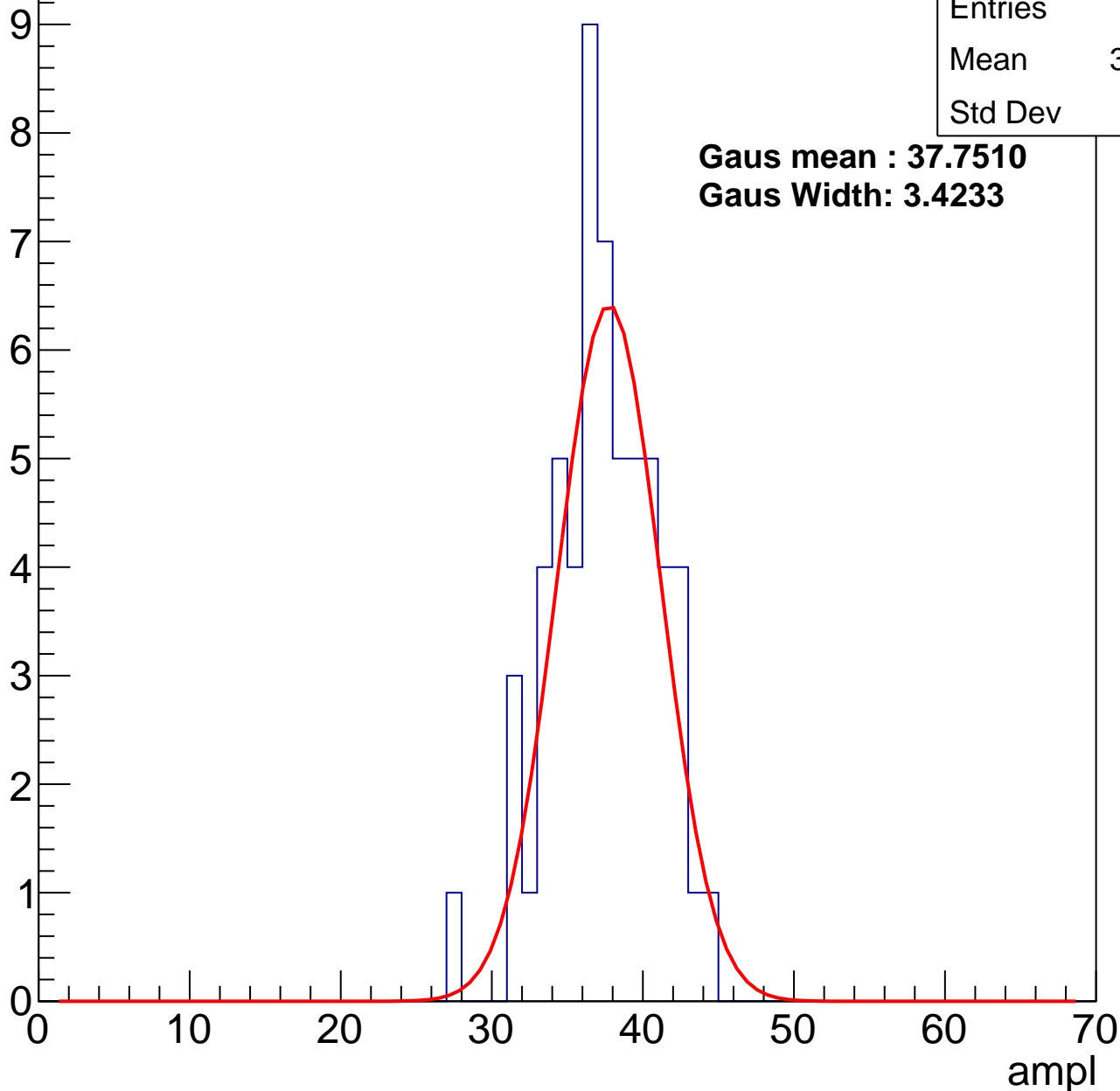
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.97
Std Dev	3.41

Gaus mean : 37.7510

Gaus Width: 3.4233



B1L101S, U26-ch52, adc2

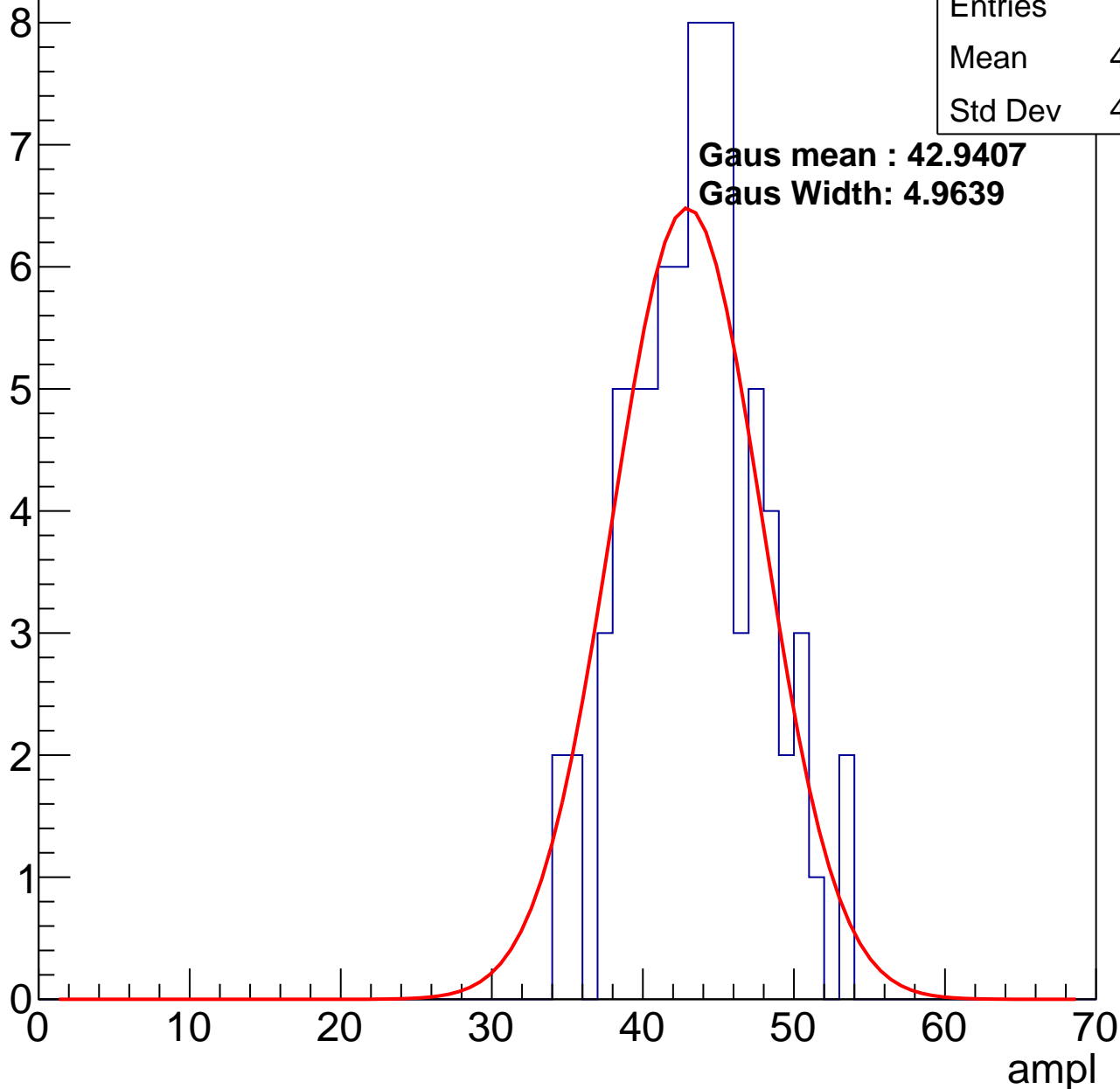
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.05
Std Dev	4.248

Gaus mean : 42.9407

Gaus Width: 4.9639

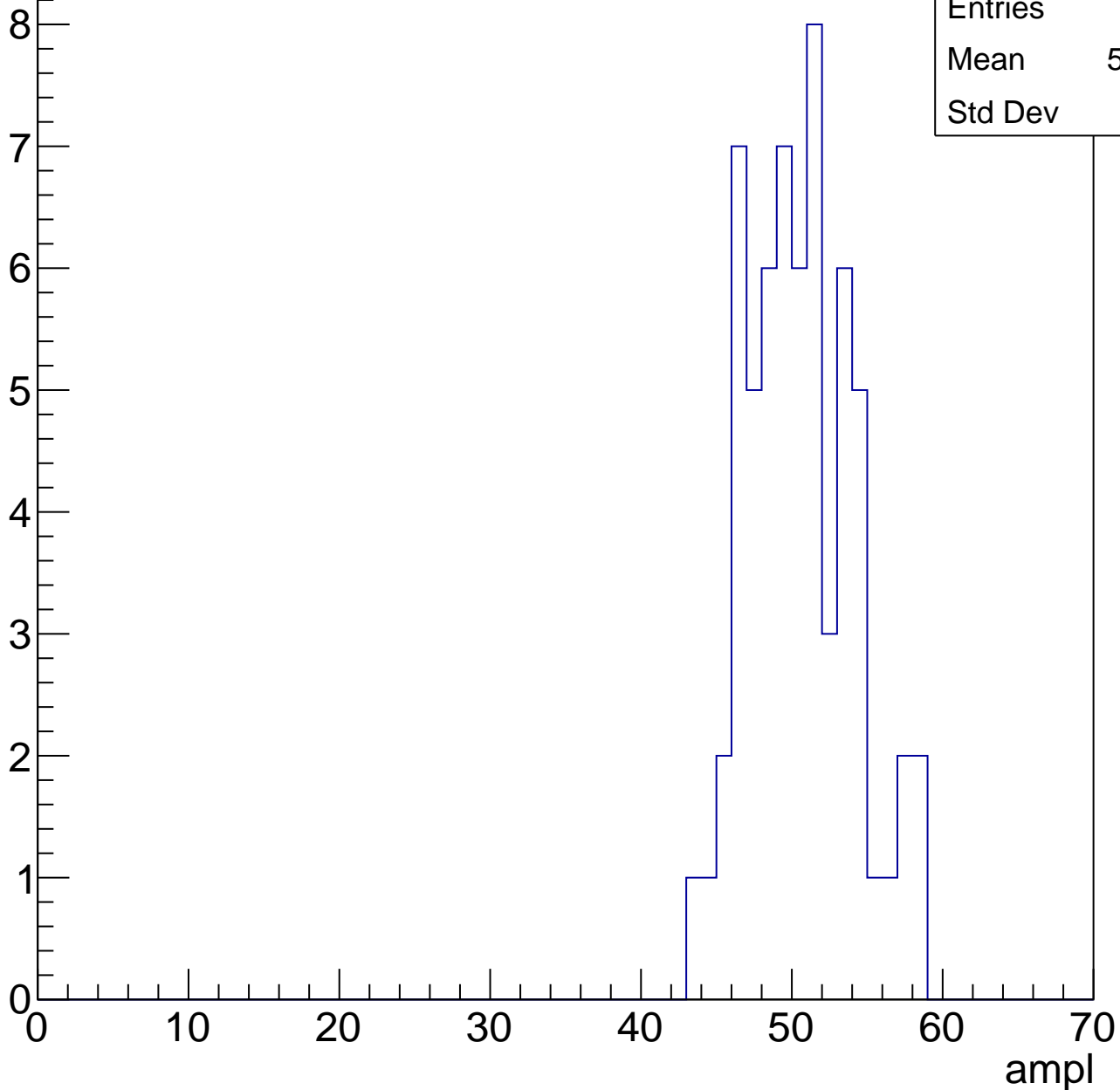


B1L101S, U26-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.13
Std Dev	3.48

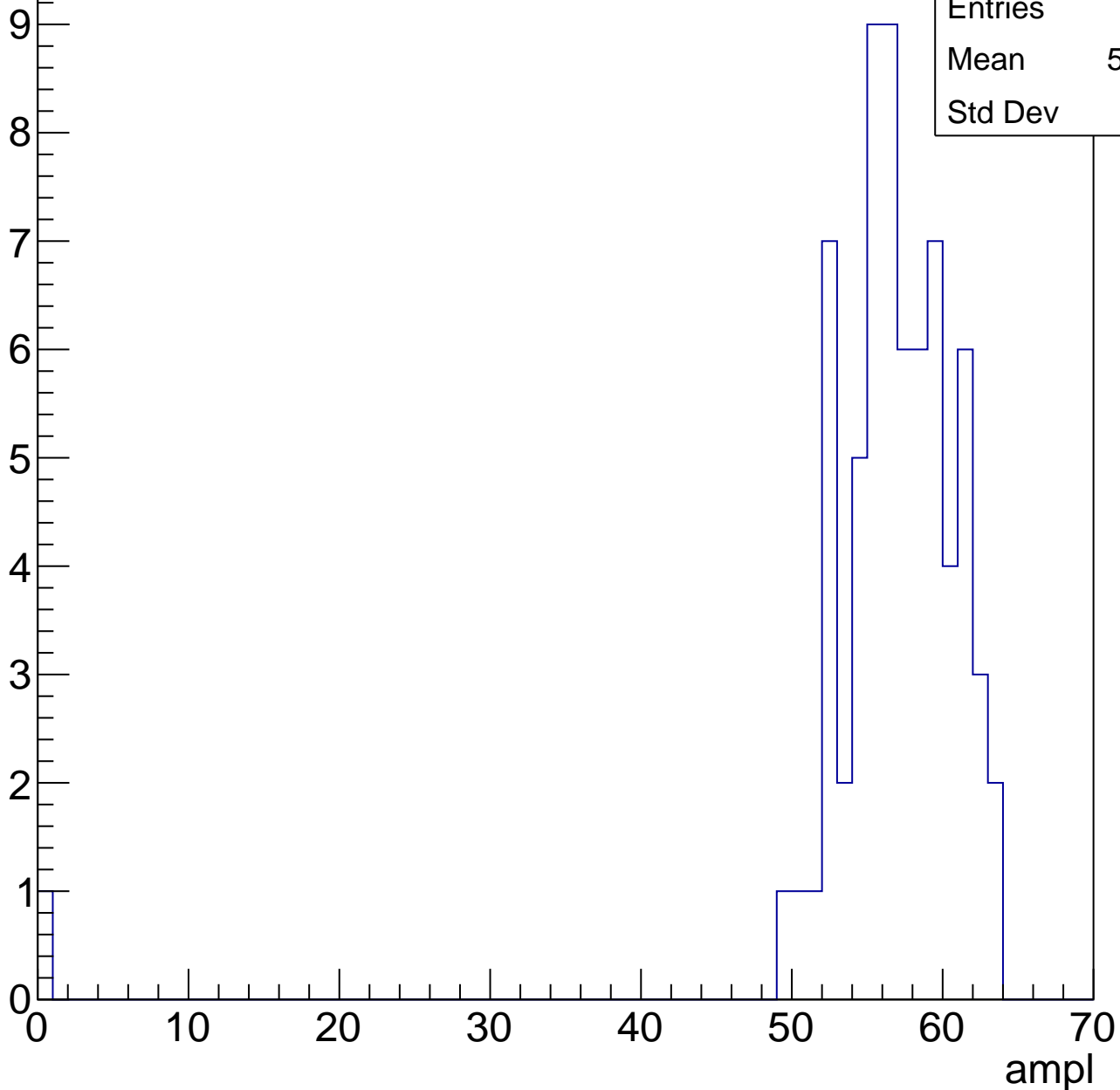


B1L101S, U26-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.86
Std Dev	7.48

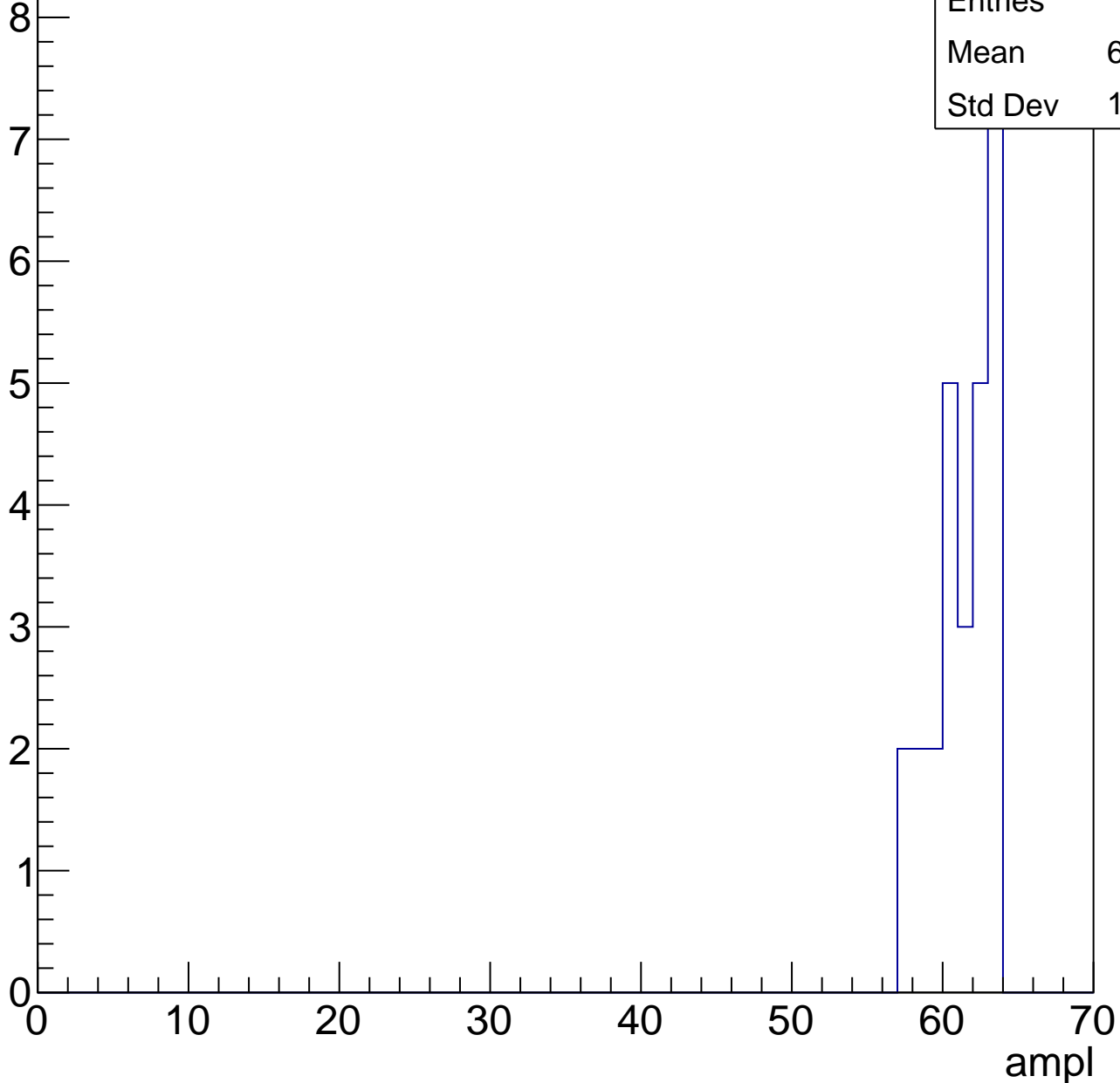


B1L101S, U26-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

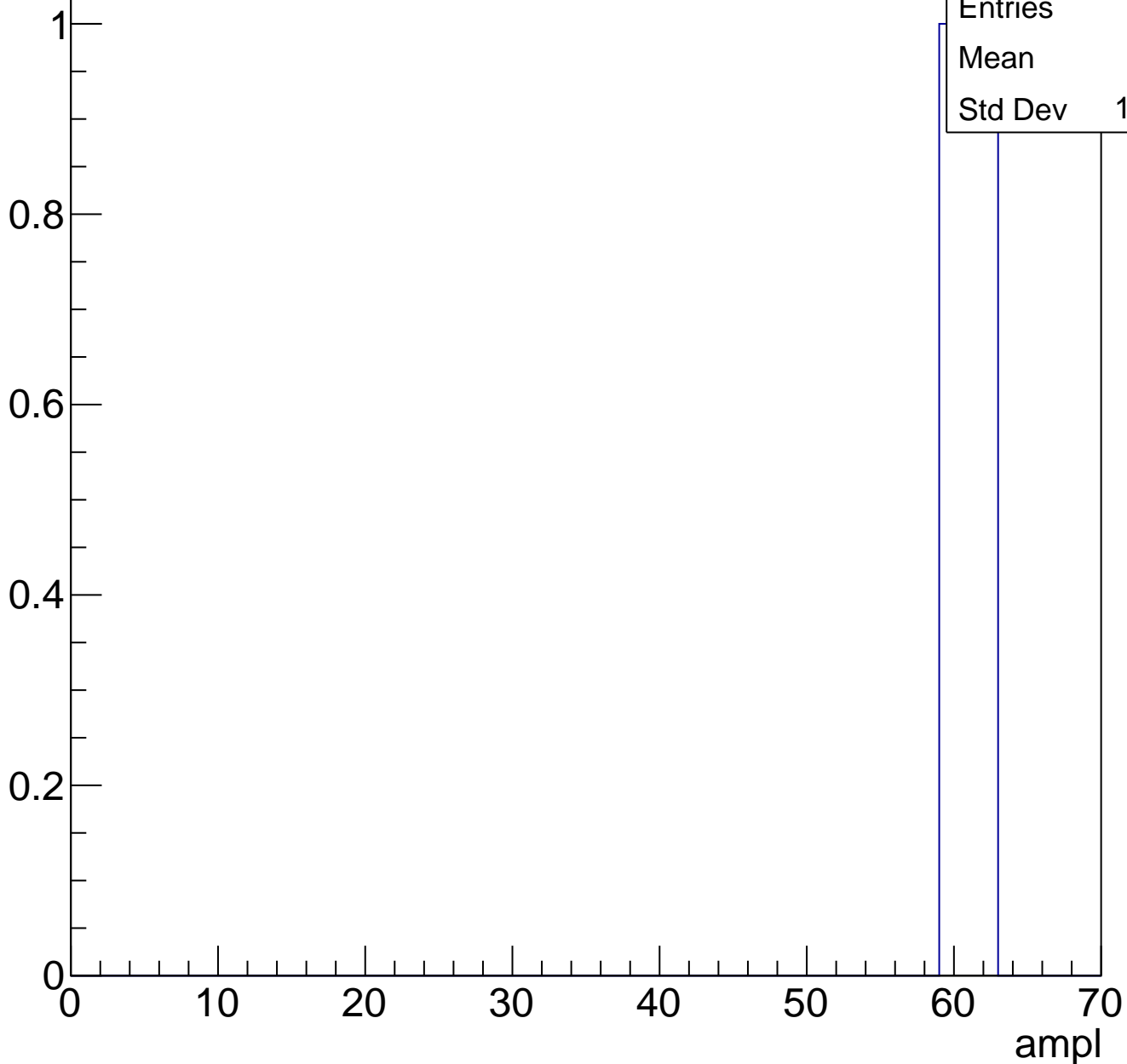
Entries	27
Mean	60.93
Std Dev	1.923



B1L101S, U26-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch52, adc7

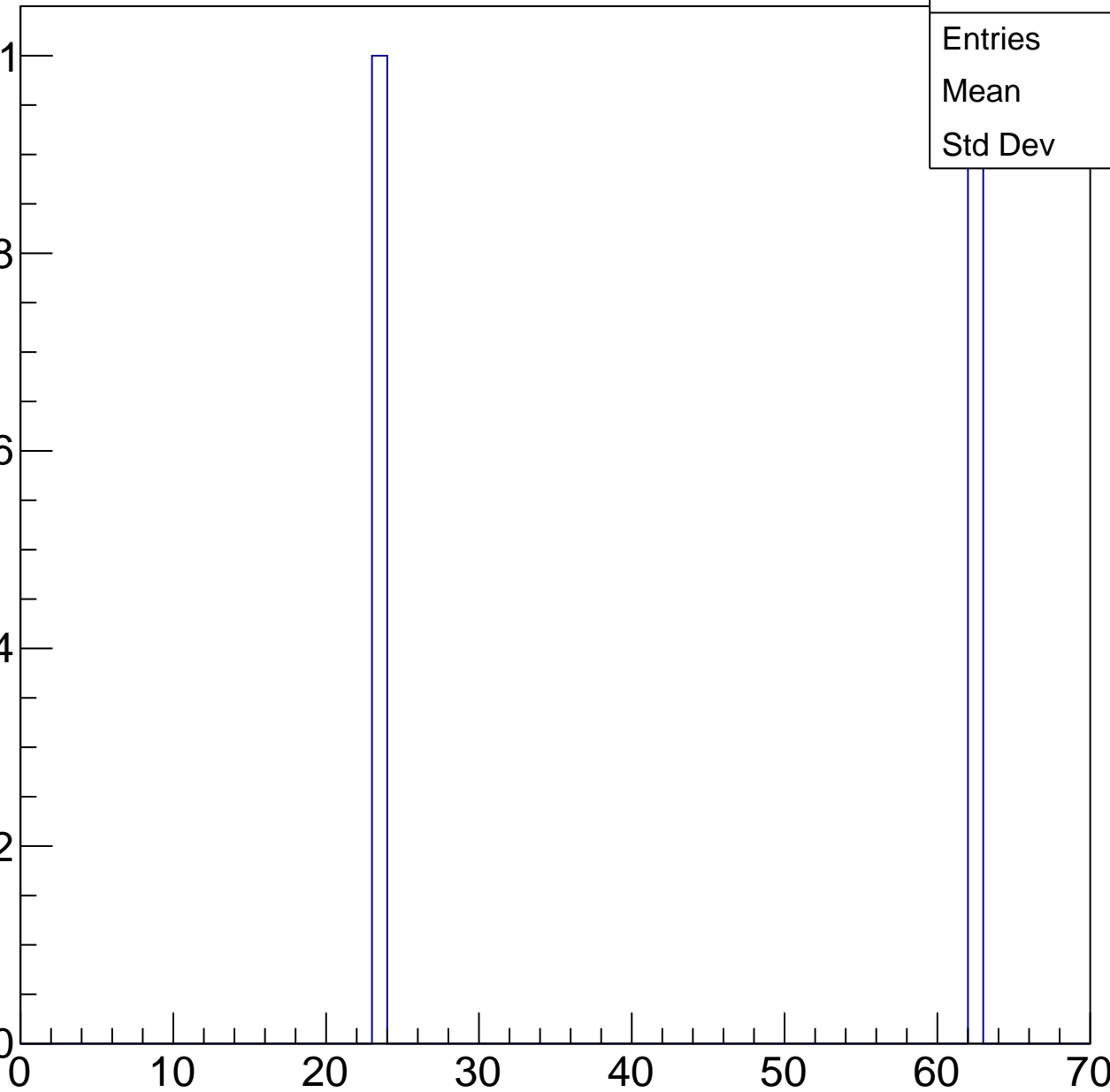
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	19.5

ampl



B1L101S, U26-ch53, adc0

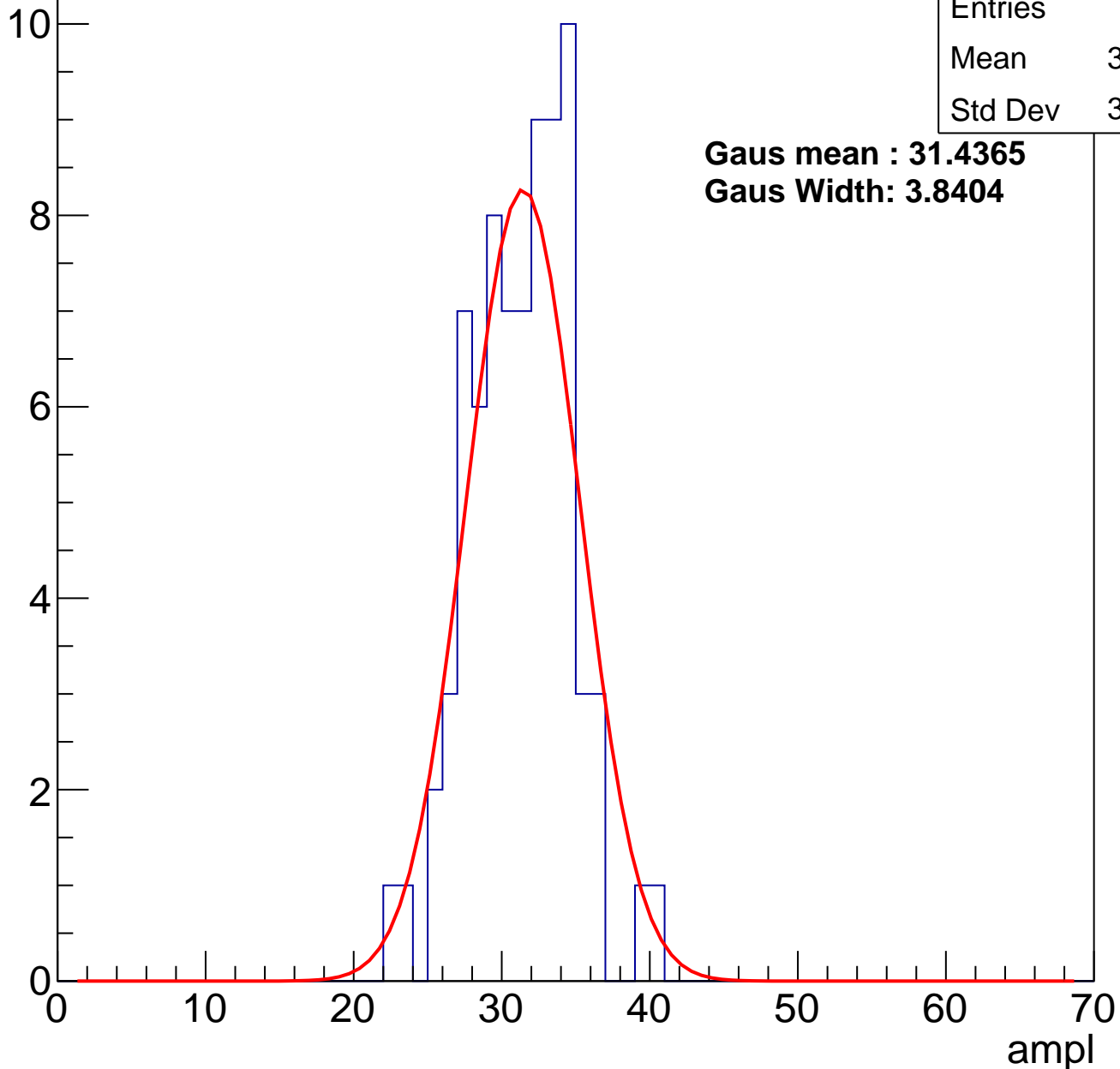
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.85
Std Dev	3.393

Gaus mean : 31.4365

Gaus Width: 3.8404

Entry



B1L101S, U26-ch53, adc1

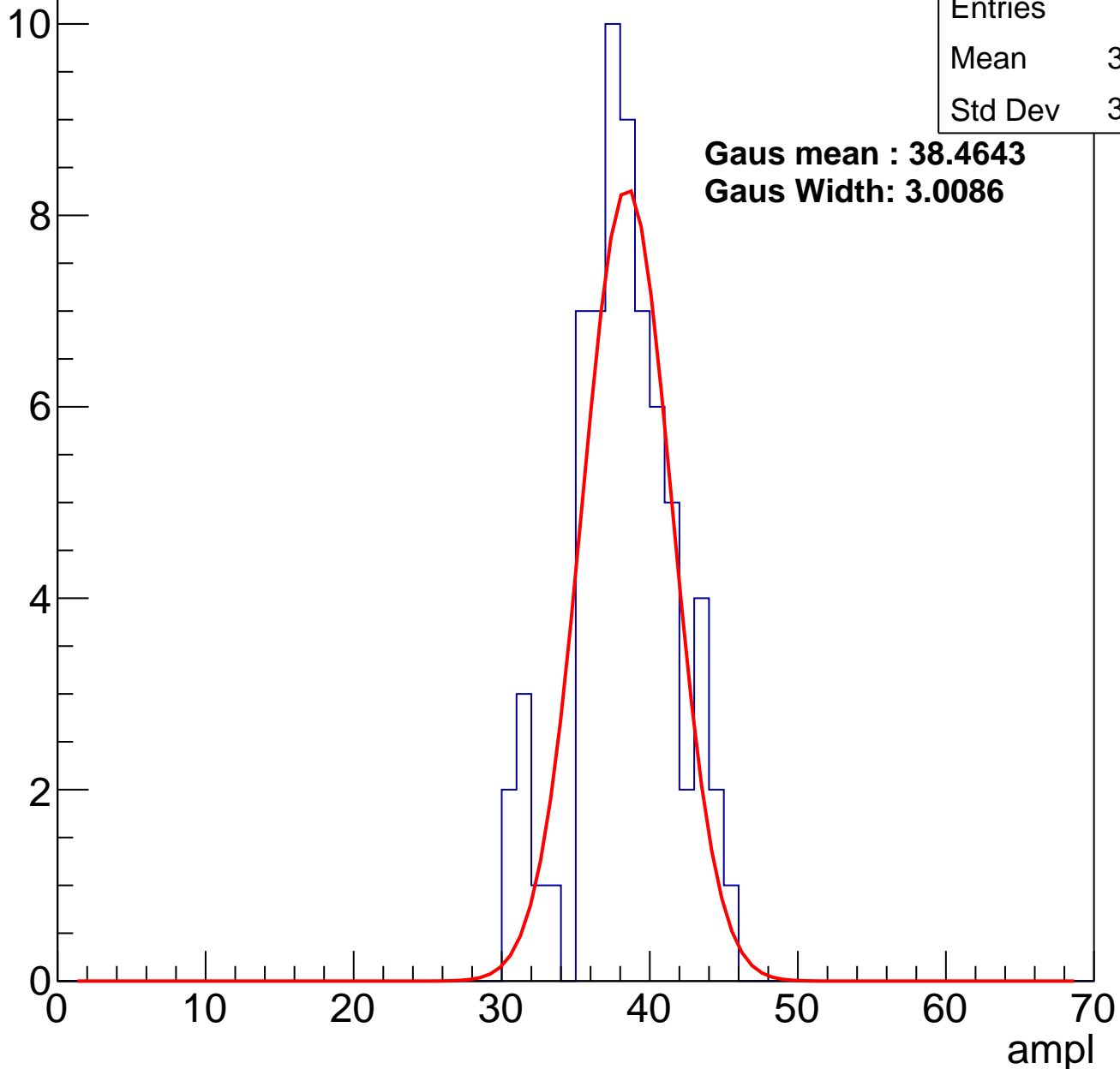
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	37.82
Std Dev	3.377

Gaus mean : 38.4643

Gaus Width: 3.0086

Entry



B1L101S, U26-ch53, adc2

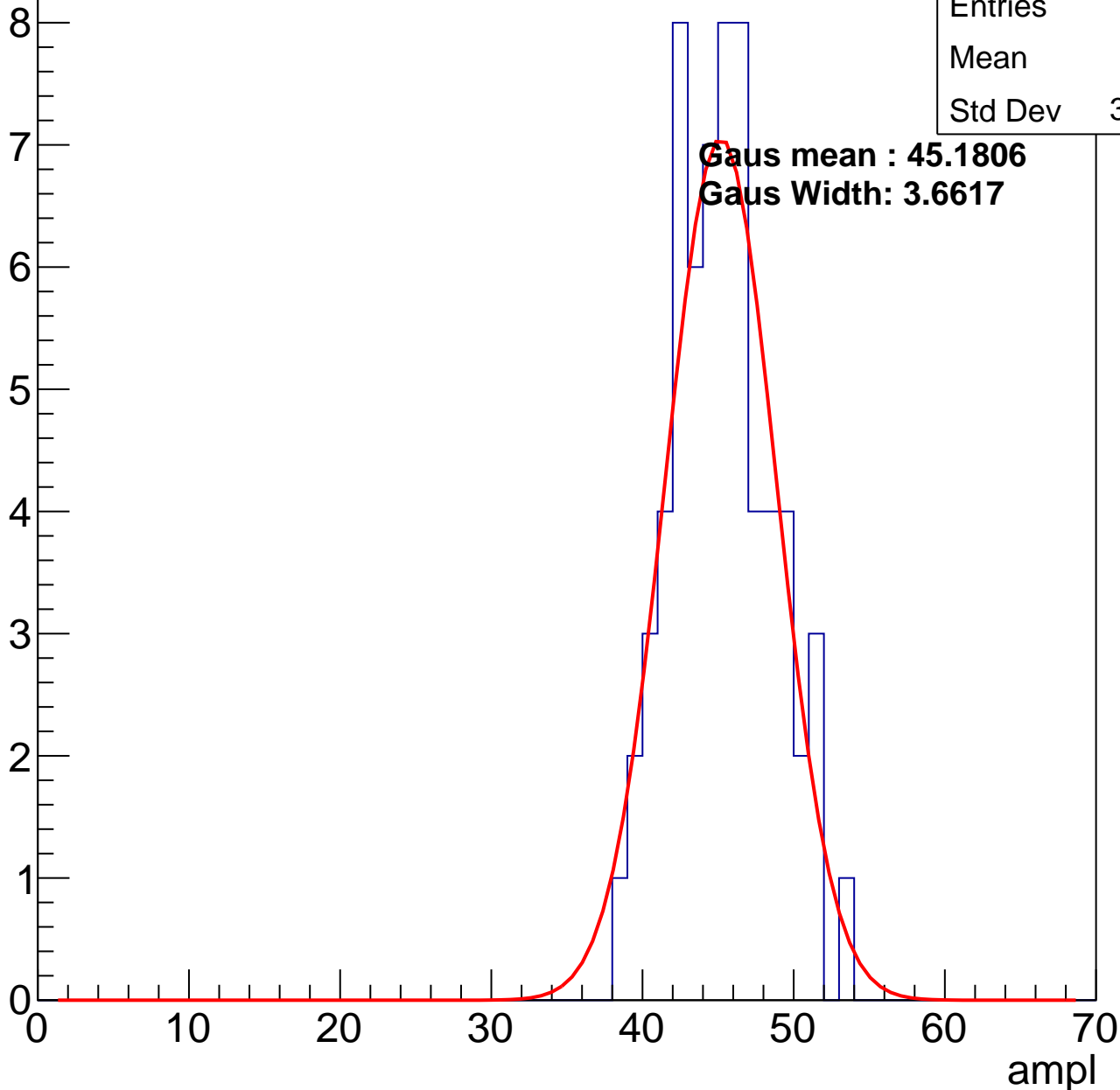
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.8
Std Dev	3.297

Gaus mean : 45.1806

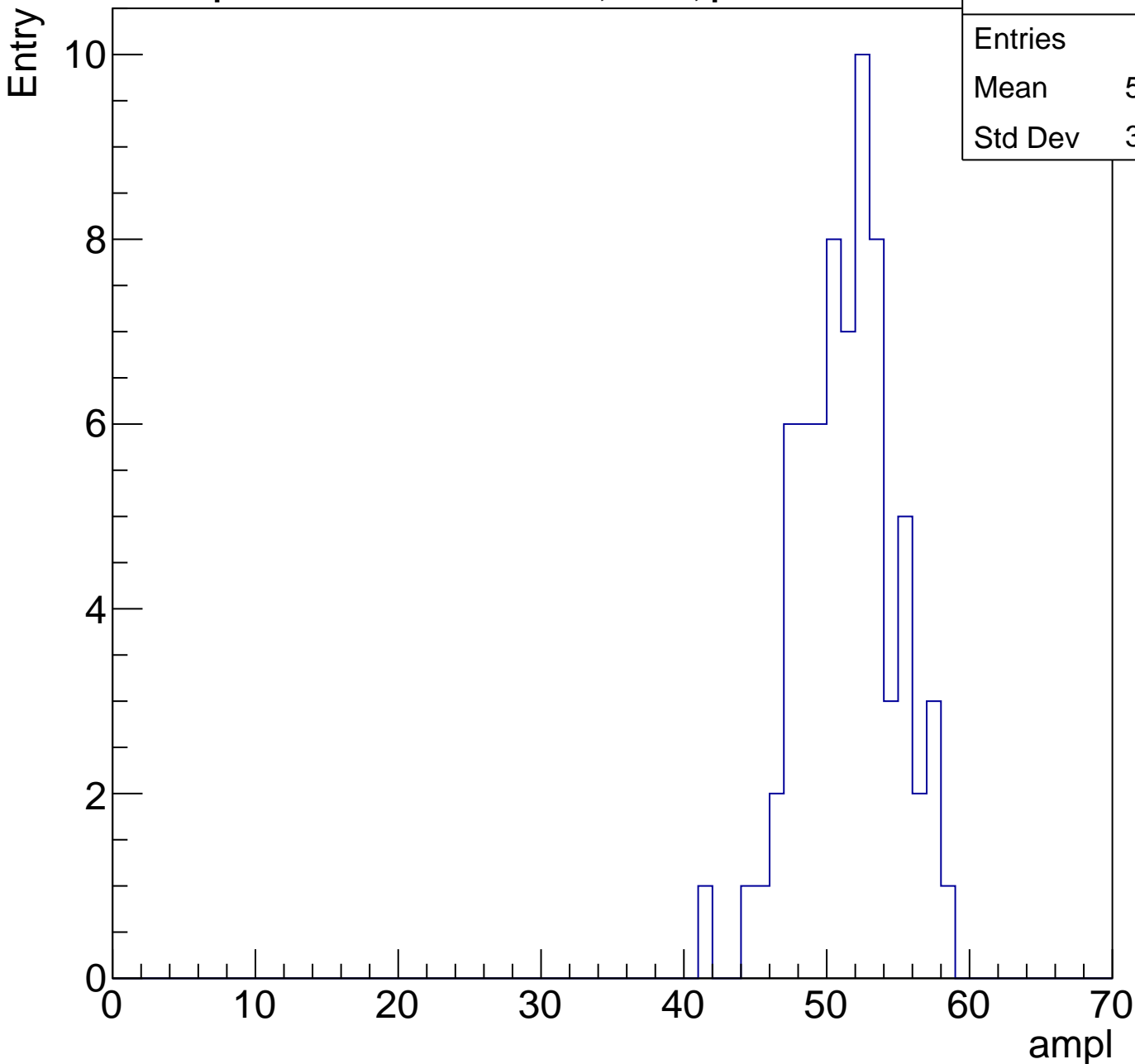
Gaus Width: 3.6617



B1L101S, U26-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

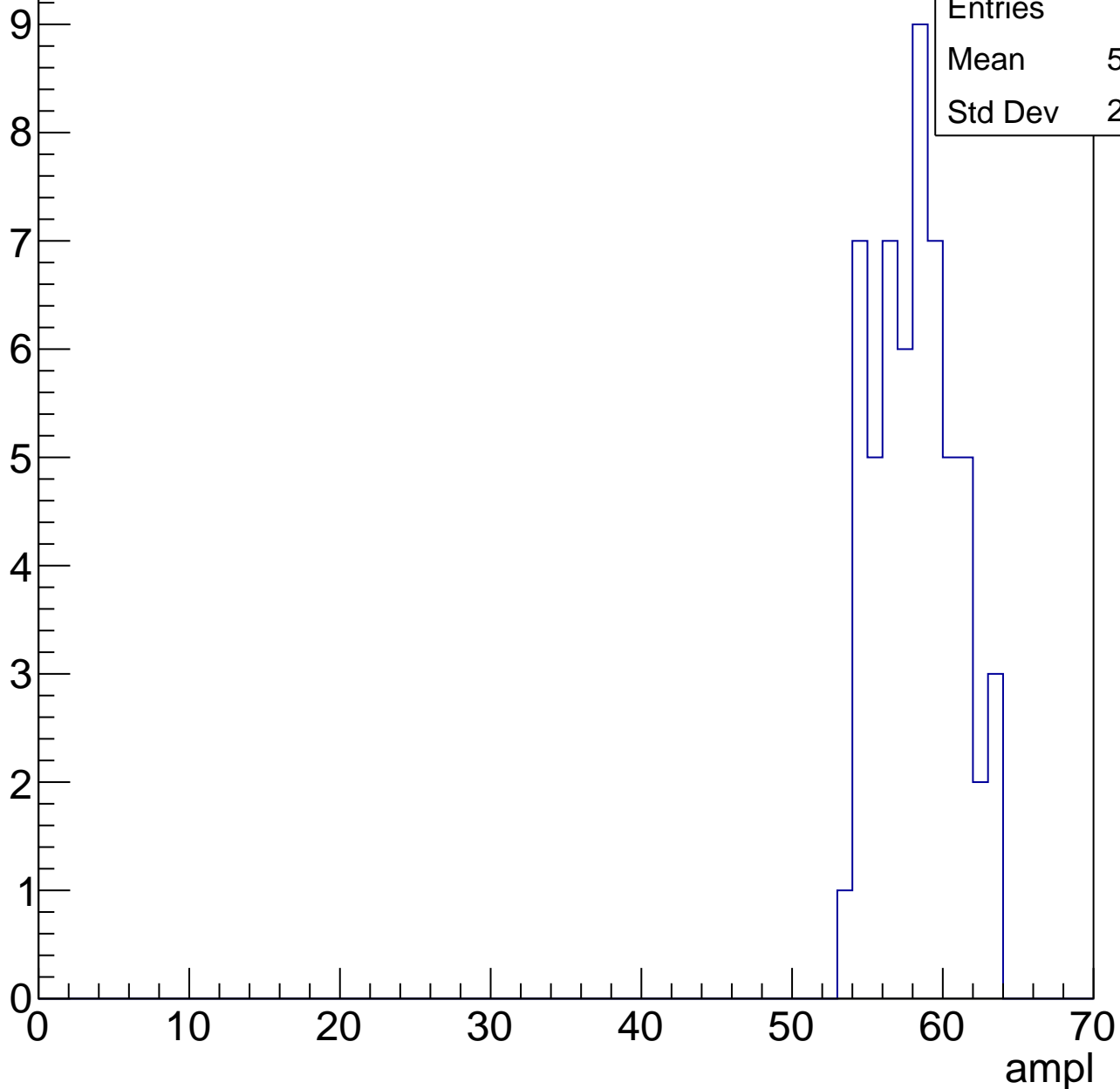
Entries	70
Mean	50.93
Std Dev	3.322



B1L101S, U26-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

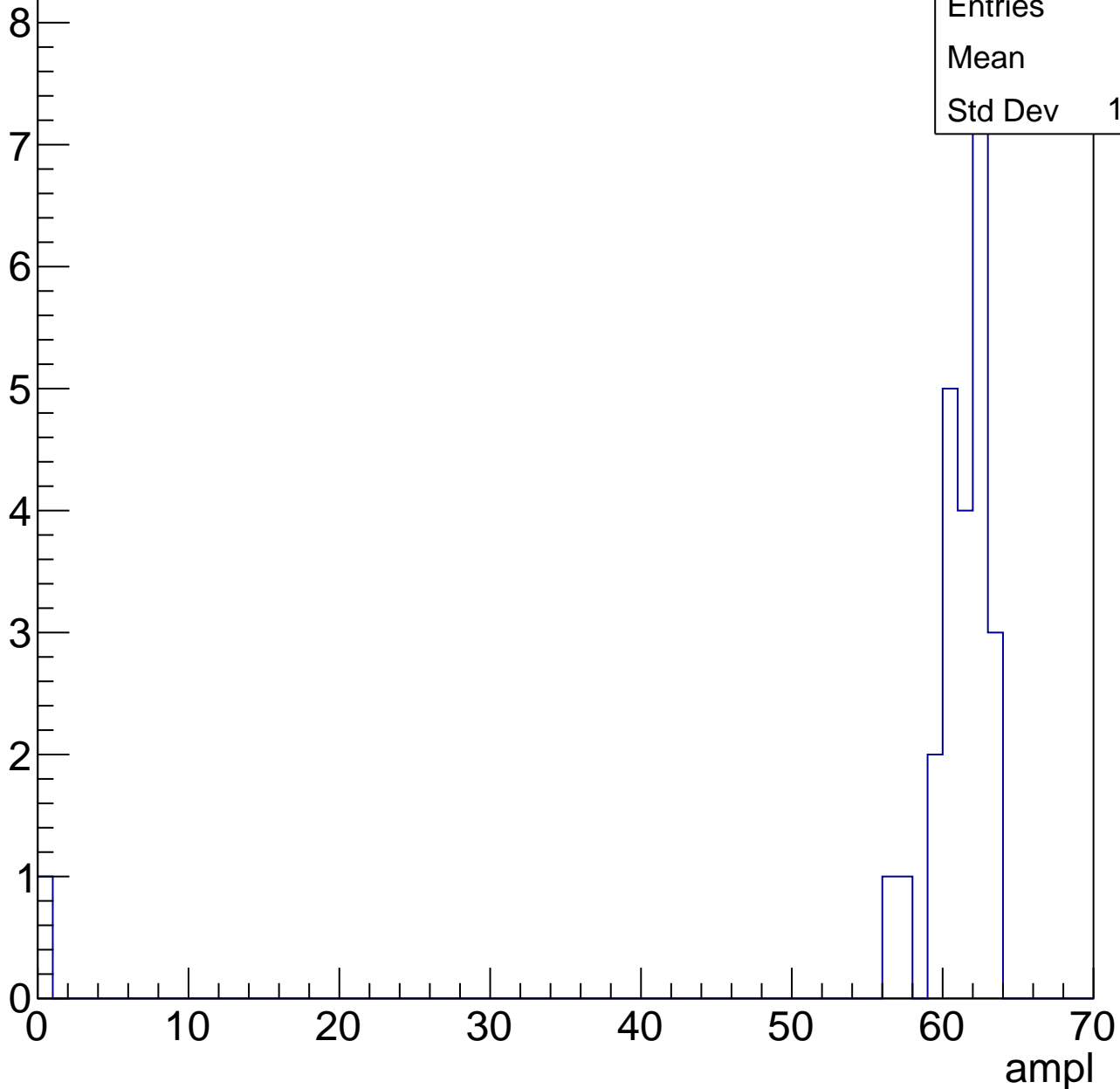


B1L101S, U26-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

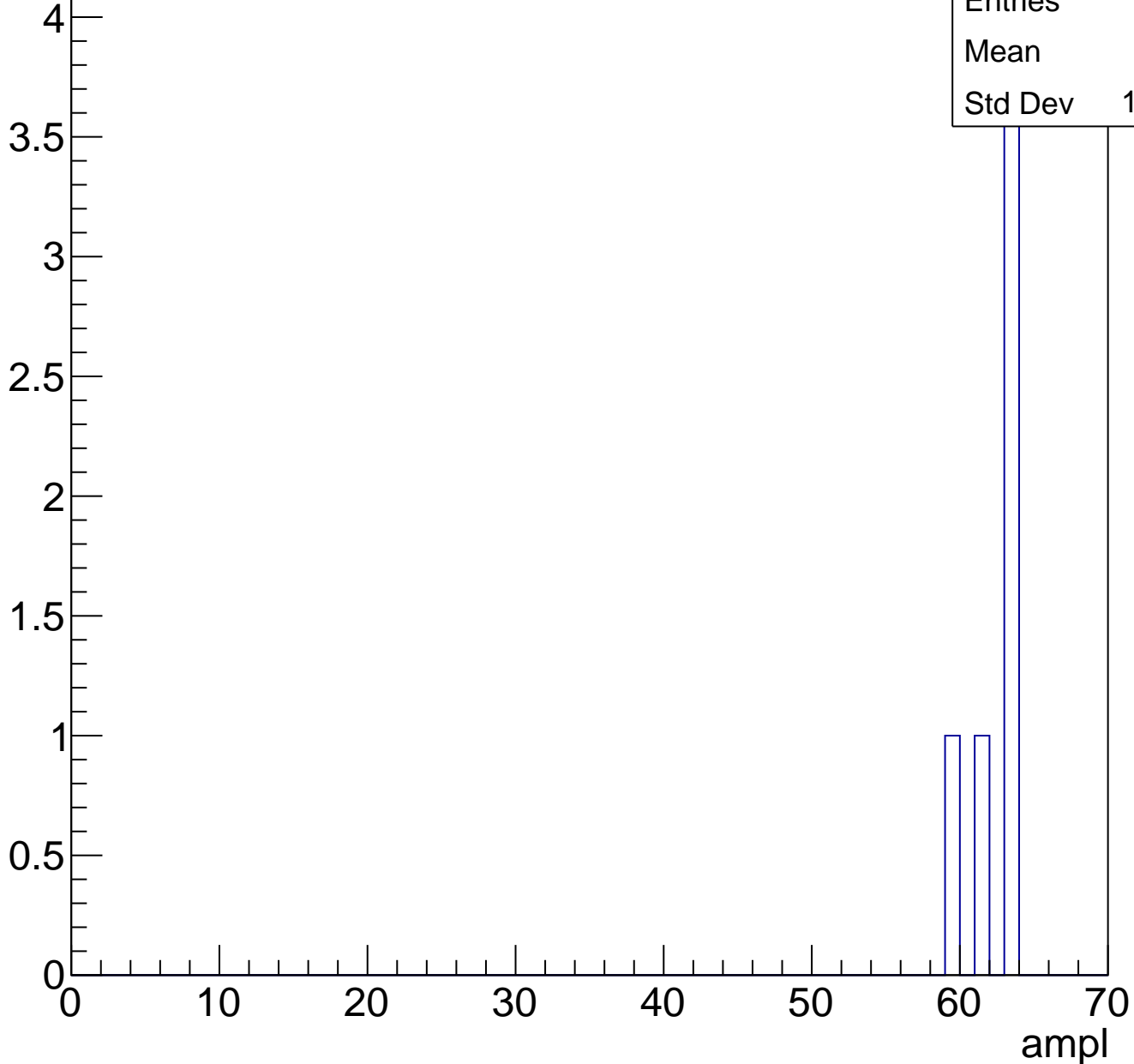
Entries	25
Mean	58.4
Std Dev	12.04



B1L101S, U26-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch54, adc0

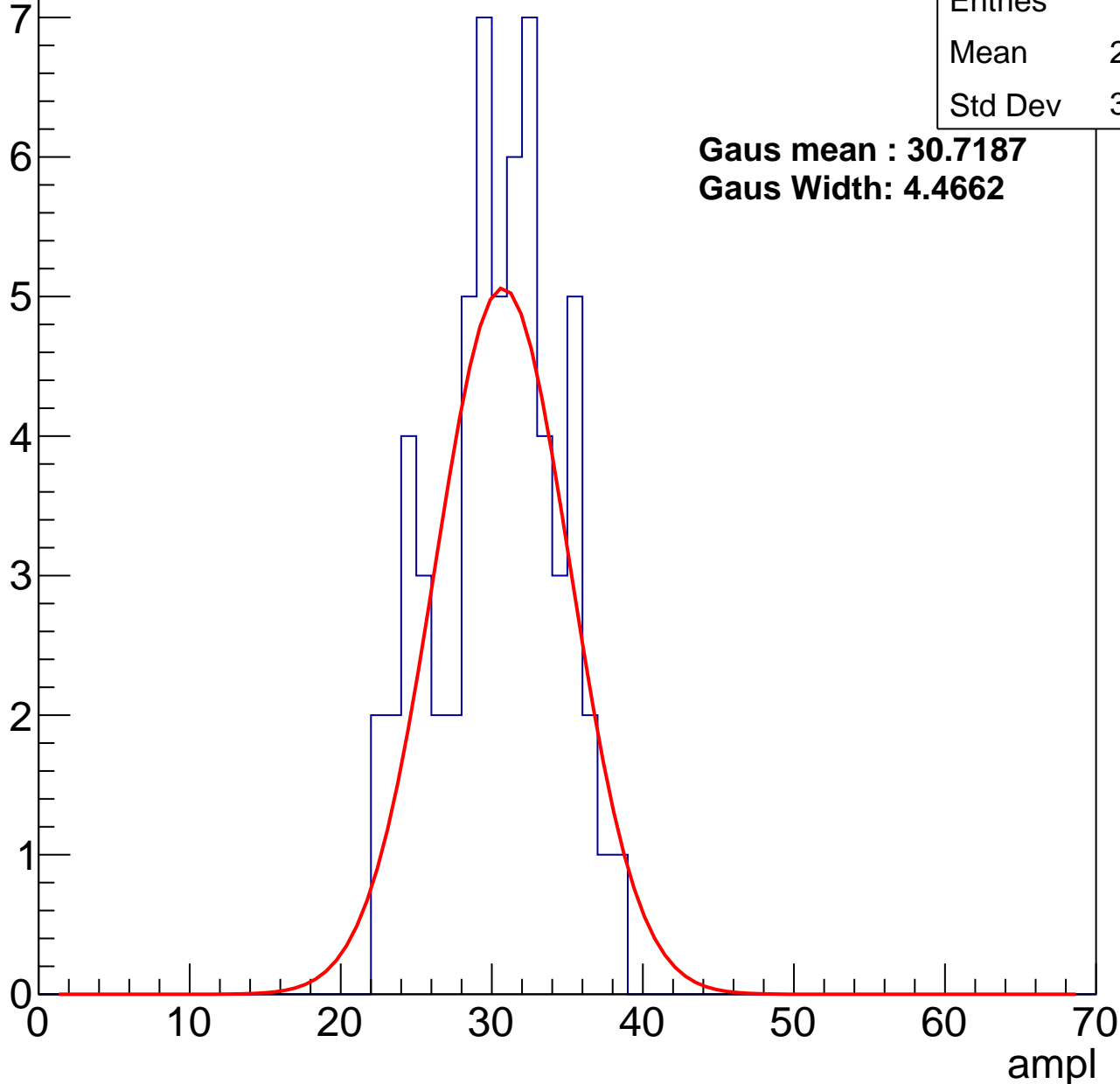
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.93
Std Dev	3.946

Gaus mean : 30.7187

Gaus Width: 4.4662



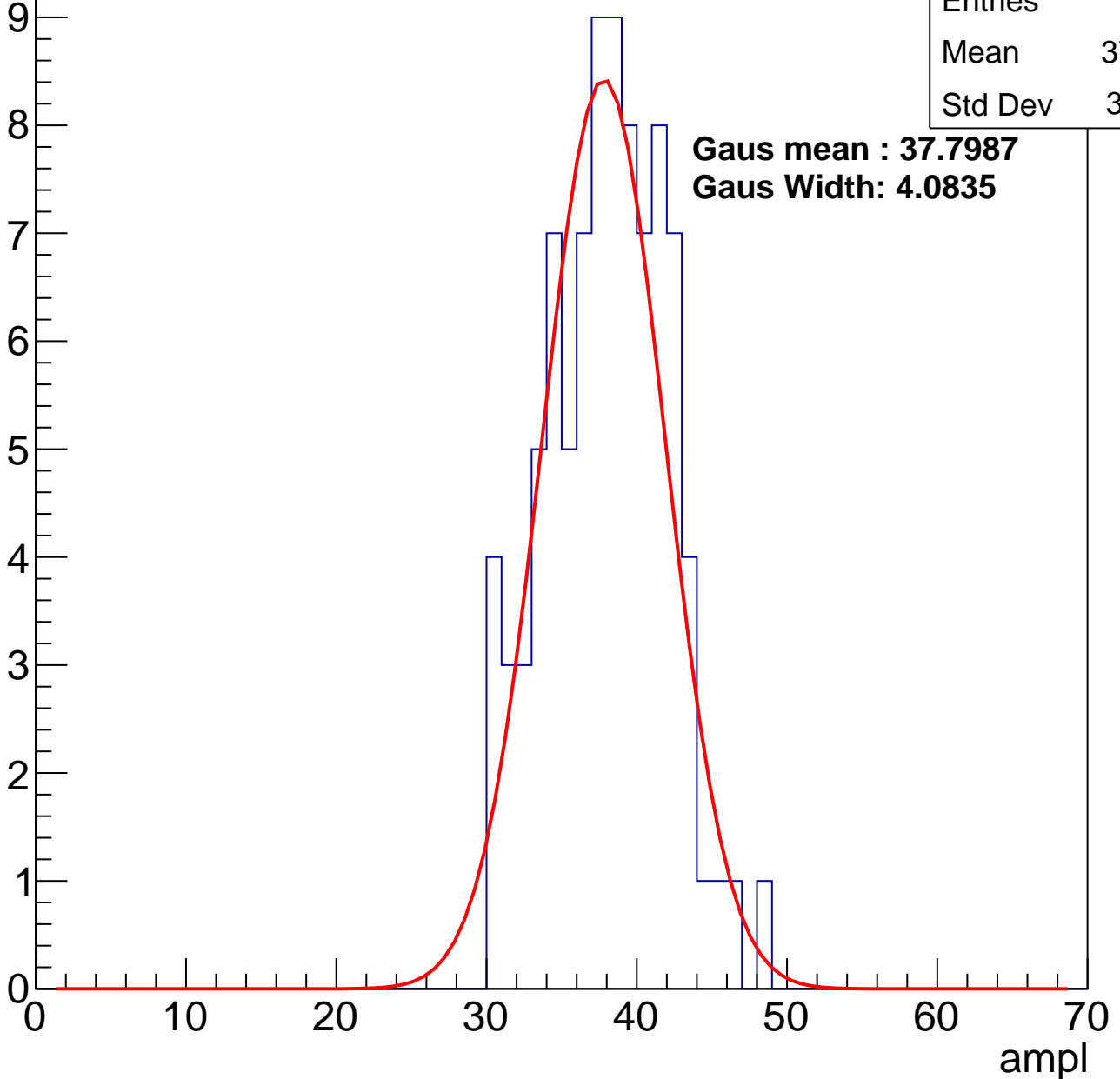
B1L101S, U26-ch54, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	37.59
Std Dev	3.921

Gaus mean : 37.7987
Gaus Width: 4.0835



B1L101S, U26-ch54, adc2

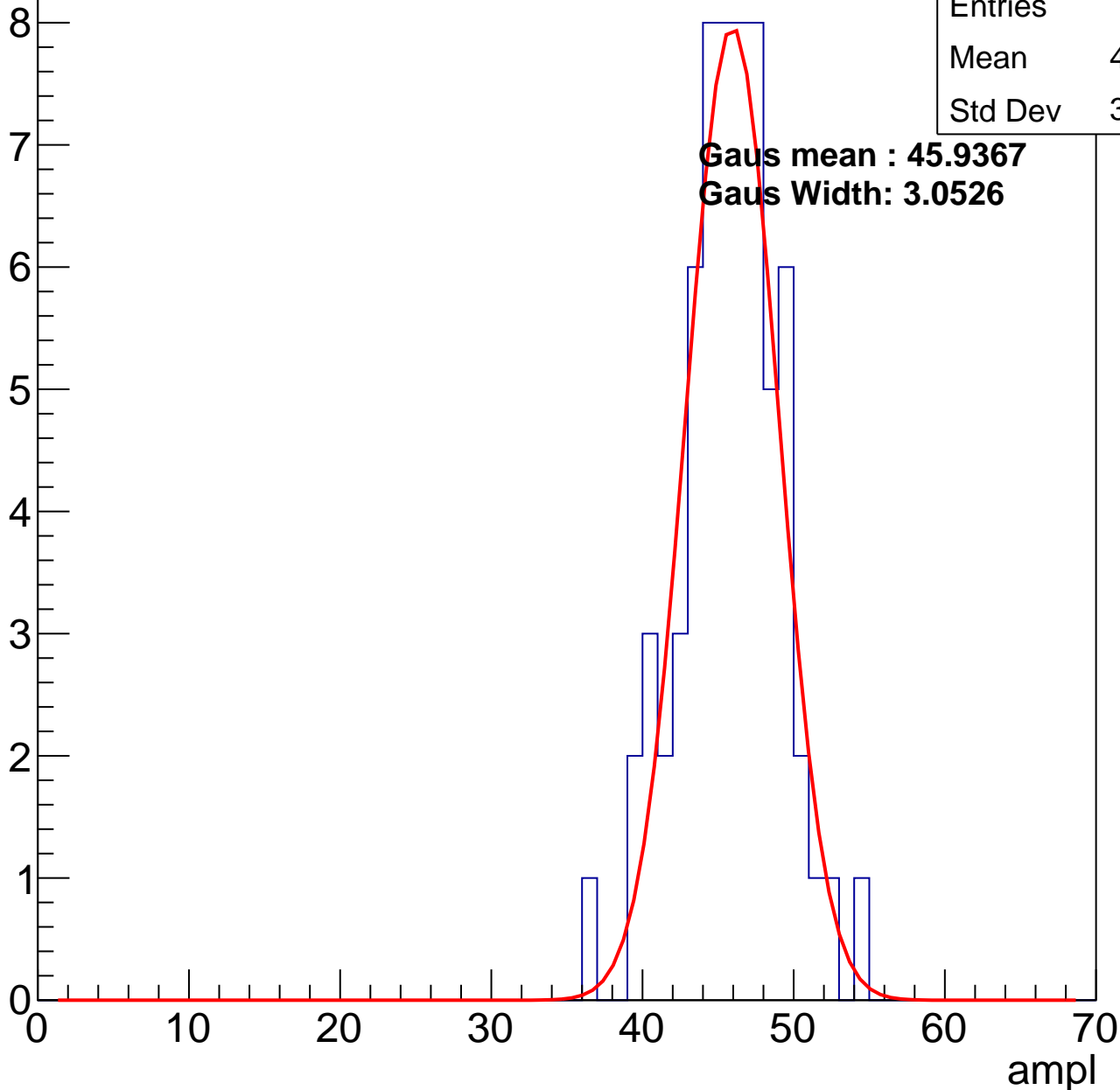
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.34
Std Dev	3.302

Gaus mean : 45.9367

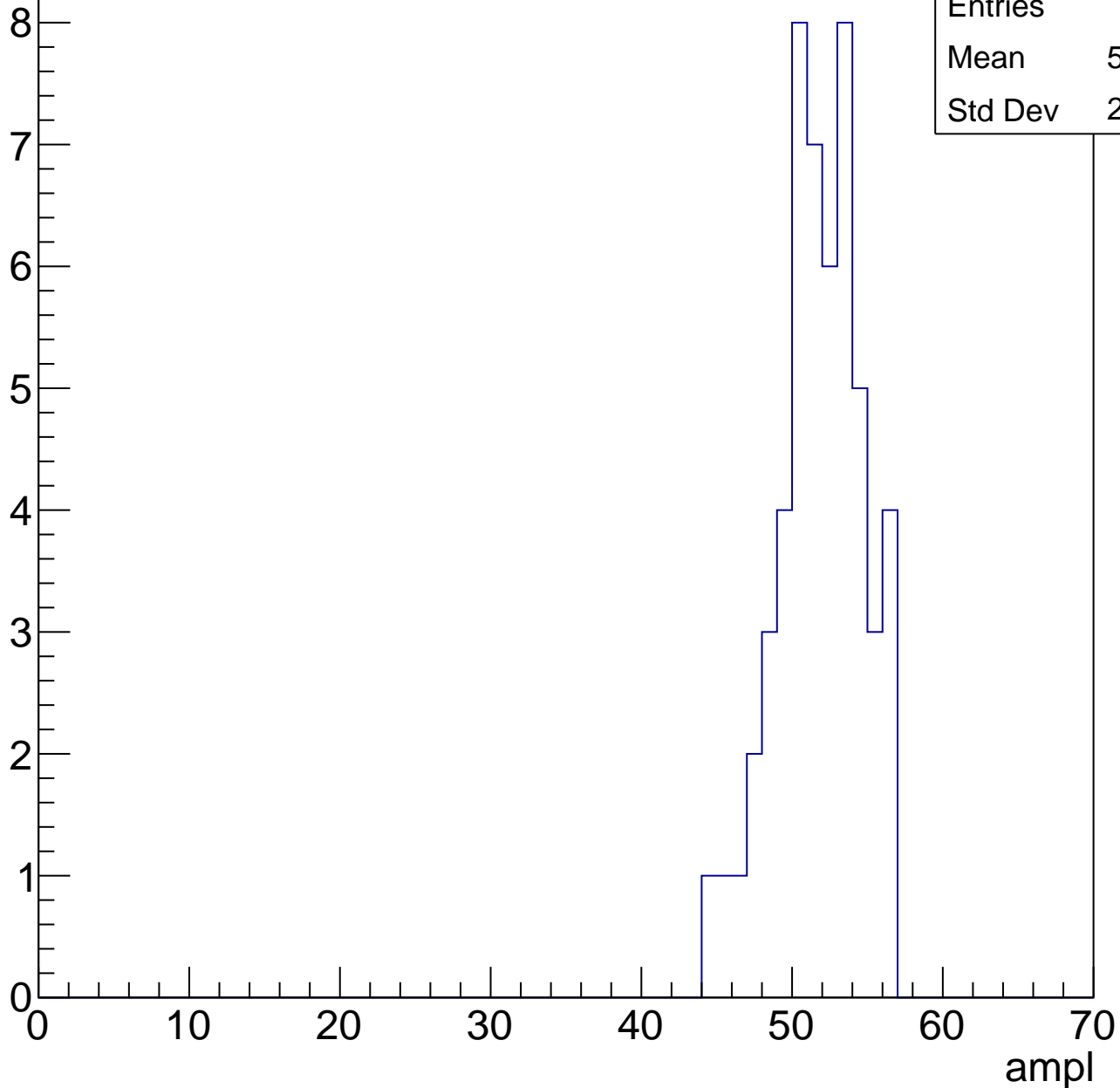
Gaus Width: 3.0526



B1L101S, U26-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



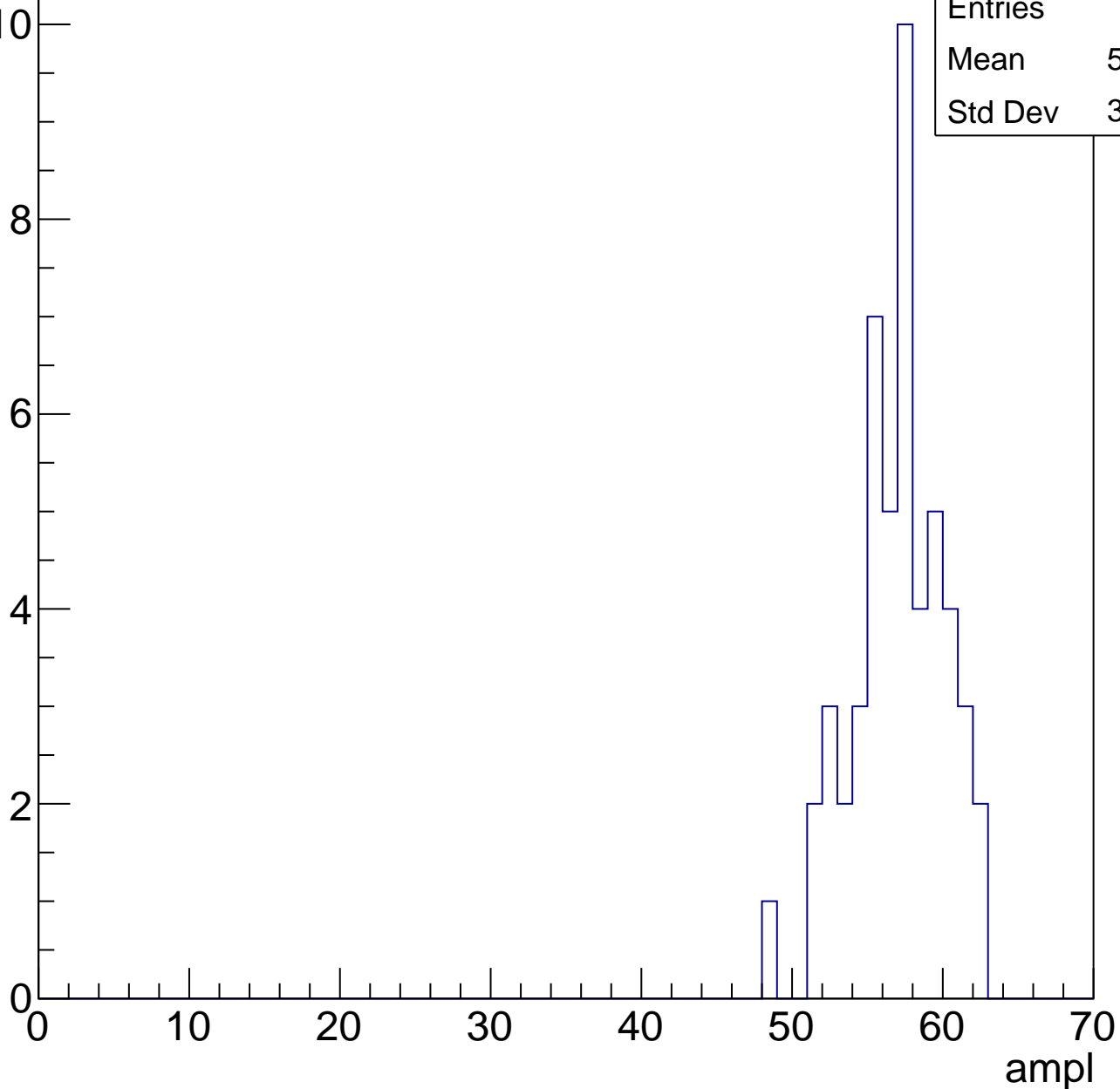
Entries	53
Mean	51.34
Std Dev	2.808

B1L101S, U26-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.53
Std Dev	3.025

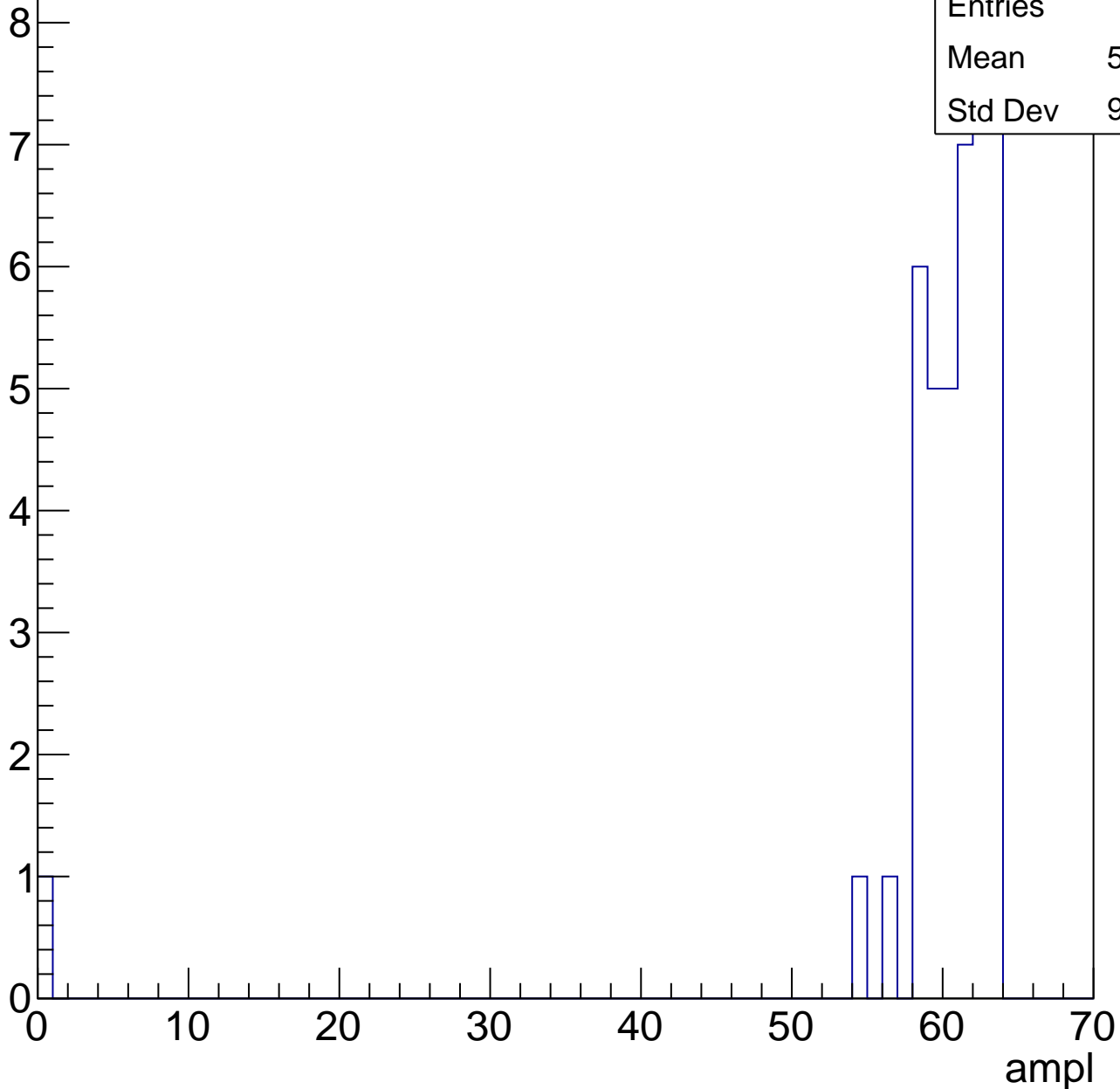


B1L101S, U26-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.05
Std Dev	9.454



B1L101S, U26-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch55, adc0

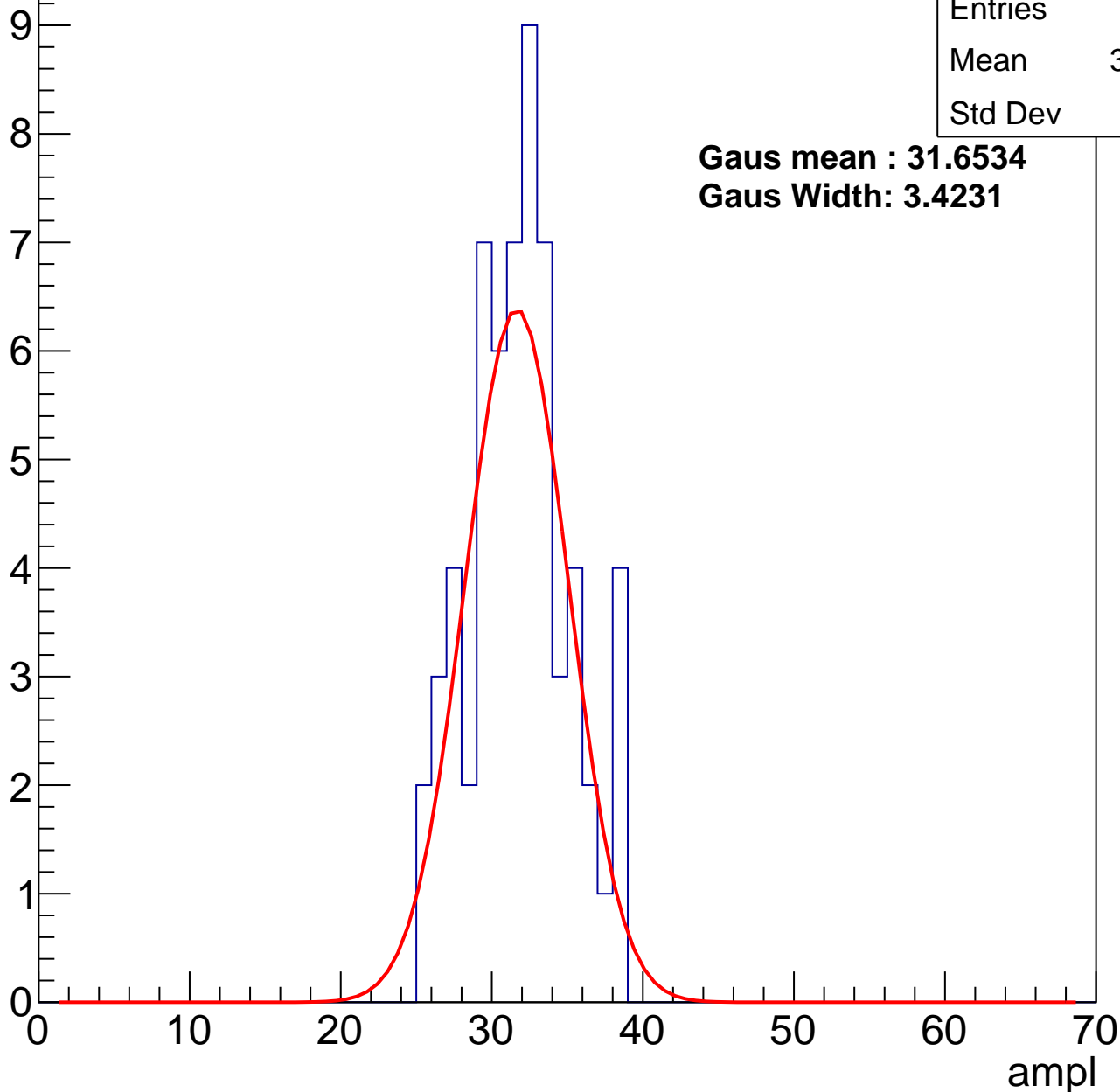
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	31.38
Std Dev	3.3

Gaus mean : 31.6534

Gaus Width: 3.4231



B1L101S, U26-ch55, adc1

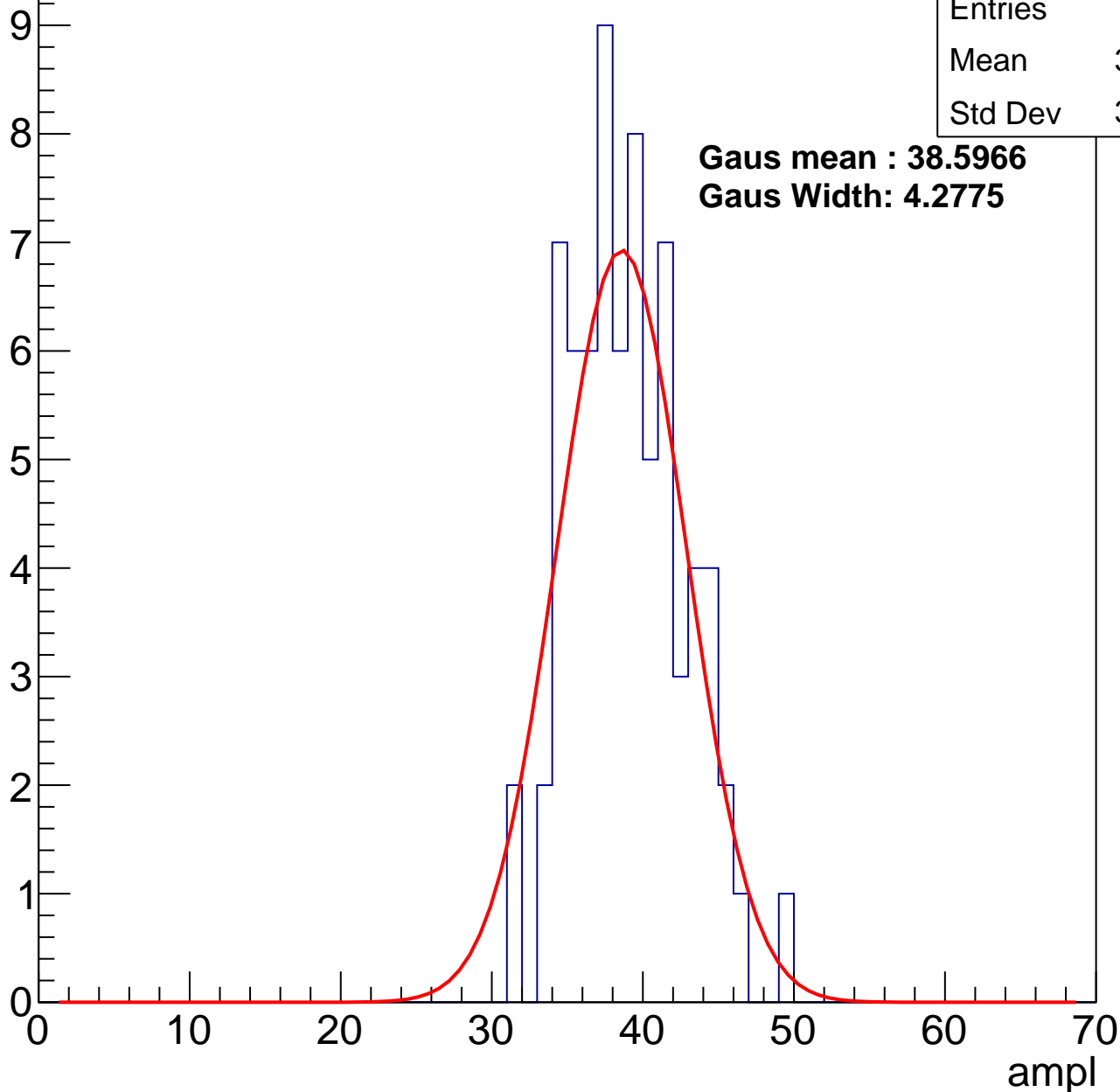
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	38.51
Std Dev	3.691

Gaus mean : 38.5966

Gaus Width: 4.2775



B1L101S, U26-ch55, adc2

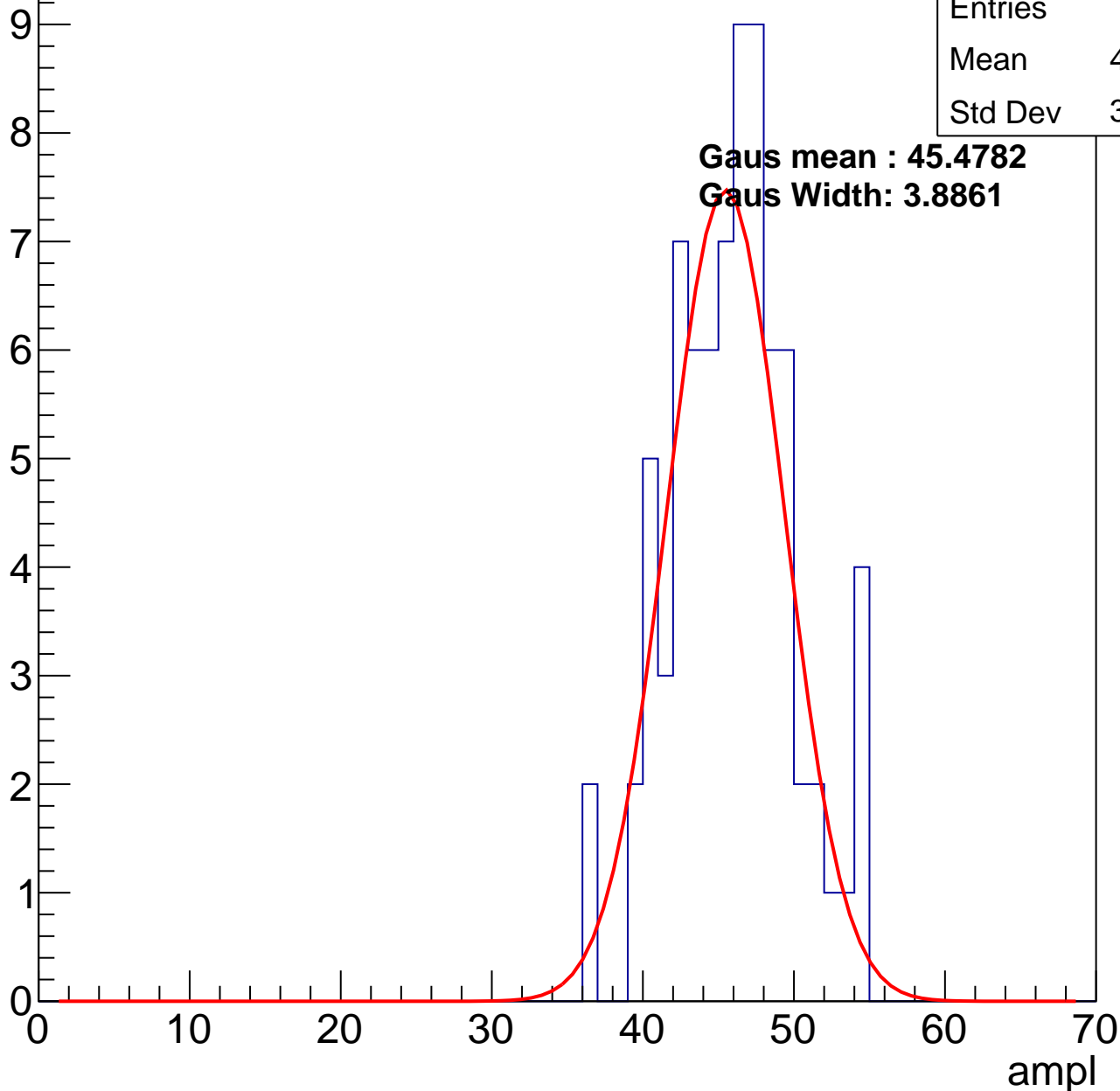
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	45.46
Std Dev	3.983

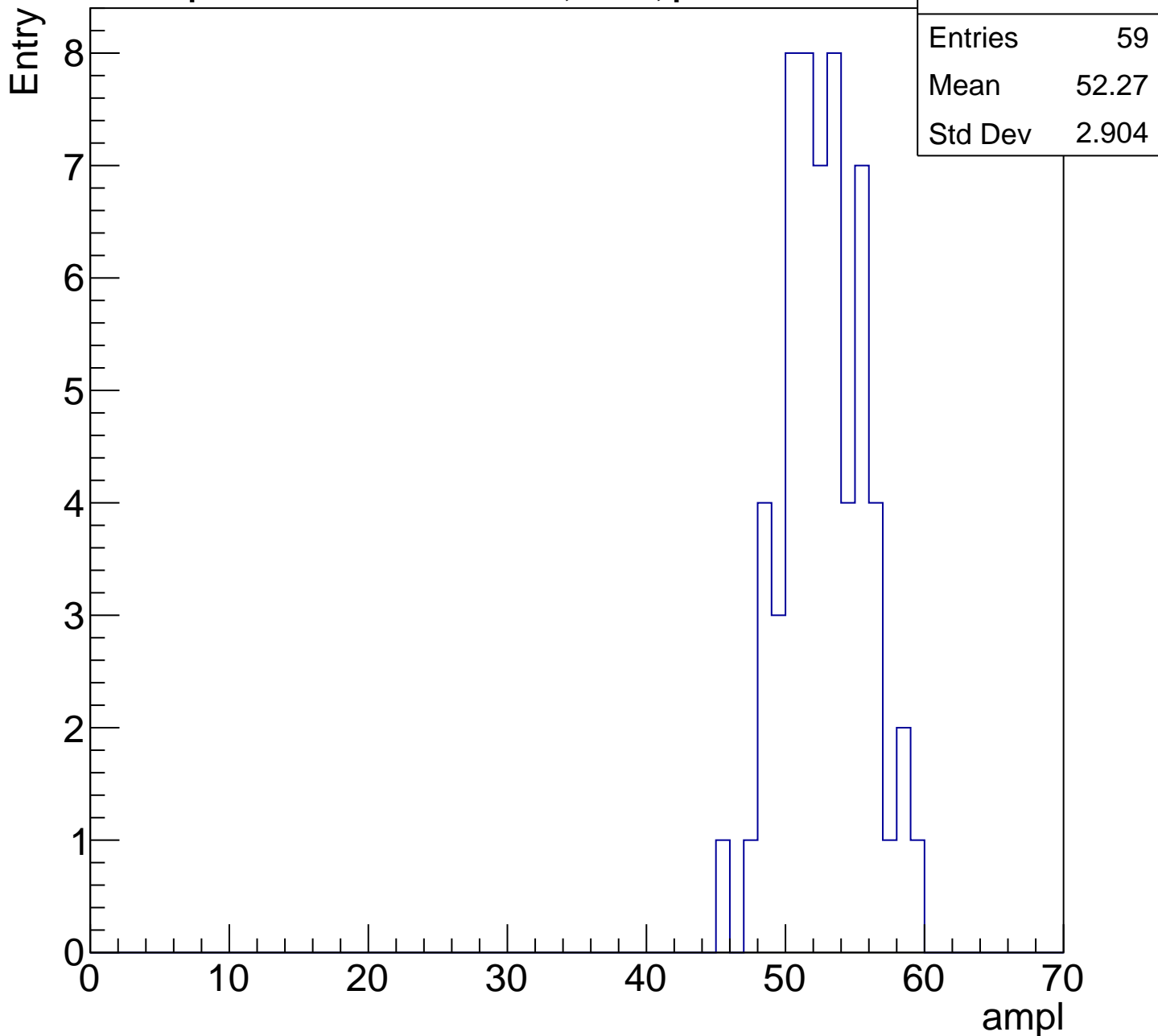
Gaus mean : 45.4782

Gaus Width: 3.8861



B1L101S, U26-ch55, adc3

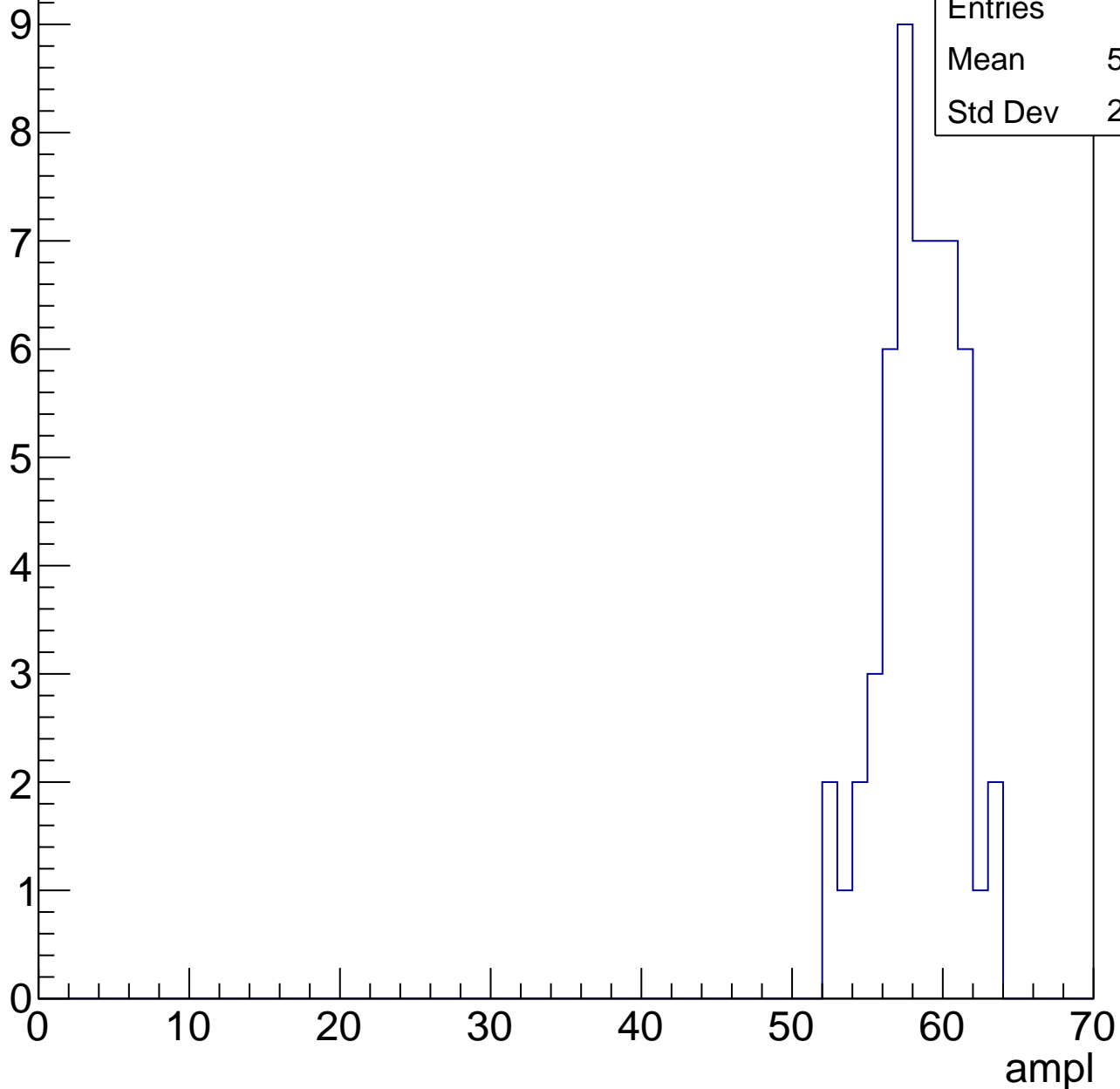
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

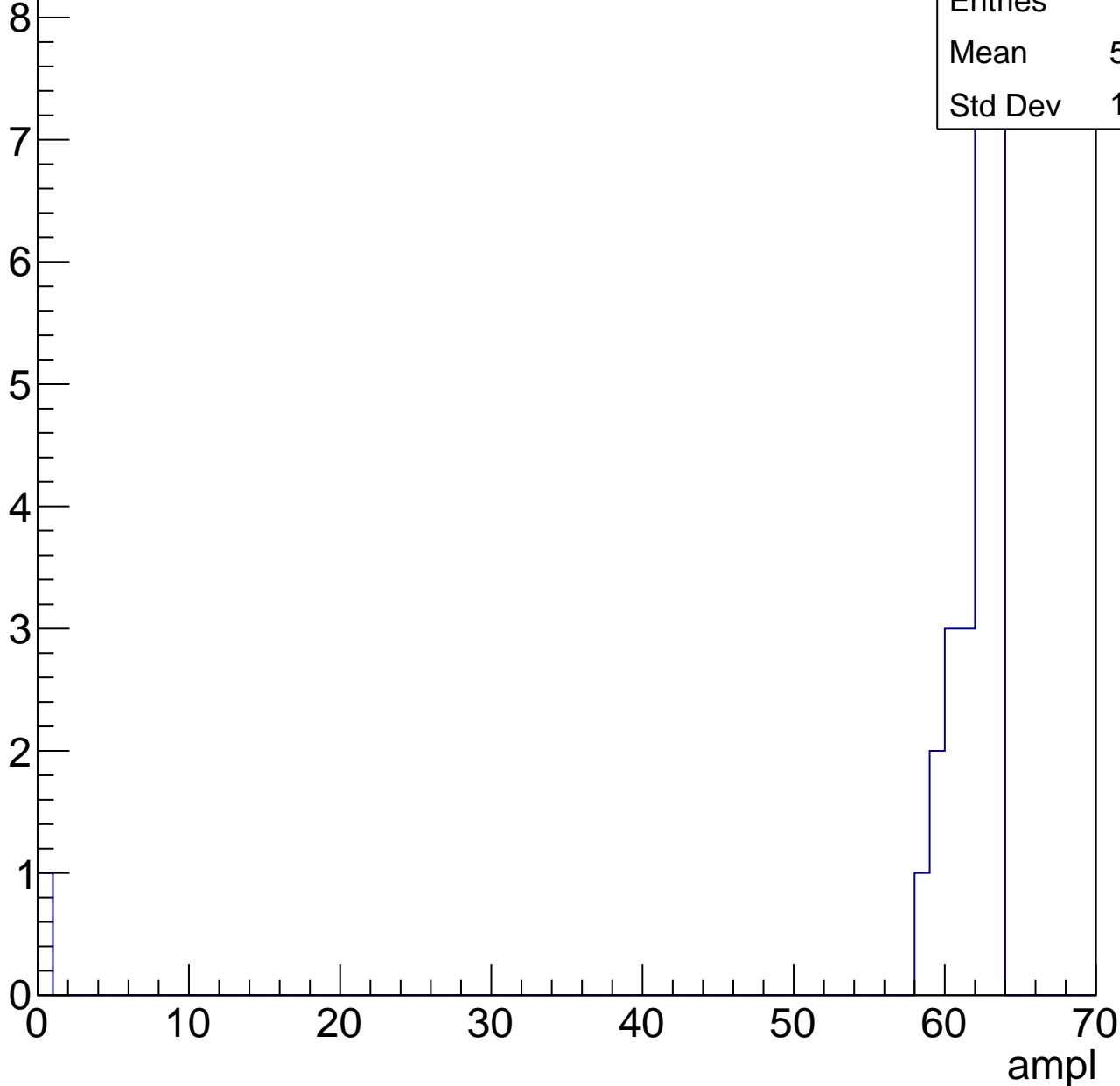


B1L101S, U26-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	59.19
Std Dev	11.92



B1L101S, U26-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch56, adc0

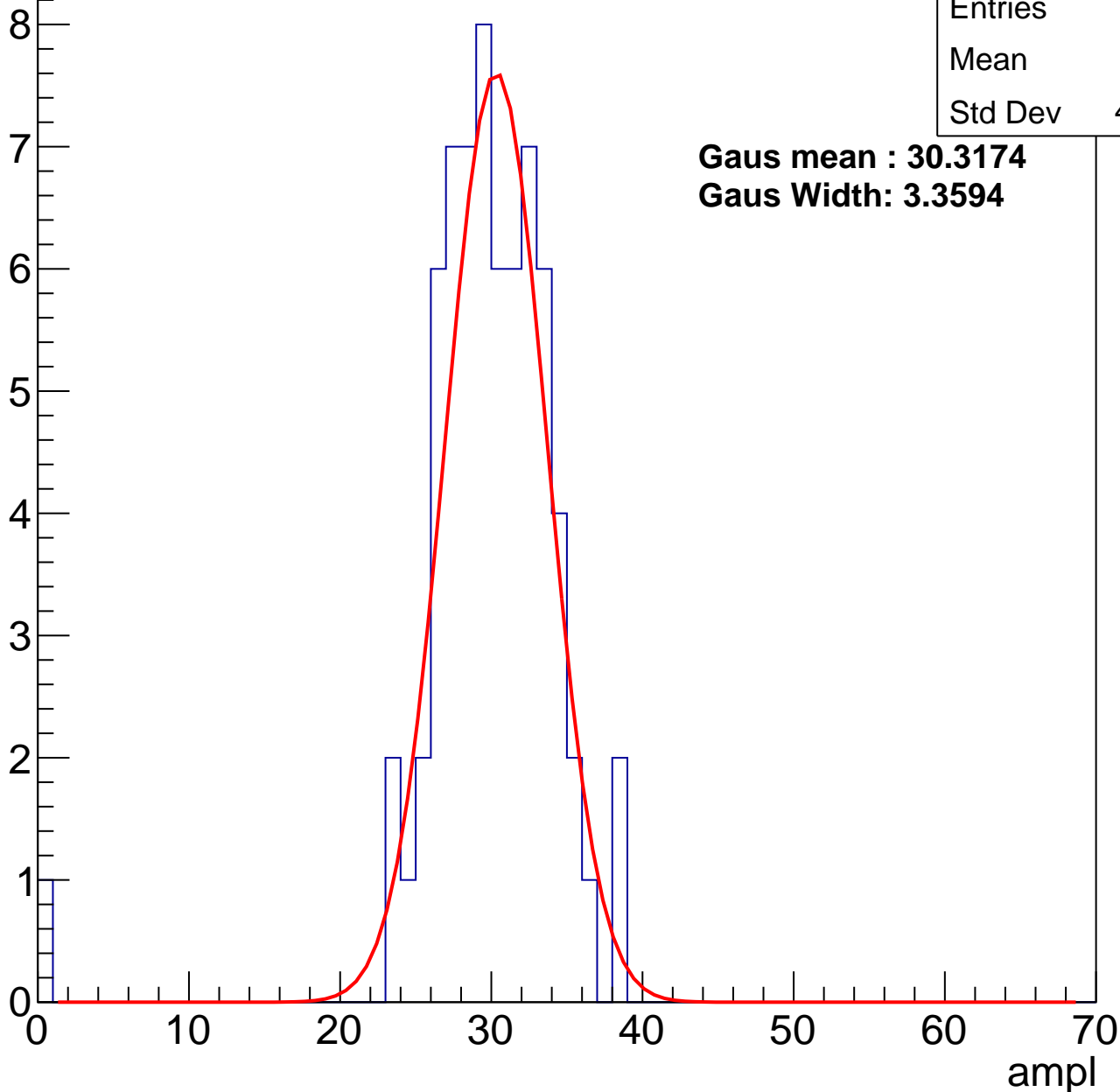
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.4
Std Dev	4.881

Gaus mean : 30.3174

Gaus Width: 3.3594



B1L101S, U26-ch56, adc1

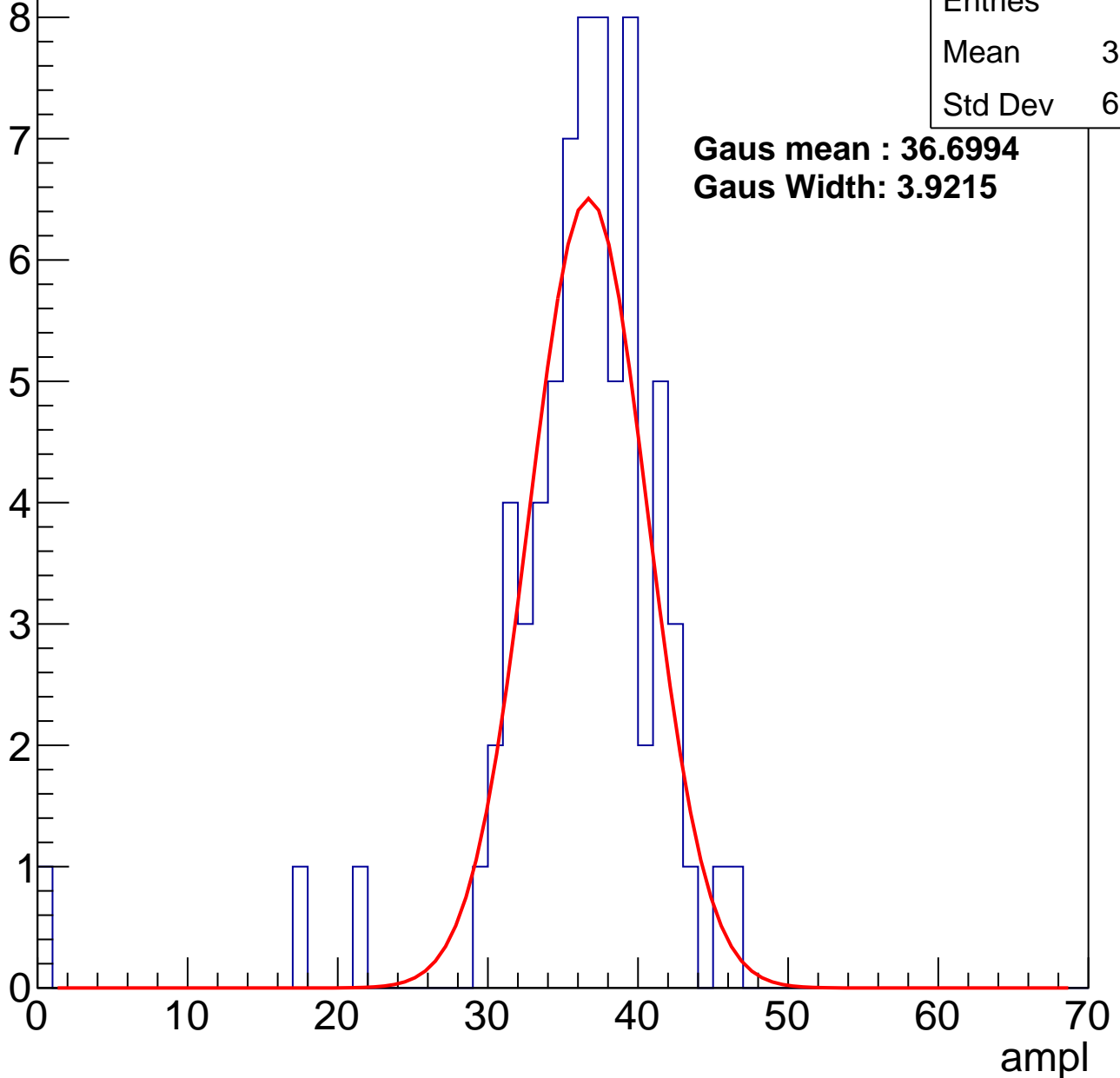
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.56
Std Dev	6.275

Gaus mean : 36.6994

Gaus Width: 3.9215



B1L101S, U26-ch56, adc2

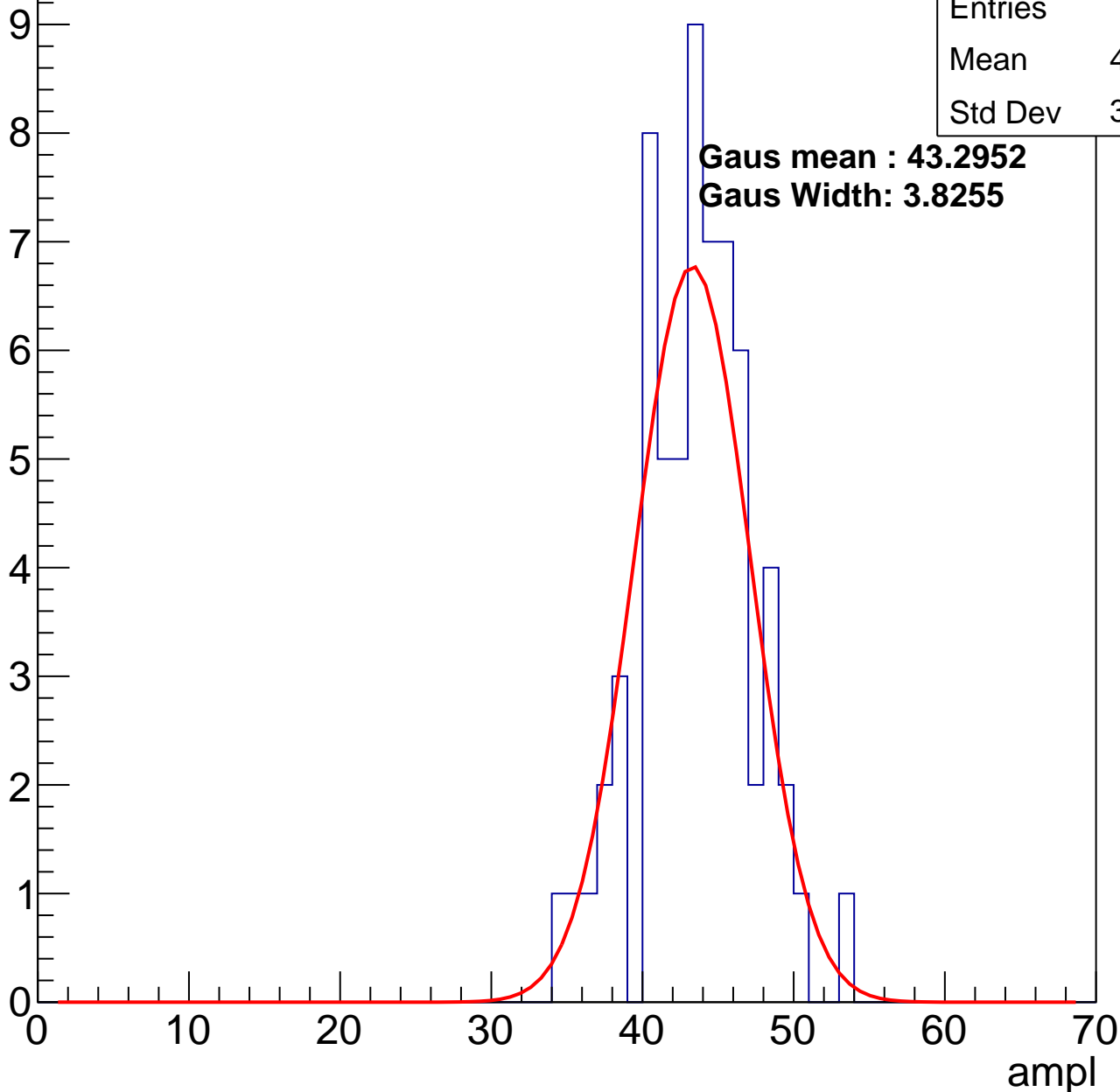
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.09
Std Dev	3.678

Gaus mean : 43.2952

Gaus Width: 3.8255

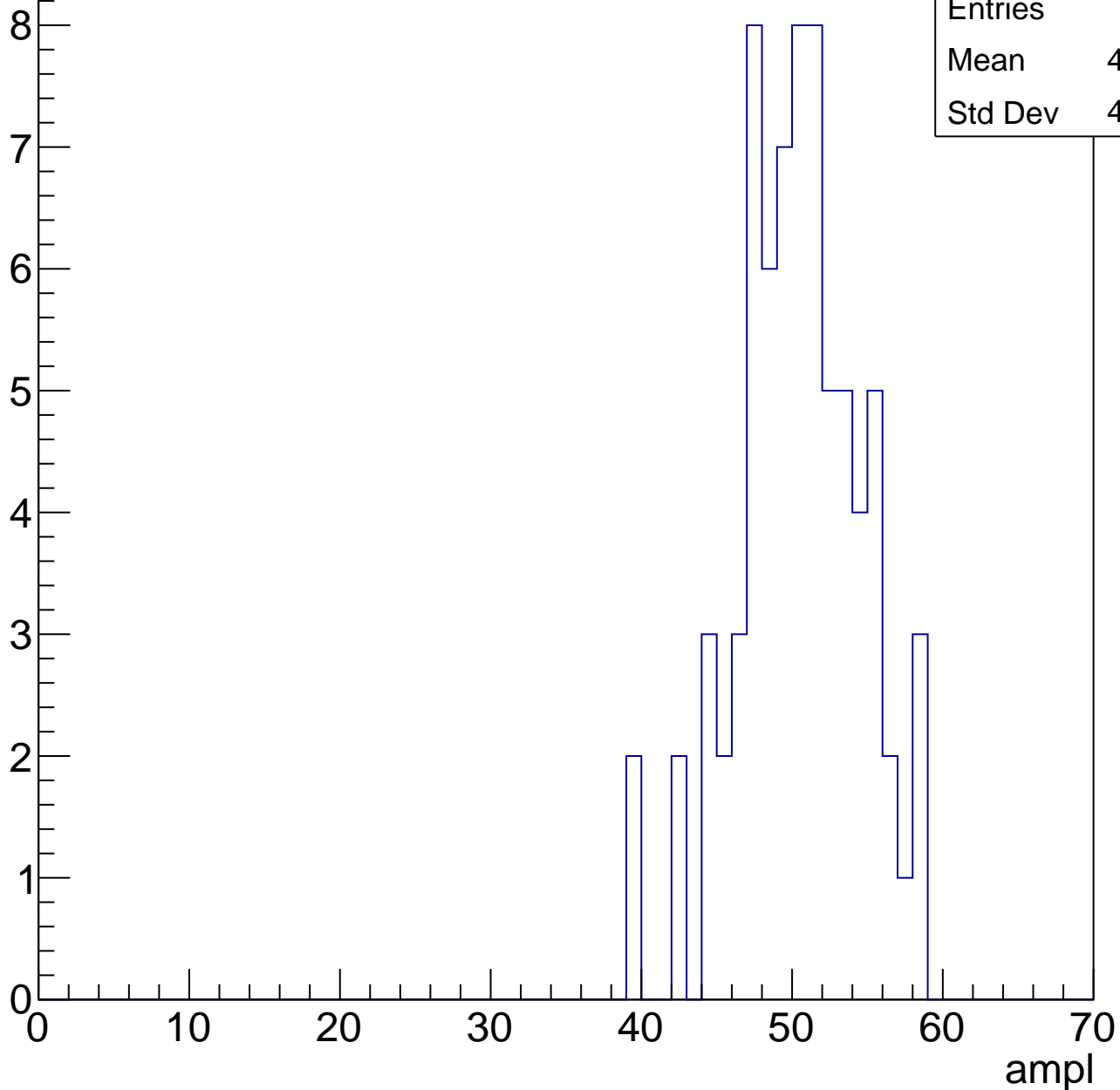


B1L101S, U26-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

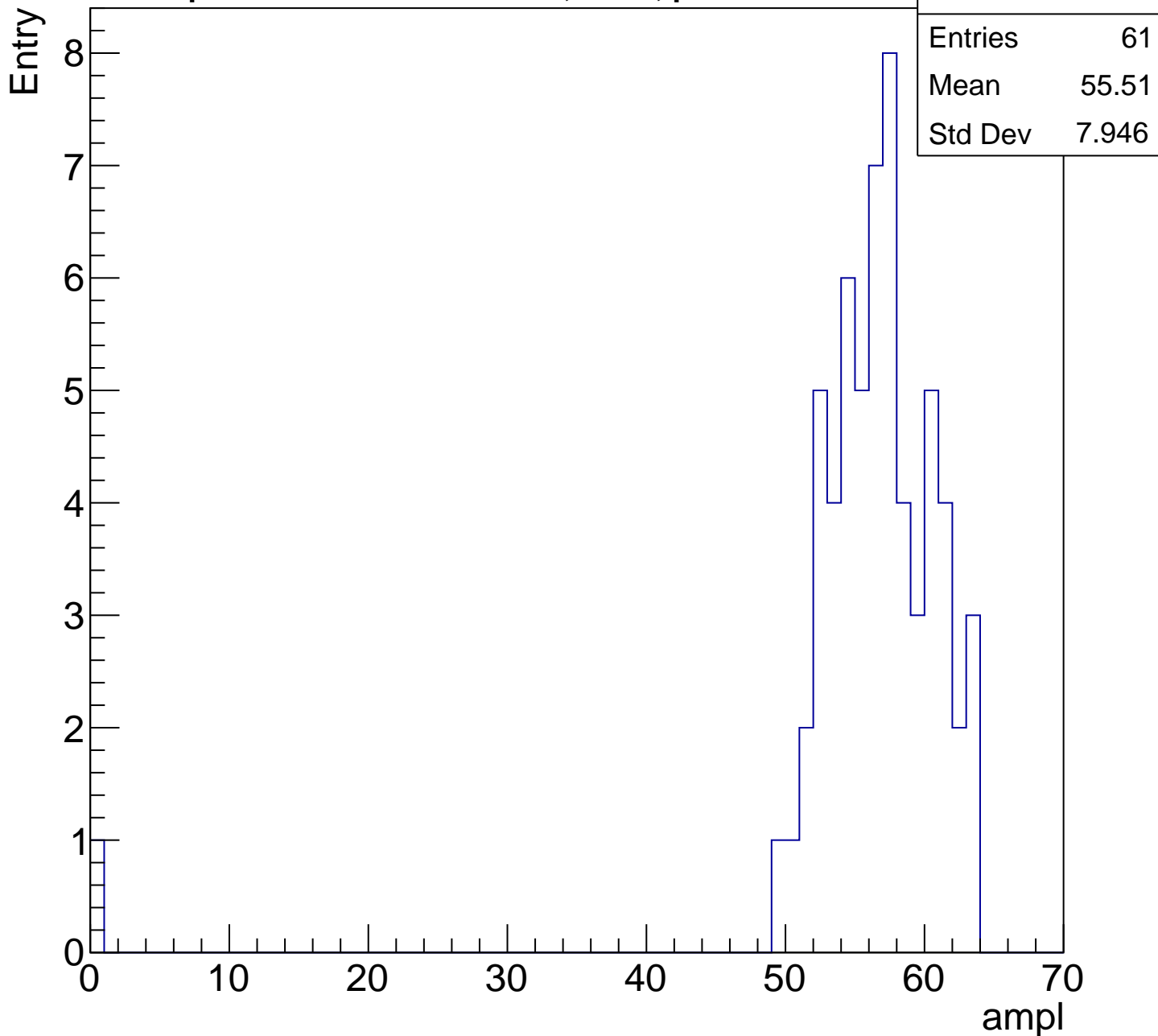
Entry

Entries	74
Mean	49.95
Std Dev	4.116



B1L101S, U26-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 34

Mean 60.35

Std Dev 1.797

ampl

0

10

20

30

40

50

60

70

B1L101S, U26-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch57, adc0

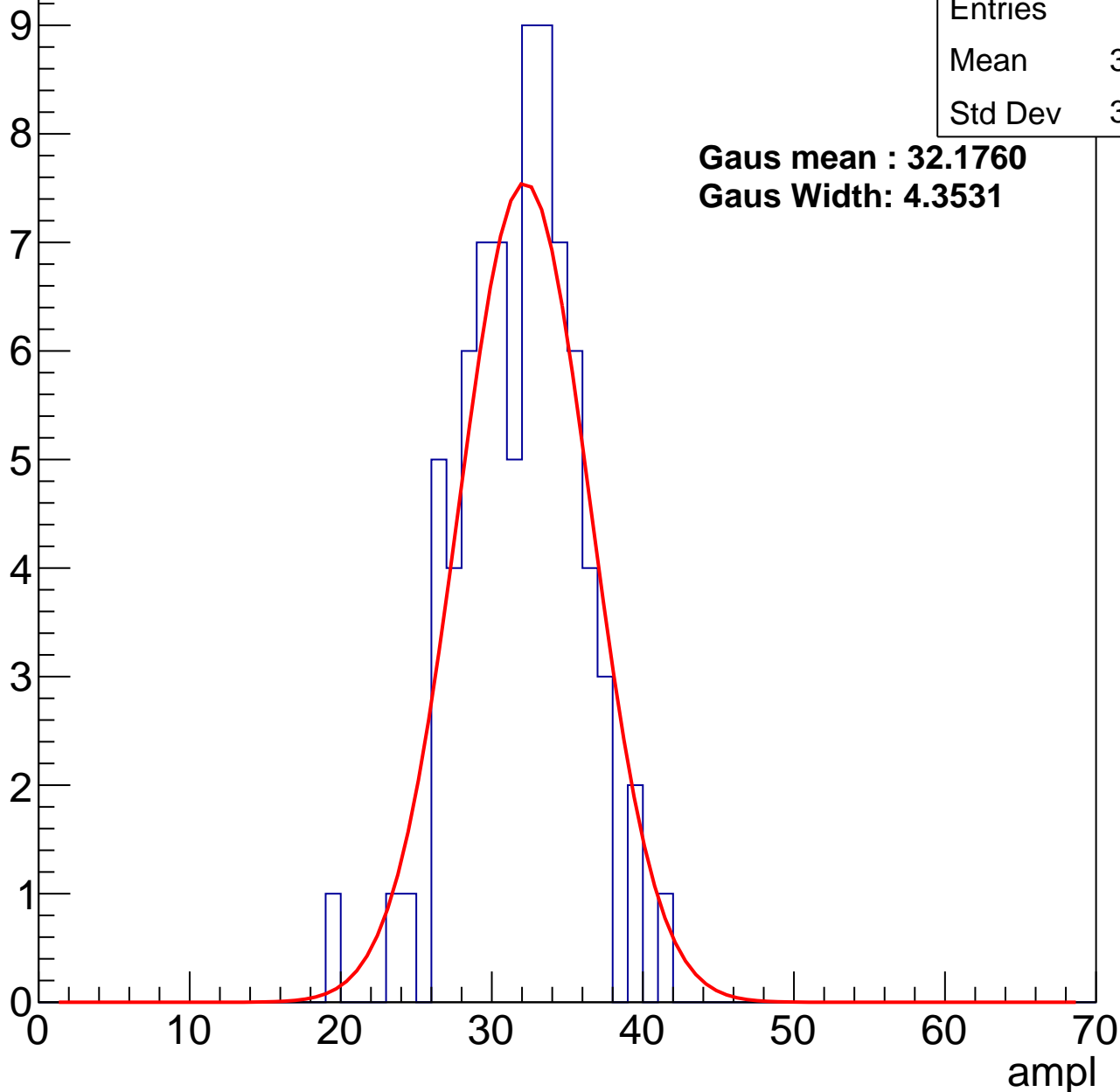
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	31.37
Std Dev	3.863

Gaus mean : 32.1760

Gaus Width: 4.3531



B1L101S, U26-ch57, adc1

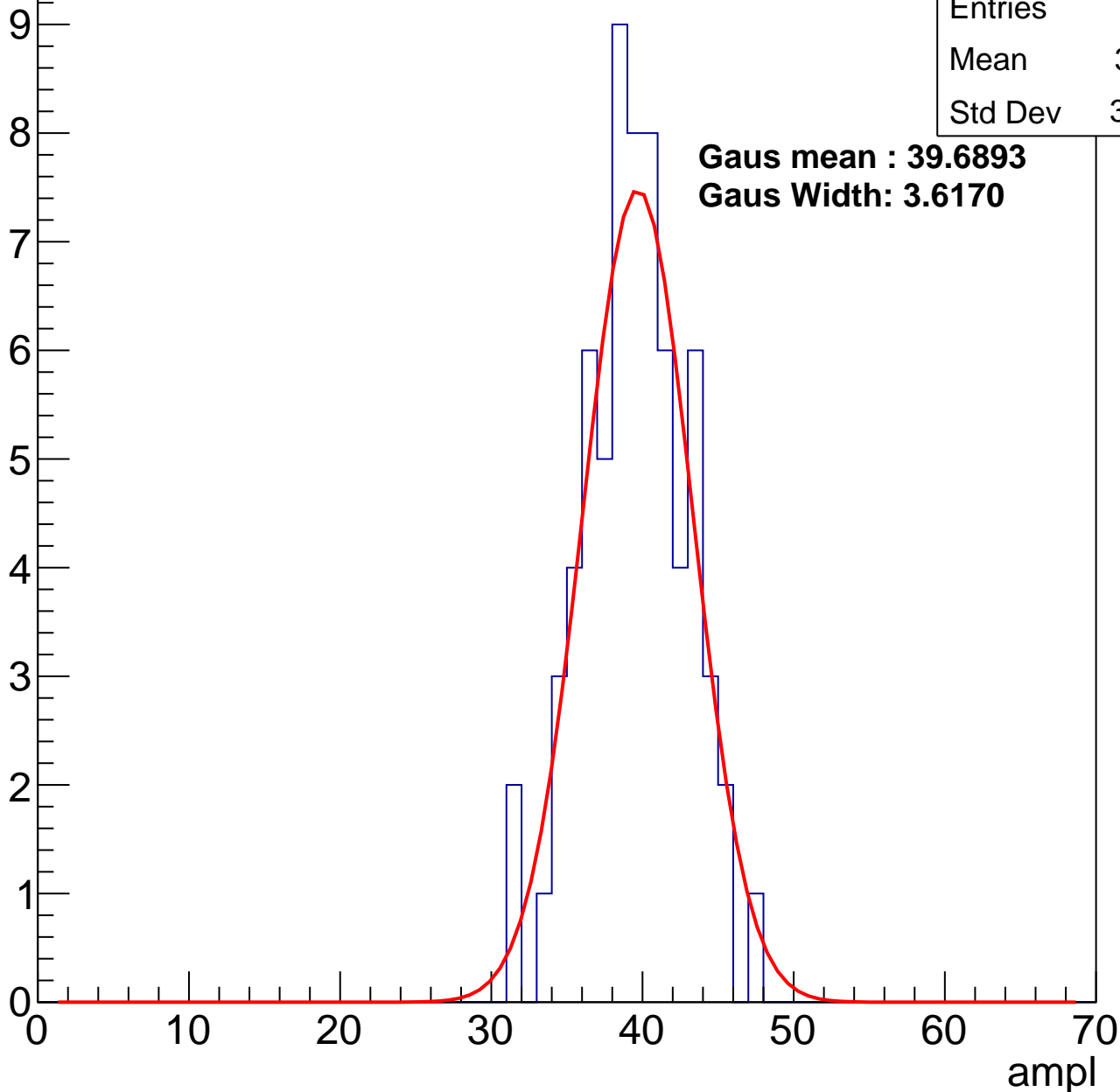
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	39.01
Std Dev	3.345

Gaus mean : 39.6893

Gaus Width: 3.6170



B1L101S, U26-ch57, adc2

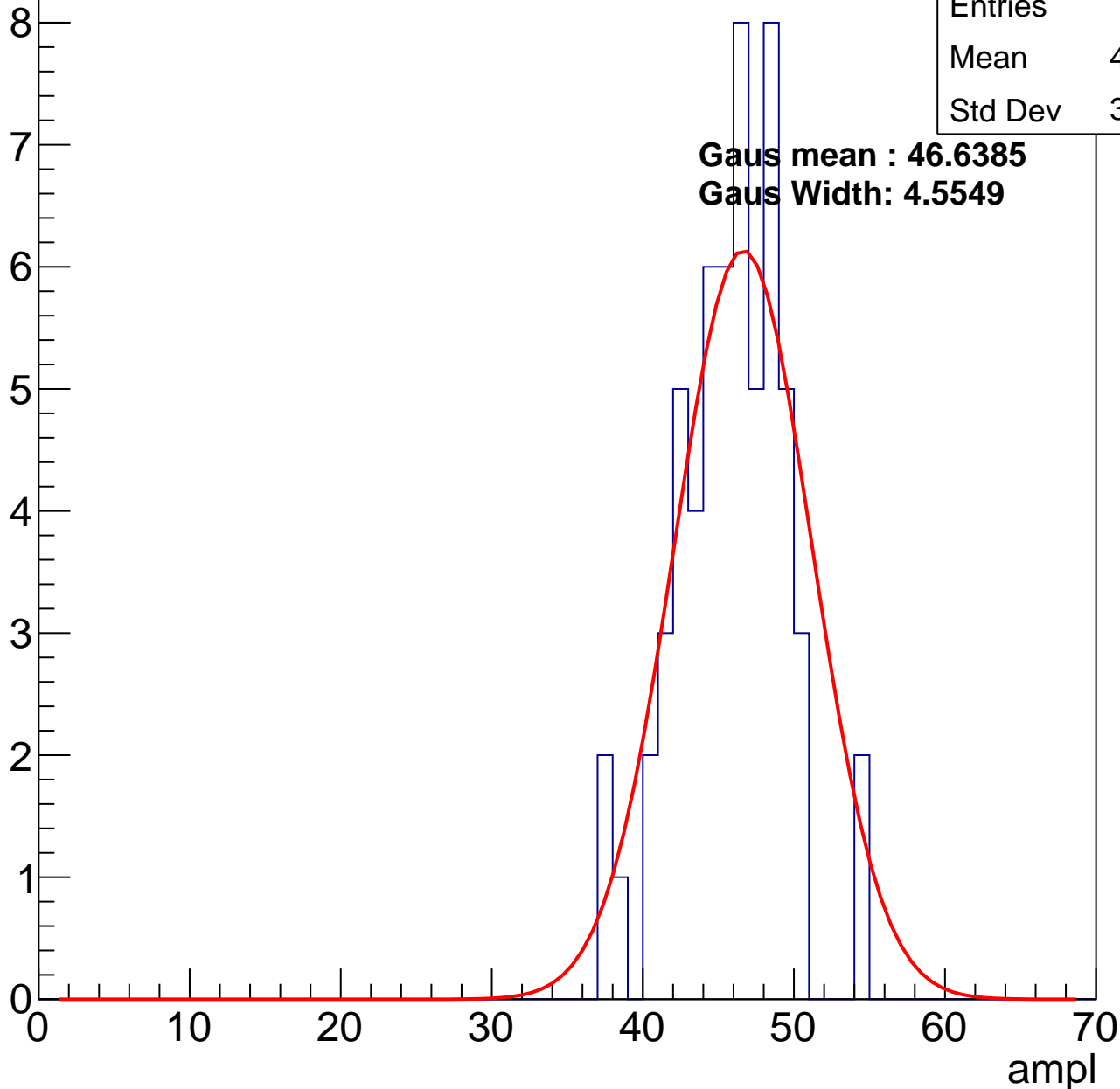
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	45.35
Std Dev	3.539

Gaus mean : 46.6385

Gaus Width: 4.5549

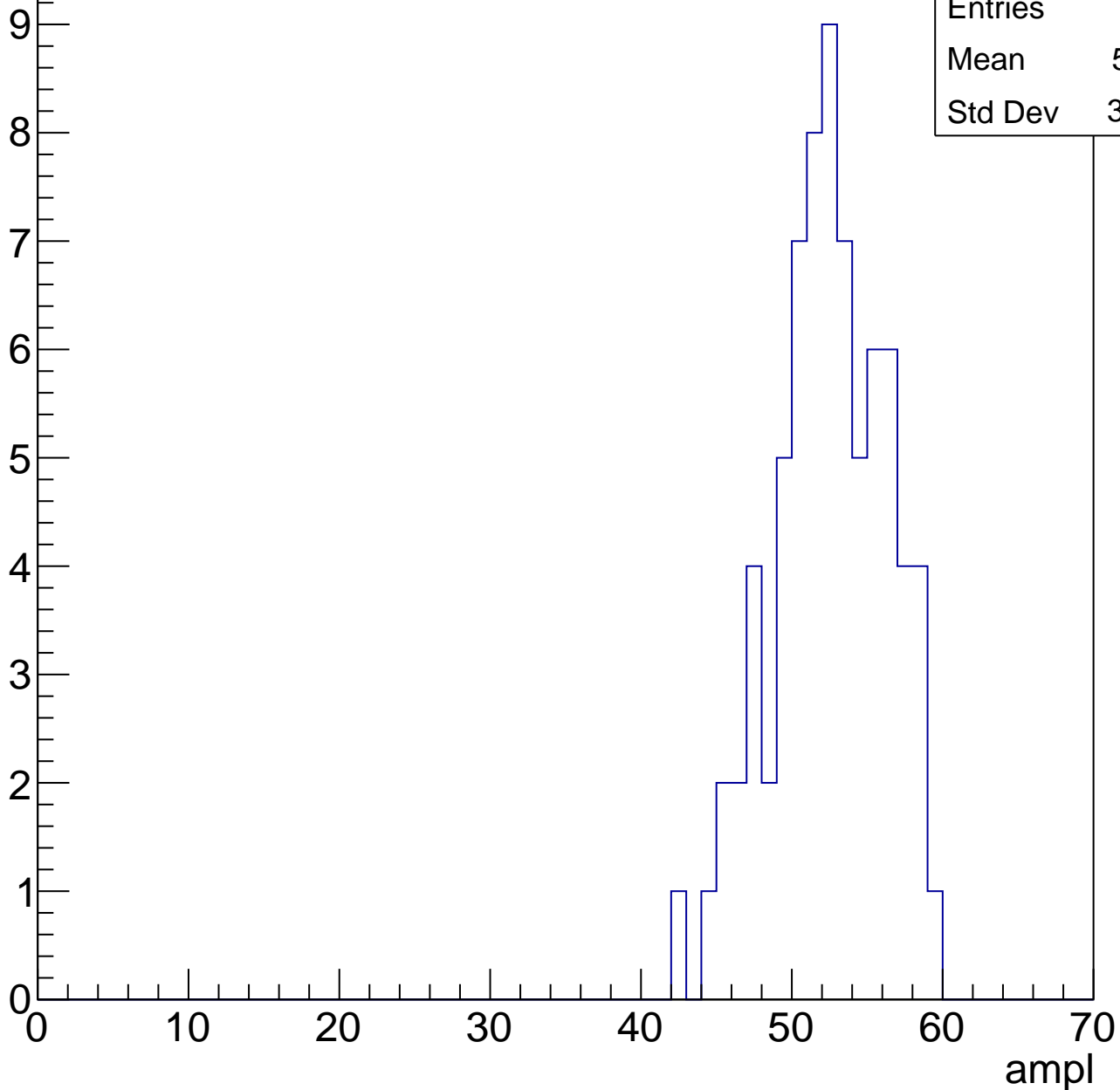


B1L101S, U26-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

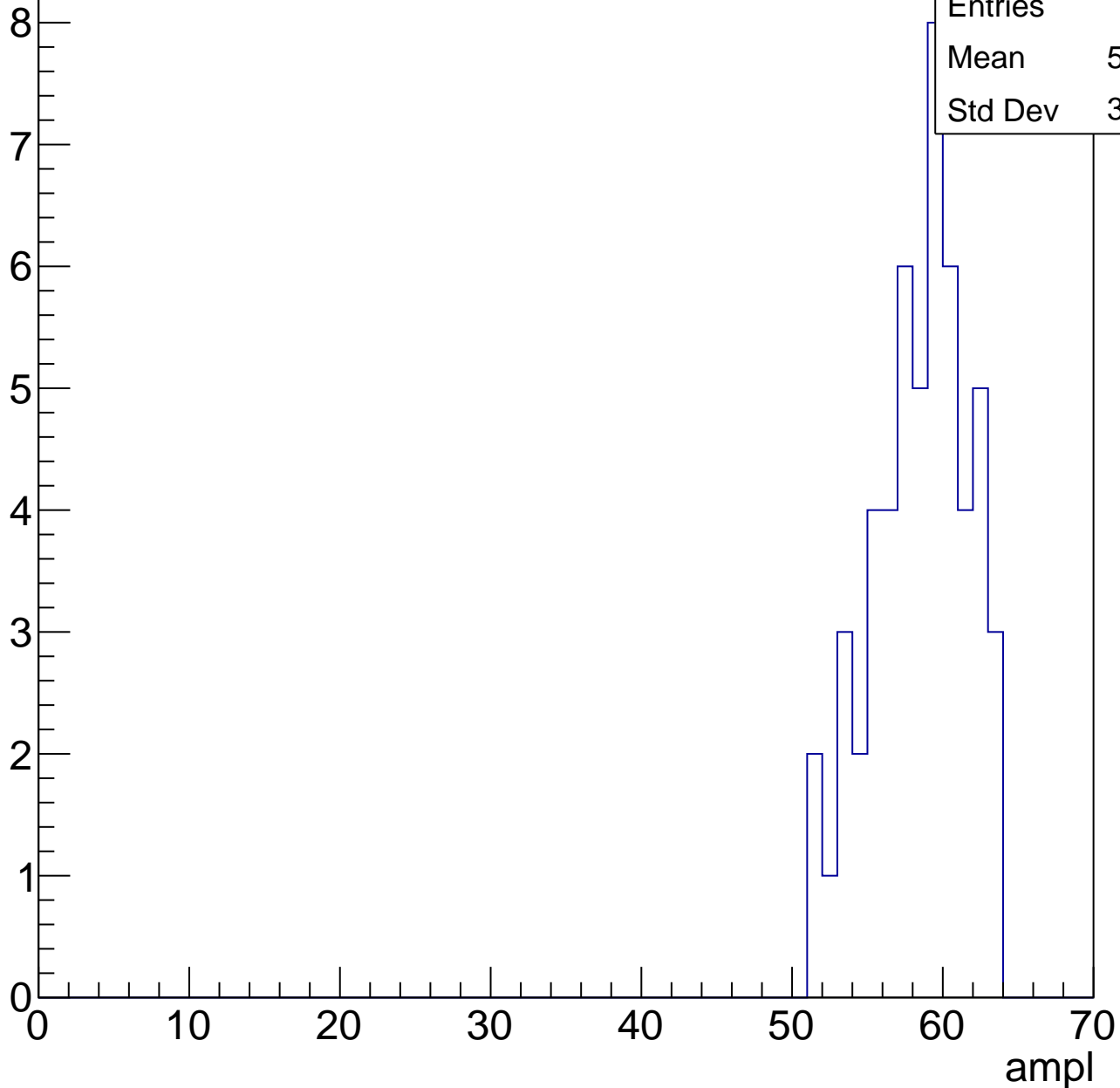
Entries	74
Mean	52.01
Std Dev	3.707



B1L101S, U26-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

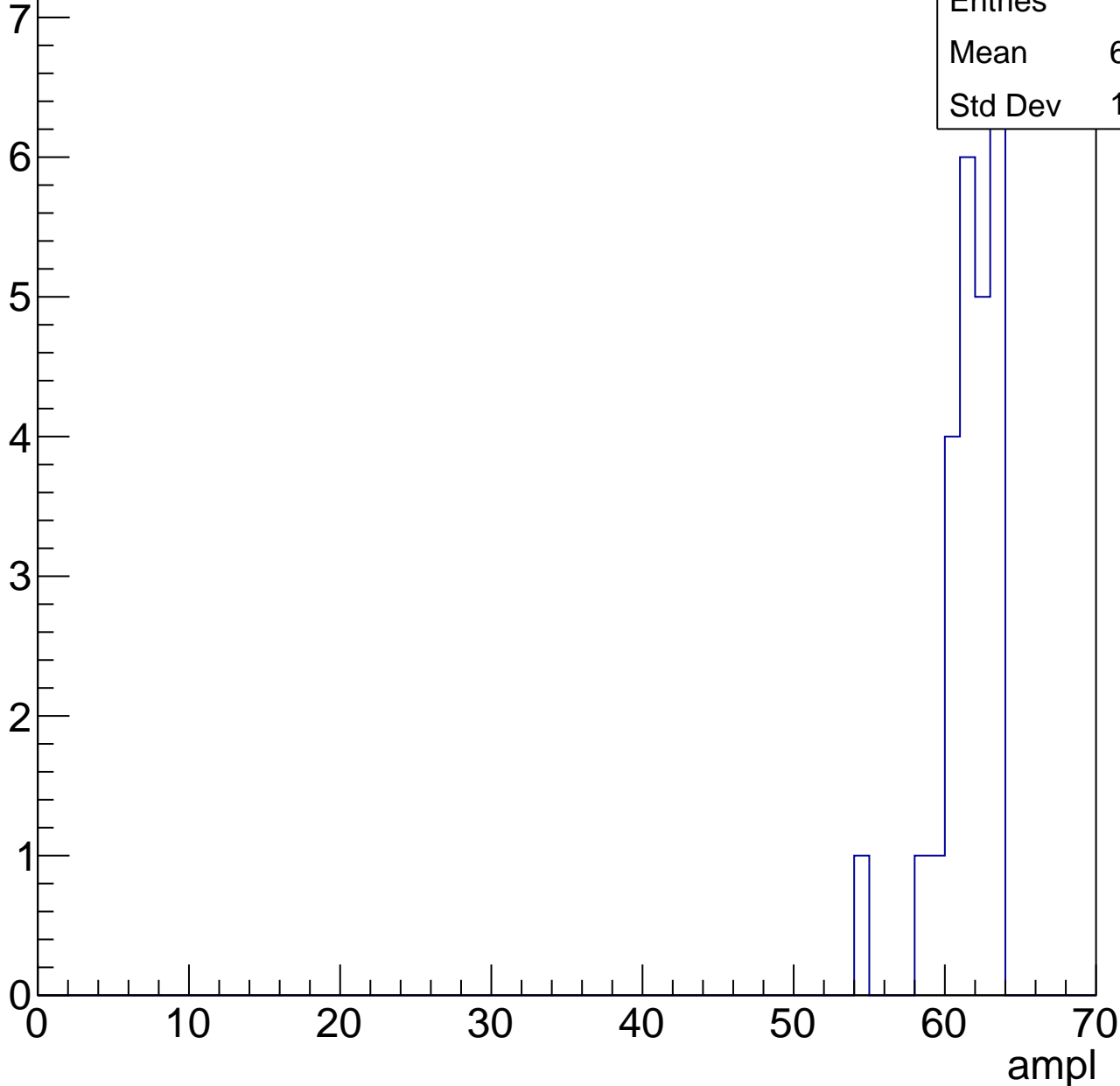


B1L101S, U26-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	61.12
Std Dev	1.986



B1L101S, U26-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch58, adc0

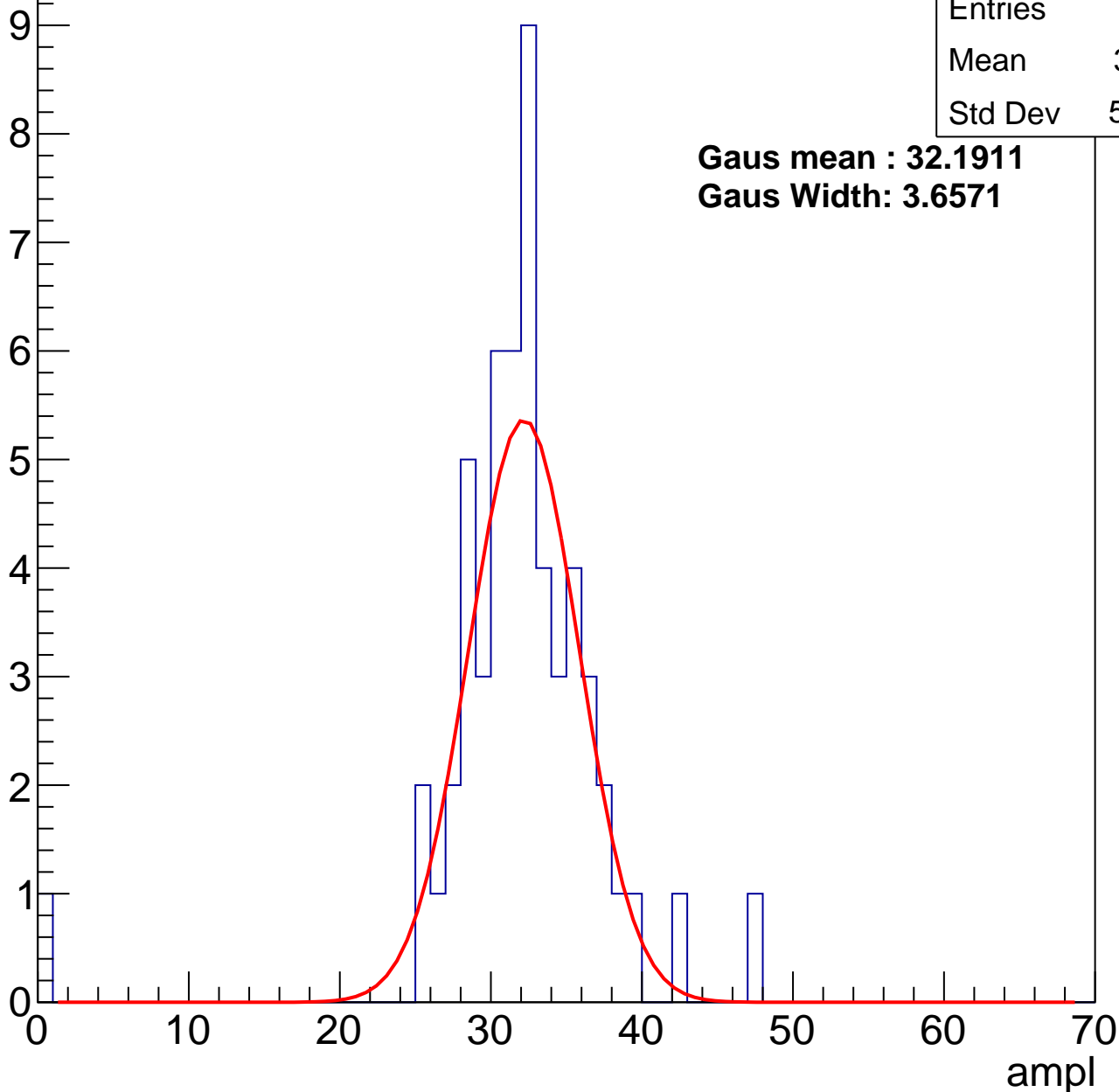
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	31.51
Std Dev	5.865

Gaus mean : 32.1911

Gaus Width: 3.6571



B1L101S, U26-ch58, adc1

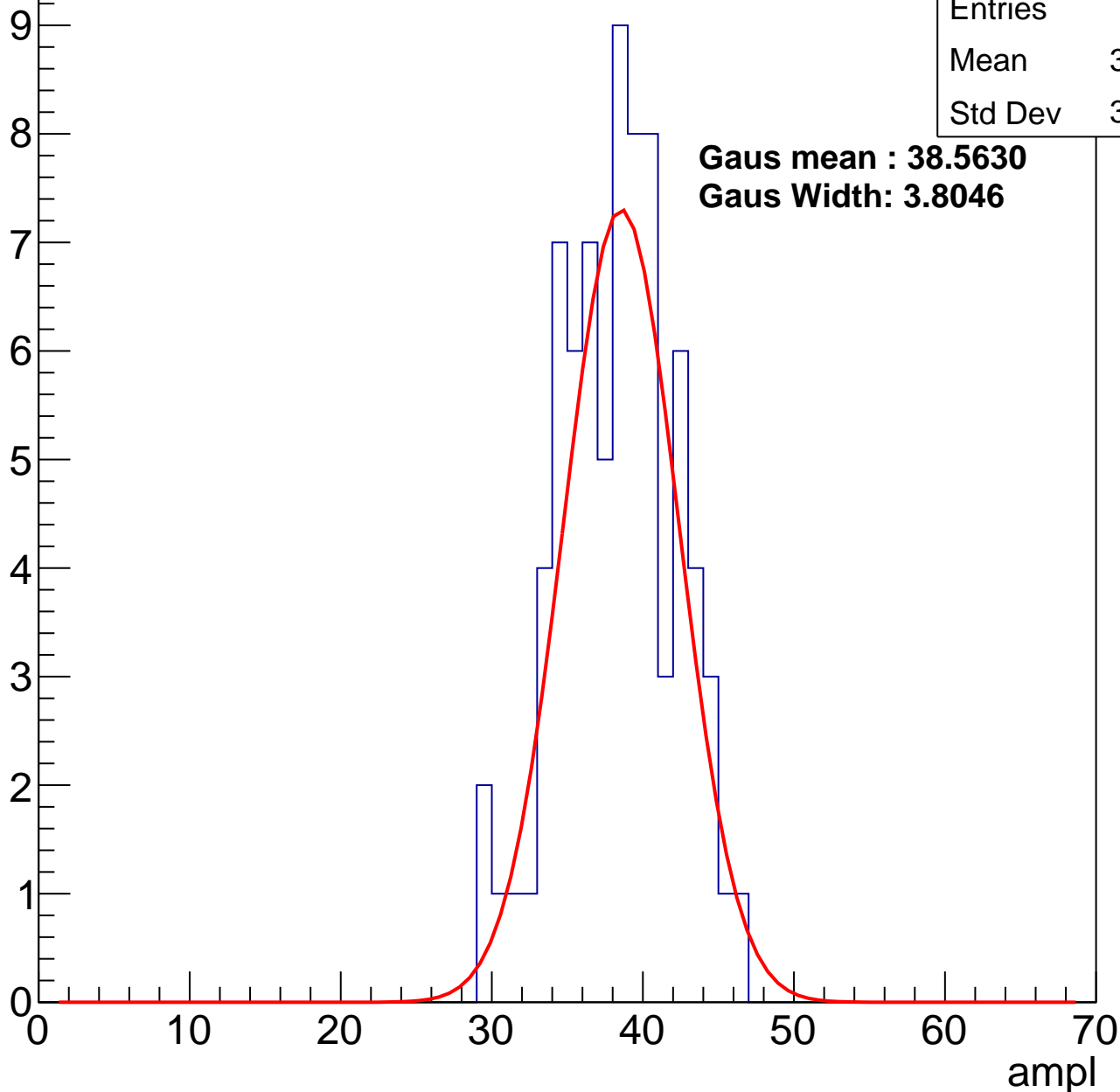
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.82
Std Dev	3.765

Gaus mean : 38.5630

Gaus Width: 3.8046



B1L101S, U26-ch58, adc2

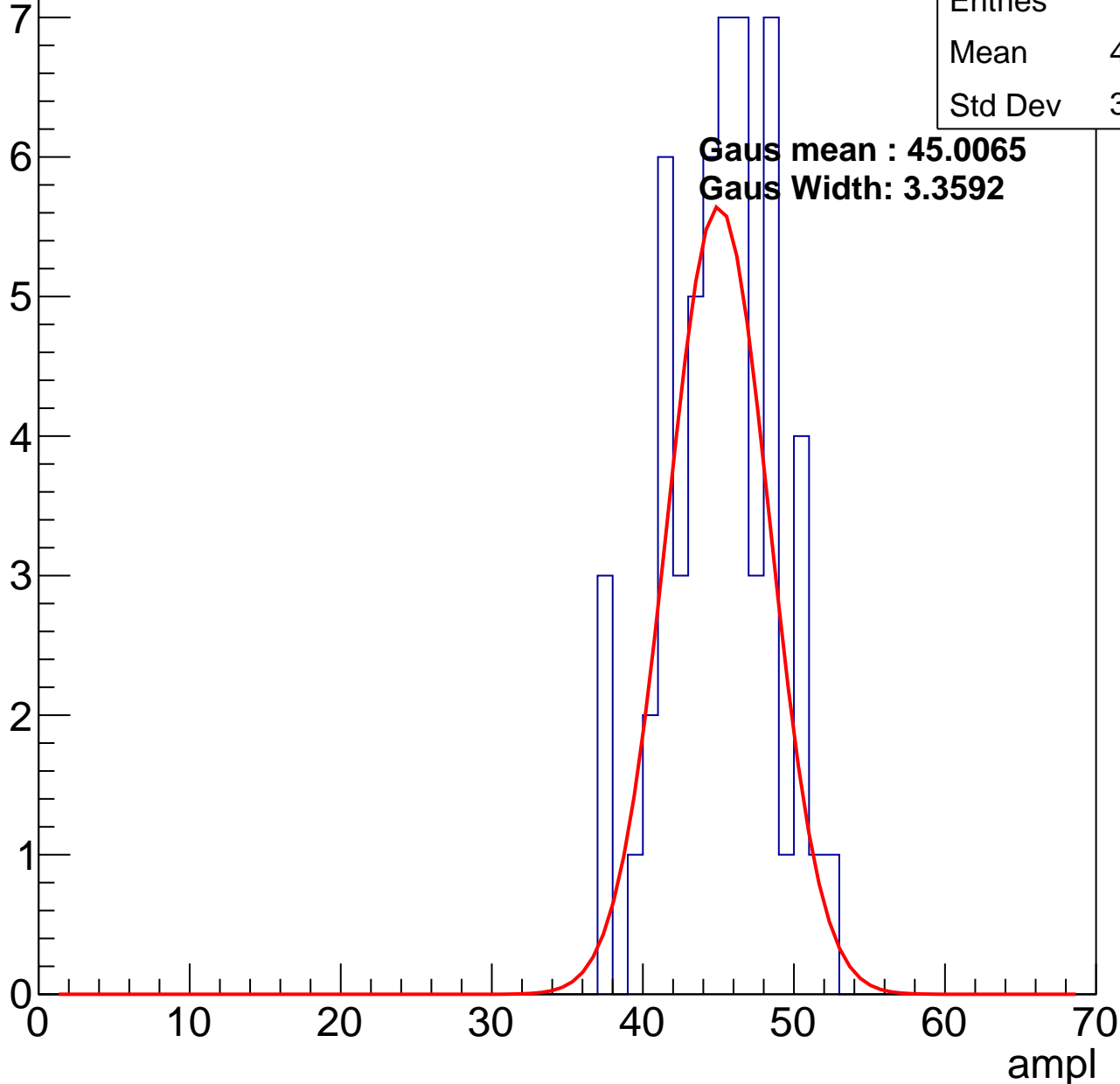
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.68
Std Dev	3.515

Gaus mean : 45.0065

Gaus Width: 3.3592

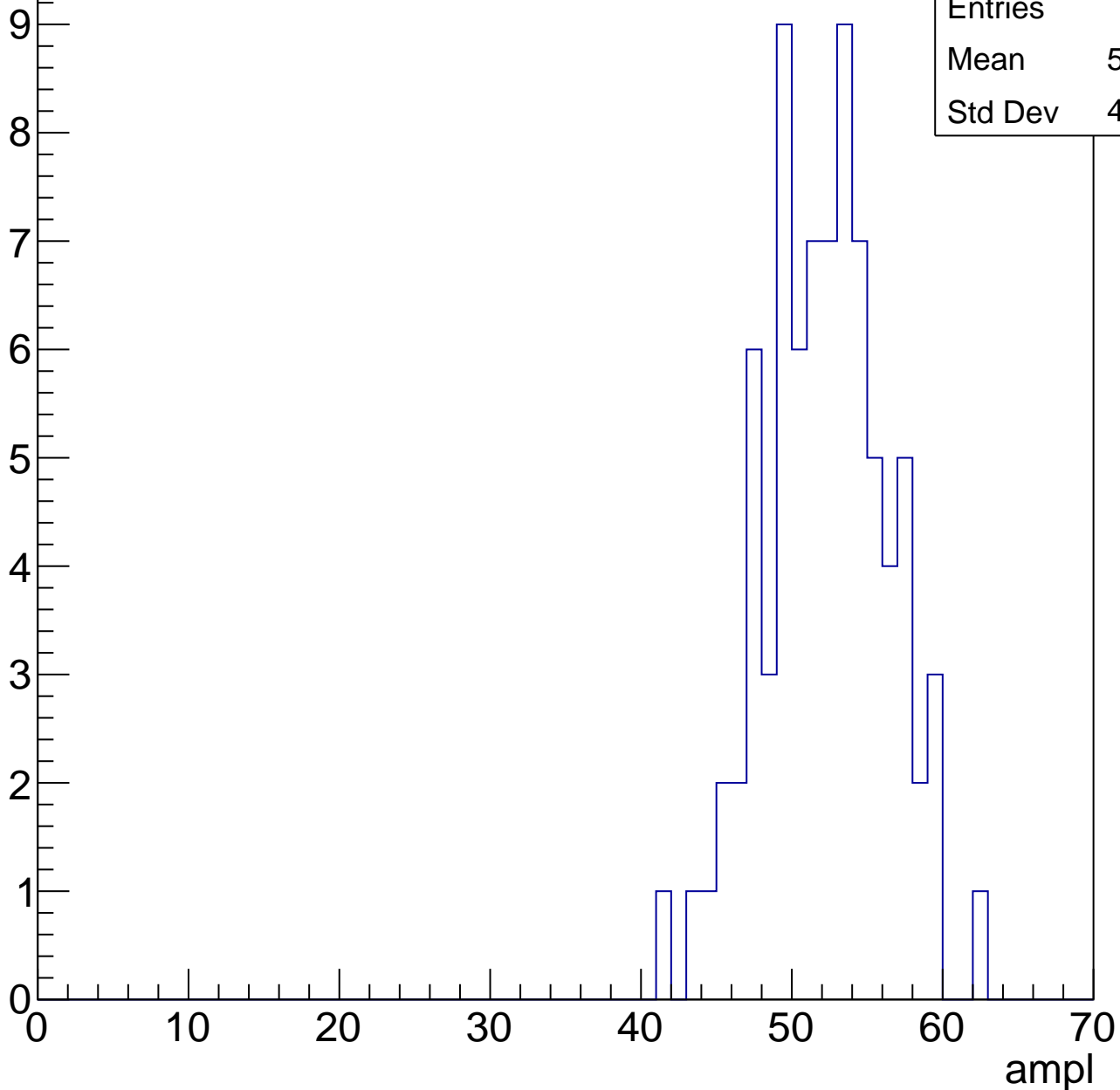


B1L101S, U26-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

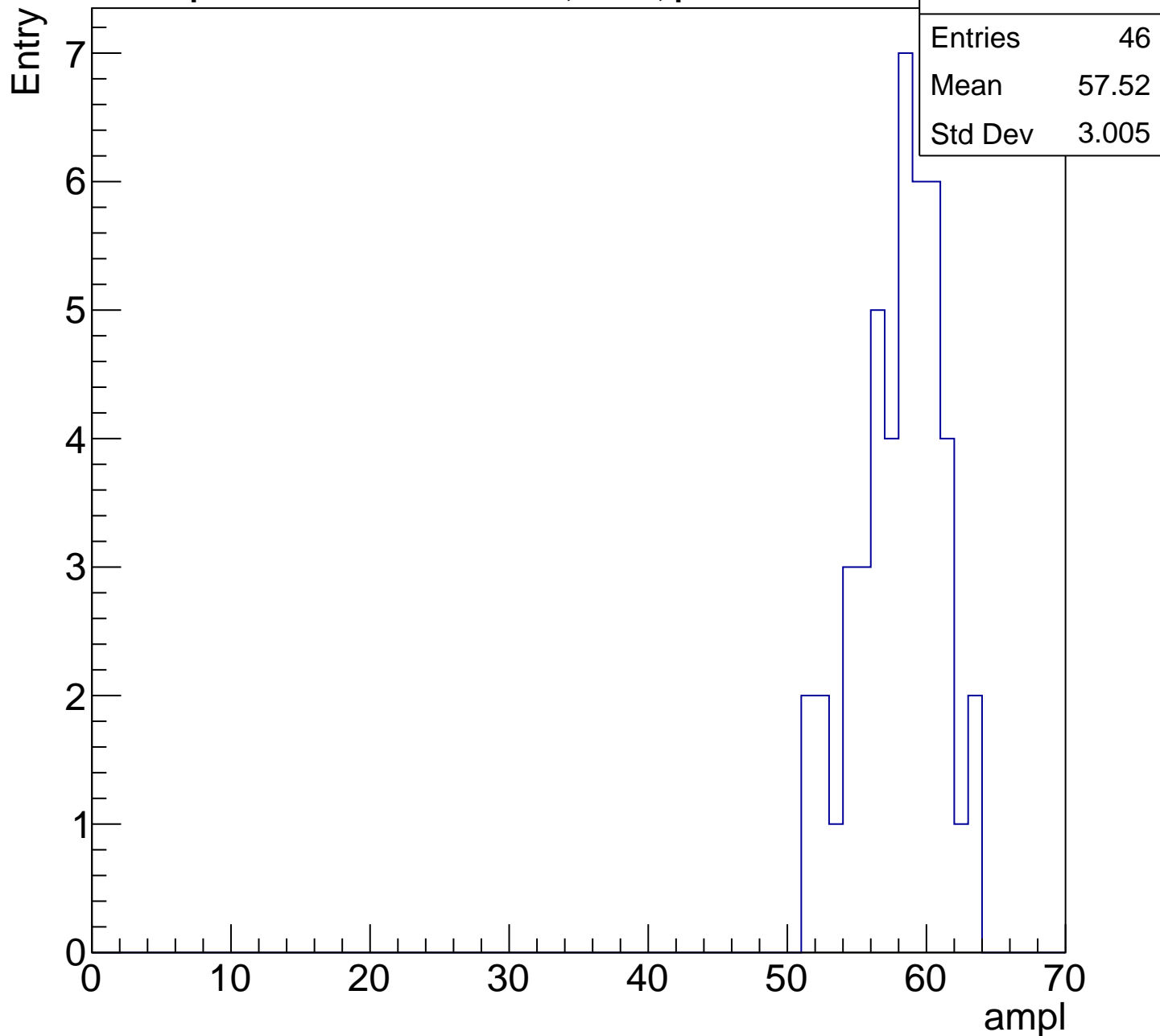
Entry

Entries	81
Mean	51.75
Std Dev	4.054



B1L101S, U26-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

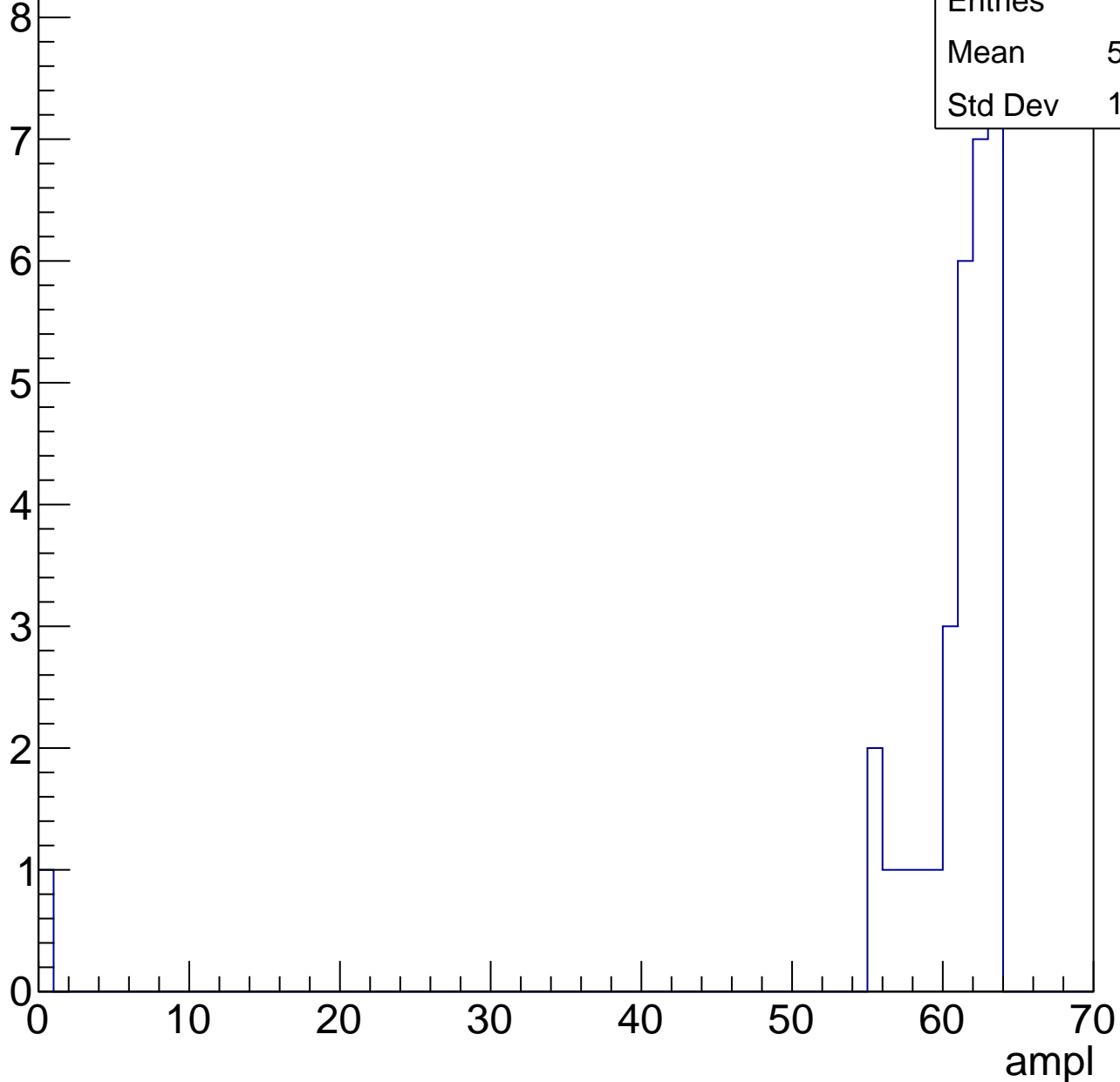


B1L101S, U26-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.84
Std Dev	10.99



B1L101S, U26-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch59, adc0

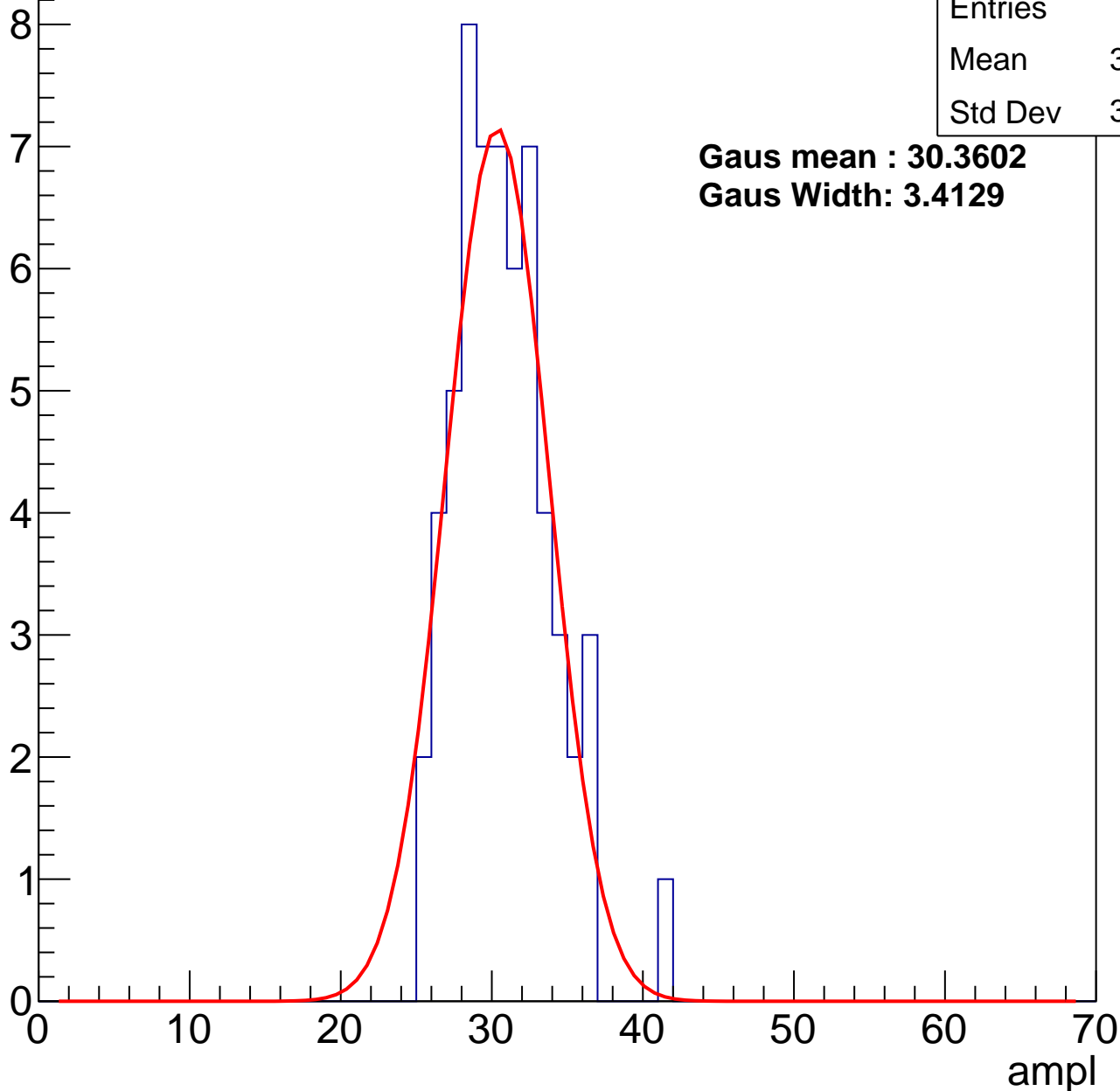
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	30.32
Std Dev	3.159

Gaus mean : 30.3602

Gaus Width: 3.4129



B1L101S, U26-ch59, adc1

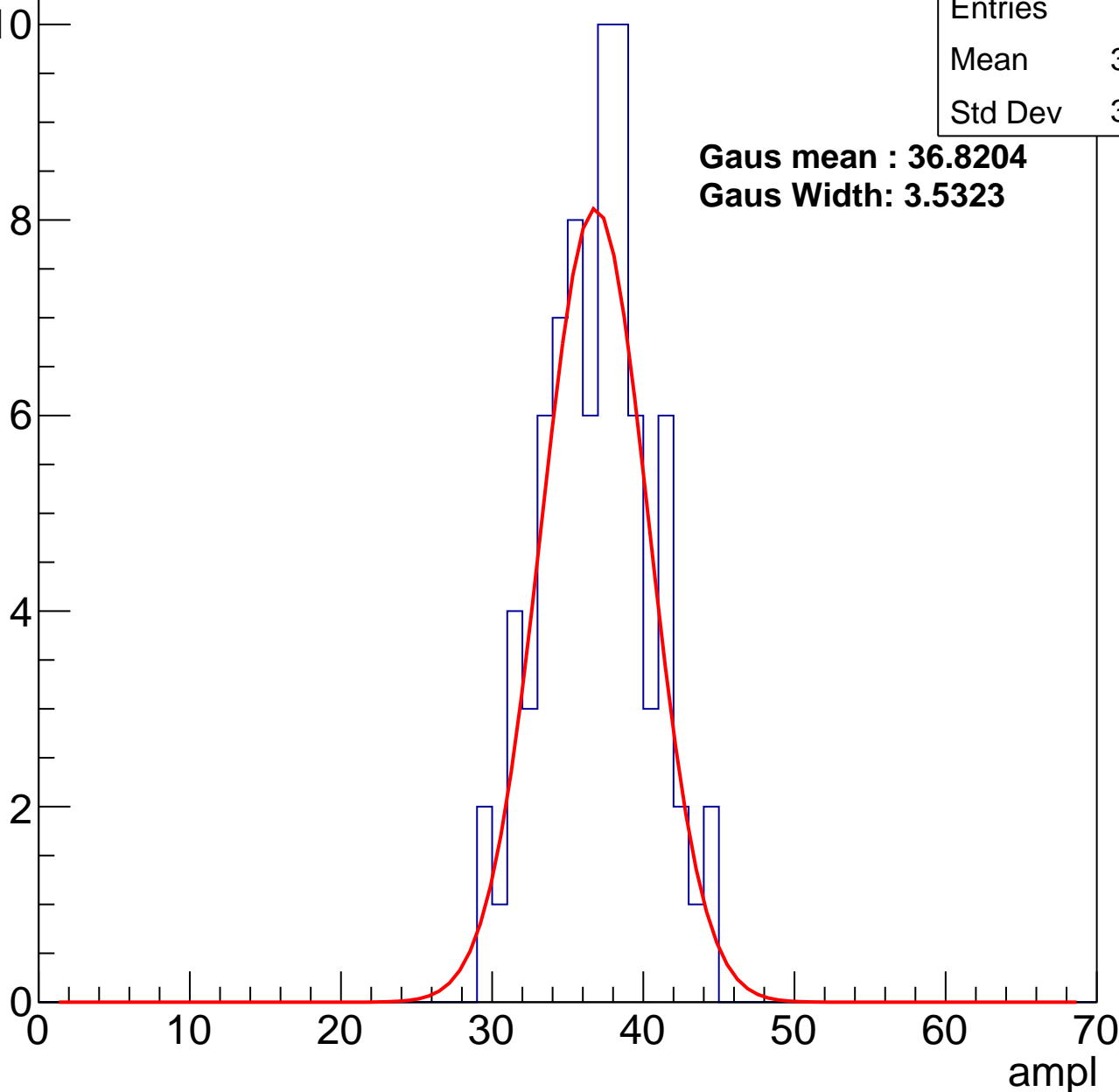
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.43
Std Dev	3.436

Gaus mean : 36.8204

Gaus Width: 3.5323



B1L101S, U26-ch59, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	44.59
Std Dev	3.279

Gaus mean : 44.9299

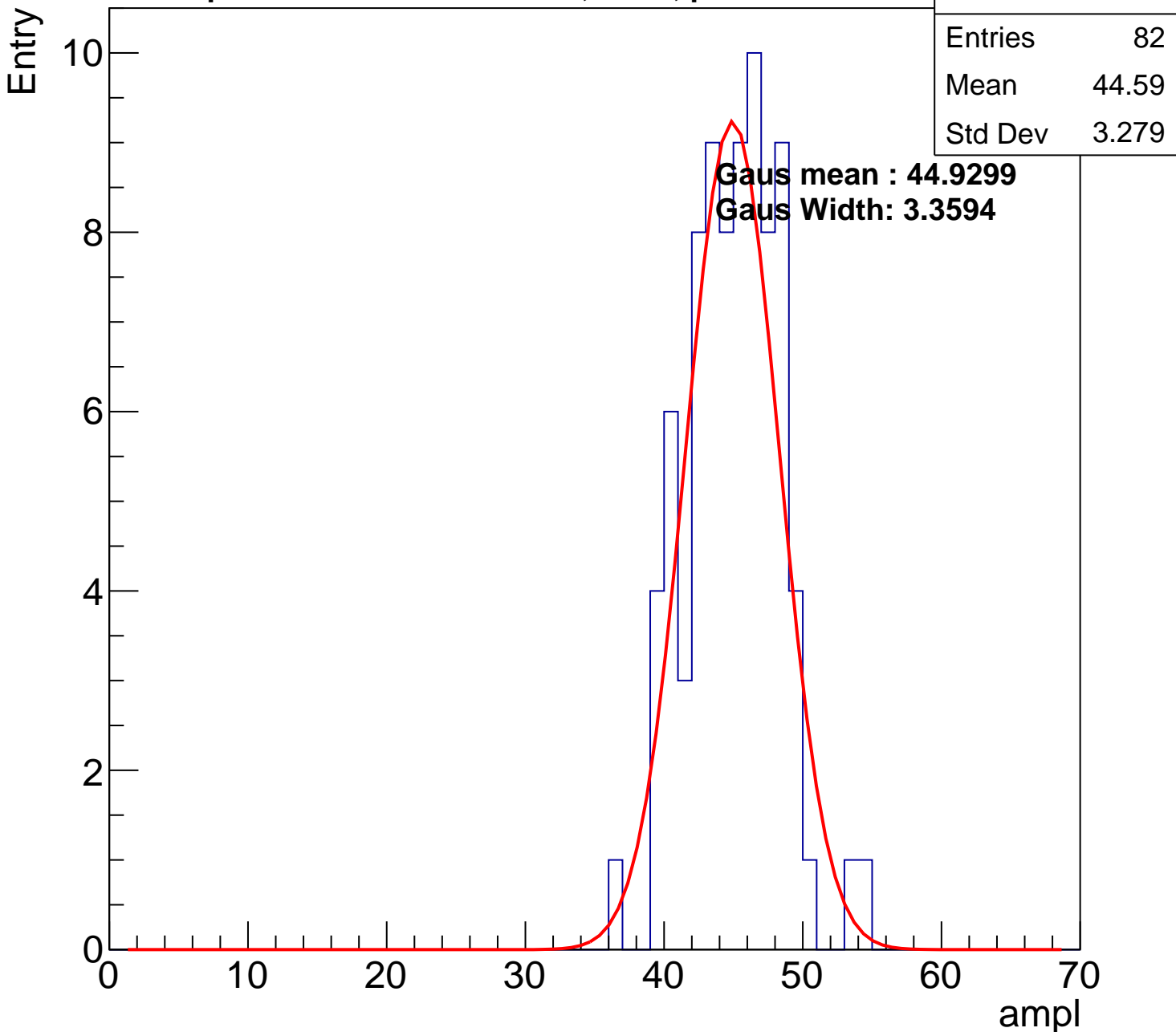
Gaus Width: 3.3594

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	58
Mean	51.93
Std Dev	3.337

Entry

10

8

6

4

2

0

0

10

20

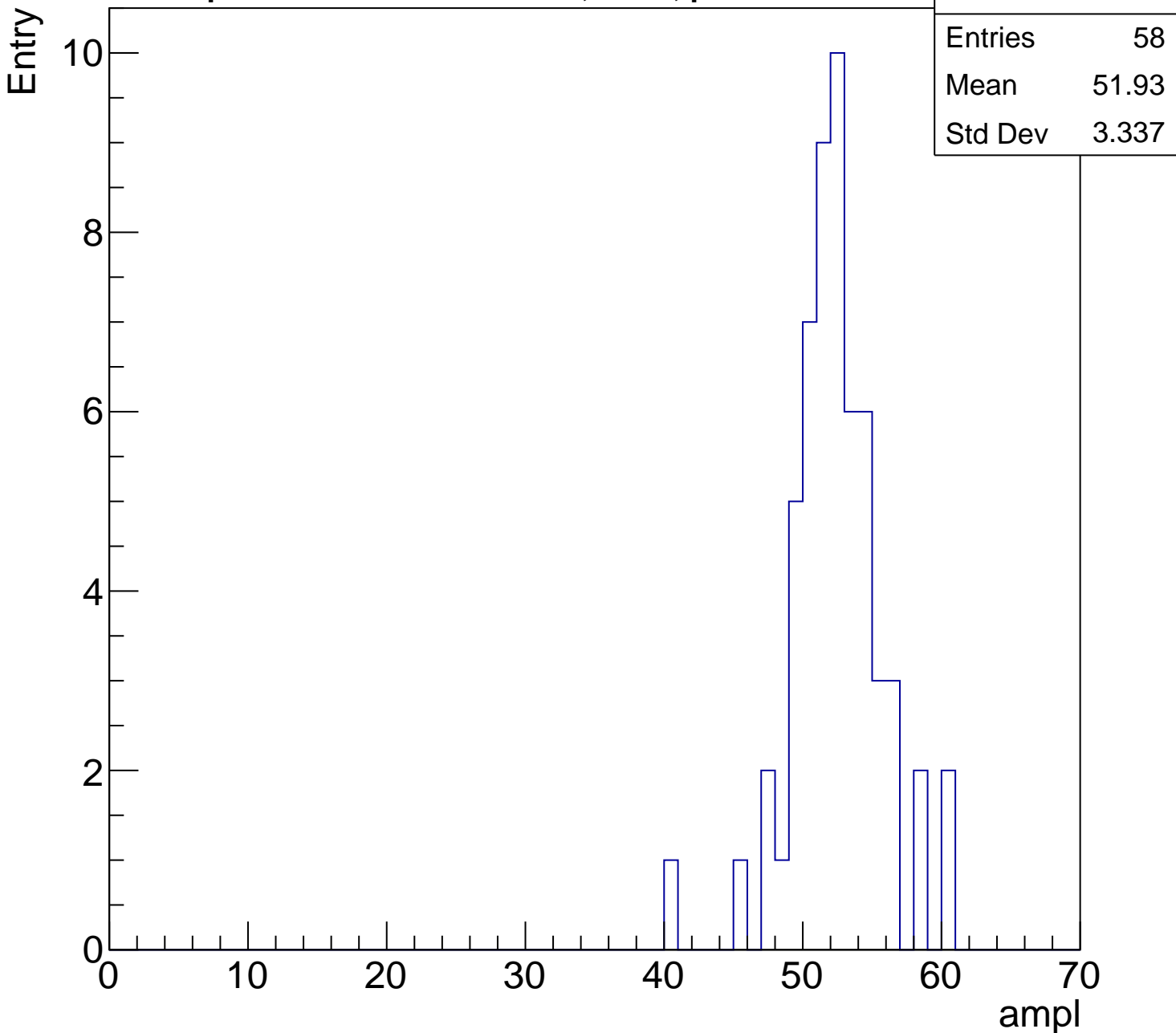
30

40

50

60

ampl

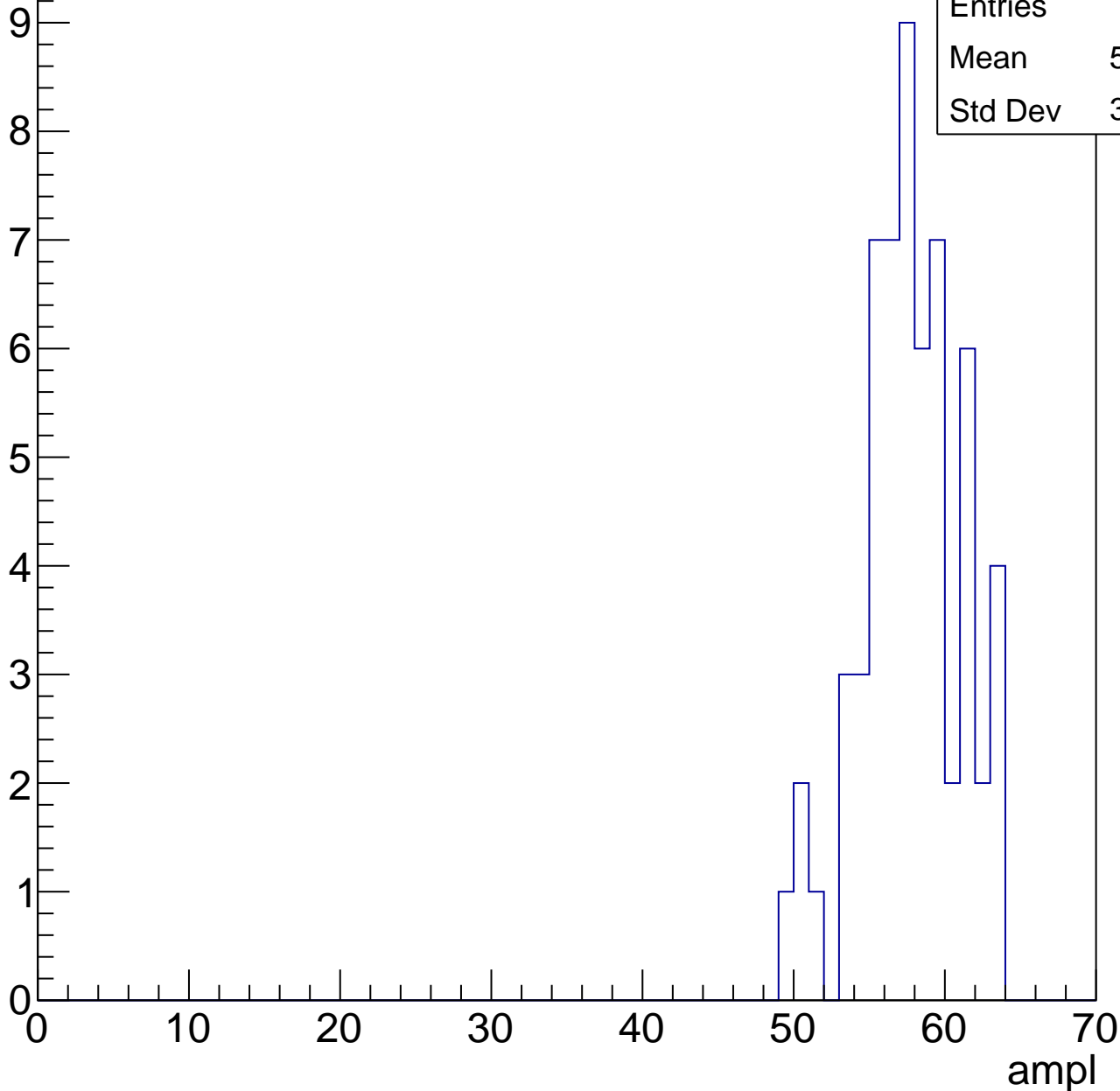


B1L101S, U26-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.23
Std Dev	3.283

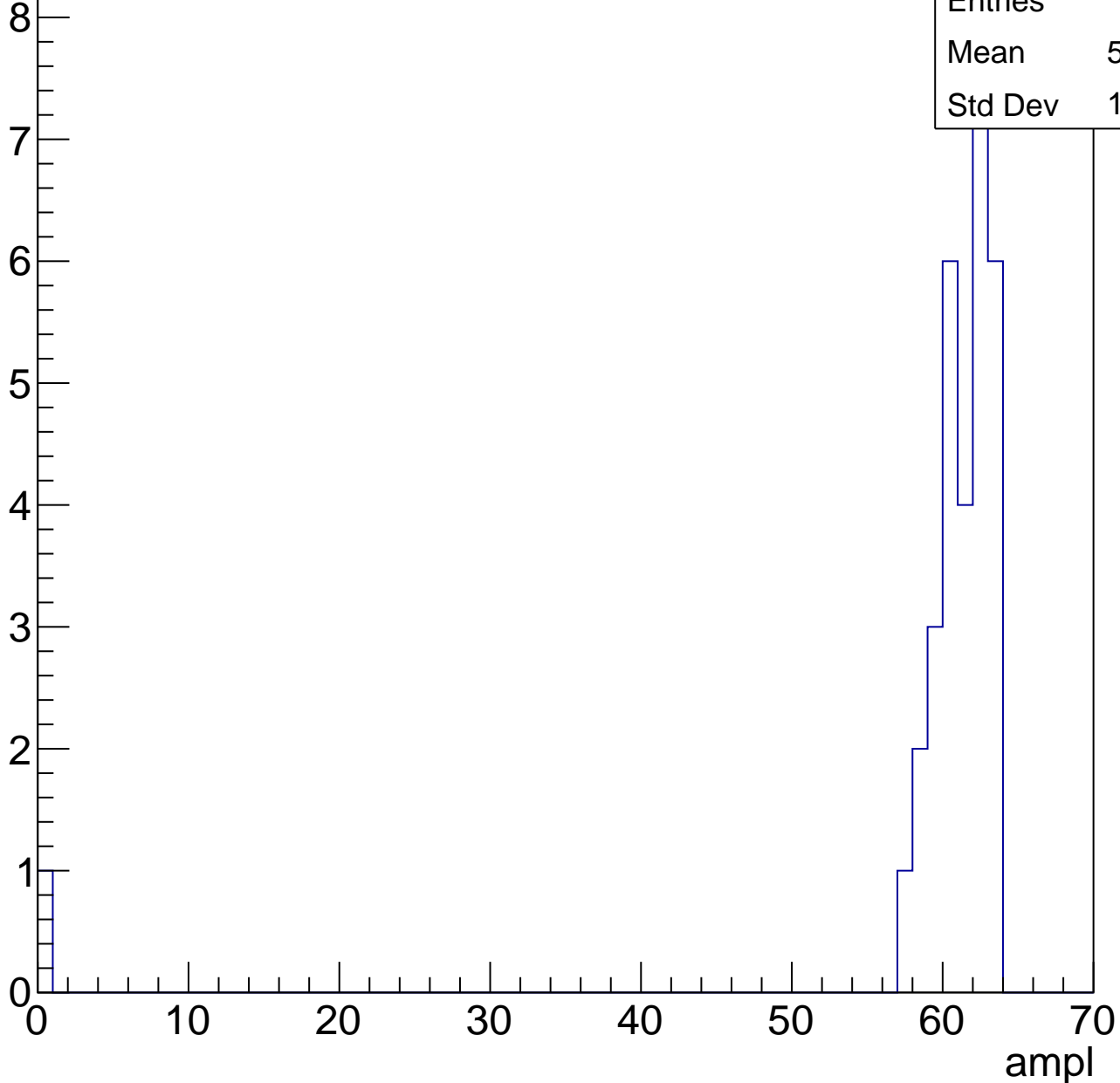


B1L101S, U26-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.97
Std Dev	10.89



B1L101S, U26-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

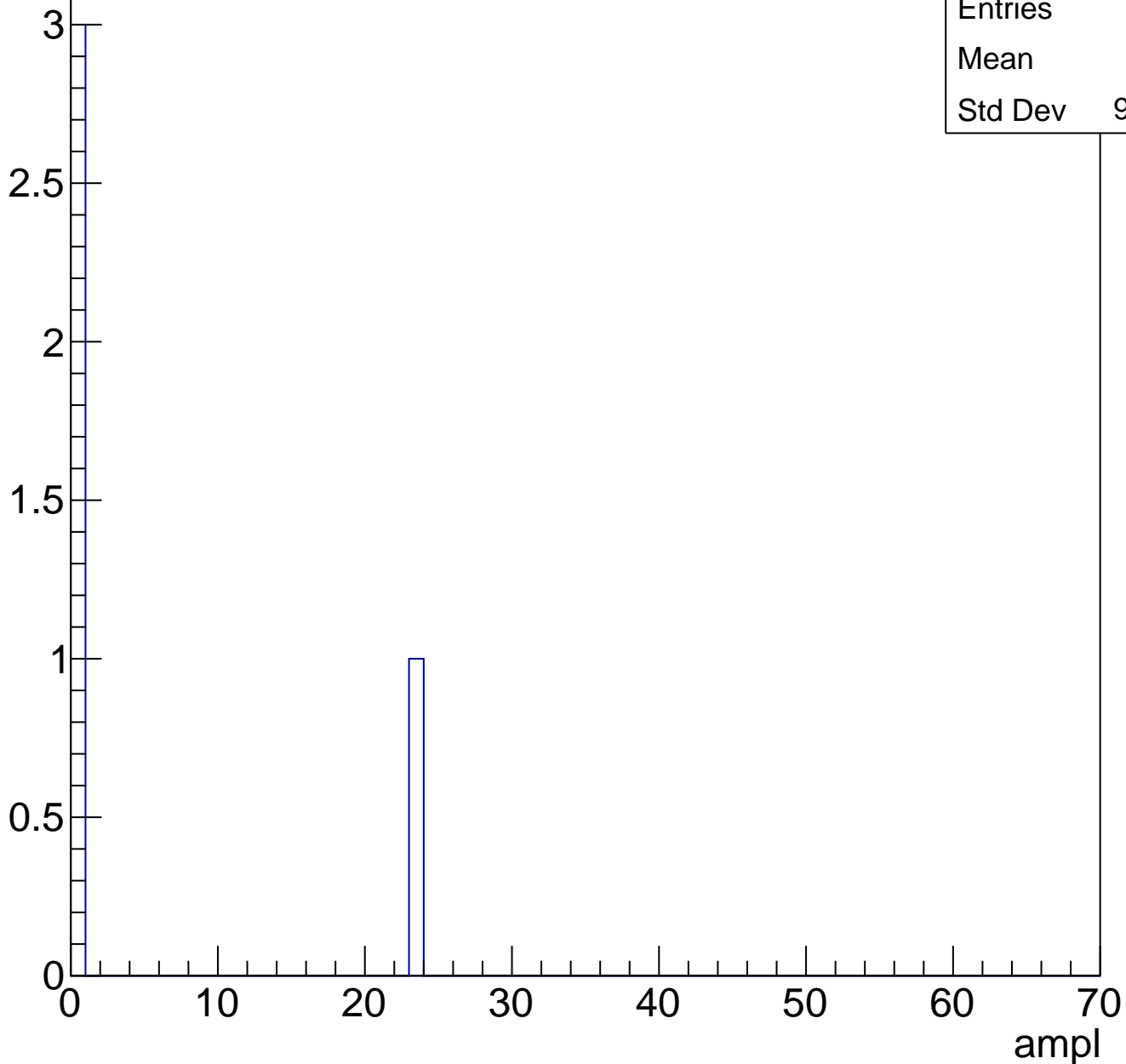


Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch60, adc0

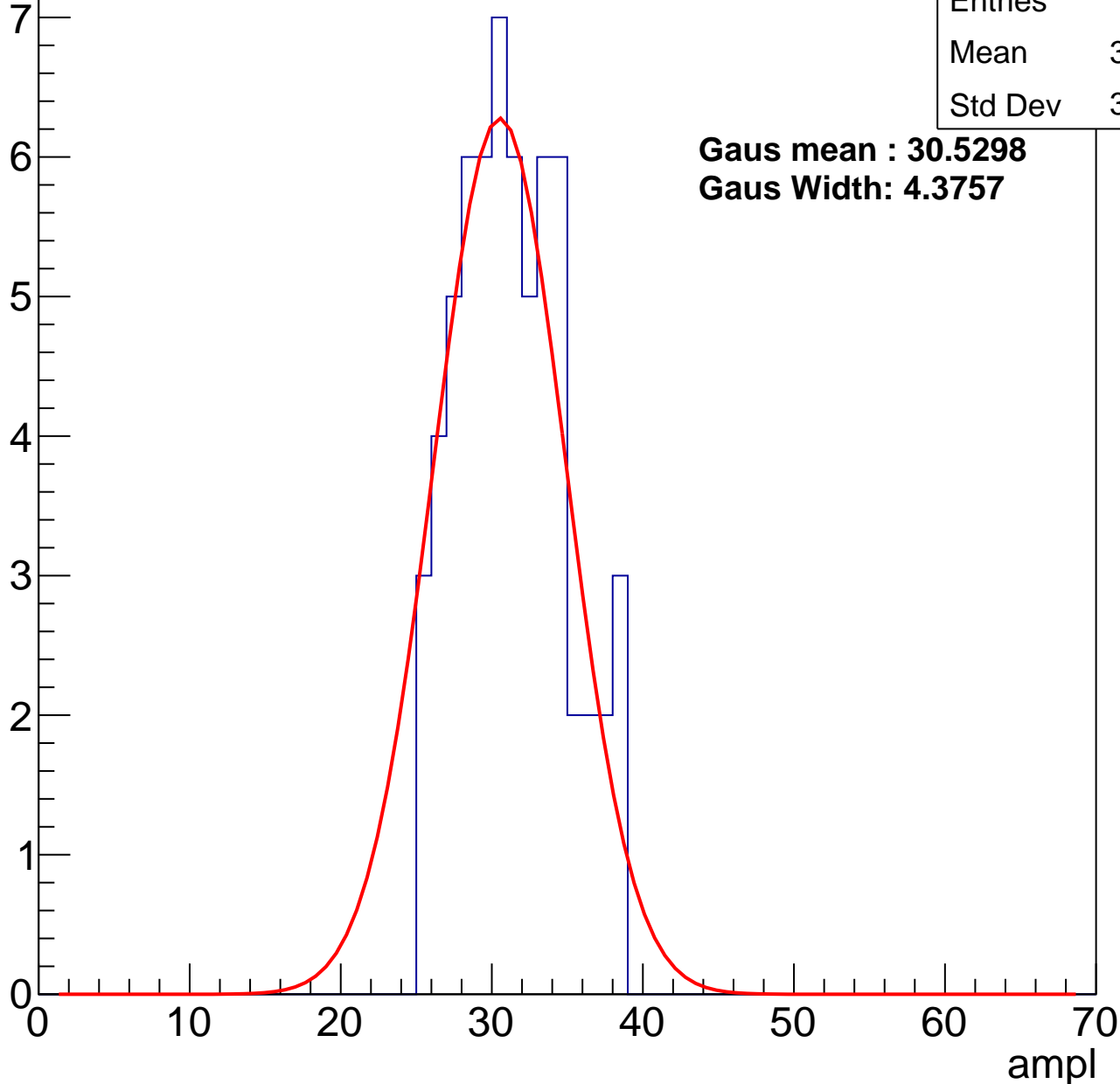
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.86
Std Dev	3.463

Gaus mean : 30.5298

Gaus Width: 4.3757



B1L101S, U26-ch60, adc1

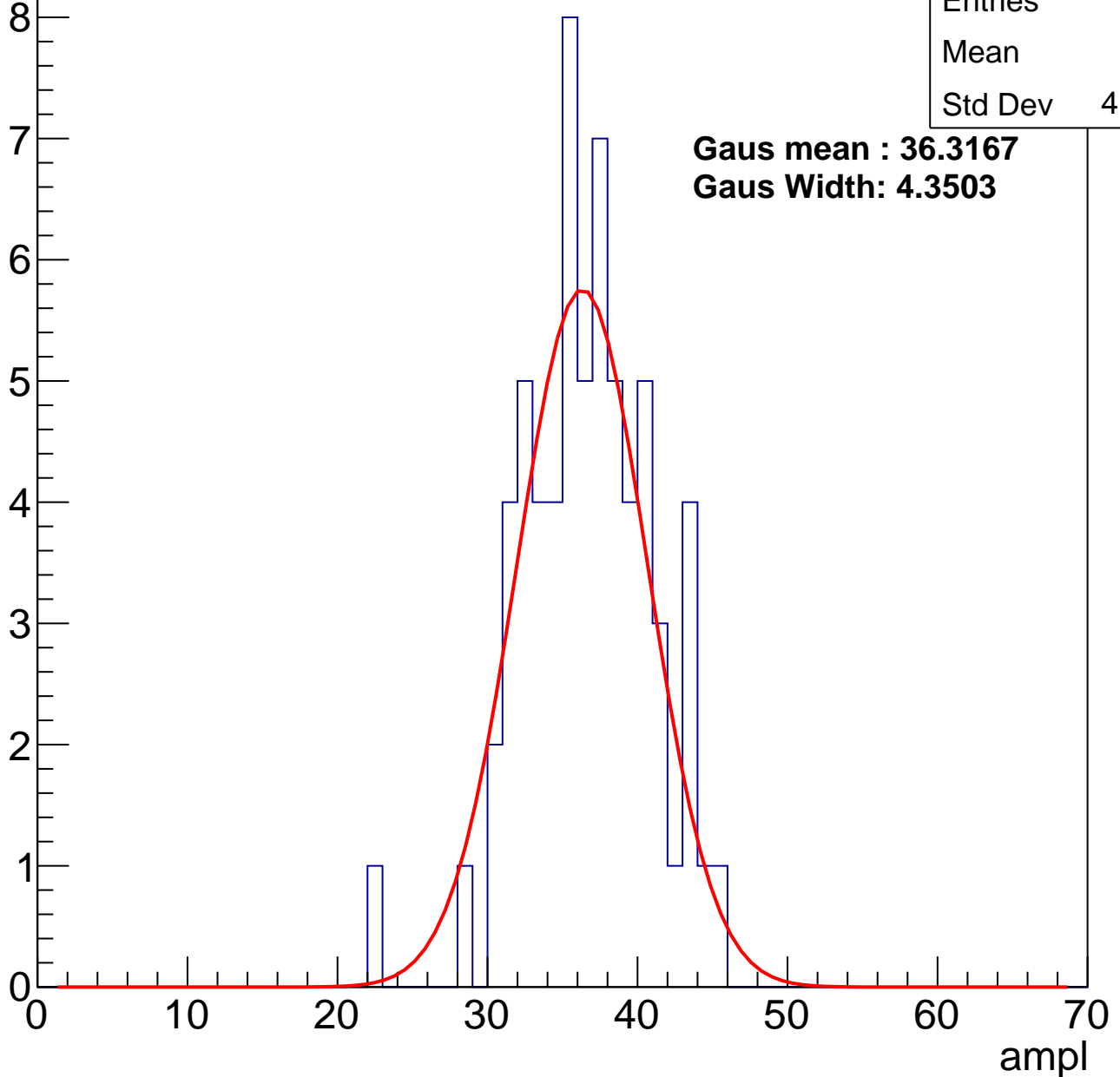
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.2
Std Dev	4.232

Gaus mean : 36.3167

Gaus Width: 4.3503



B1L101S, U26-ch60, adc2

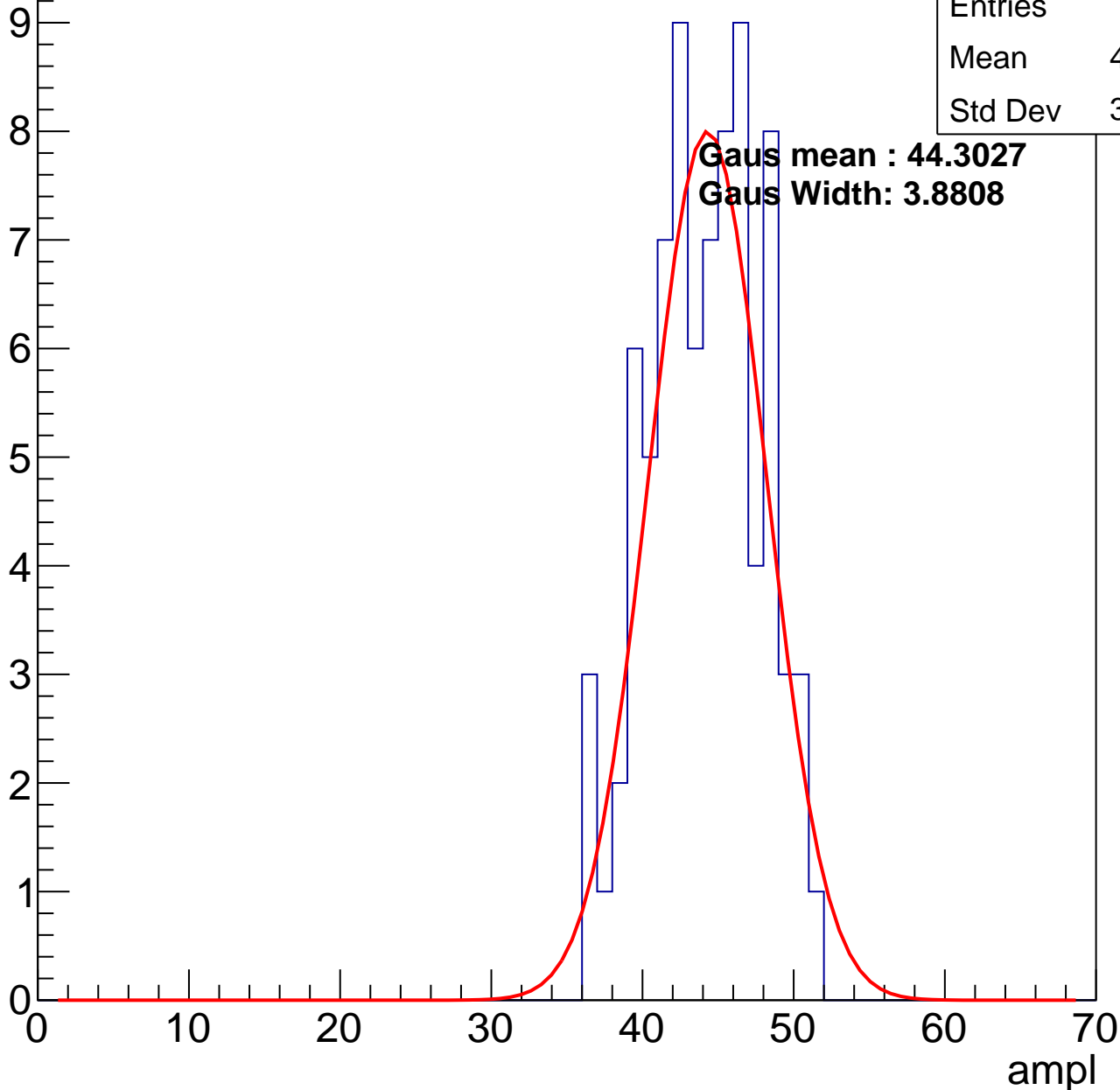
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	43.66
Std Dev	3.627

Gaus mean : 44.3027

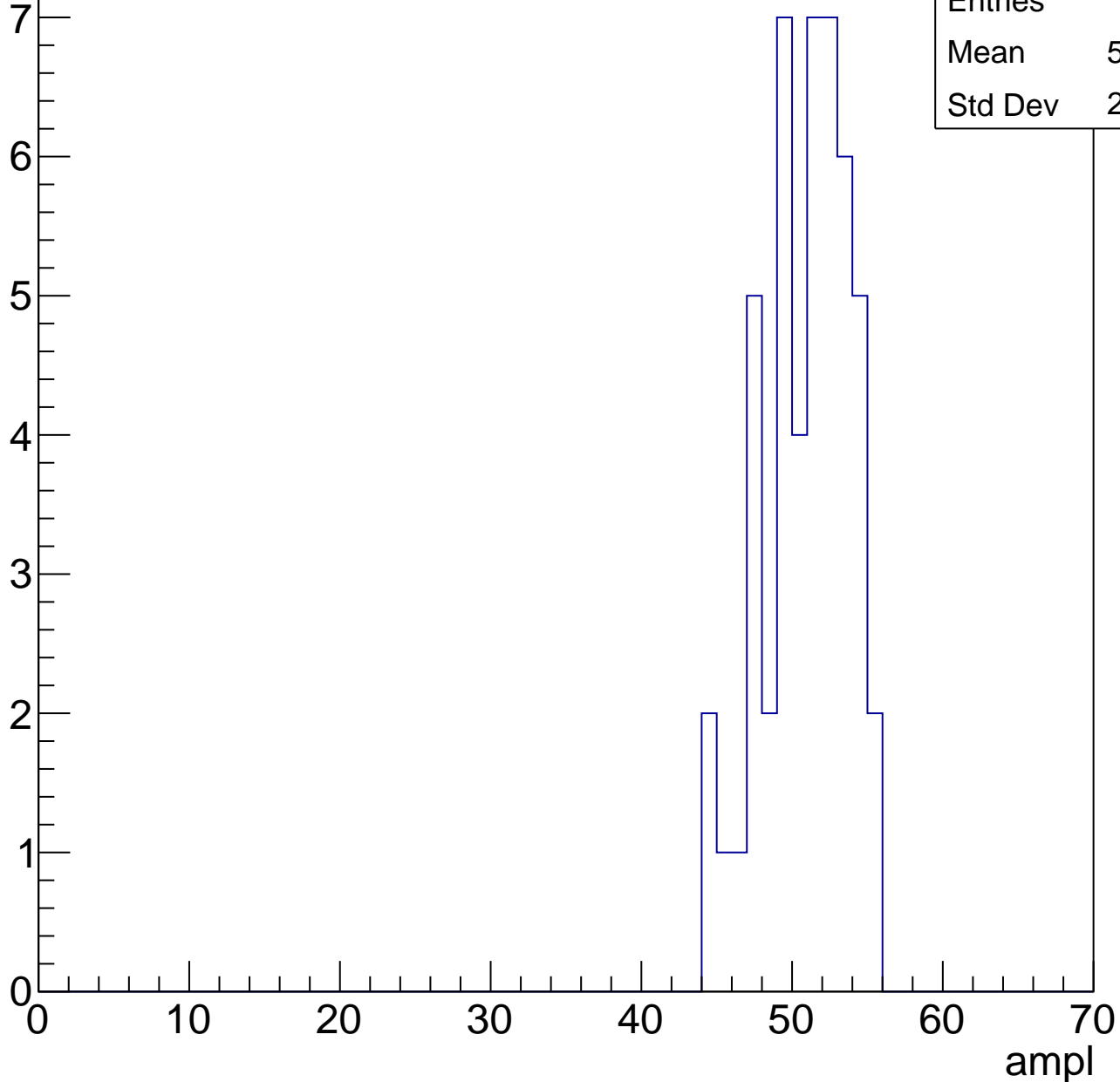
Gaus Width: 3.8808



B1L101S, U26-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



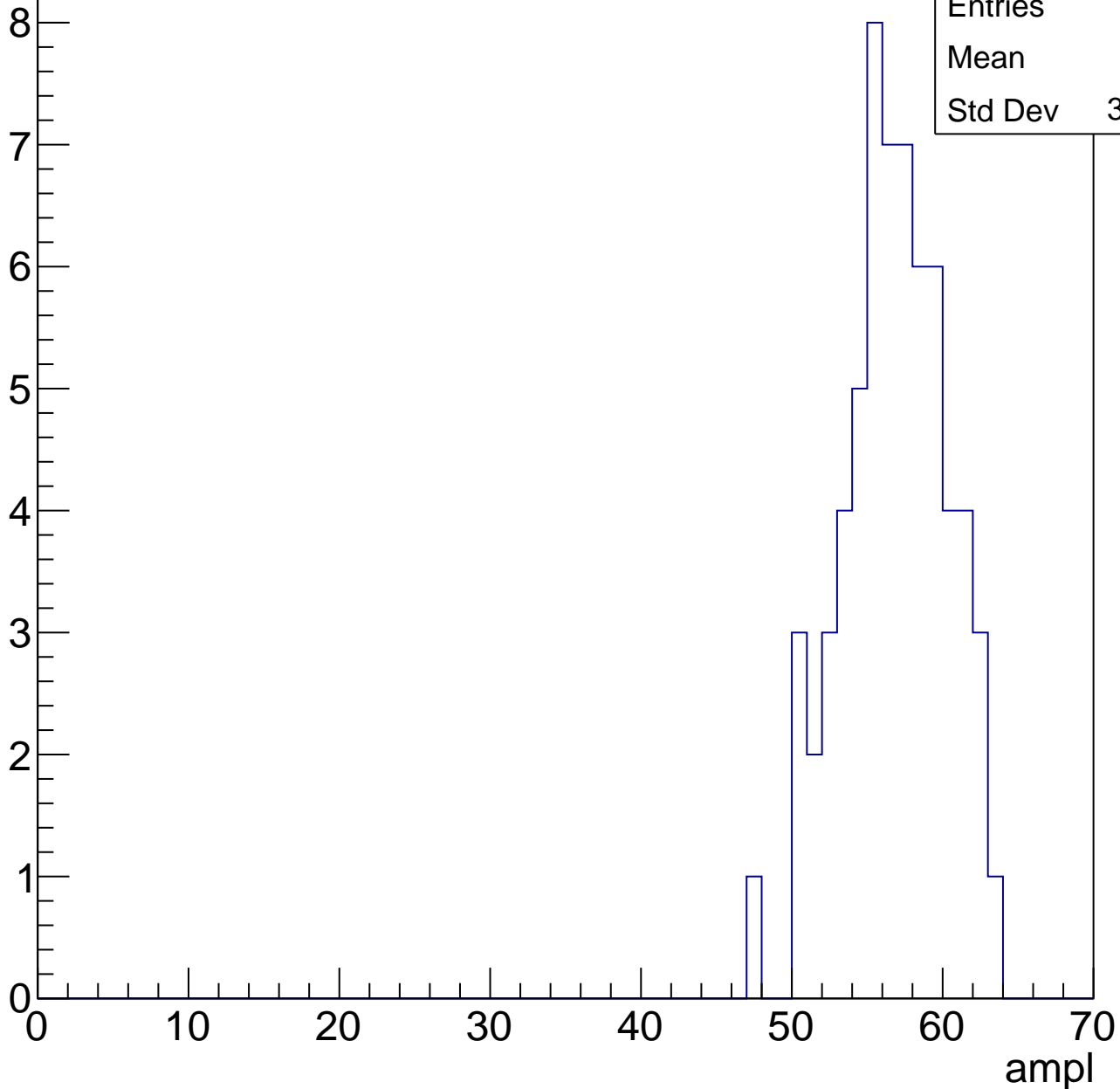
Entries	49
Mean	50.45
Std Dev	2.793

B1L101S, U26-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	56.3
Std Dev	3.426

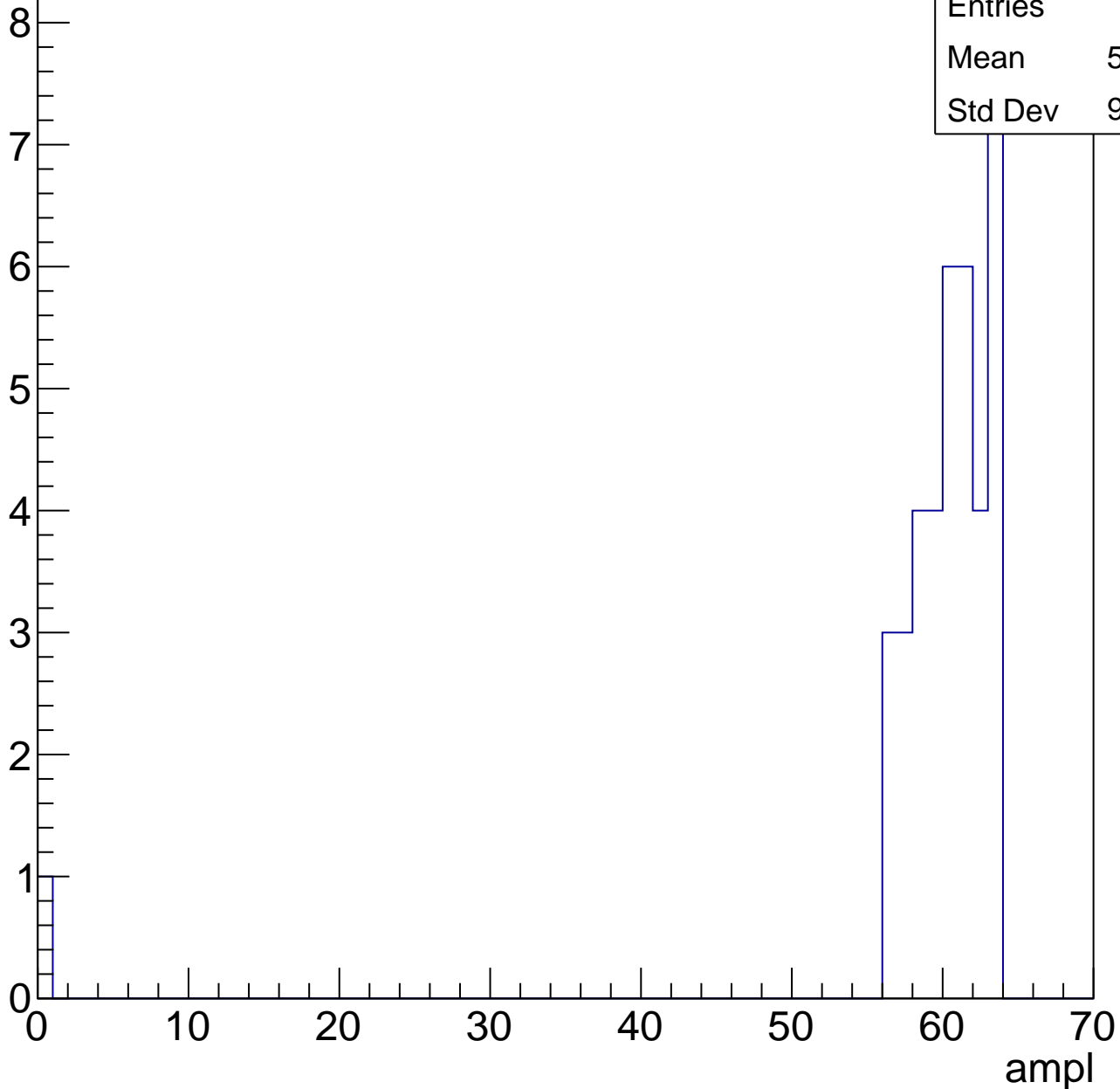


B1L101S, U26-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.59
Std Dev	9.755



B1L101S, U26-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch61, adc0

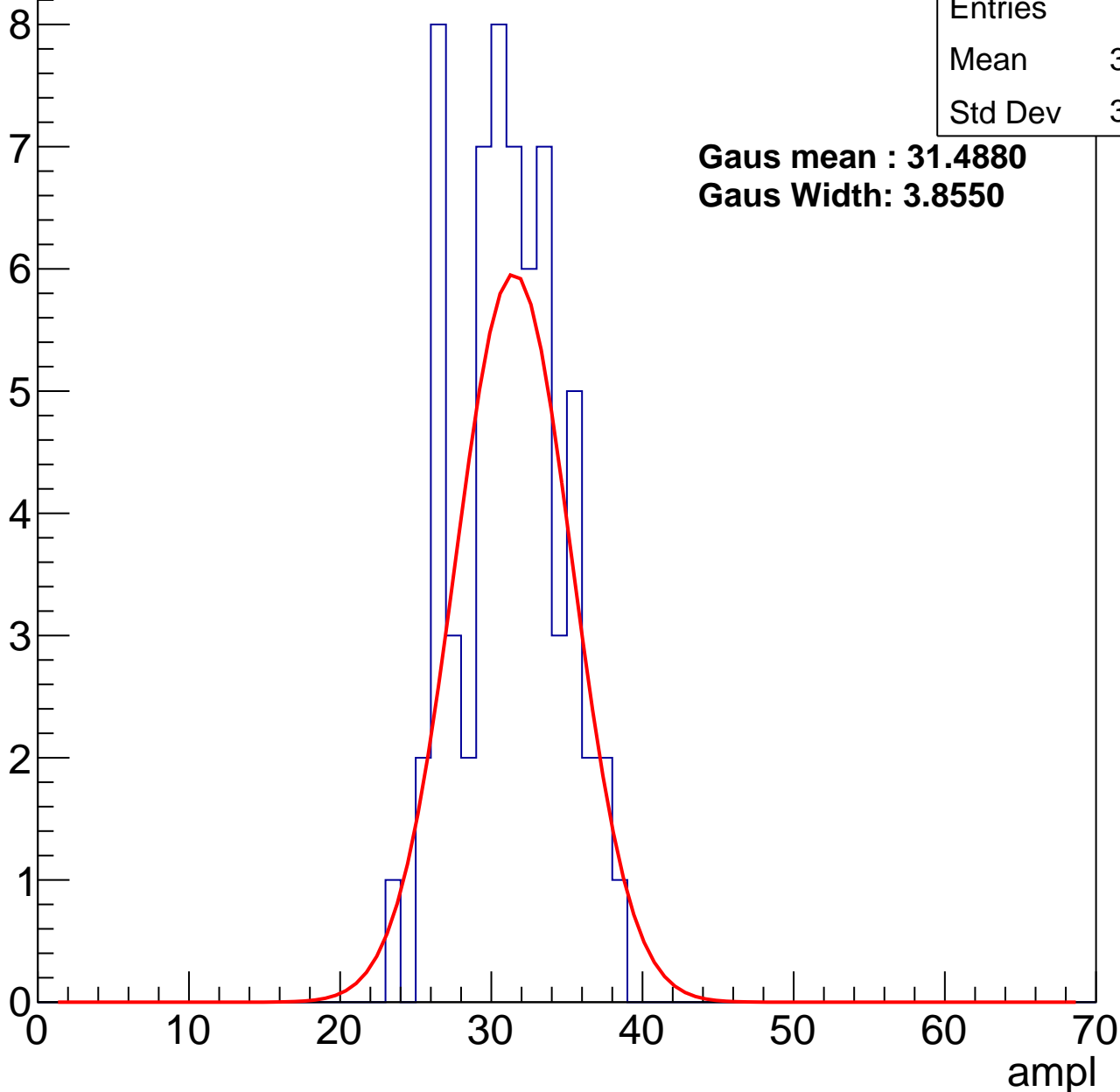
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.66
Std Dev	3.406

Gaus mean : 31.4880

Gaus Width: 3.8550



B1L101S, U26-ch61, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	37.83
Std Dev	3.473

Gaus mean : 38.3944

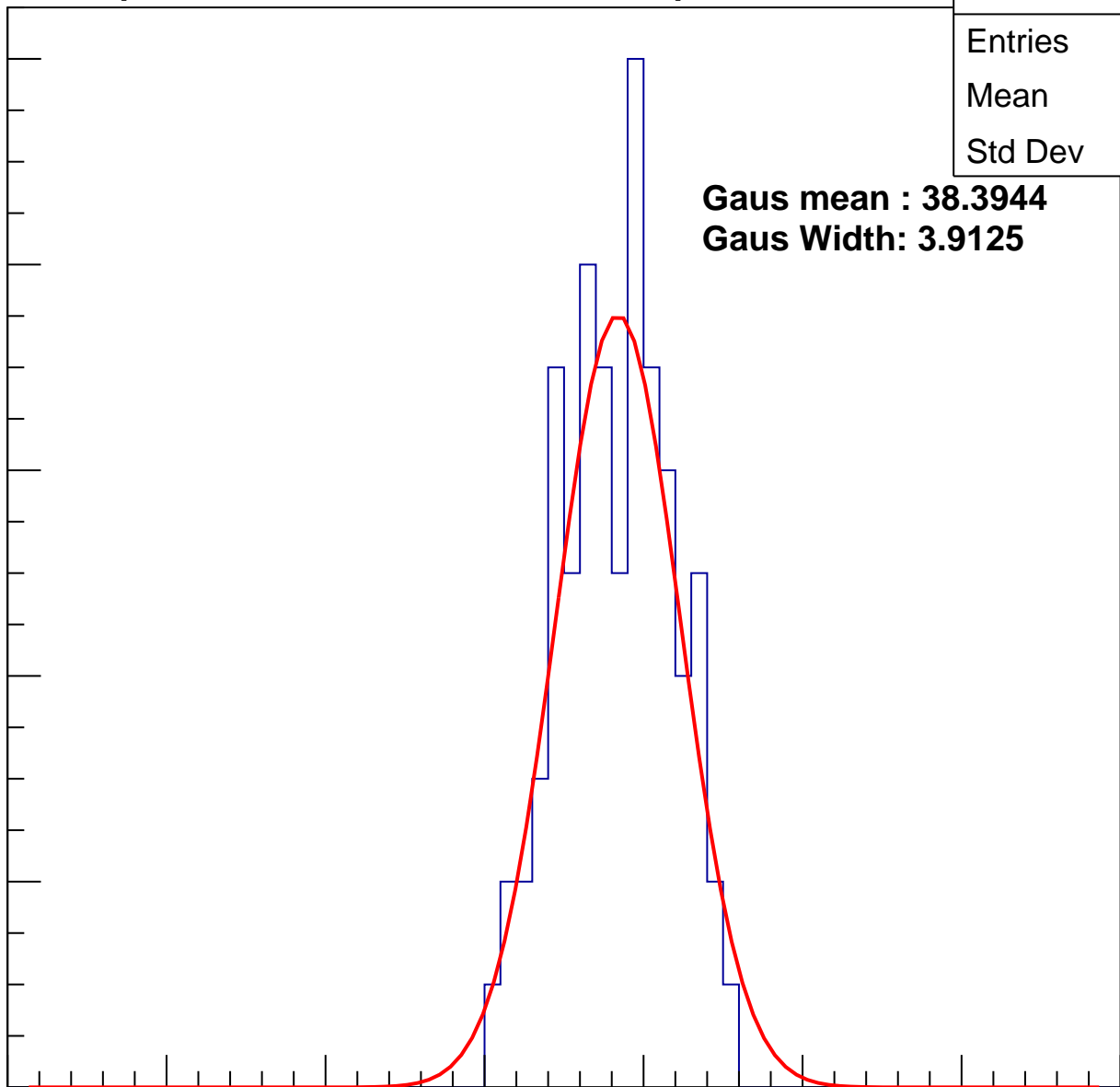
Gaus Width: 3.9125

Entry

10
8
6
4
2
0

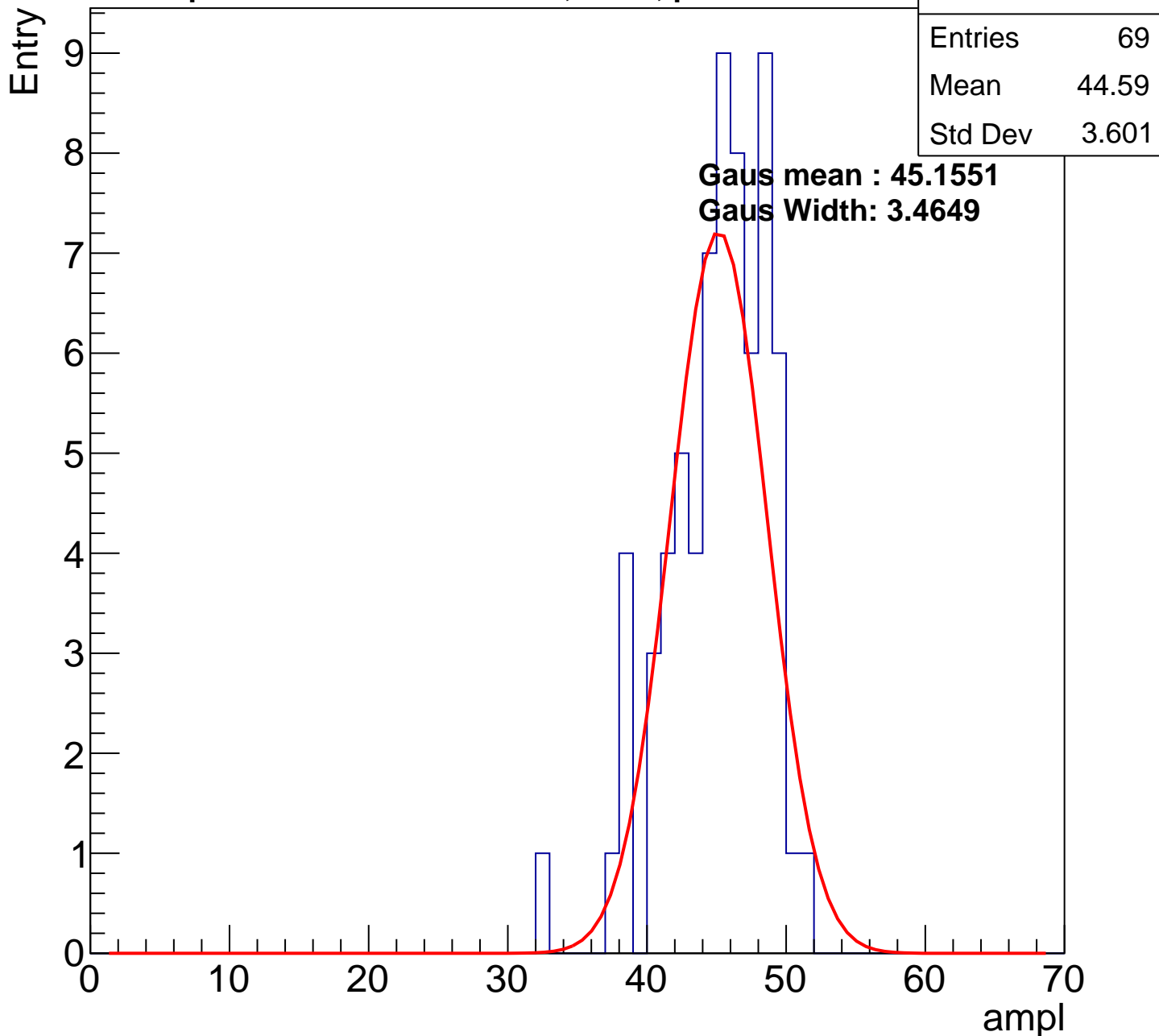
ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch61, adc2

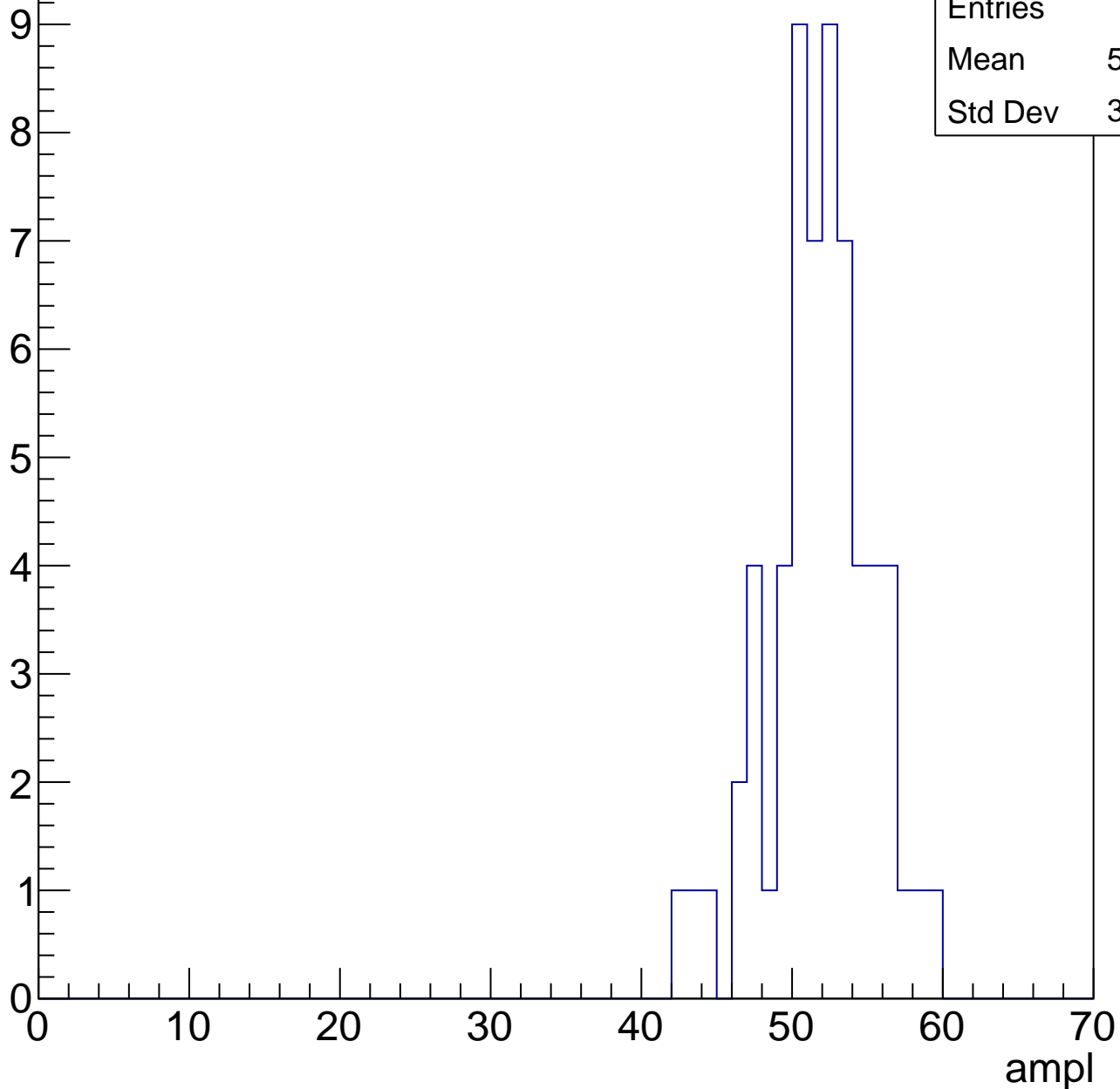
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

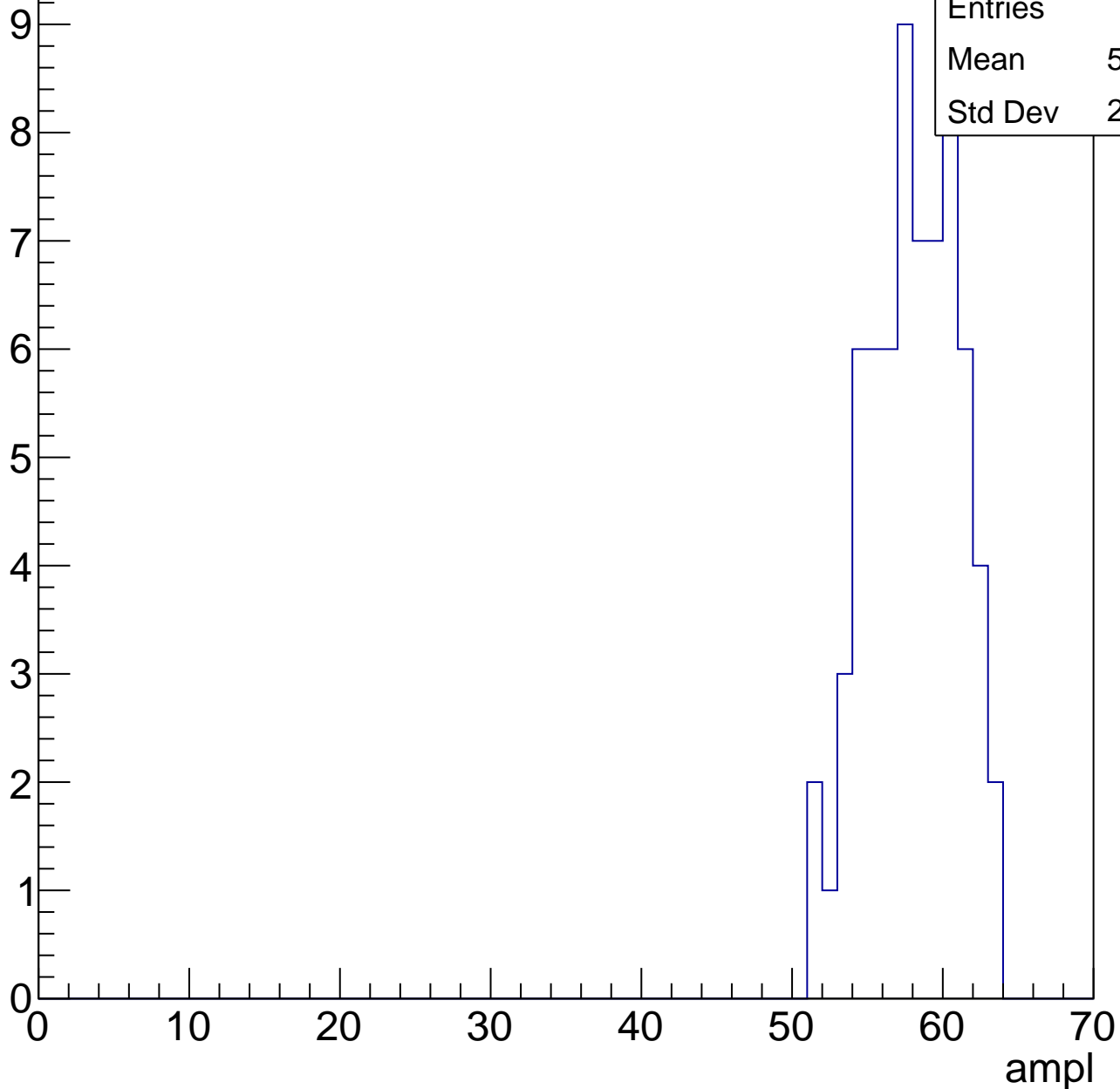


Entries	61
Mean	51.36
Std Dev	3.436

B1L101S, U26-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



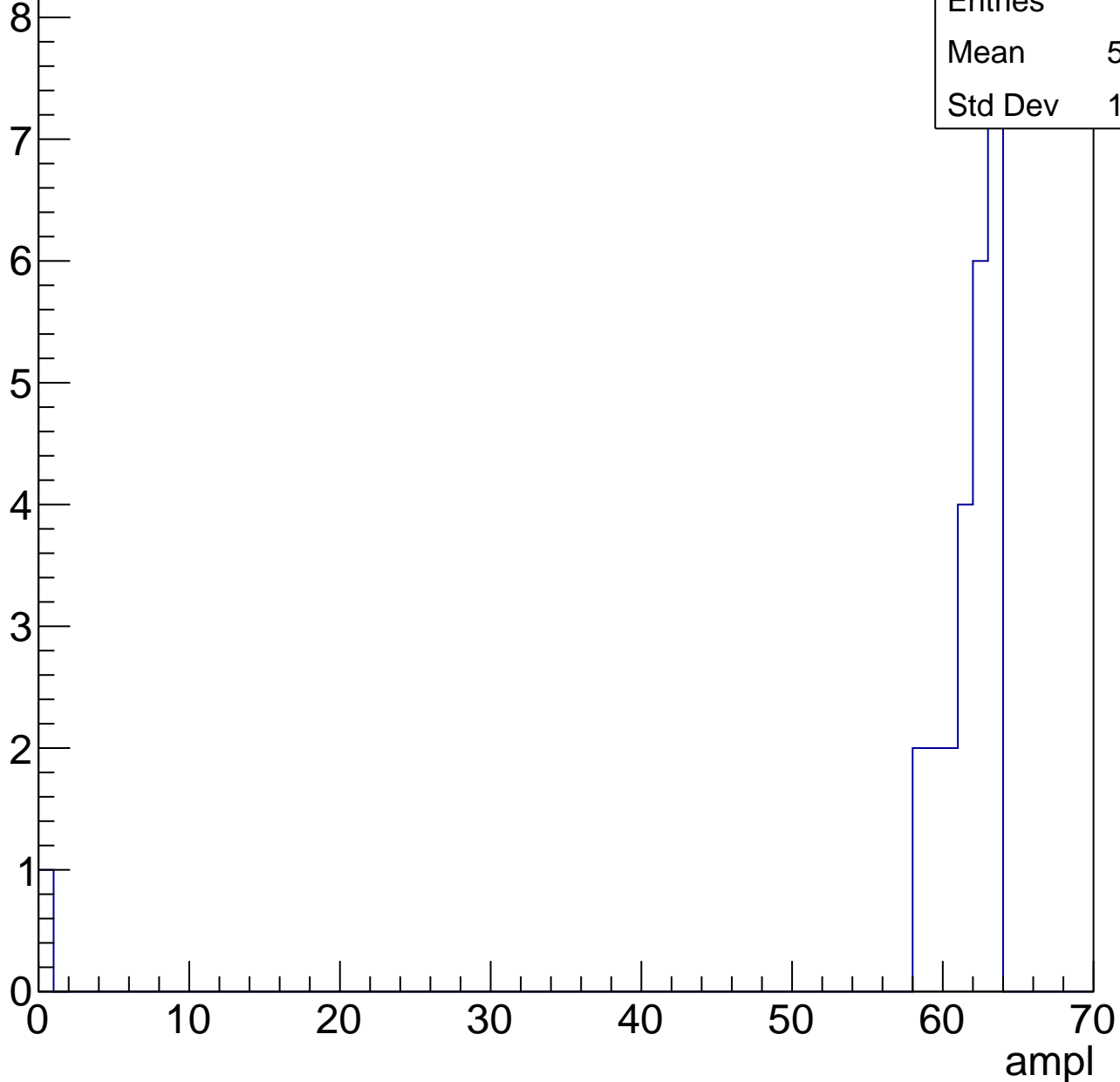
Entries	67
Mean	57.54
Std Dev	2.954

B1L101S, U26-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.96
Std Dev	12.14



B1L101S, U26-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

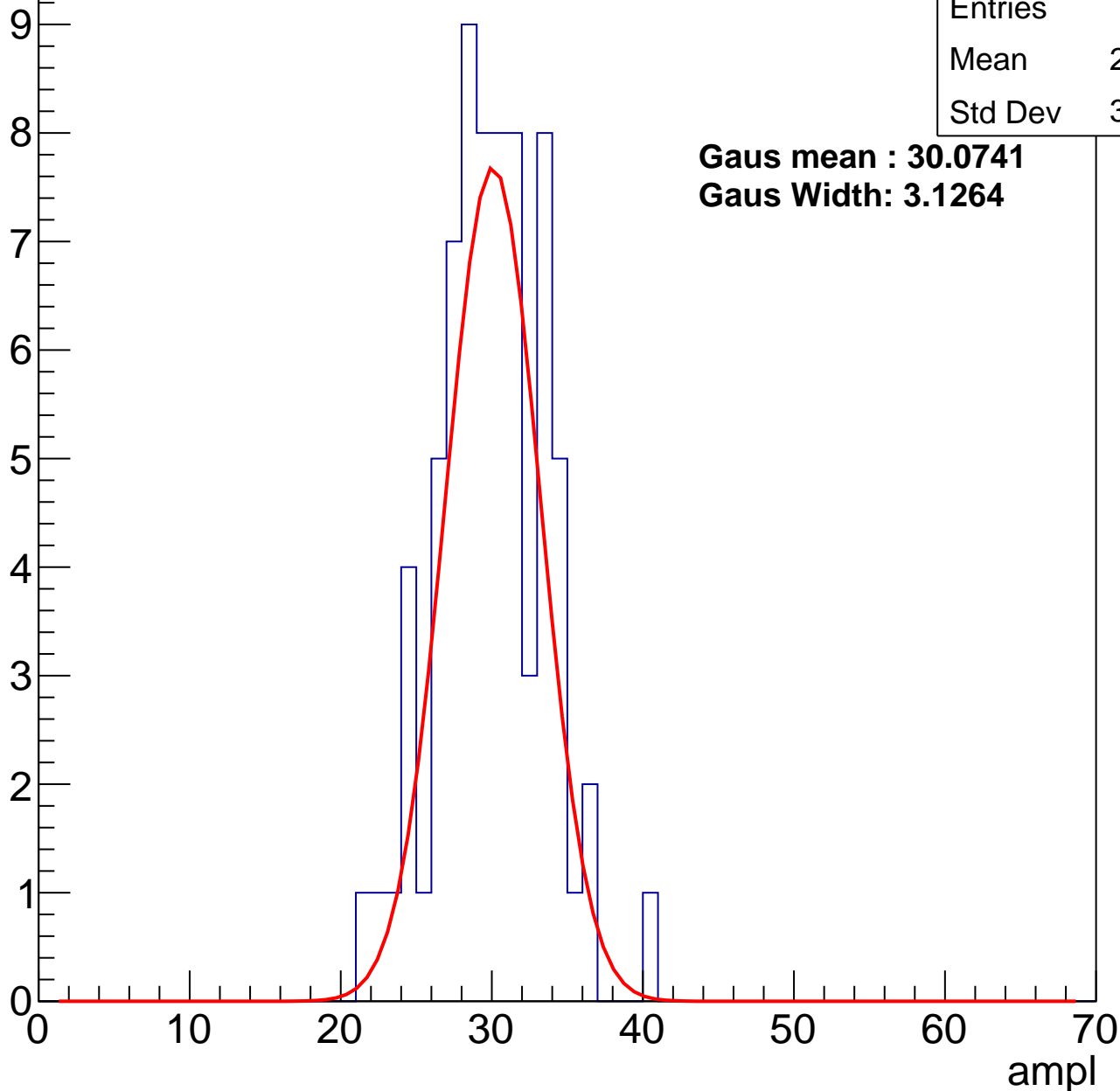


Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch62, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



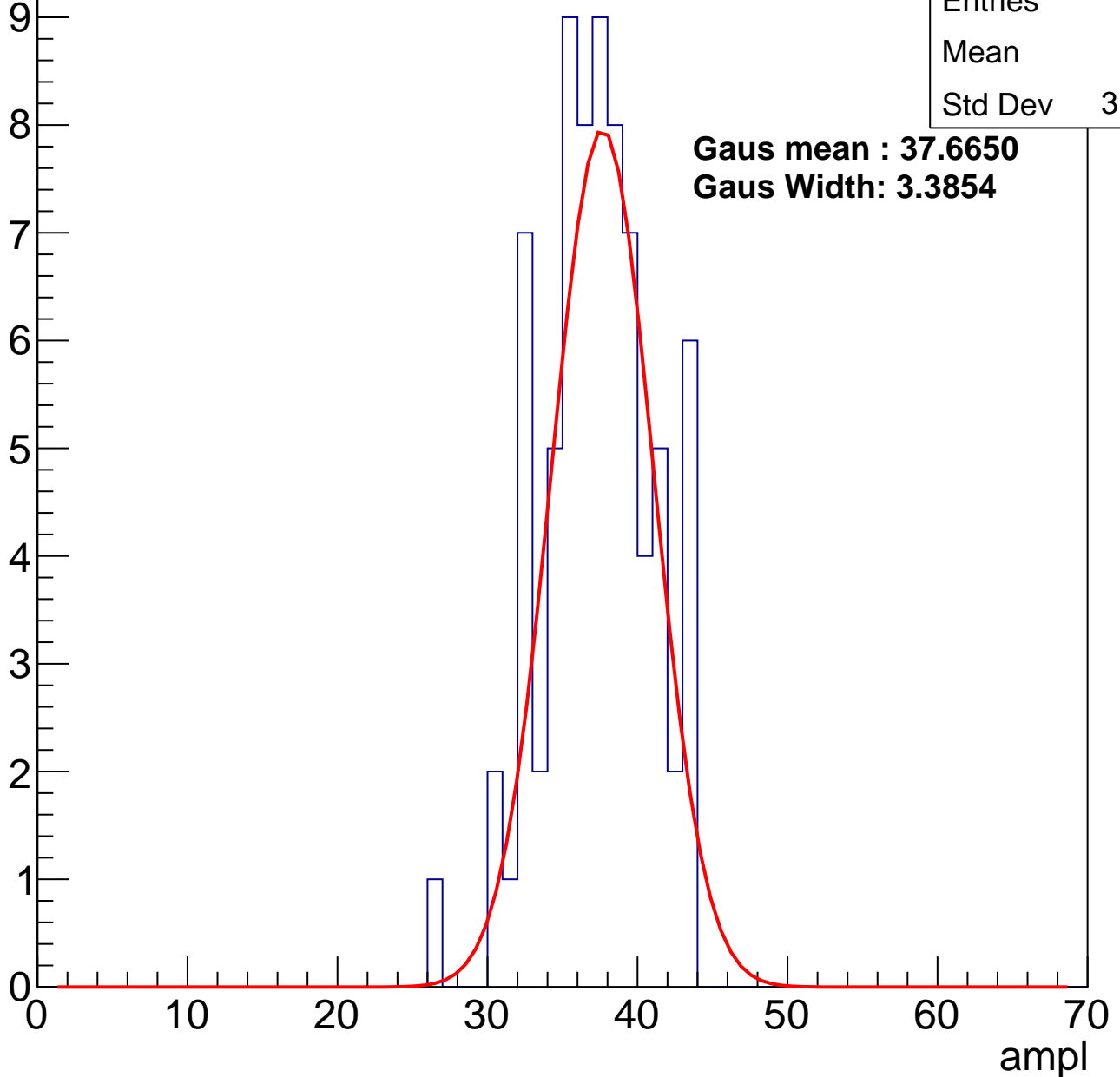
B1L101S, U26-ch62, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.8
Std Dev	3.565

Gaus mean : 37.6650
Gaus Width: 3.3854



B1L101S, U26-ch62, adc2

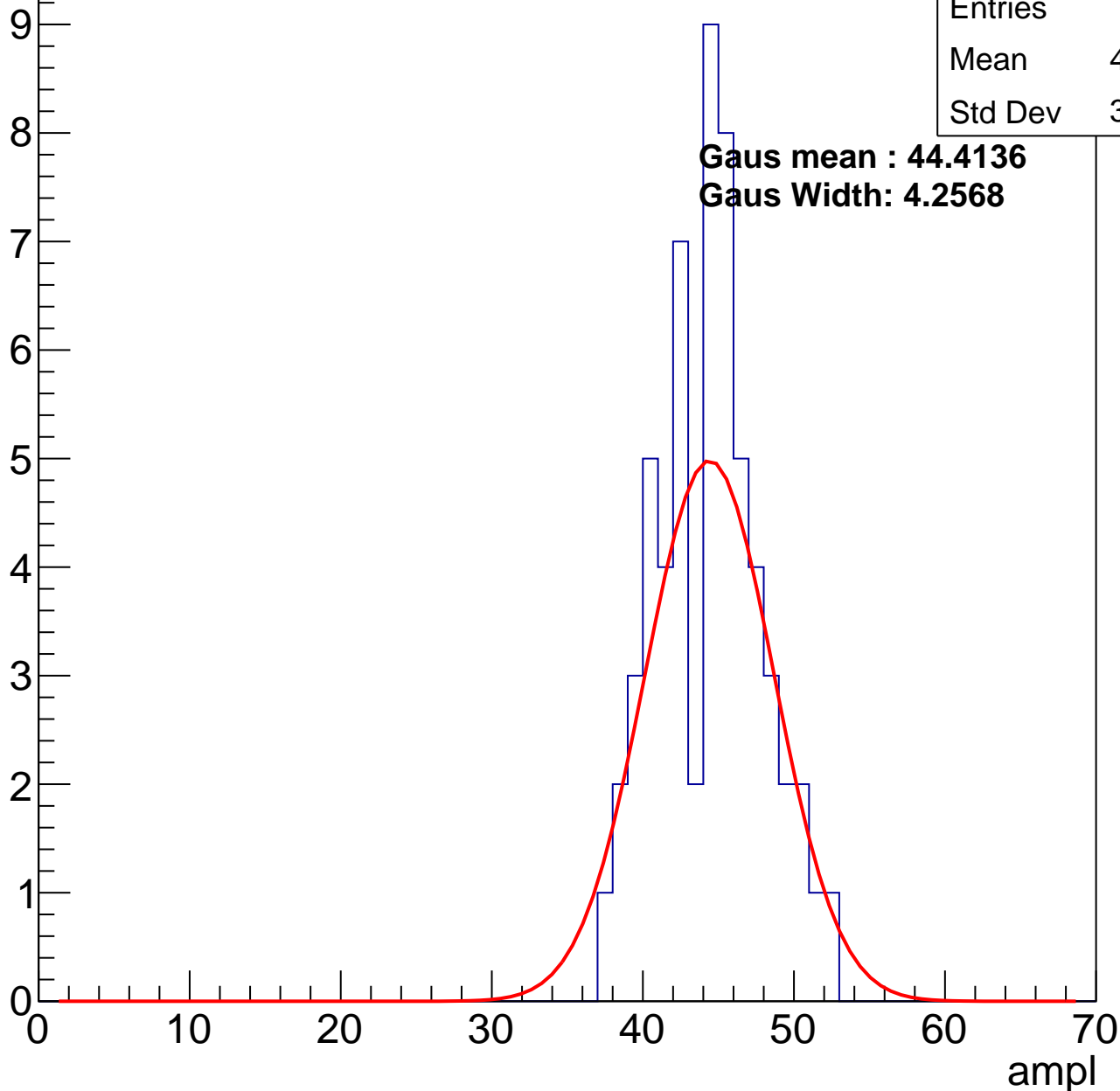
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.95
Std Dev	3.417

Gaus mean : 44.4136

Gaus Width: 4.2568

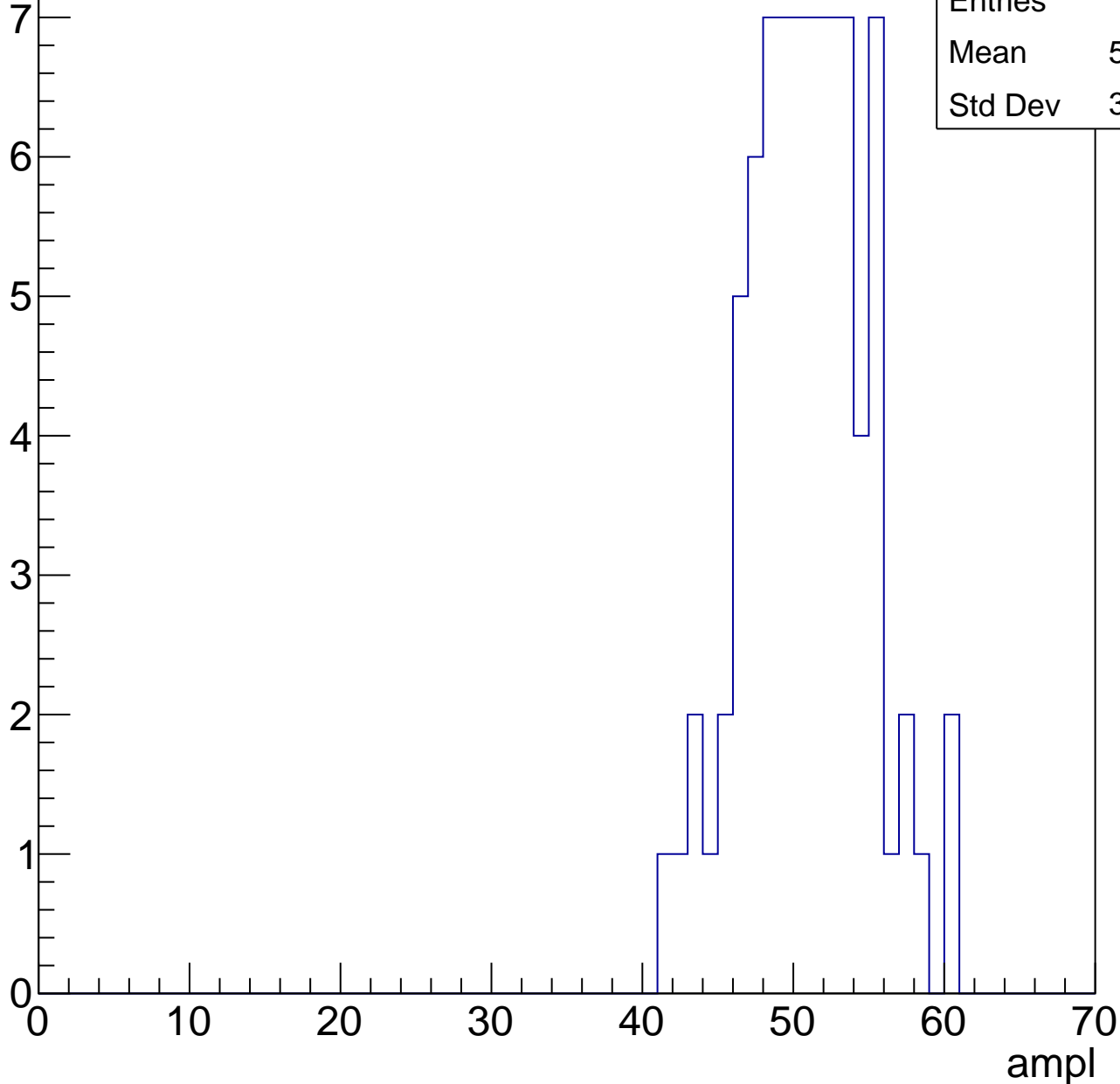


B1L101S, U26-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

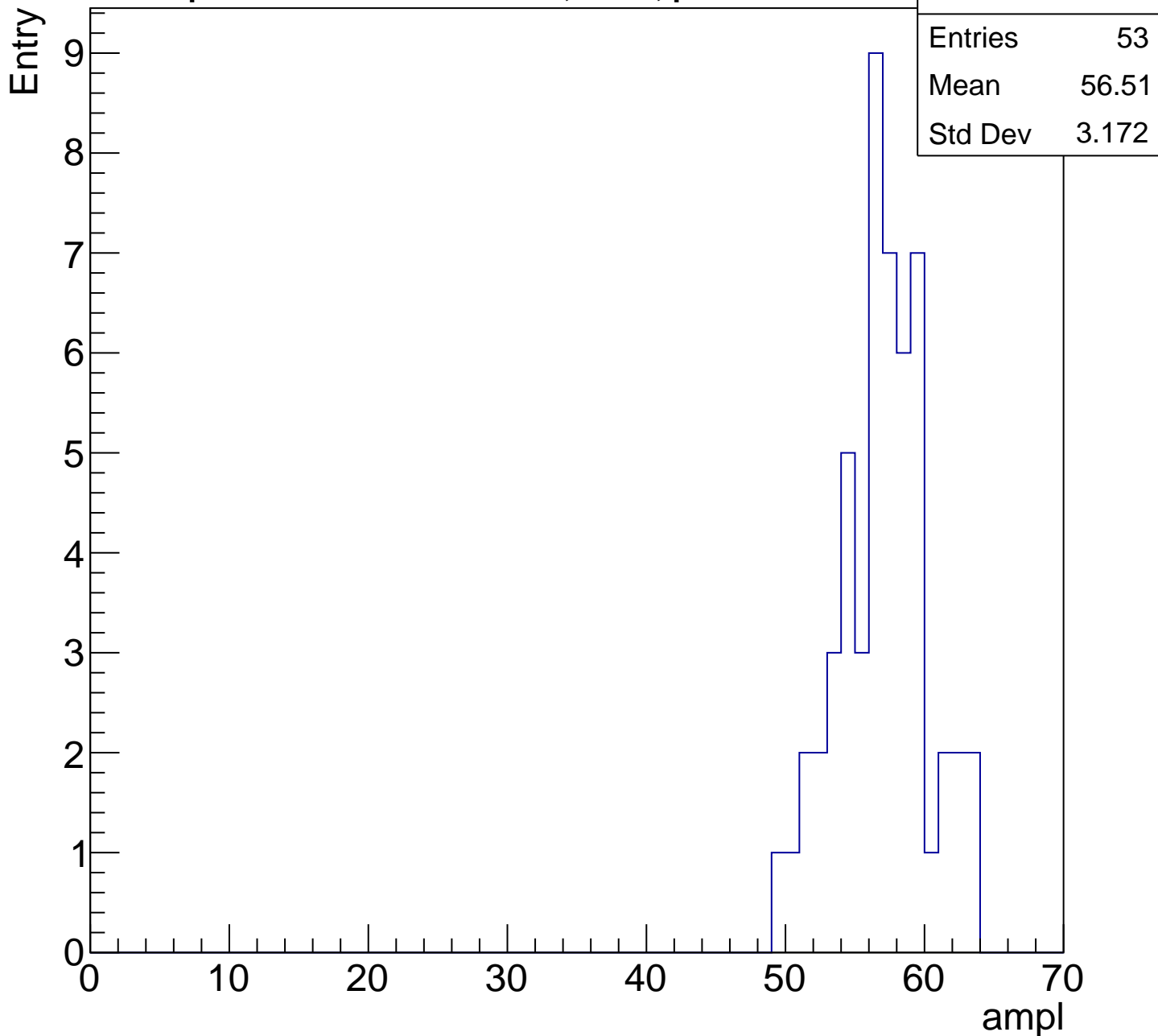
Entry

Entries	77
Mean	50.45
Std Dev	3.976



B1L101S, U26-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

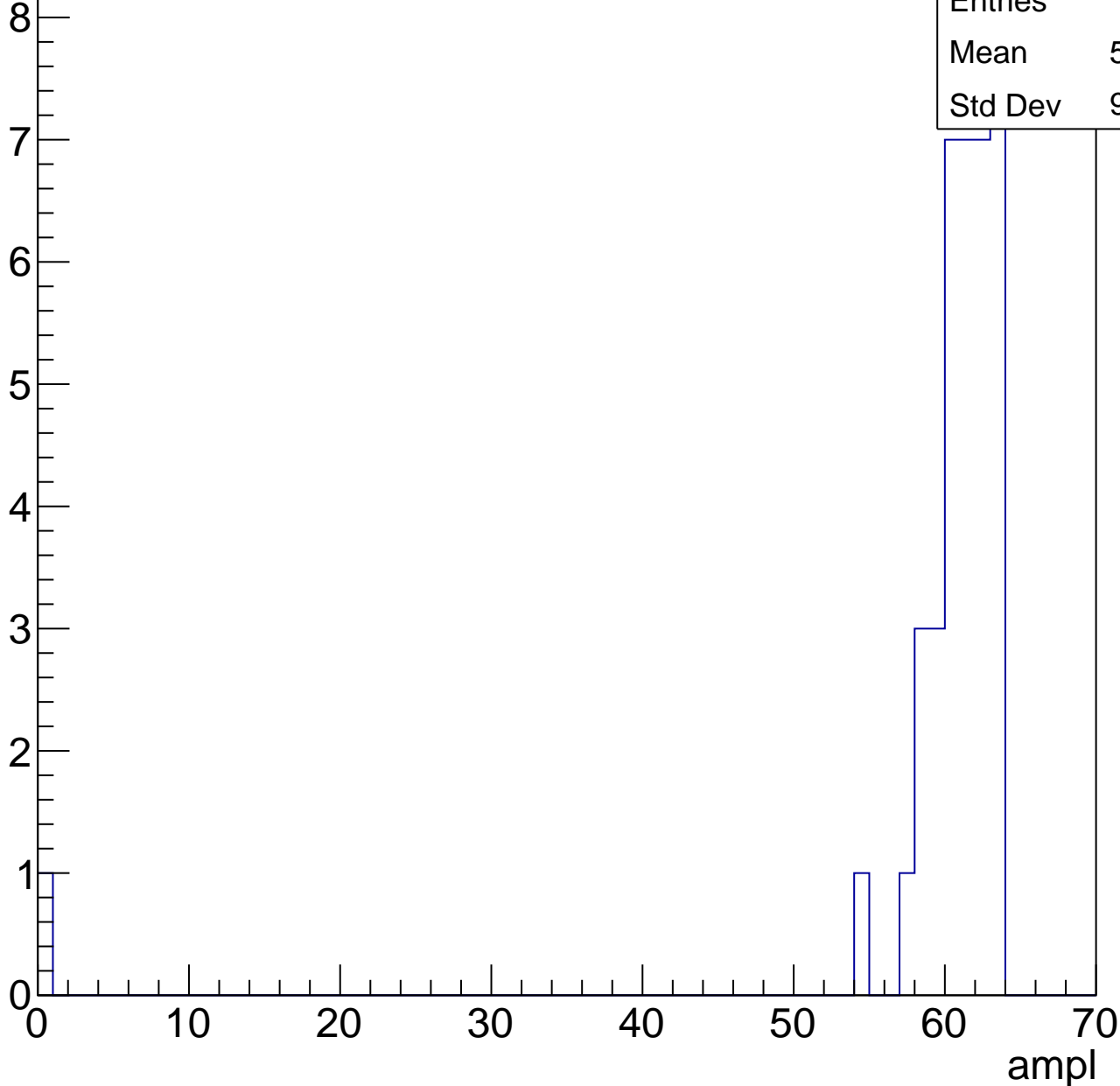


B1L101S, U26-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

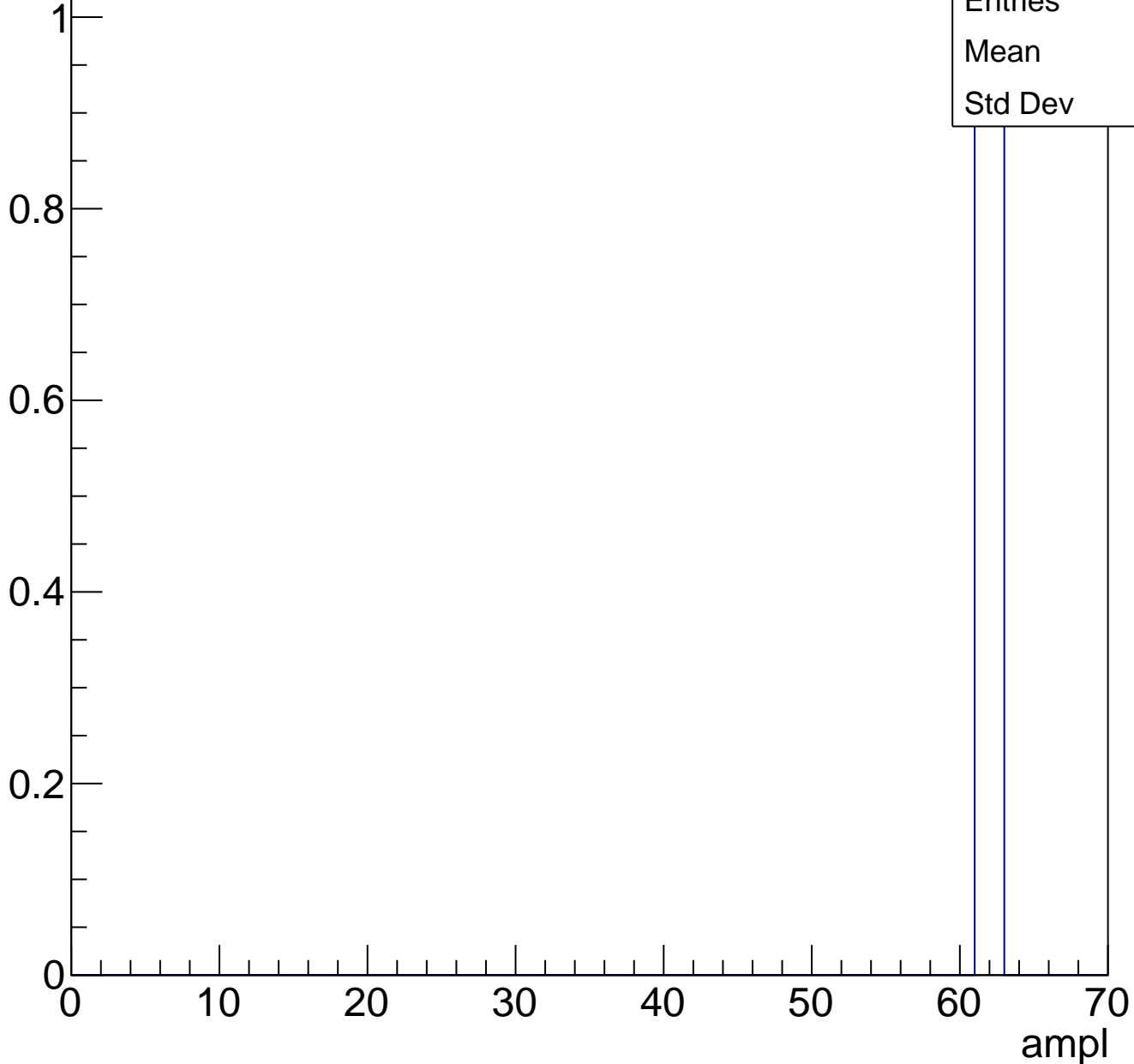
Entries	38
Mean	59.13
Std Dev	9.919



B1L101S, U26-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch63, adc0

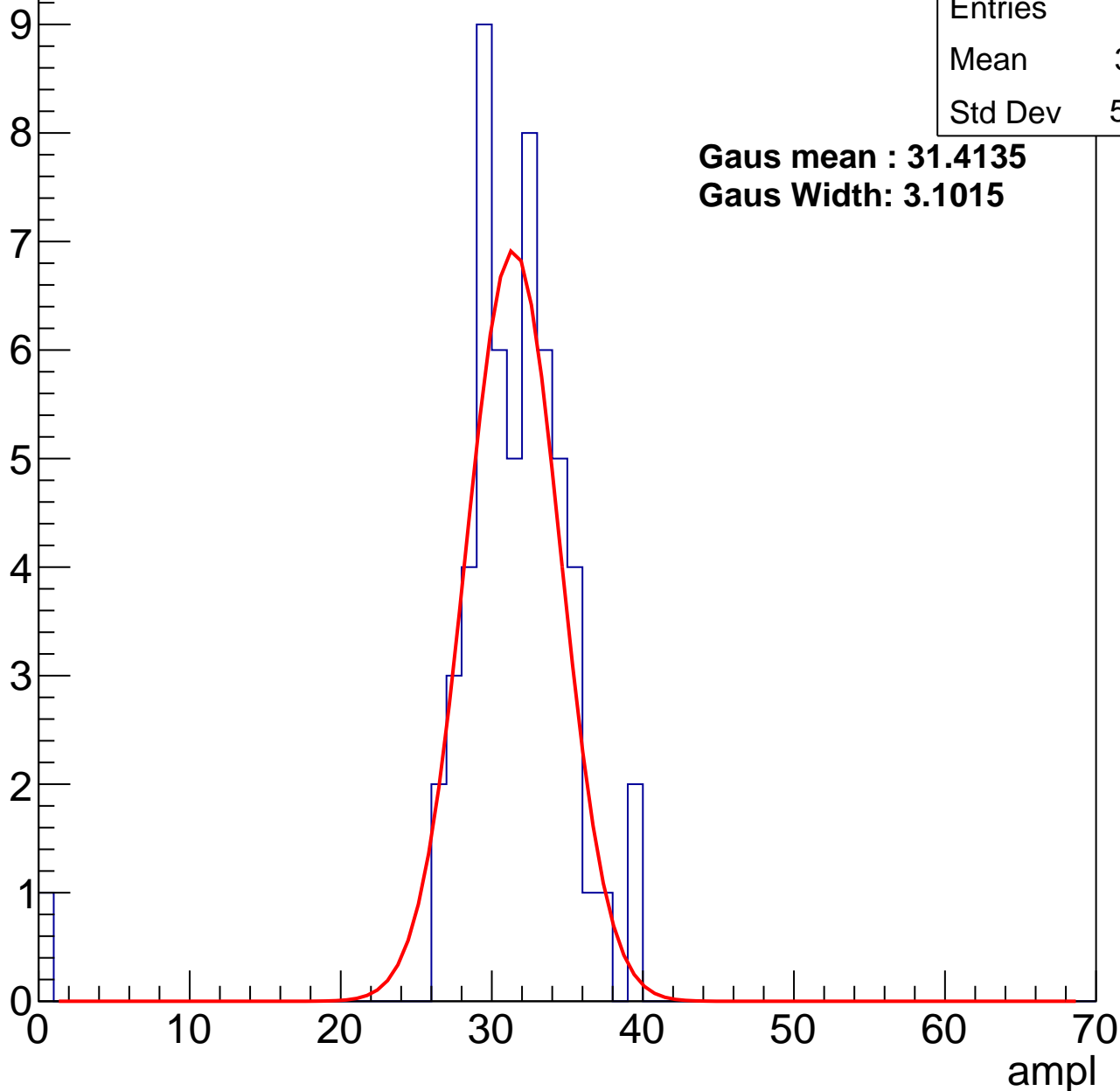
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	30.81
Std Dev	5.069

Gaus mean : 31.4135

Gaus Width: 3.1015



B1L101S, U26-ch63, adc1

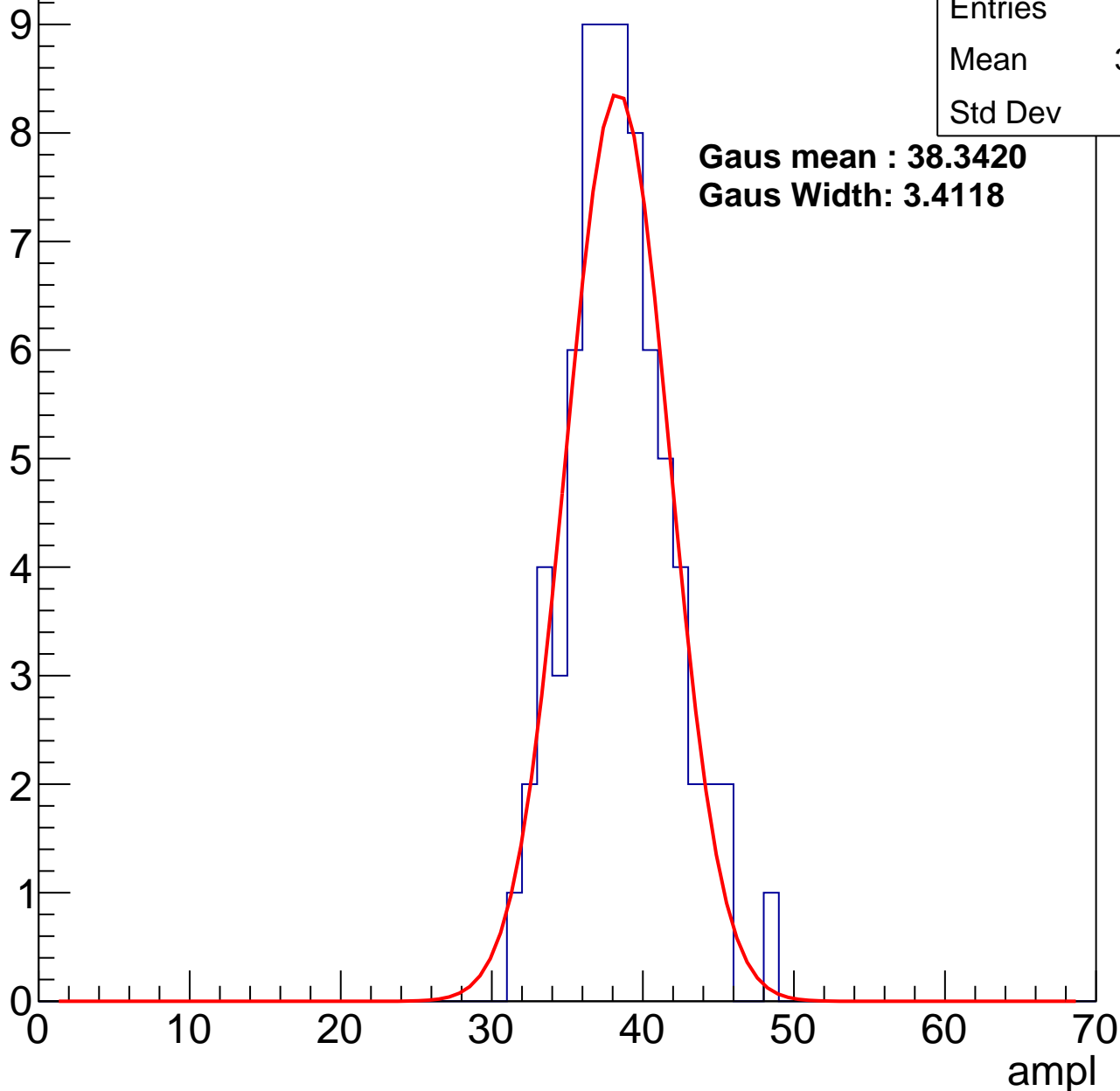
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	38.01
Std Dev	3.37

Gaus mean : 38.3420

Gaus Width: 3.4118



B1L101S, U26-ch63, adc2

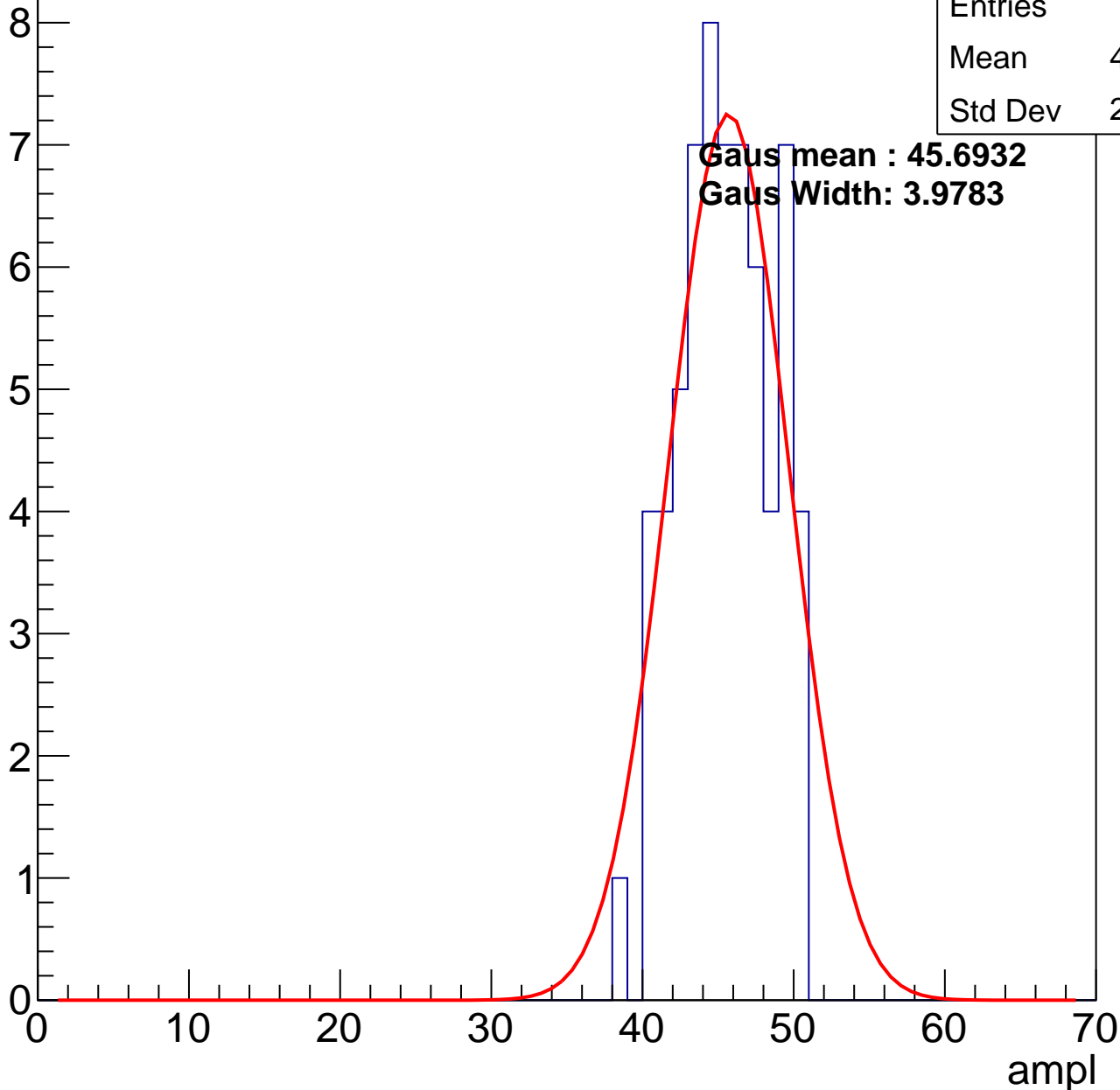
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.98
Std Dev	2.992

Gaus mean : 45.6932

Gaus Width: 3.9783

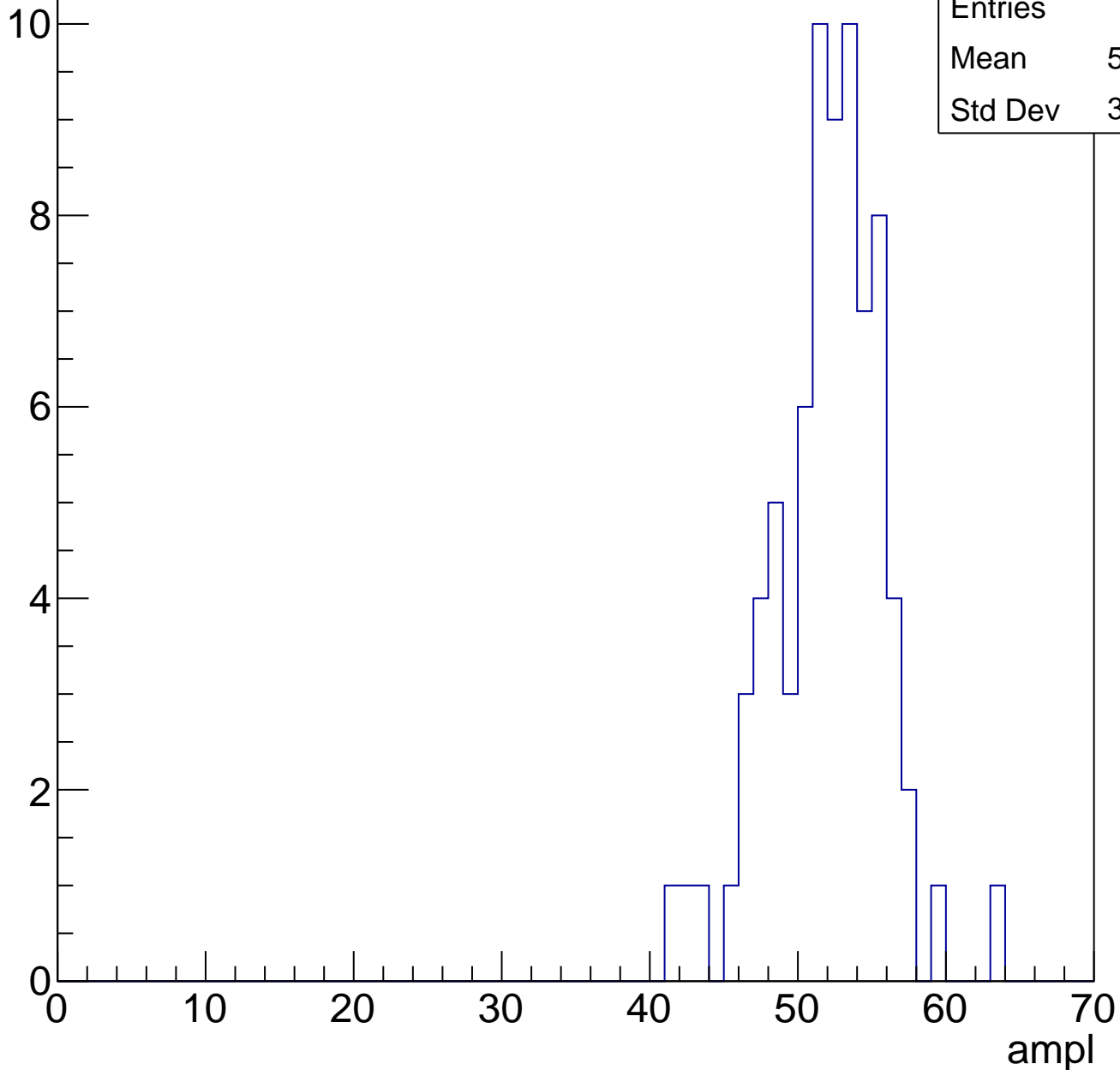


B1L101S, U26-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	51.56
Std Dev	3.743

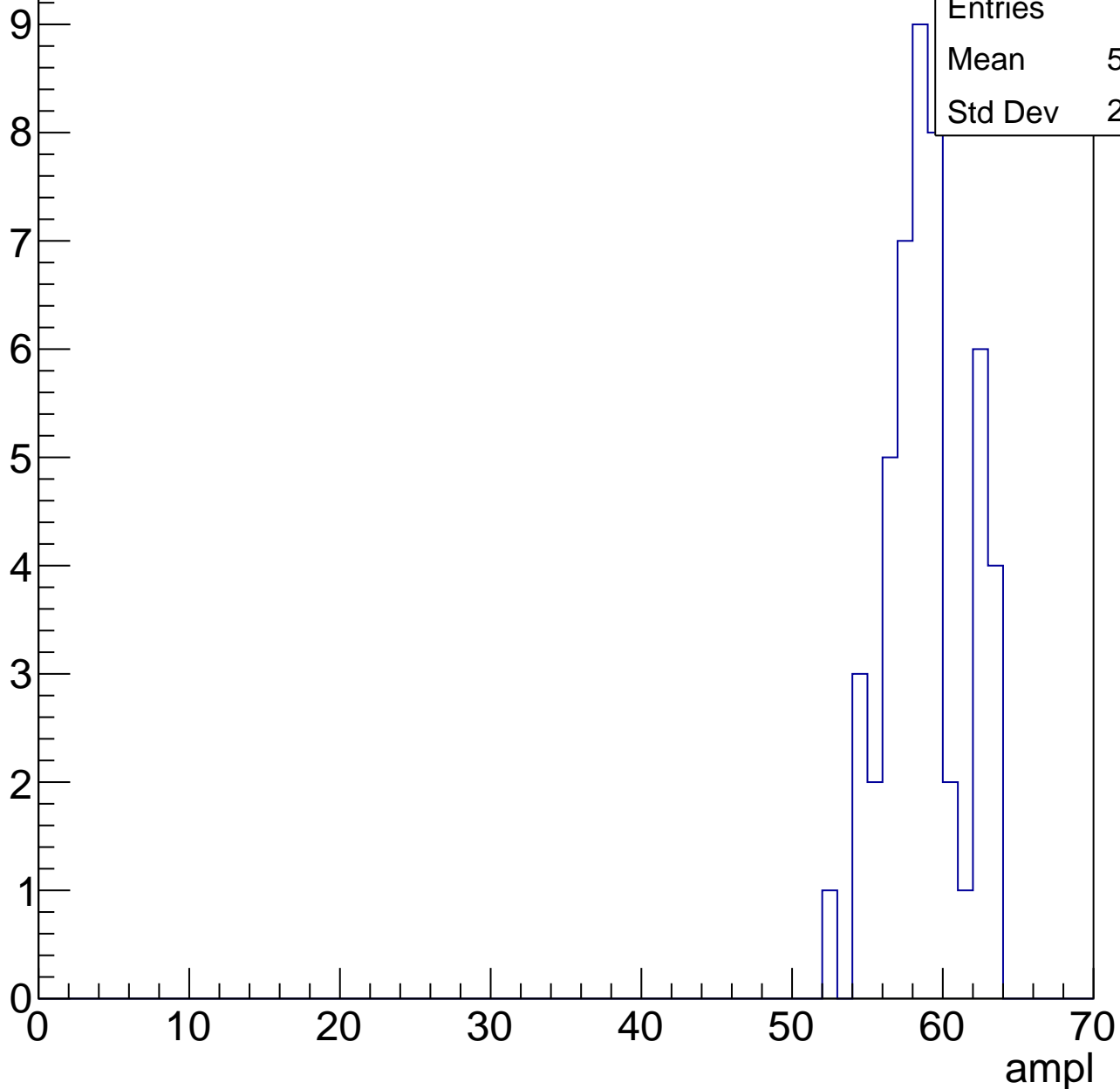
Entry



B1L101S, U26-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

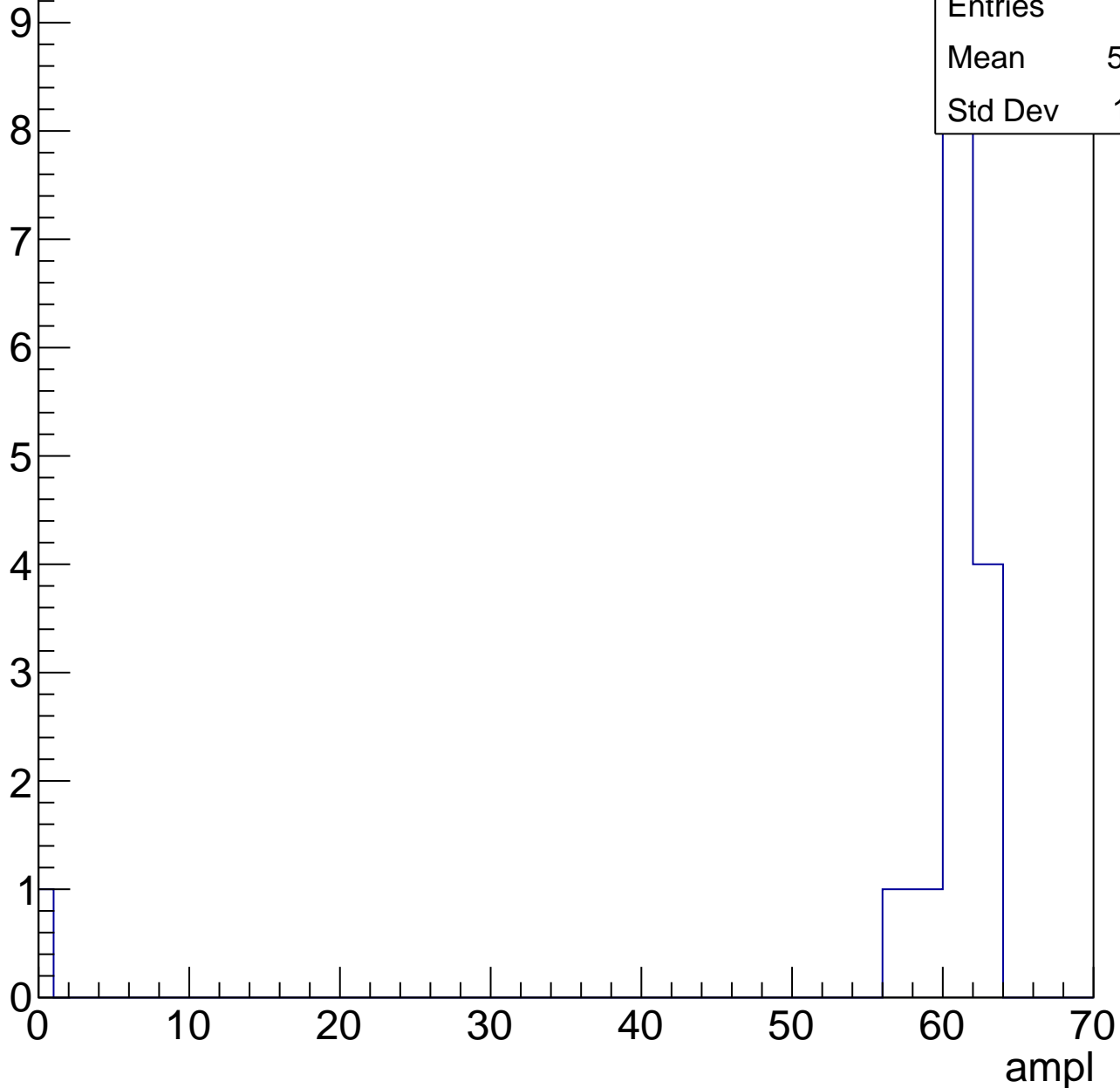
Entry



B1L101S, U26-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch64, adc0

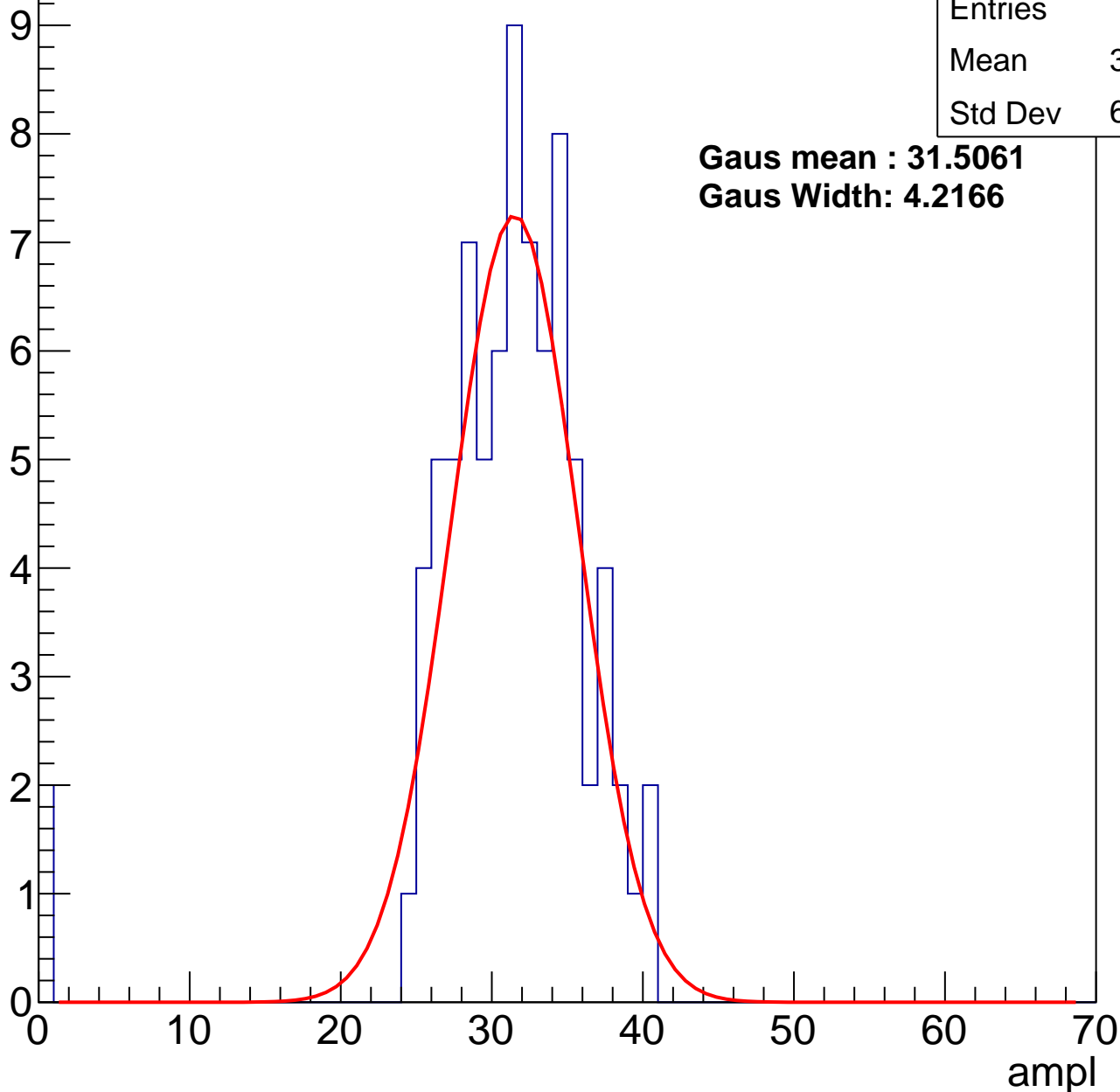
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.53
Std Dev	6.179

Gaus mean : 31.5061

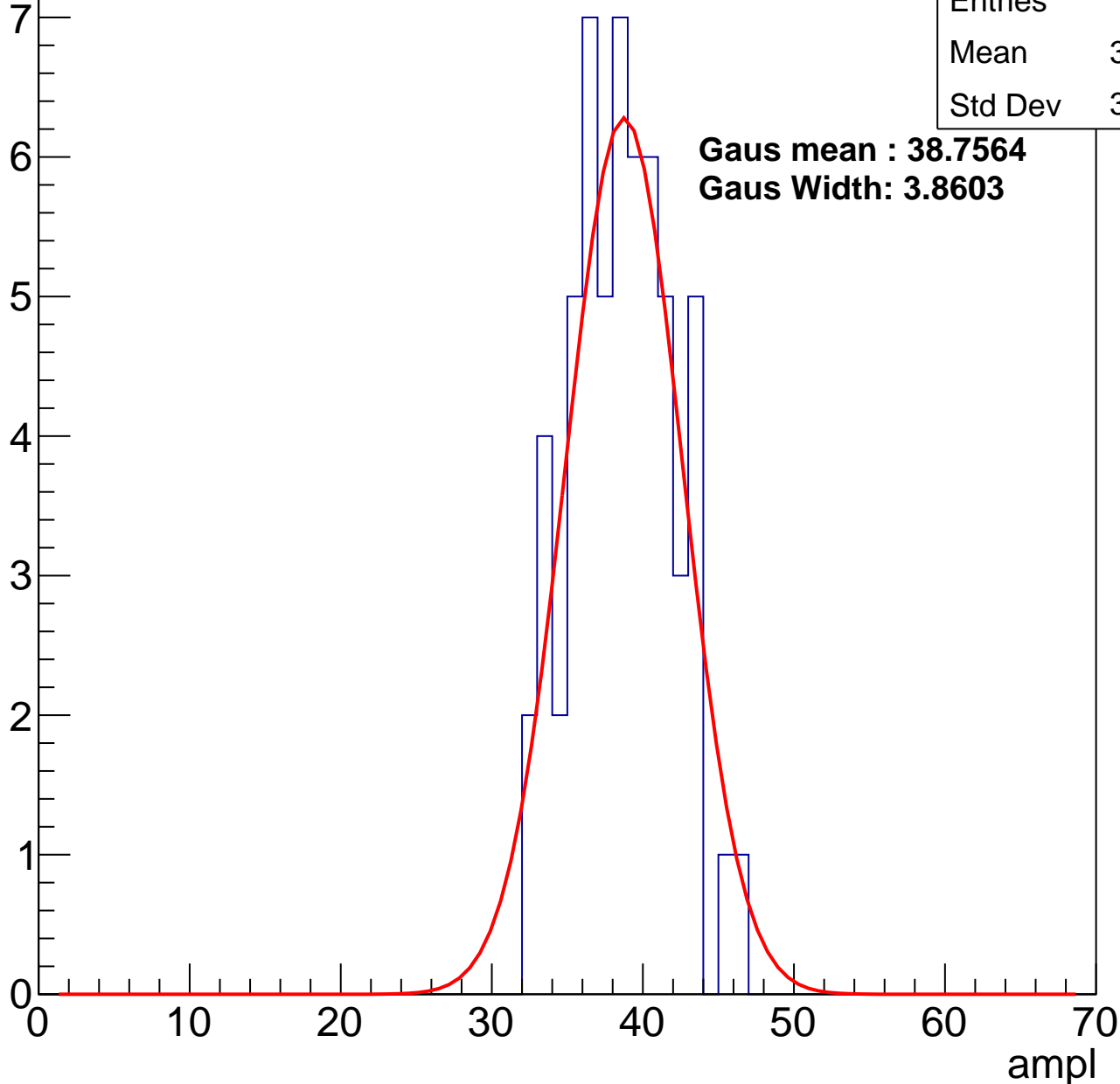
Gaus Width: 4.2166



B1L101S, U26-ch64, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch64, adc2

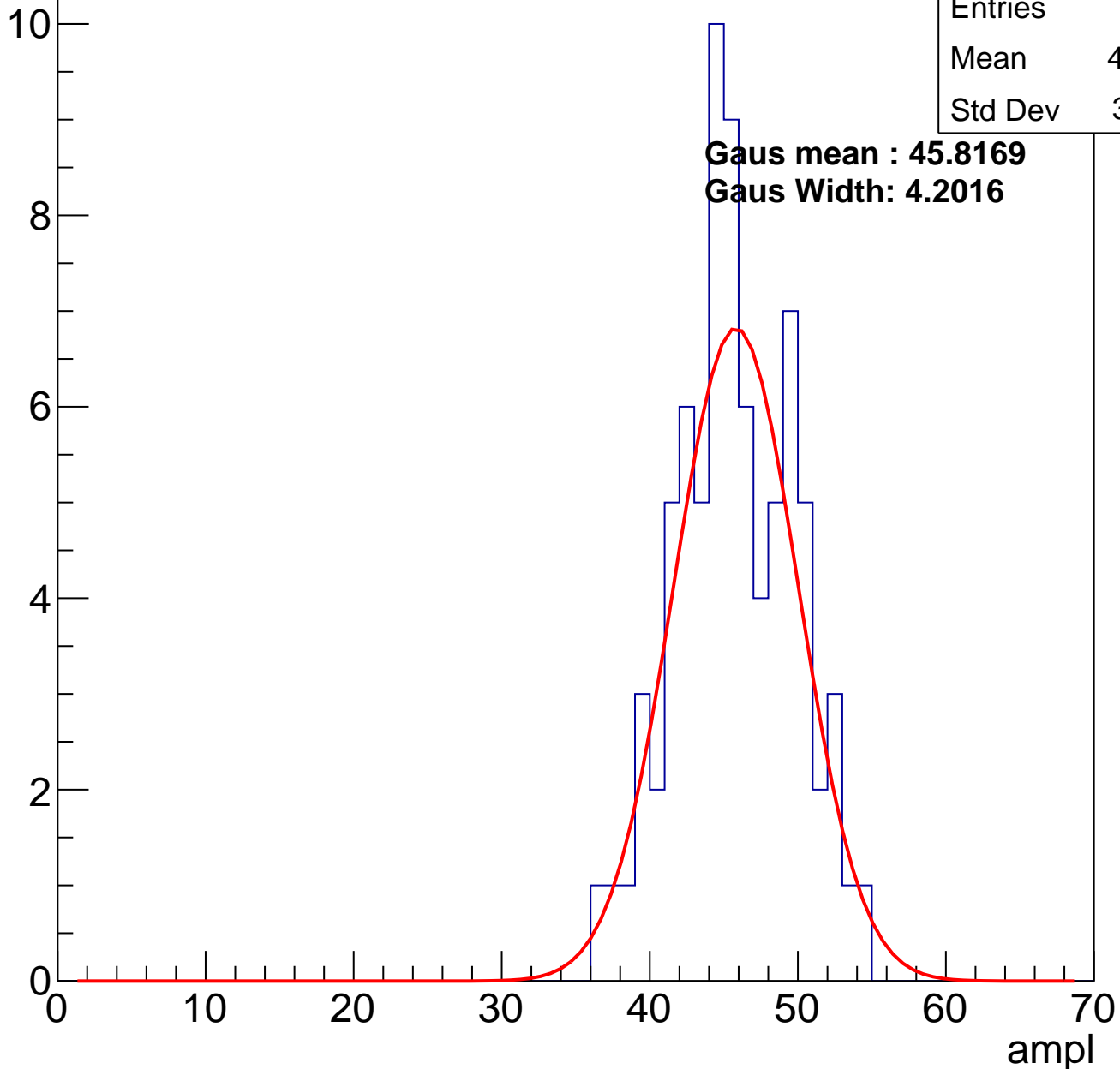
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	45.29
Std Dev	3.911

Gaus mean : 45.8169

Gaus Width: 4.2016

Entry

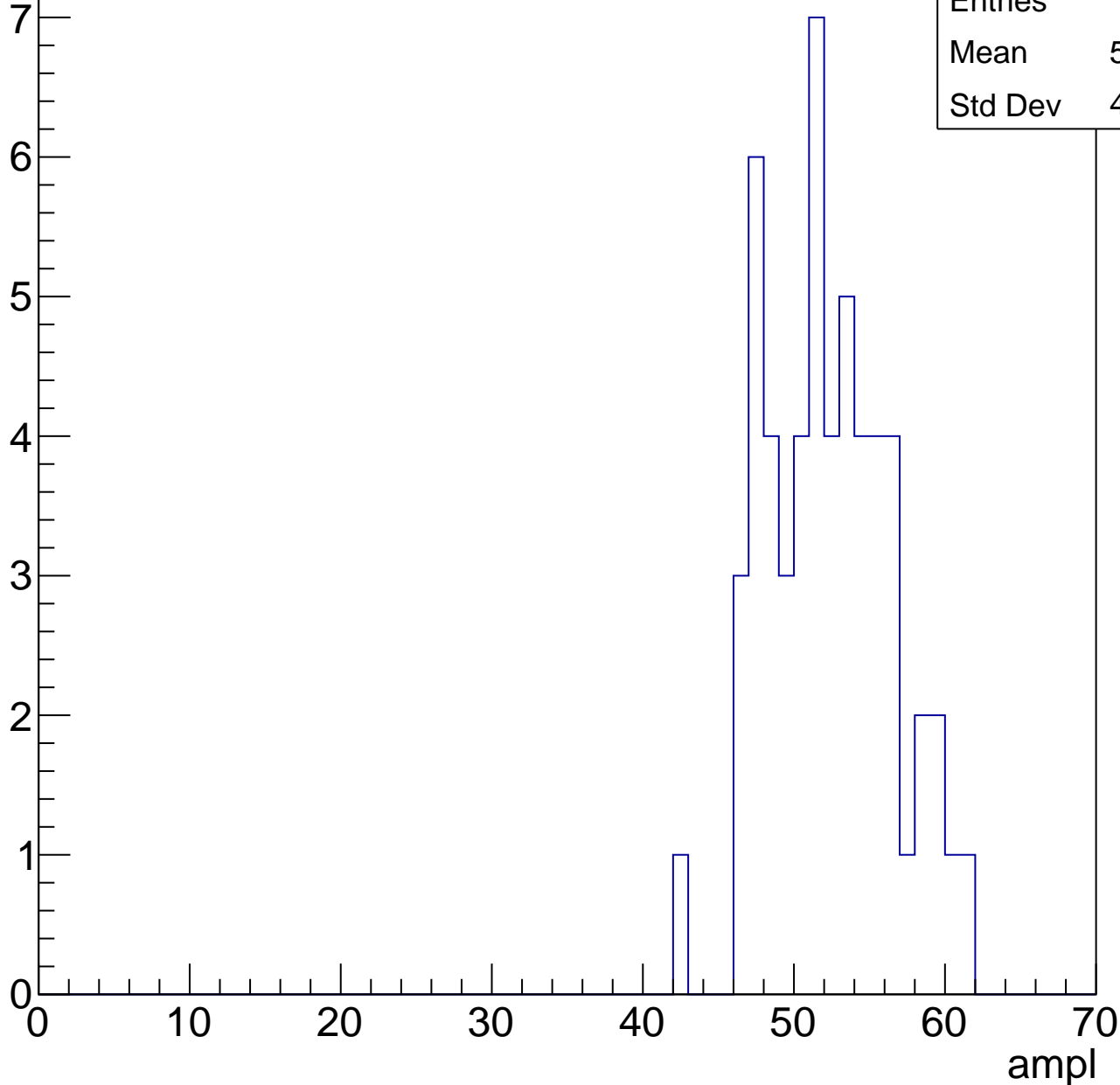


B1L101S, U26-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

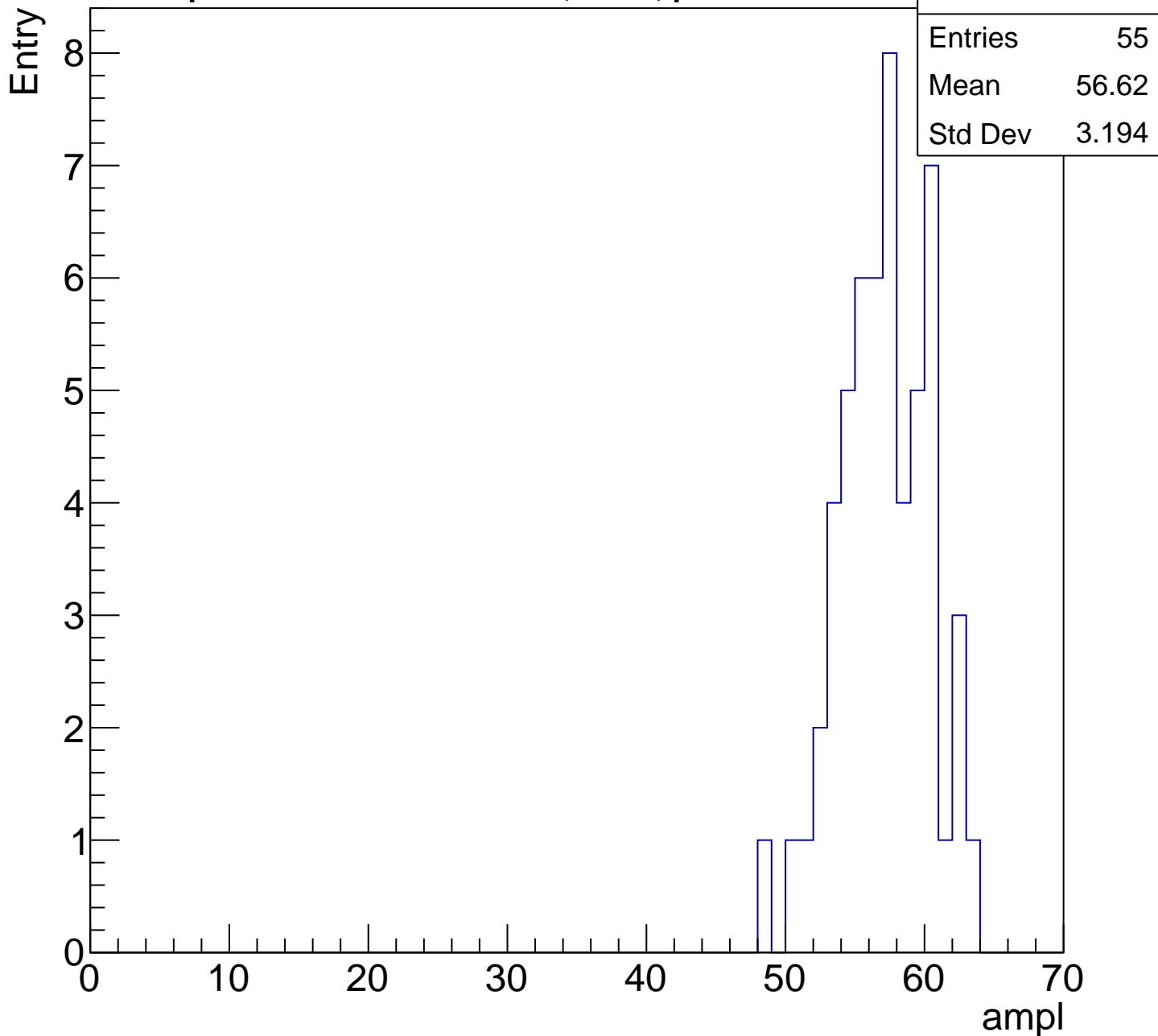
Entry

Entries	56
Mean	51.84
Std Dev	4.079



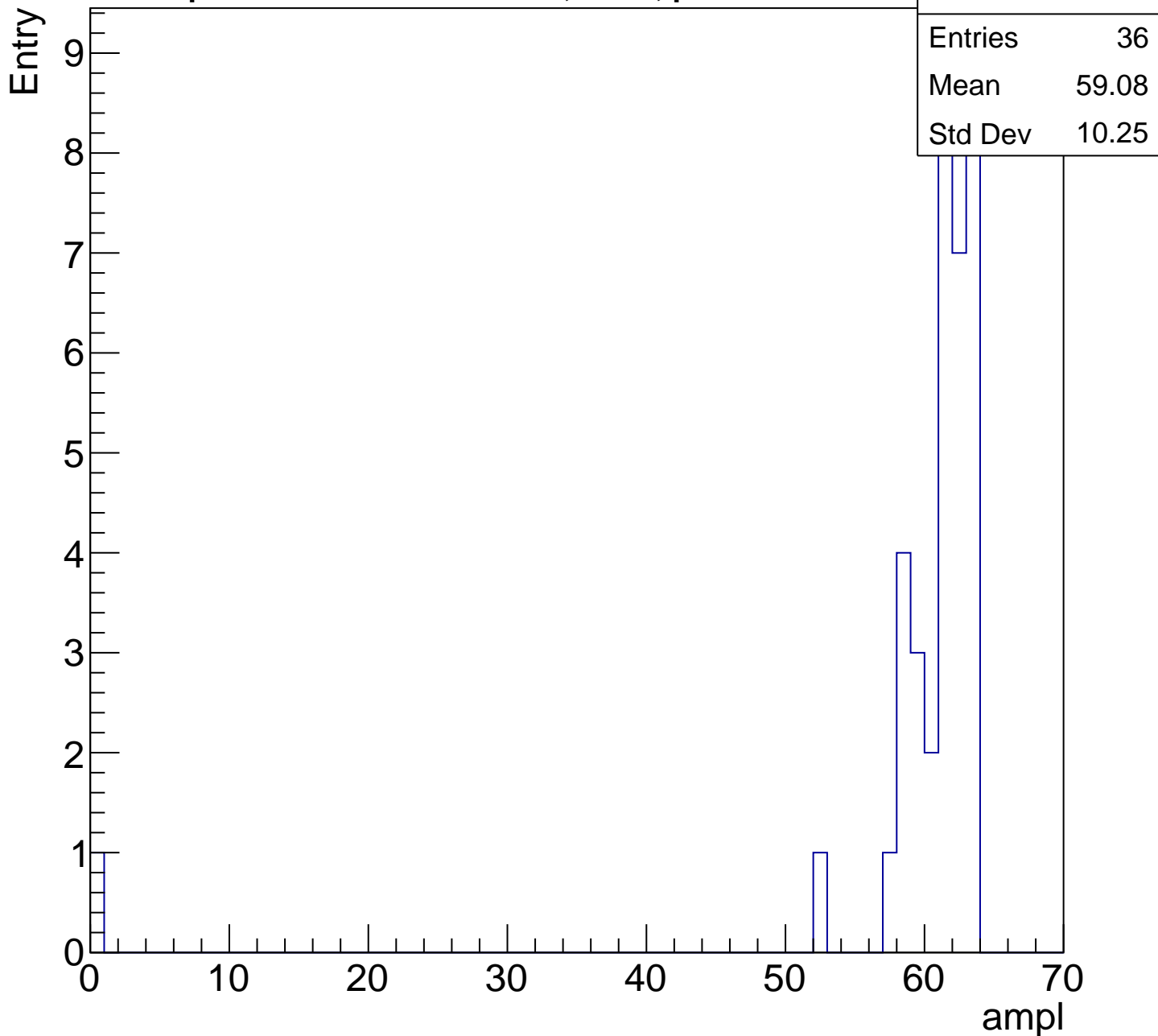
B1L101S, U26-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch65, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	30.43
Std Dev	3.823

Gaus mean : 30.5120

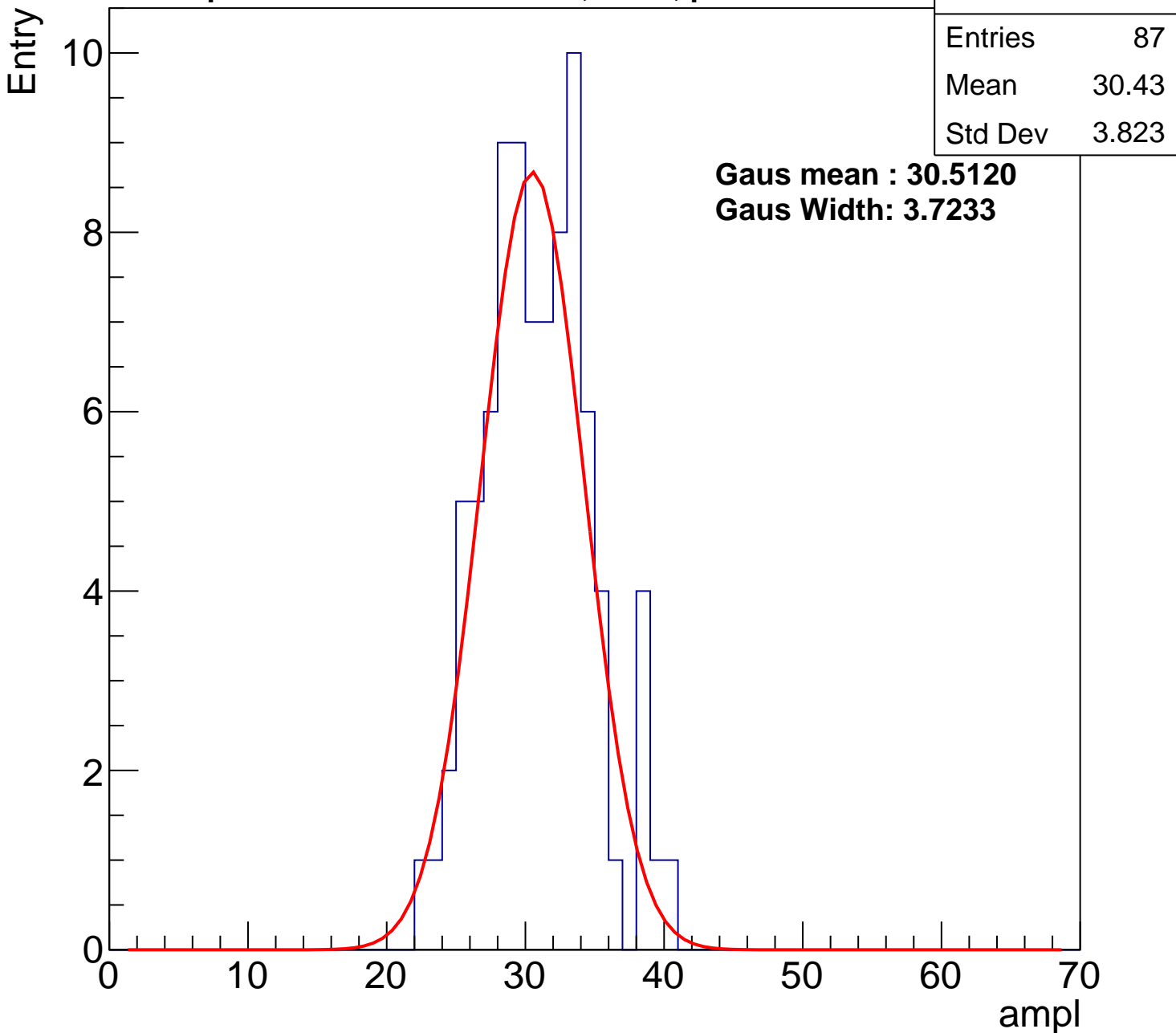
Gaus Width: 3.7233

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

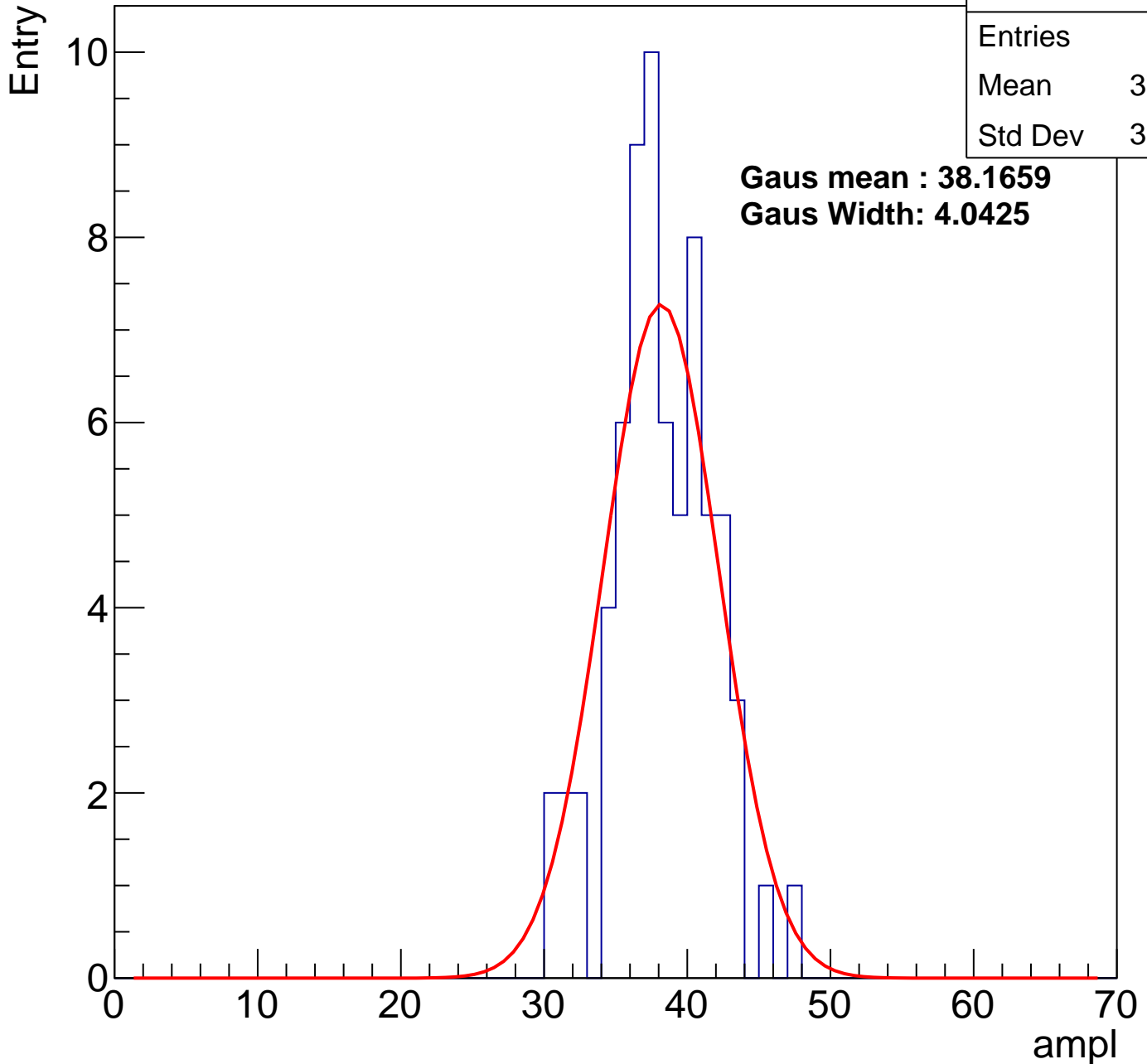


B1L101S, U26-ch65, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	37.75
Std Dev	3.457

Gaus mean : 38.1659
Gaus Width: 4.0425



B1L101S, U26-ch65, adc2

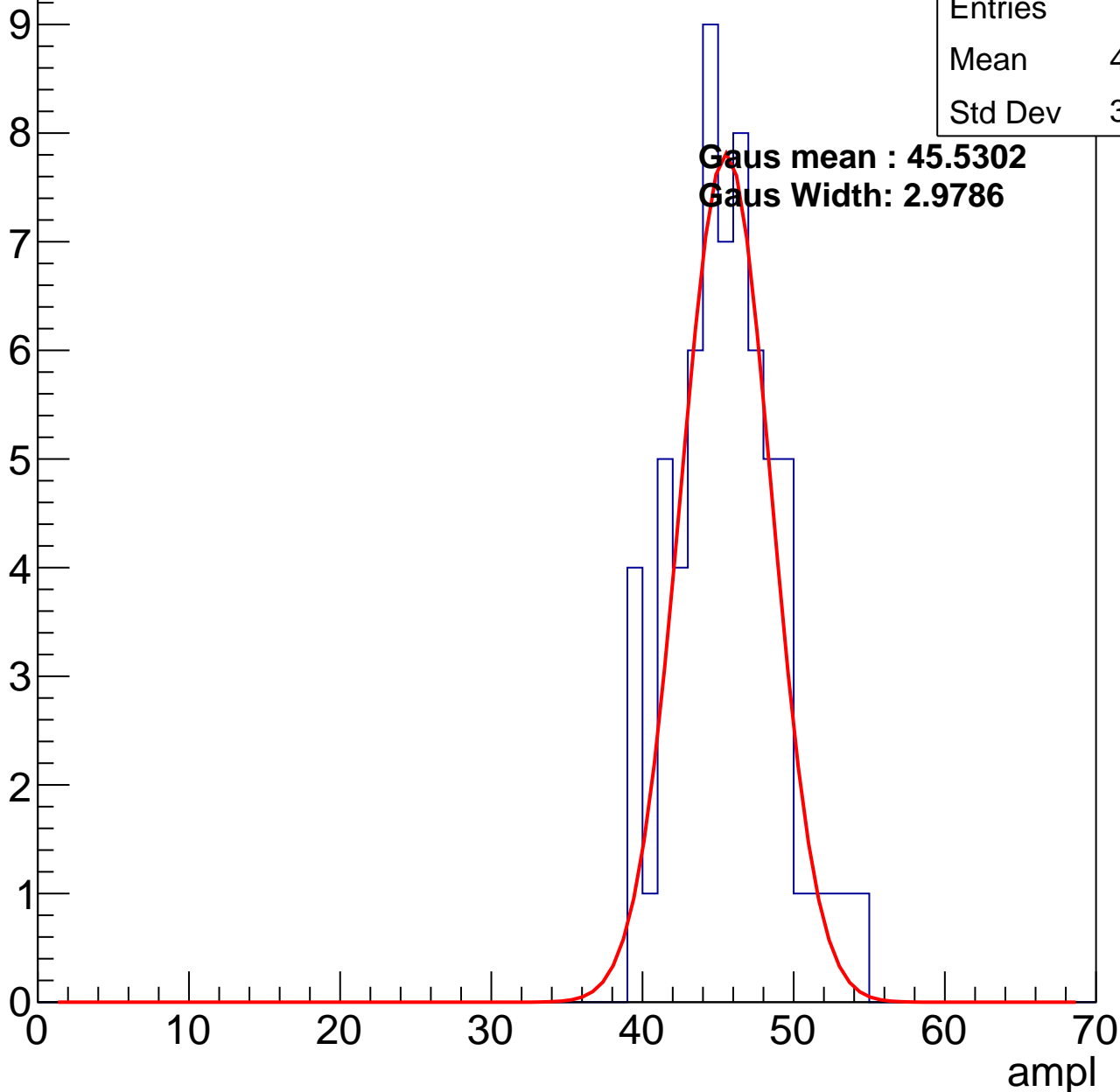
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.12
Std Dev	3.349

Gaus mean : 45.5302

Gaus Width: 2.9786

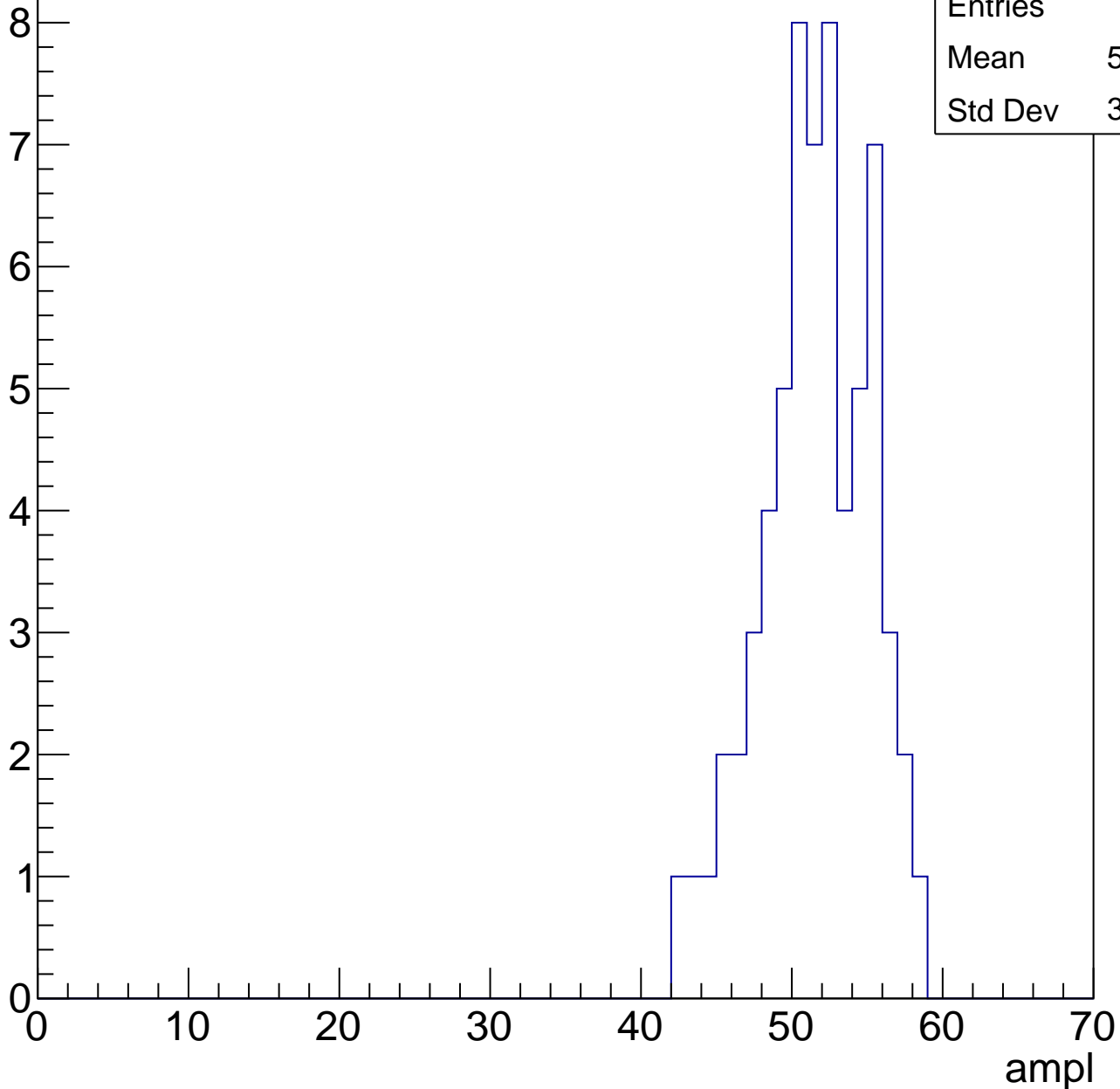


B1L101S, U26-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.08
Std Dev	3.546

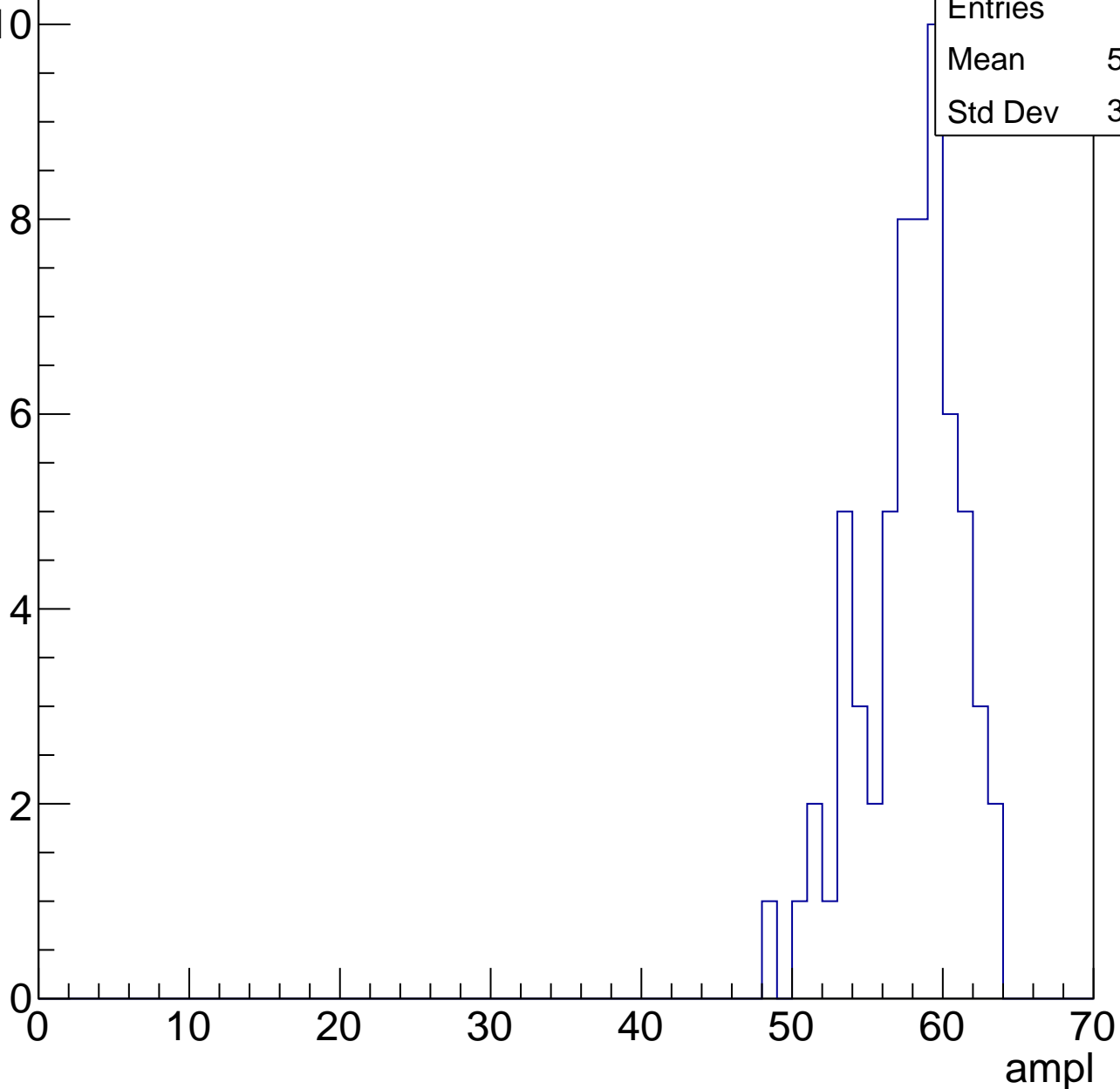


B1L101S, U26-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	57.35
Std Dev	3.283

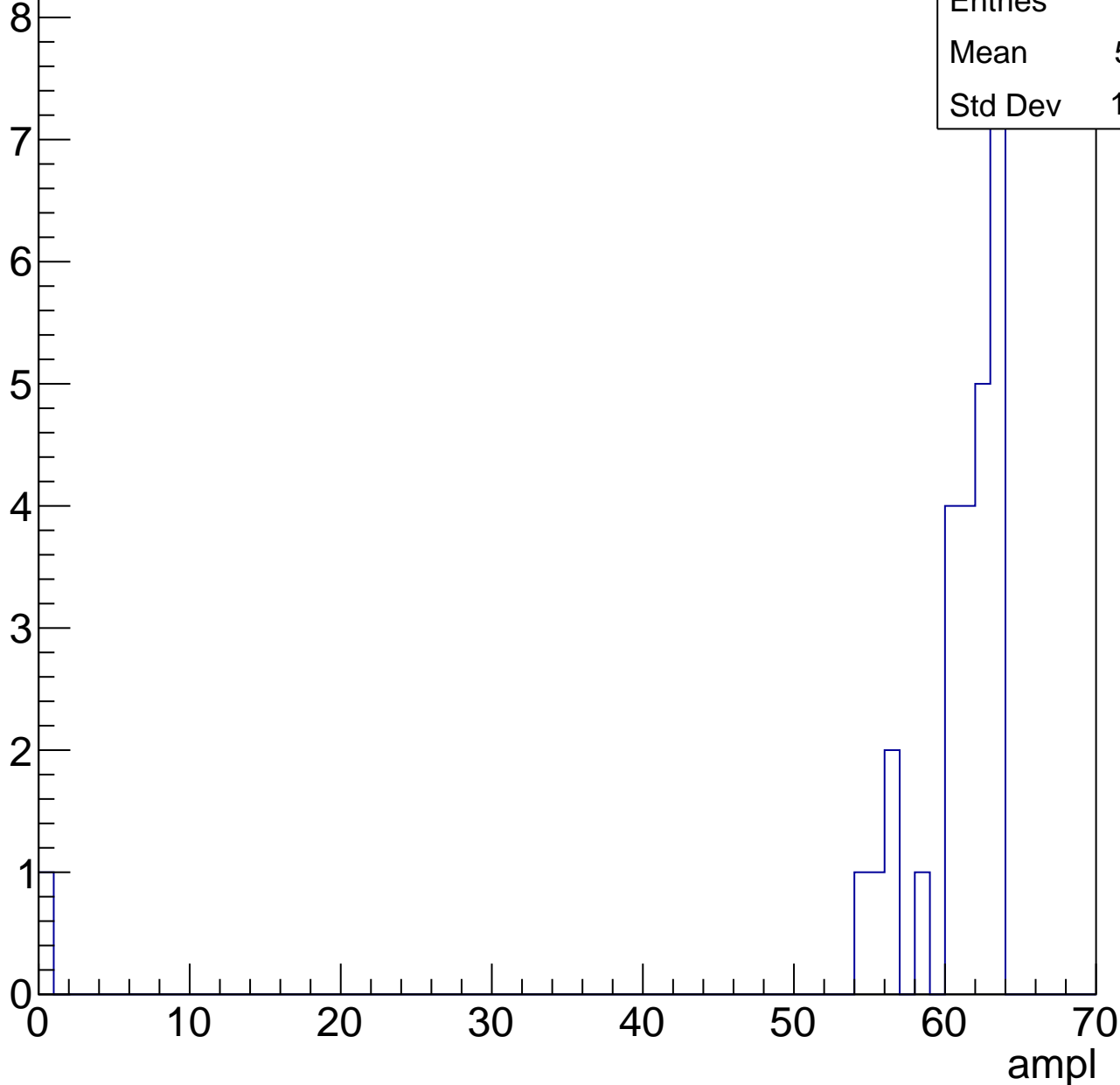


B1L101S, U26-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.41
Std Dev	11.75



B1L101S, U26-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch66, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	30.1
Std Dev	3.836

Gaus mean : 31.2824

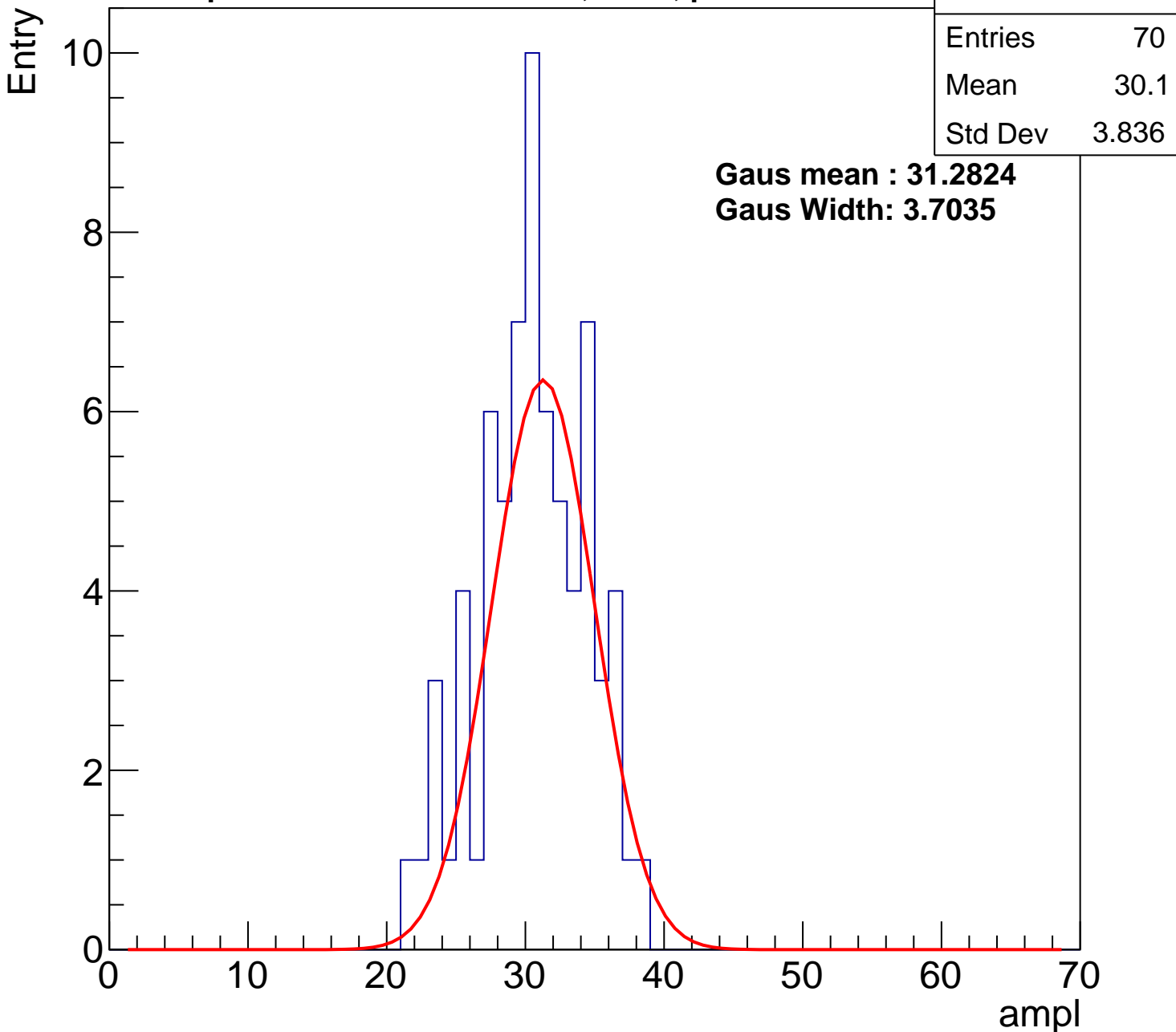
Gaus Width: 3.7035

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch66, adc1

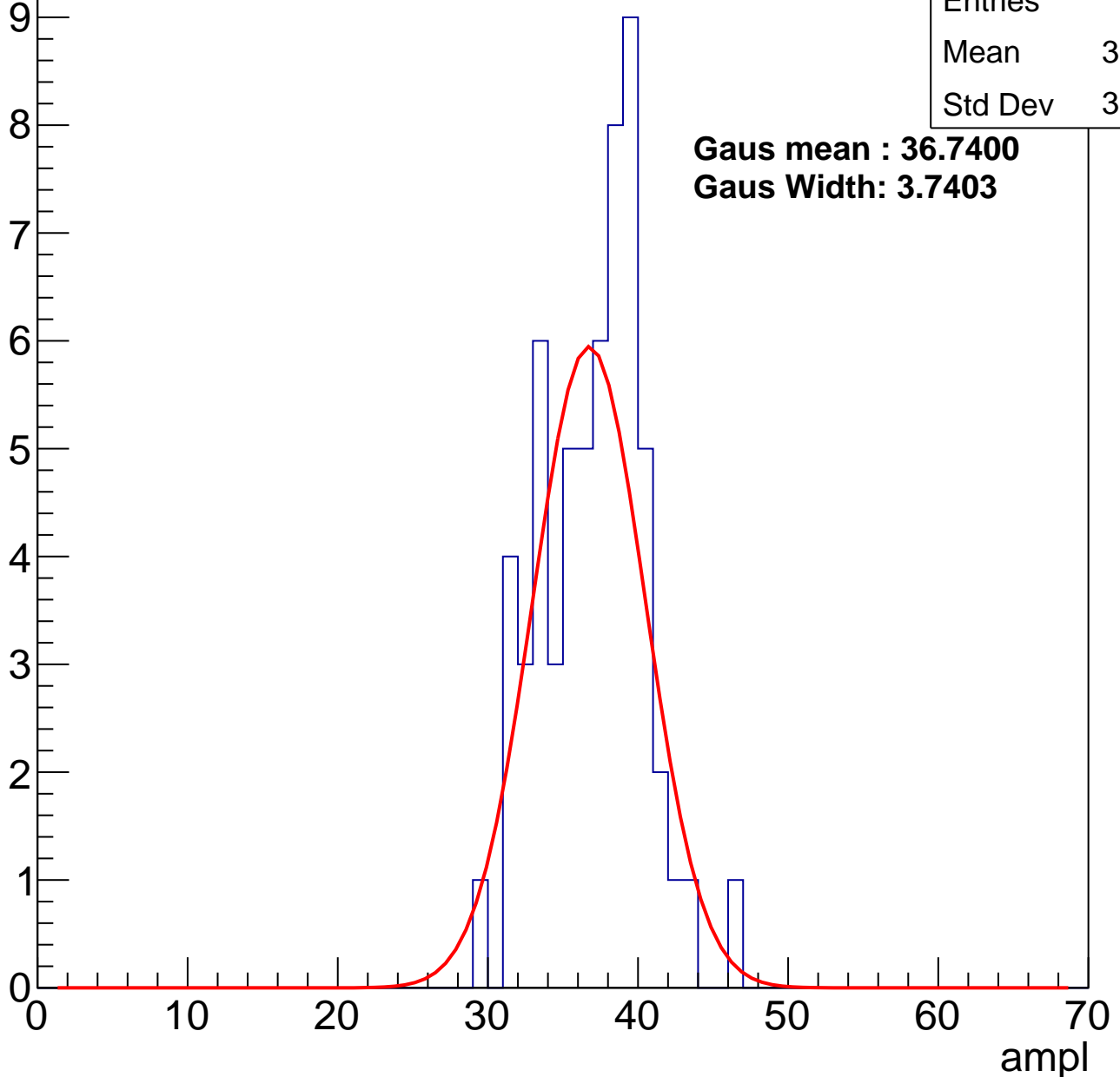
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.57
Std Dev	3.373

Gaus mean : 36.7400

Gaus Width: 3.7403



B1L101S, U26-ch66, adc2

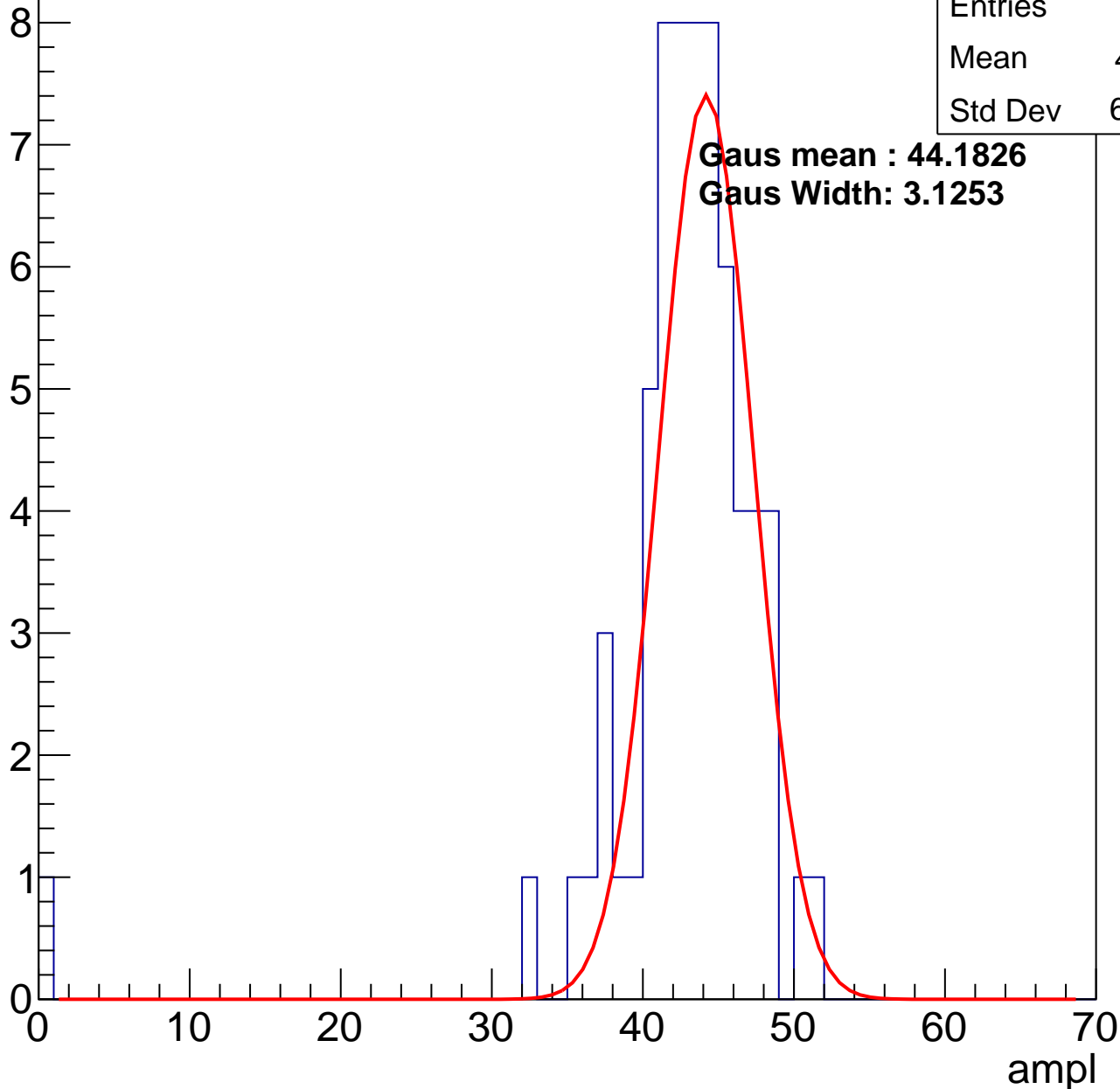
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.21
Std Dev	6.302

Gaus mean : 44.1826

Gaus Width: 3.1253



B1L101S, U26-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

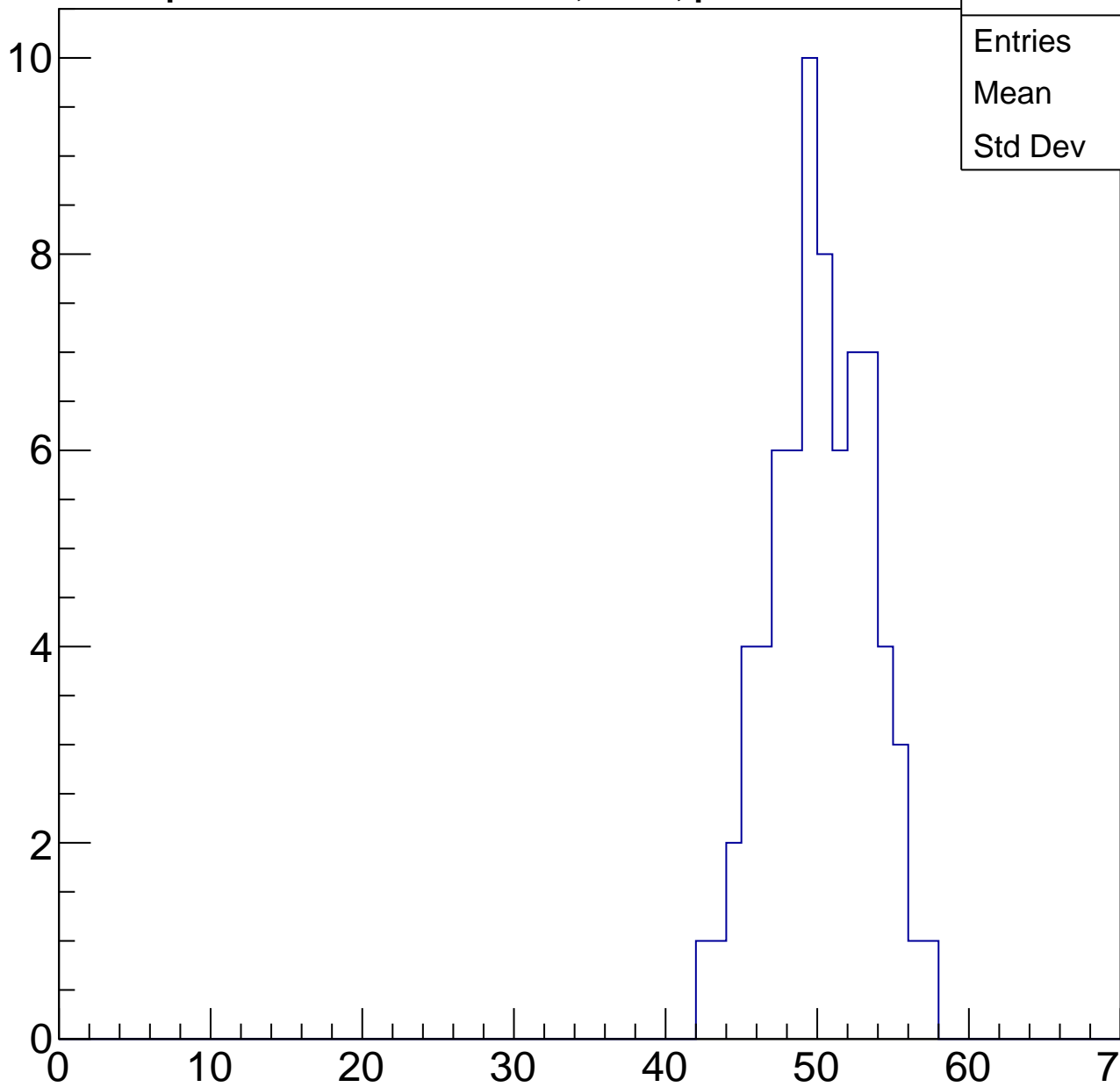
Entries	71
Mean	49.75
Std Dev	3.258

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

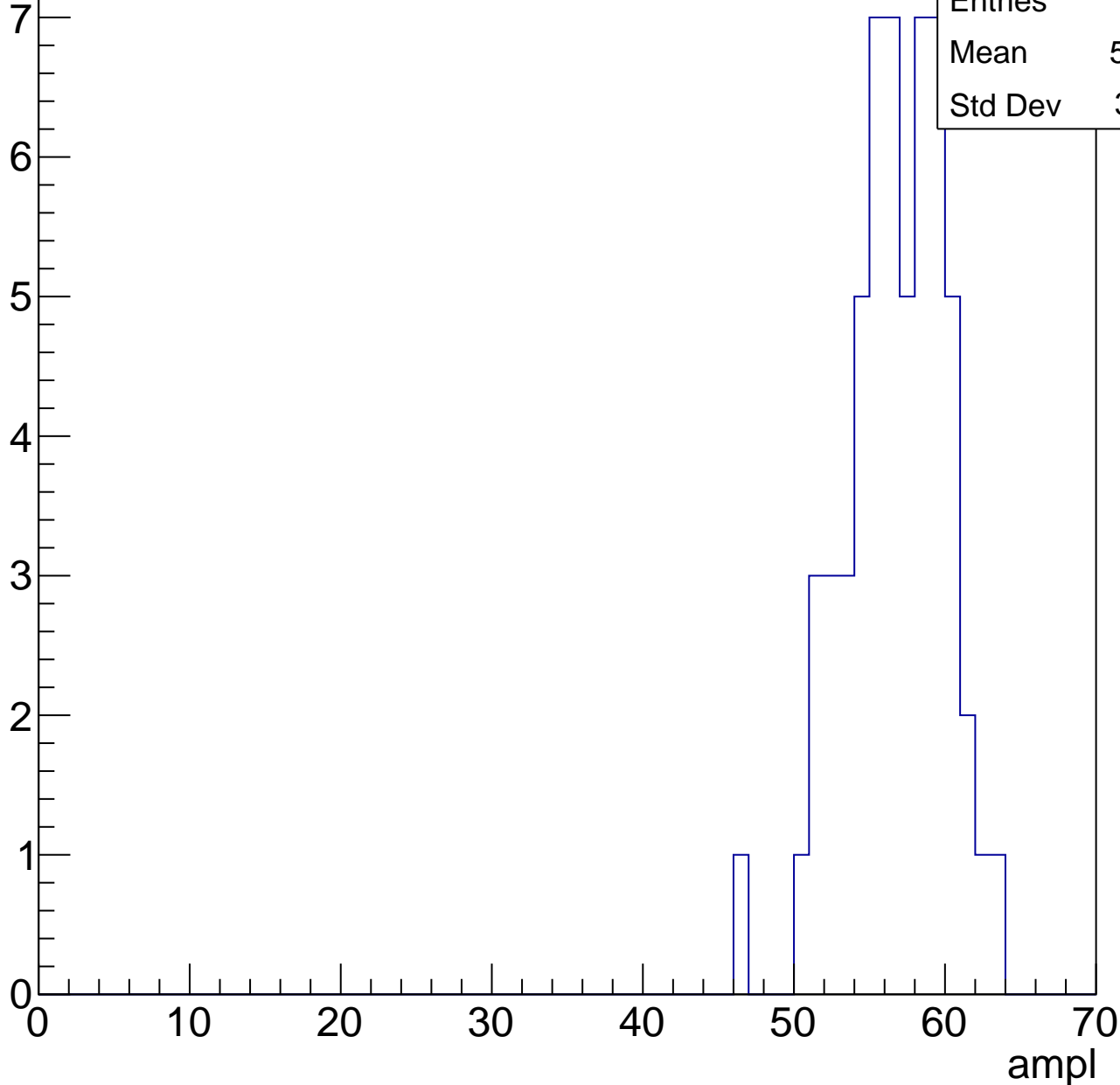


B1L101S, U26-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

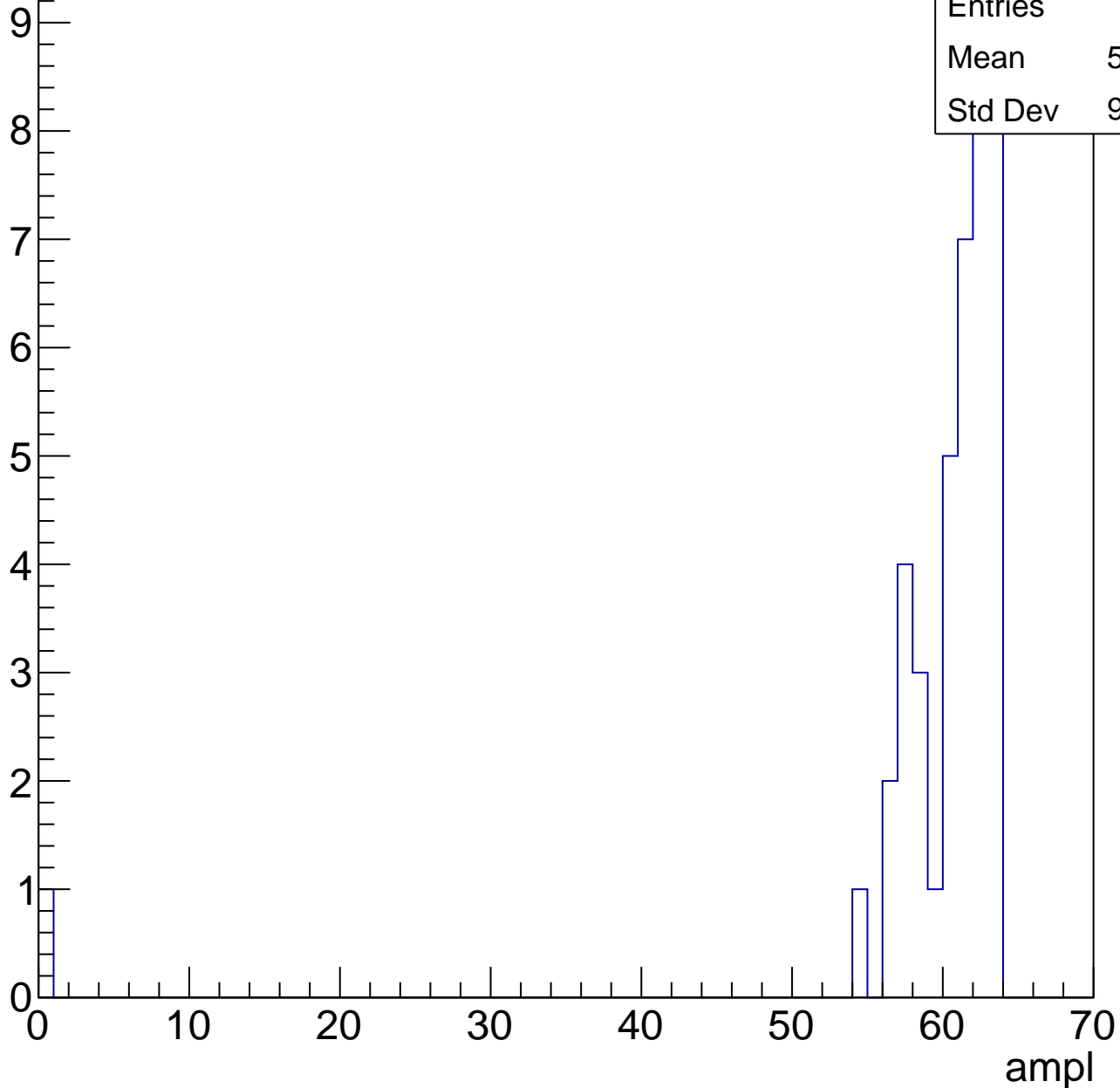
Entries	58
Mean	56.24
Std Dev	3.271



B1L101S, U26-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

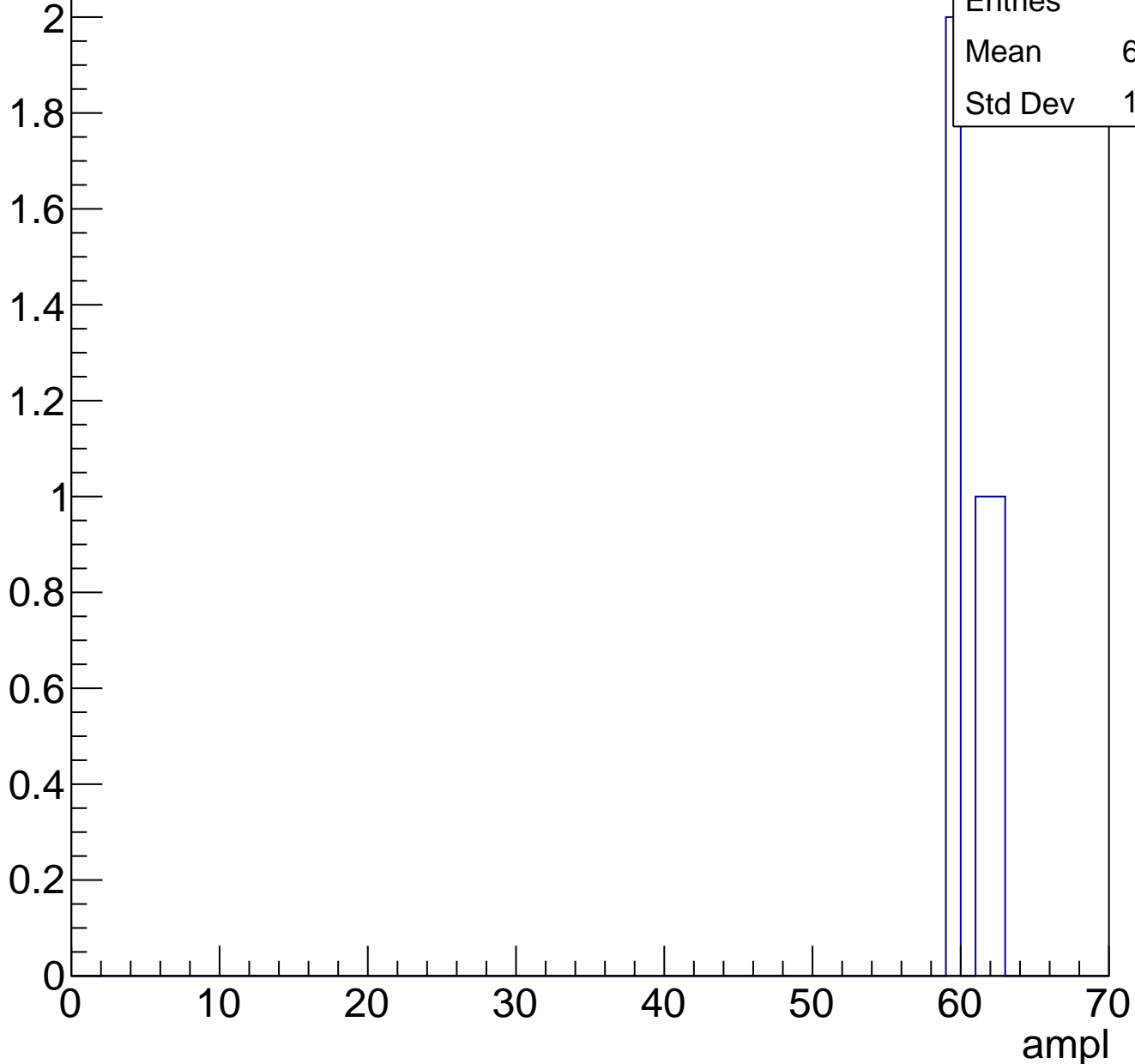
Entry



B1L101S, U26-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	60.25
Std Dev	1.299

B1L101S, U26-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch67, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	32.2
Std Dev	3.046

Gaus mean : 32.6750

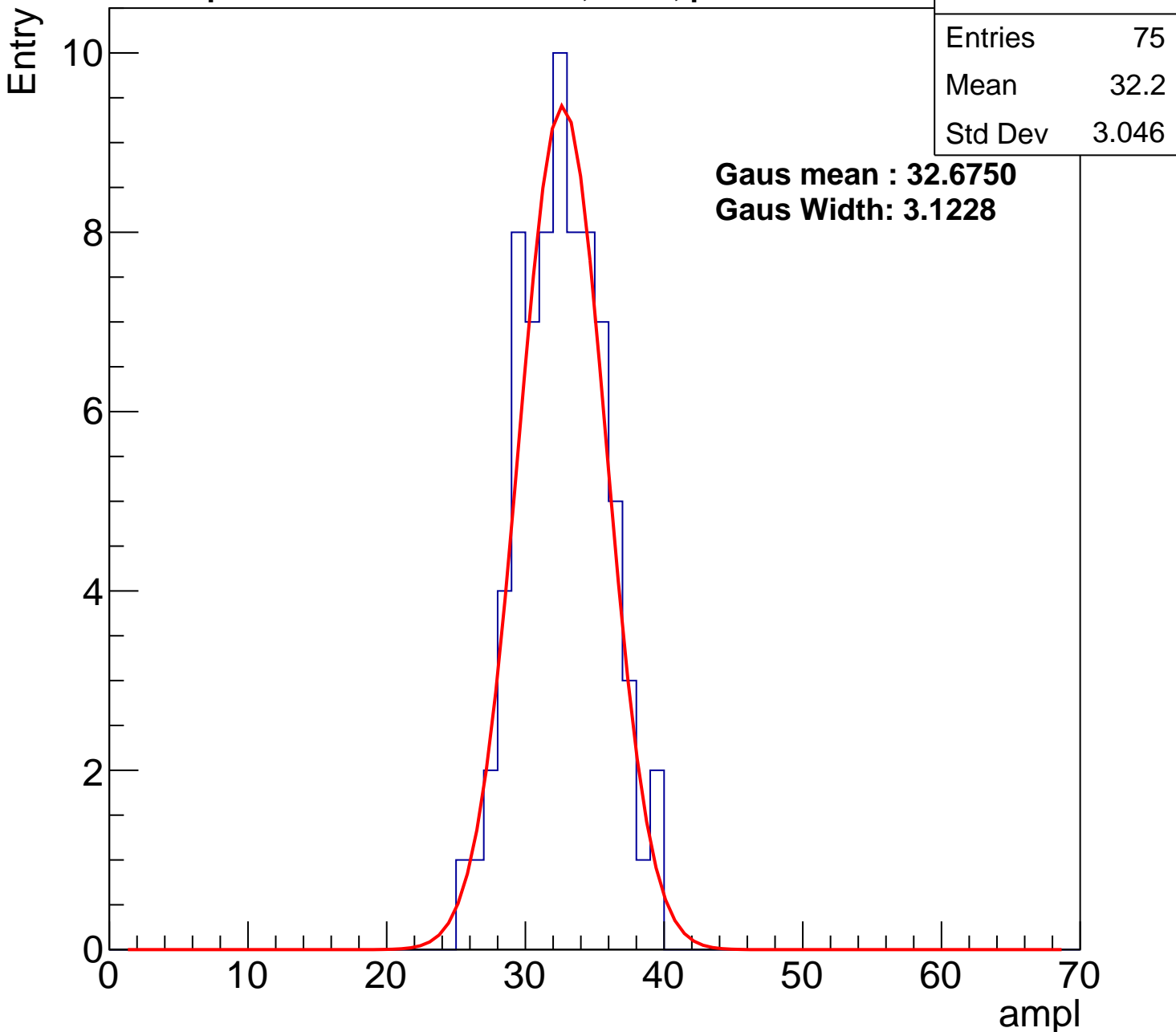
Gaus Width: 3.1228

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



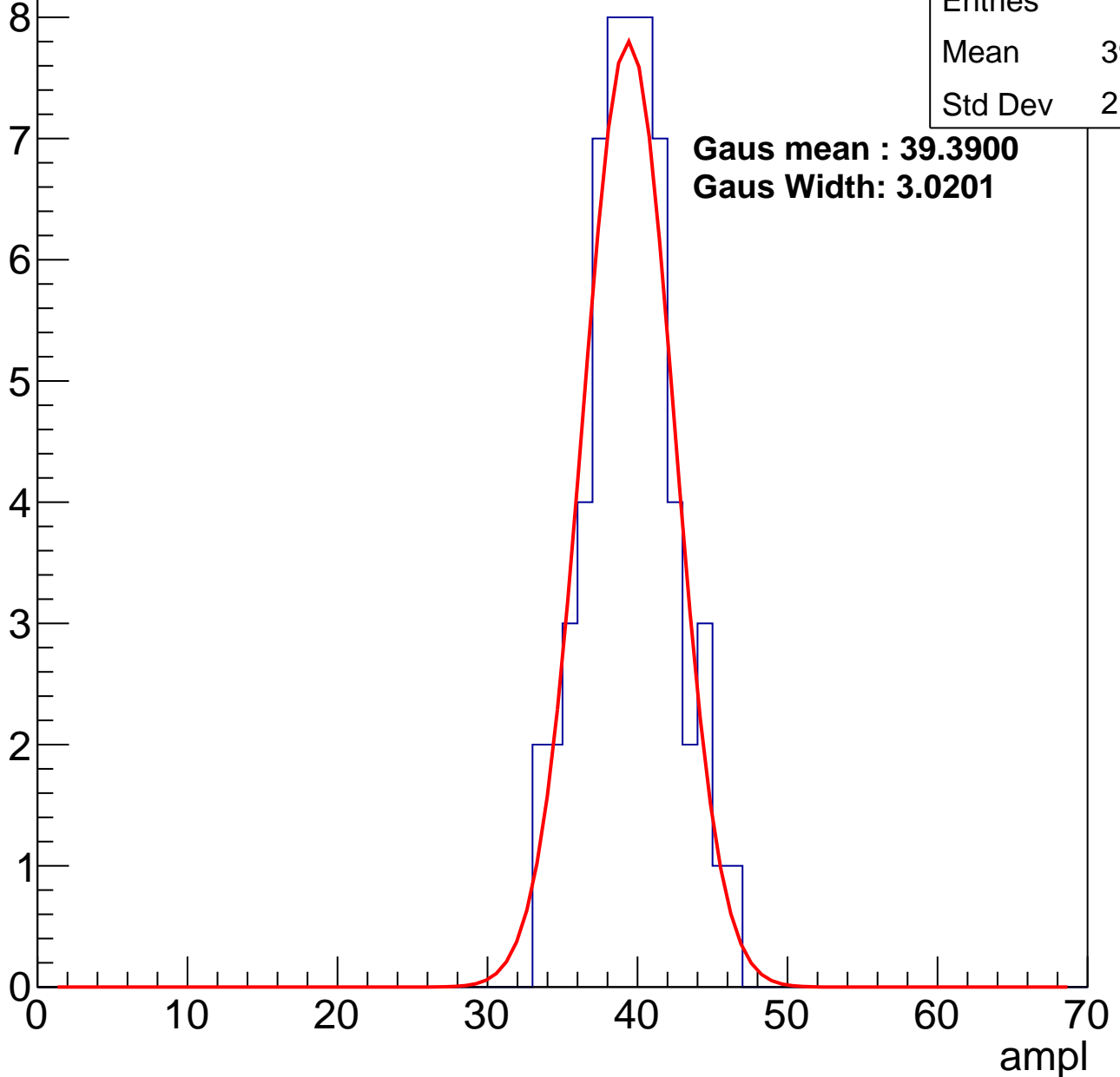
B1L101S, U26-ch67, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	39.03
Std Dev	2.904

Gaus mean : 39.3900
Gaus Width: 3.0201



B1L101S, U26-ch67, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	46.32
Std Dev	3.628

Gaus mean : 46.6554

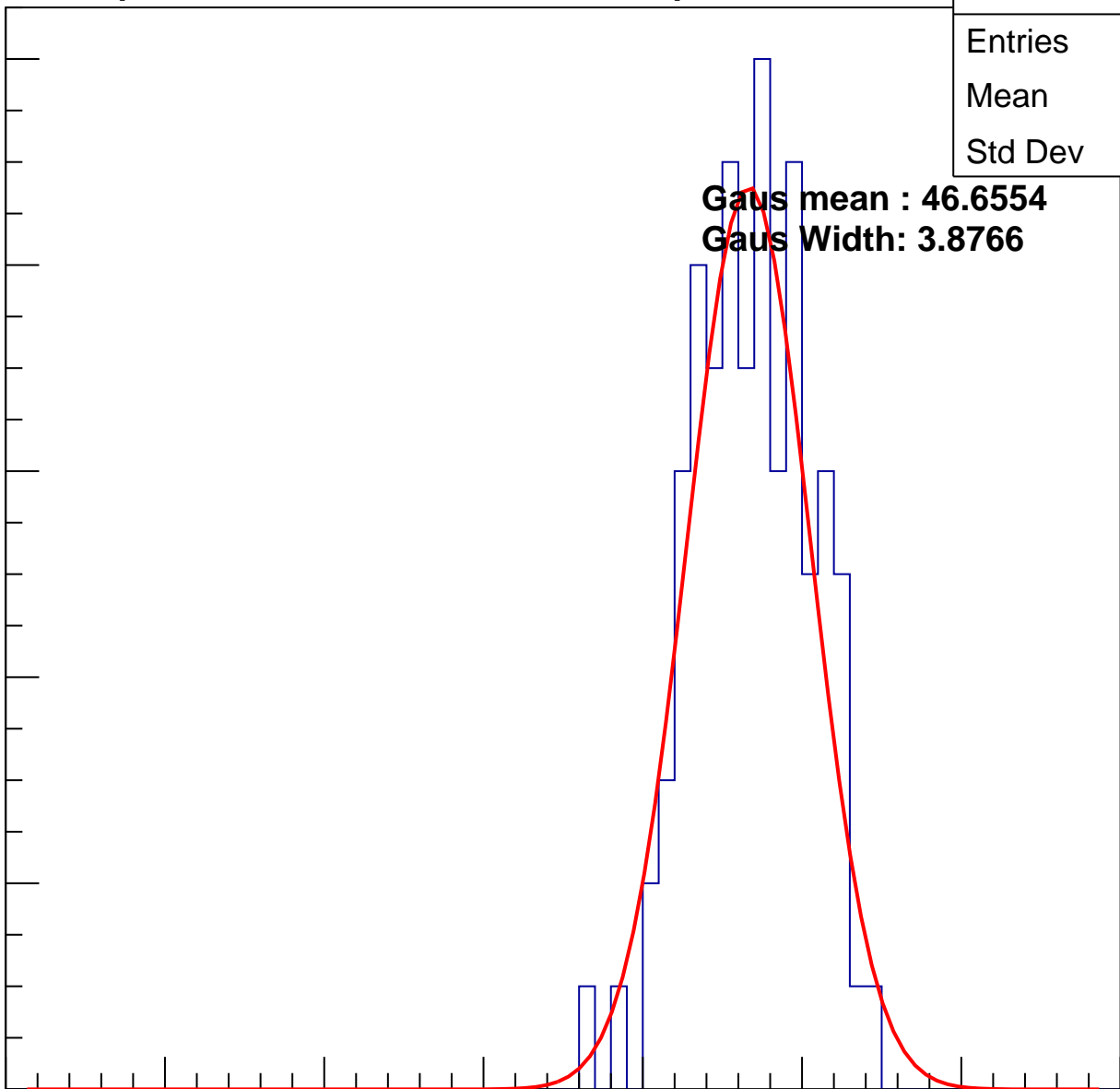
Gaus Width: 3.8766

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

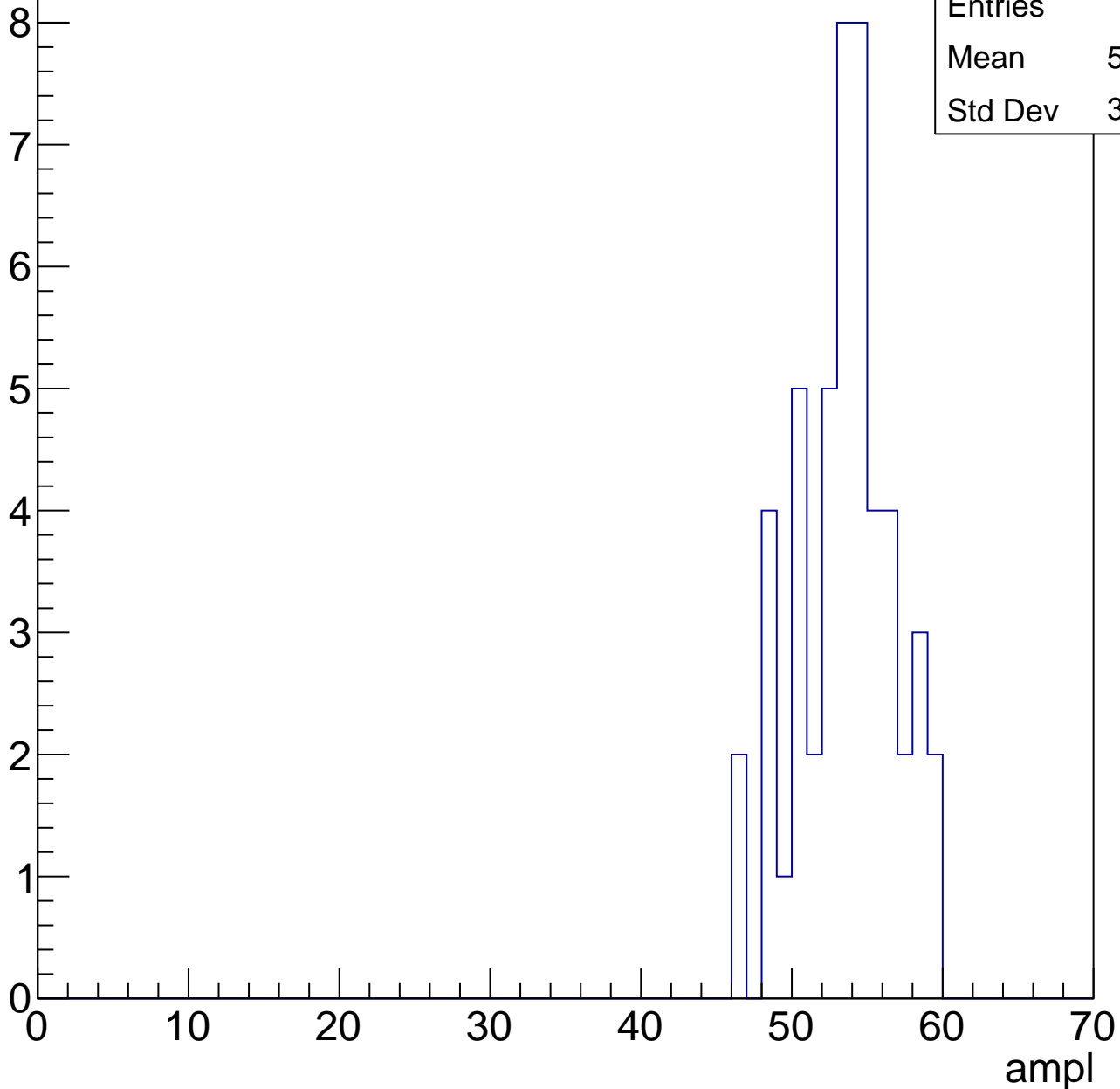


B1L101S, U26-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	53.02
Std Dev	3.197

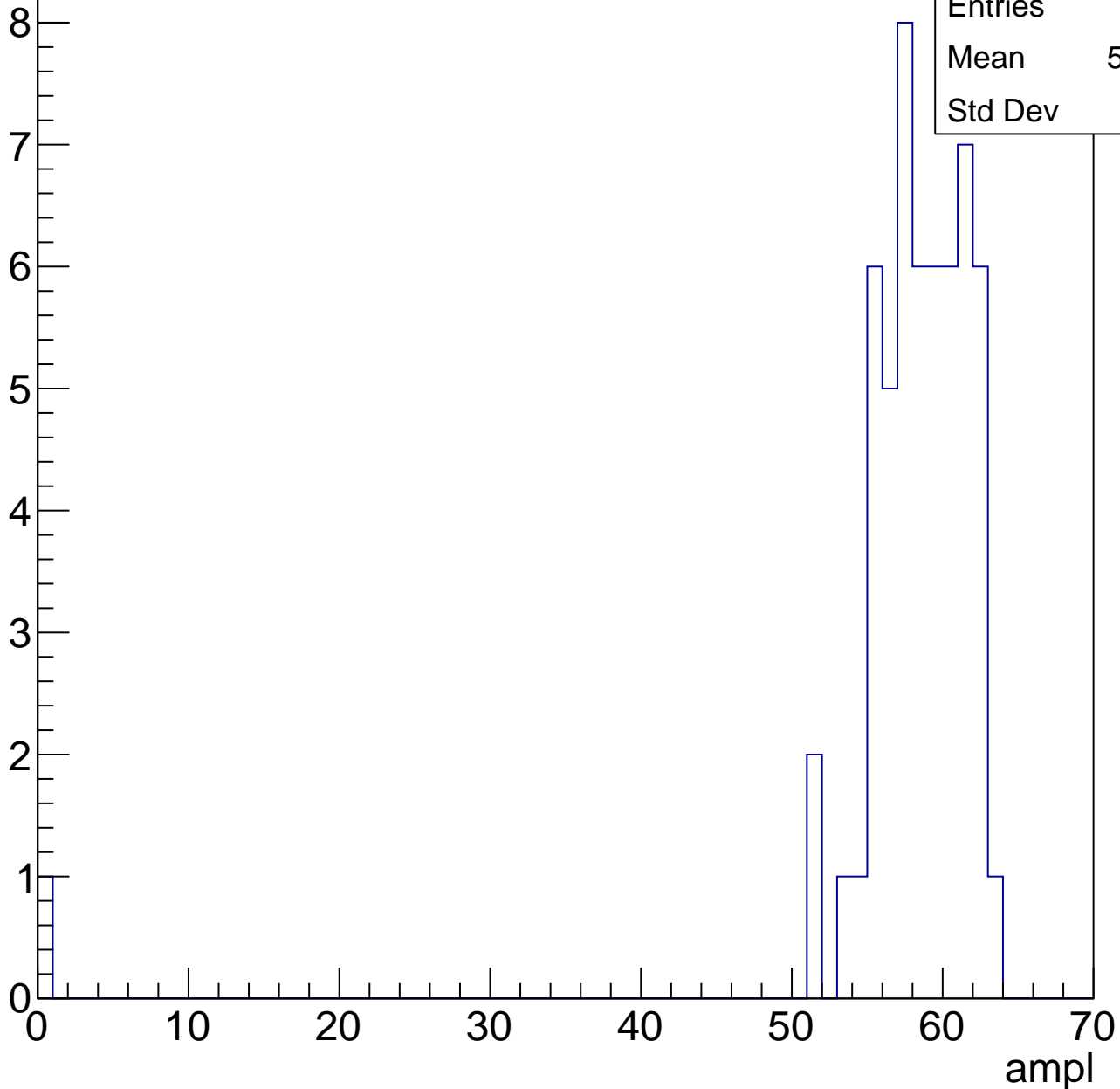


B1L101S, U26-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.12
Std Dev	8.19

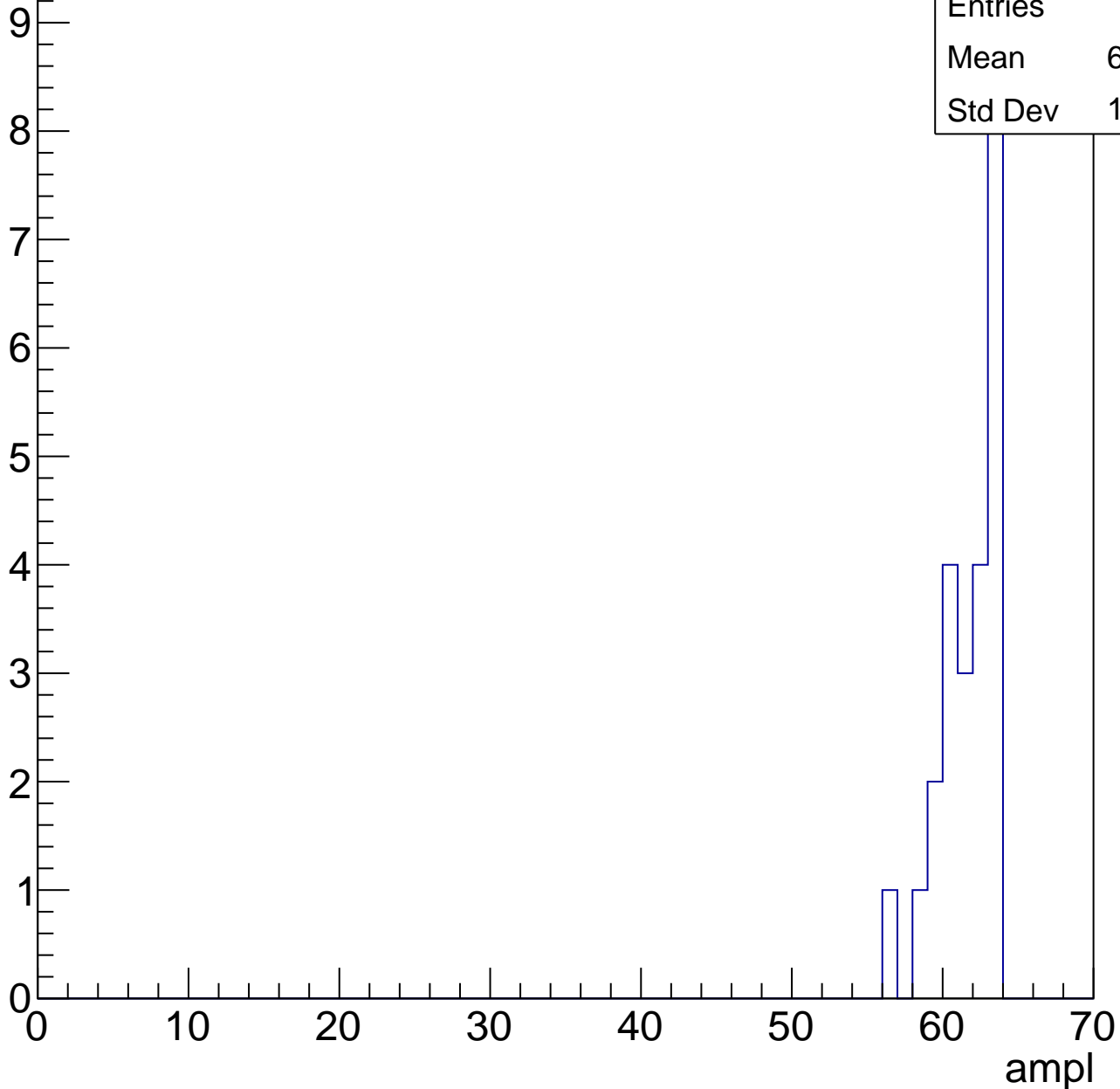


B1L101S, U26-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	61.25
Std Dev	1.876



B1L101S, U26-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

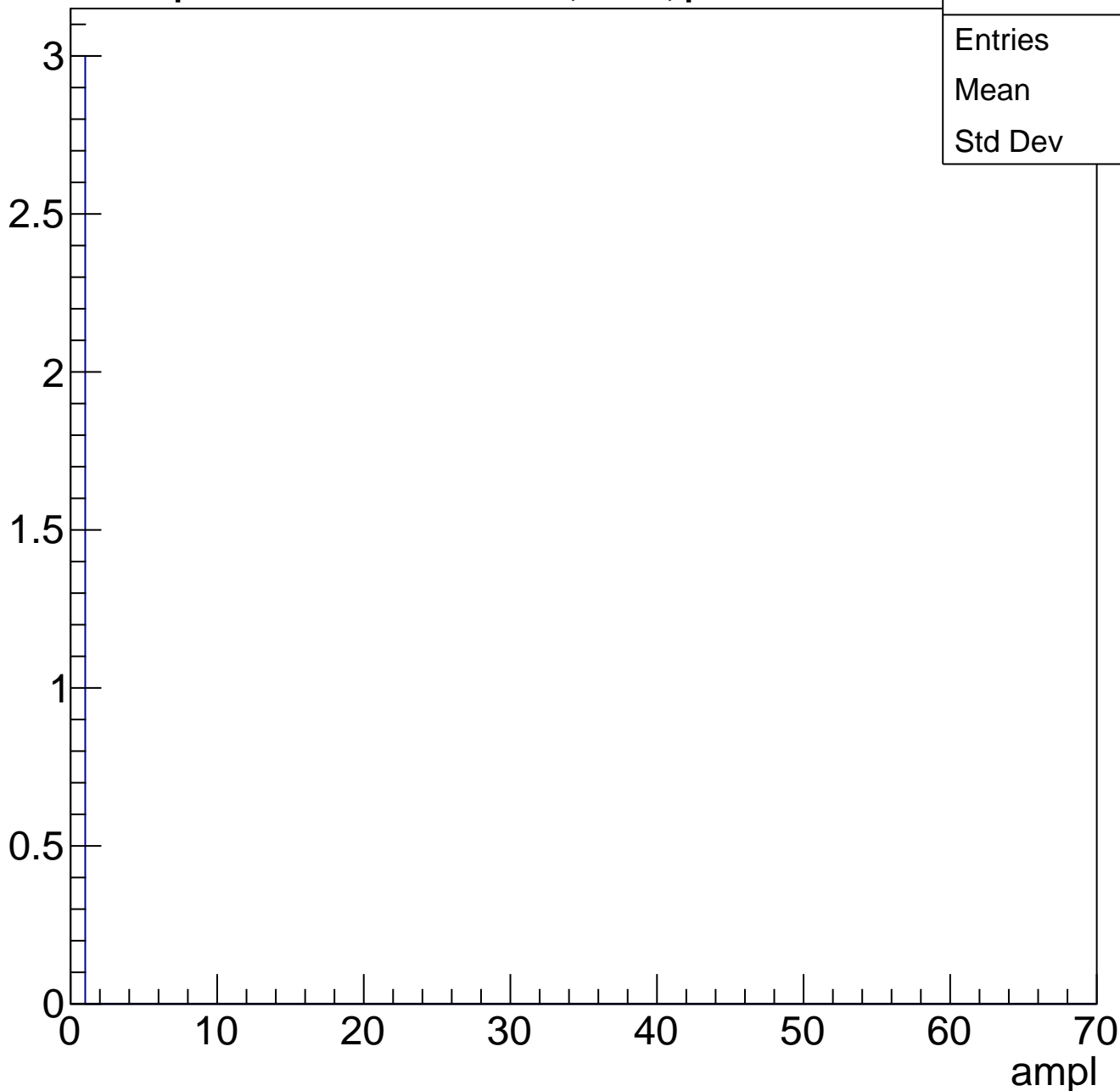


Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch68, adc0

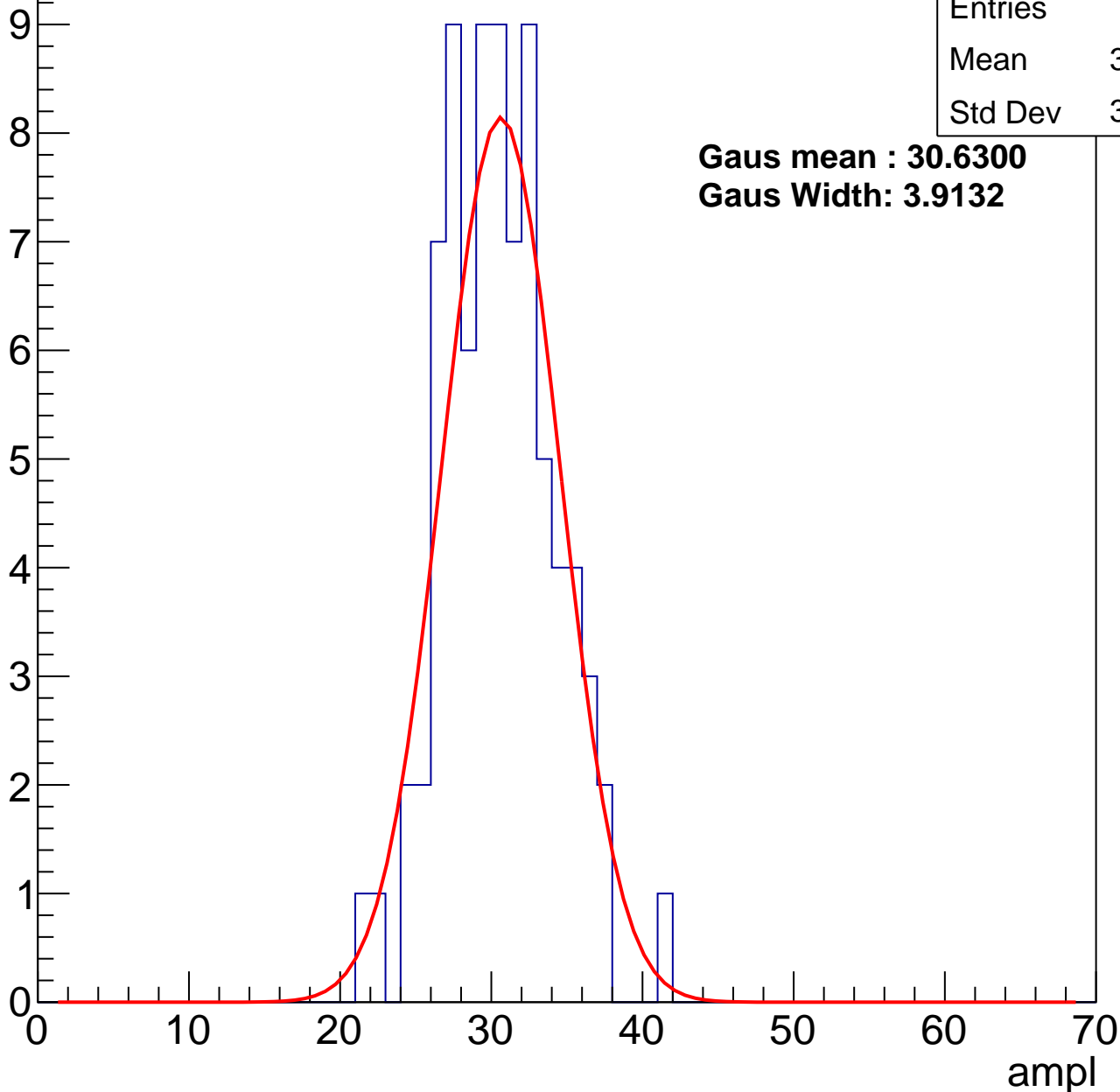
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.05
Std Dev	3.638

Gaus mean : 30.6300

Gaus Width: 3.9132



B1L101S, U26-ch68, adc1

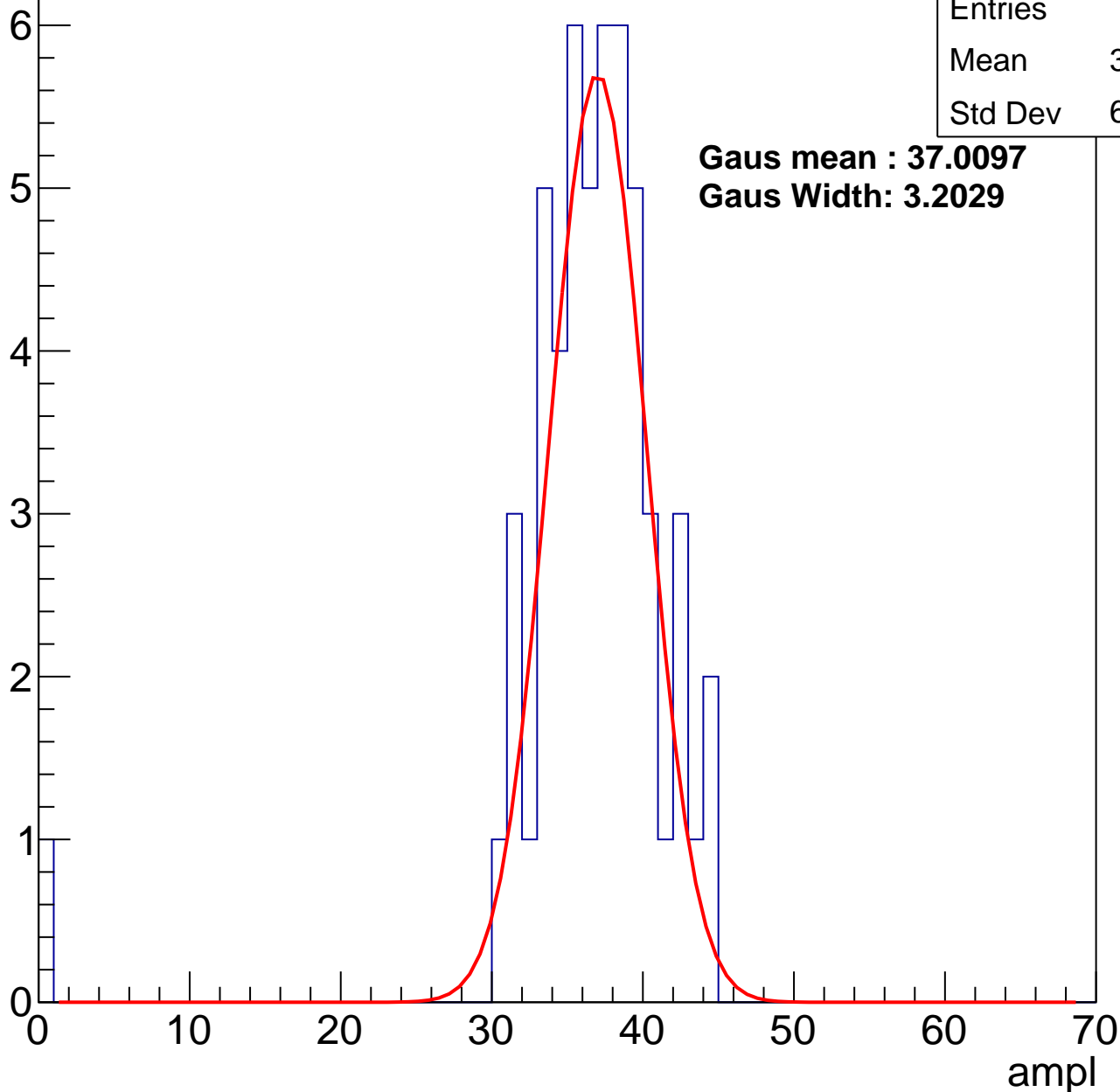
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	36.02
Std Dev	6.024

Gaus mean : 37.0097

Gaus Width: 3.2029



B1L101S, U26-ch68, adc2

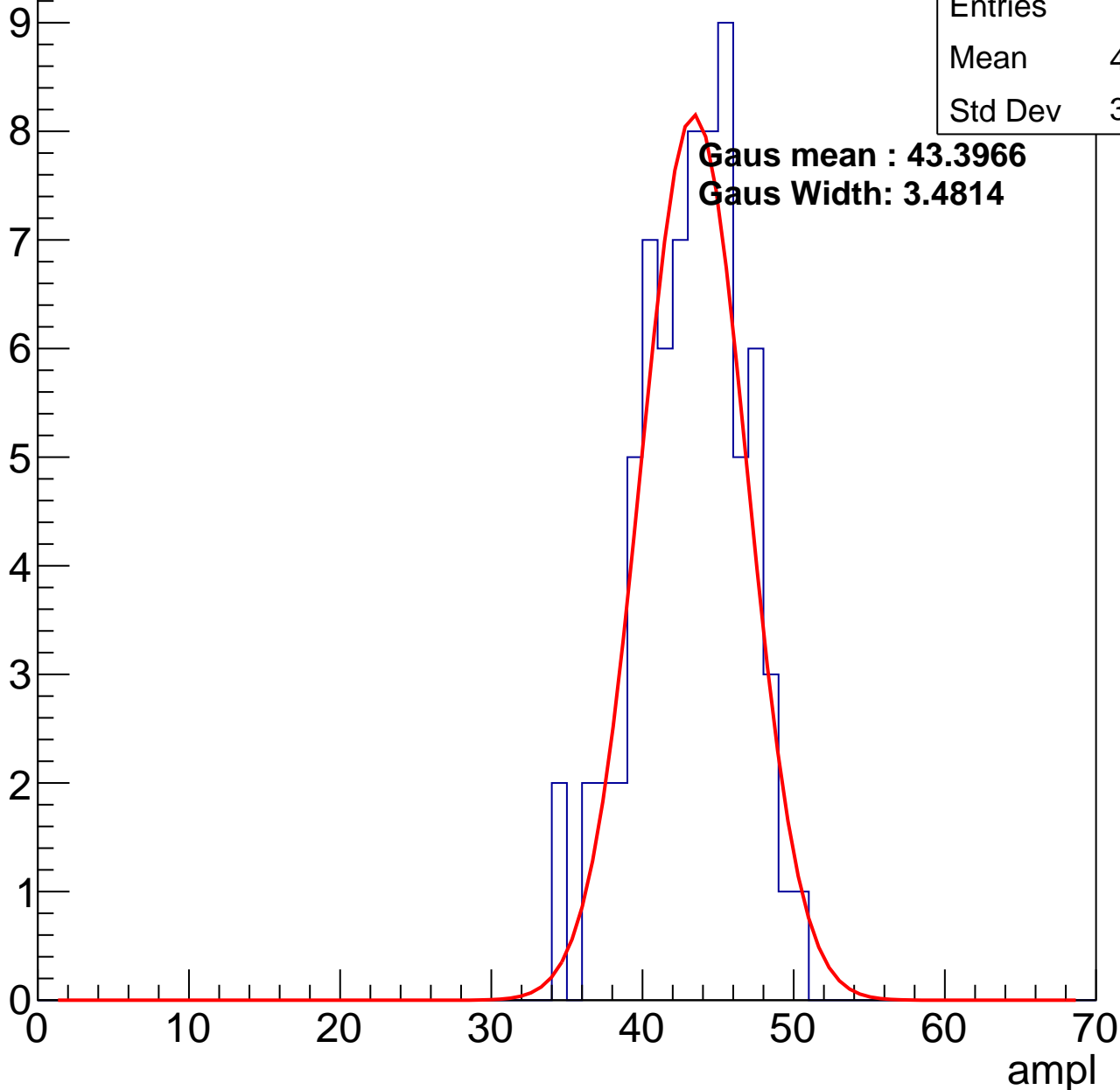
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.72
Std Dev	3.478

Gaus mean : 43.3966

Gaus Width: 3.4814

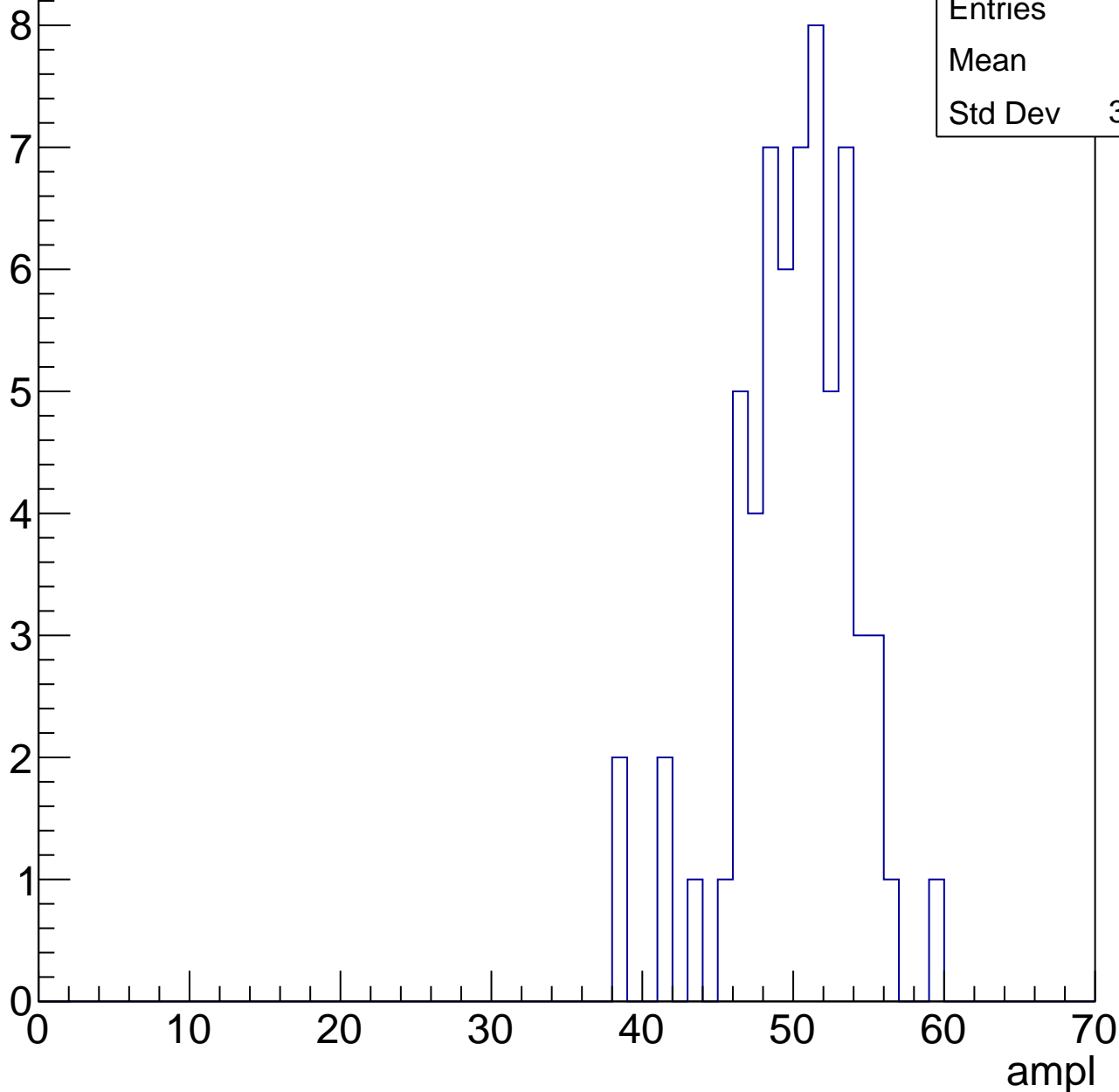


B1L101S, U26-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

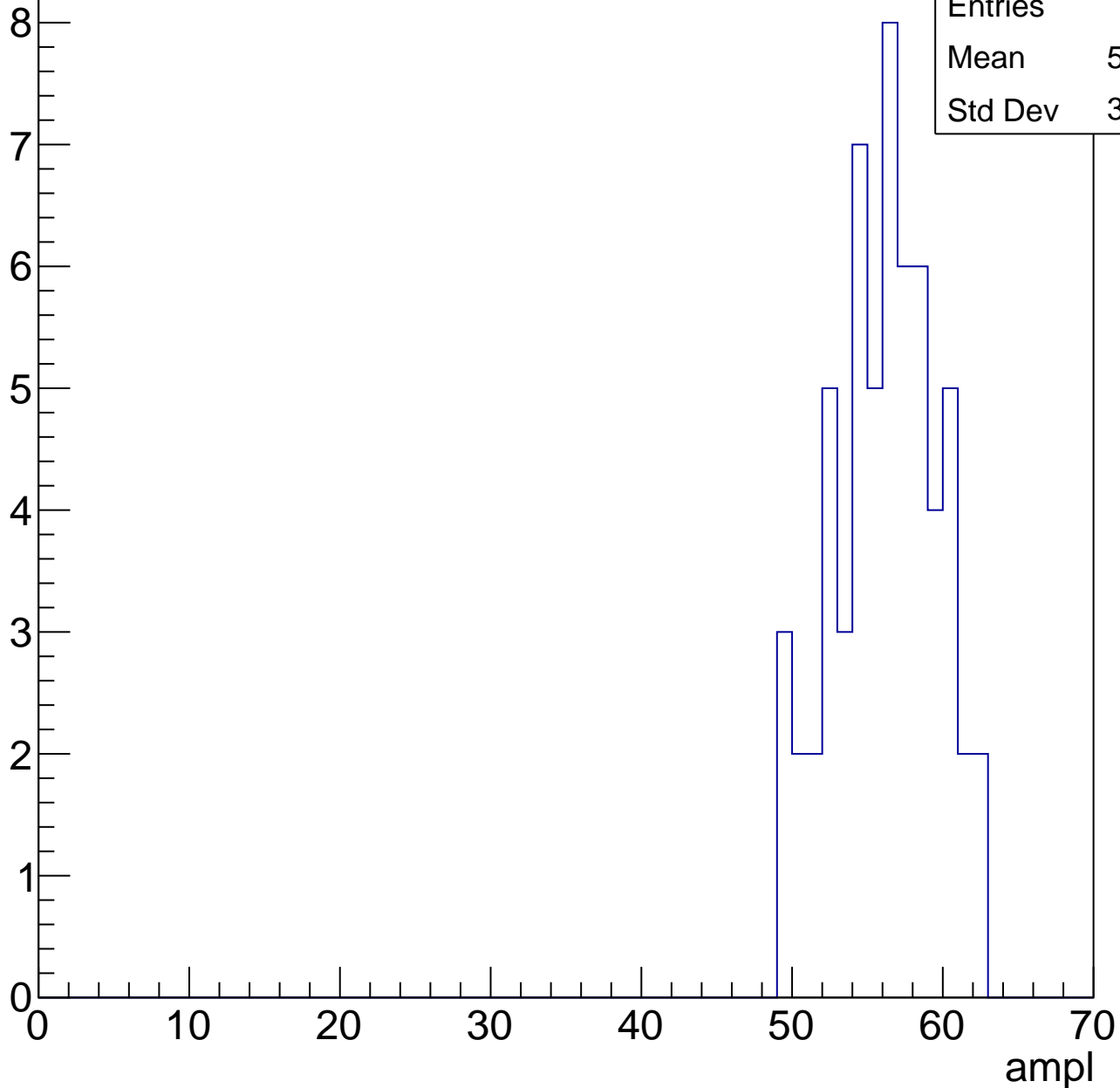
Entries	63
Mean	49.6
Std Dev	3.974



B1L101S, U26-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

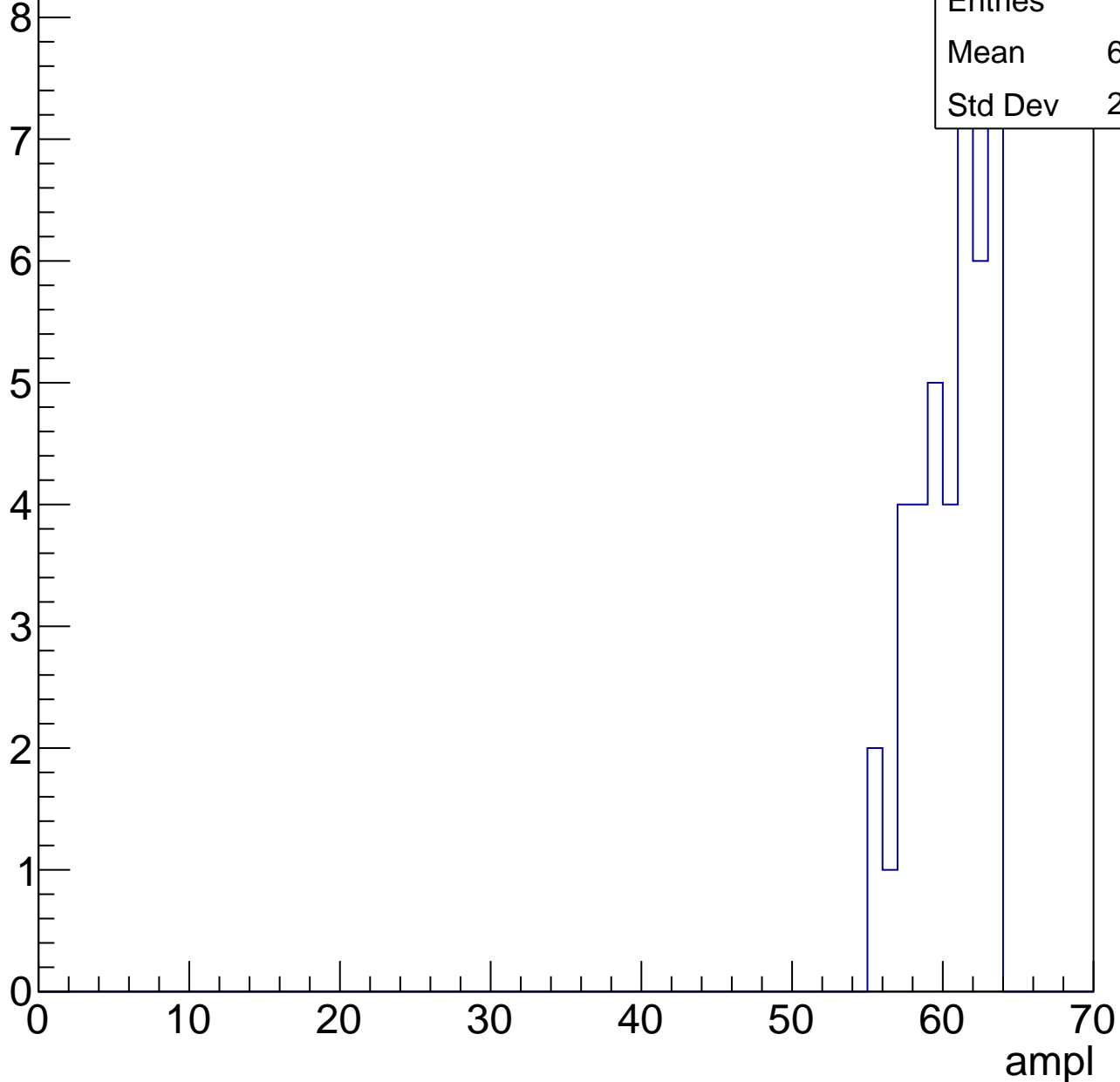
Entry



B1L101S, U26-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

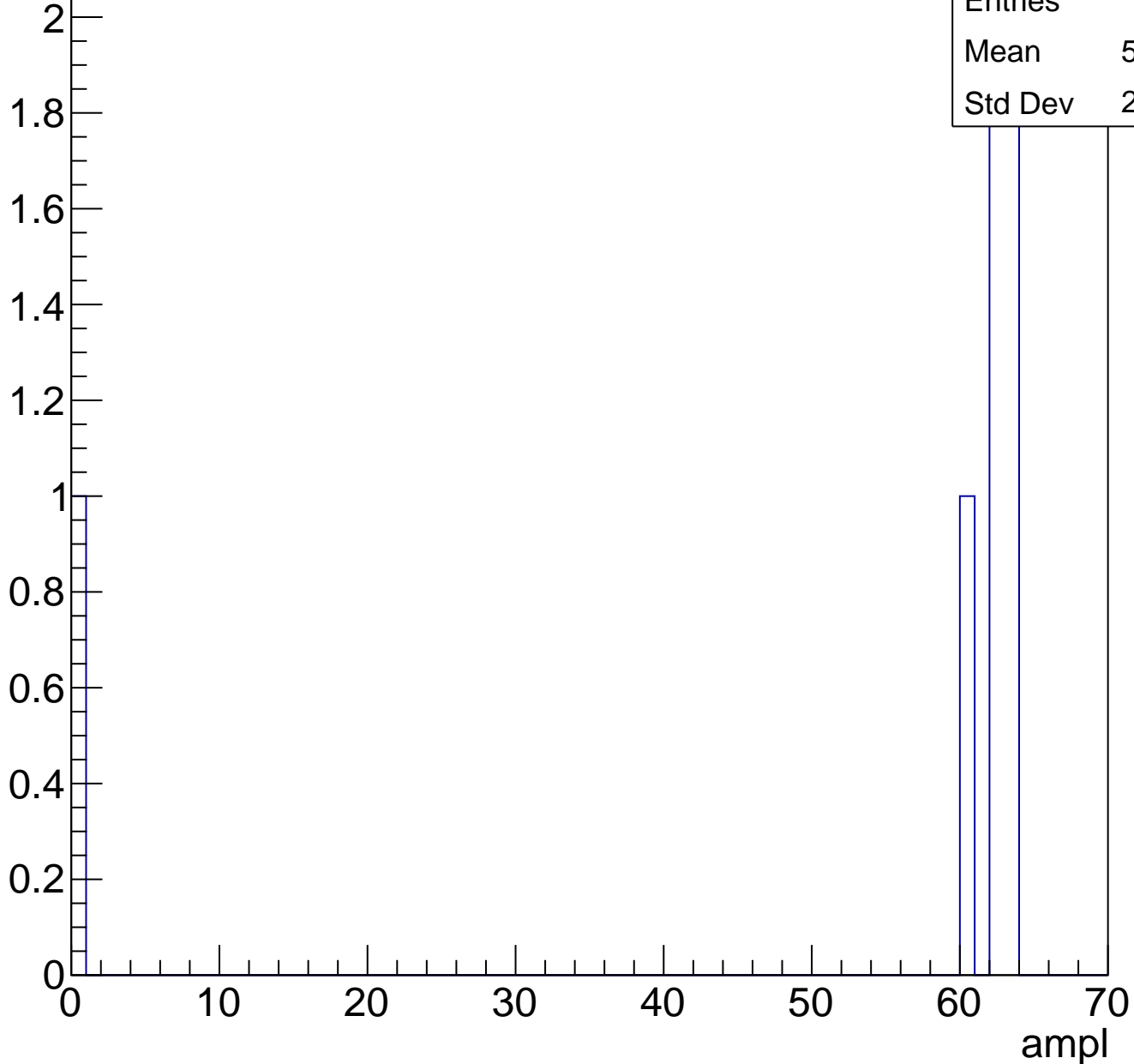


Entries	42
Mean	60.12
Std Dev	2.322

B1L101S, U26-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

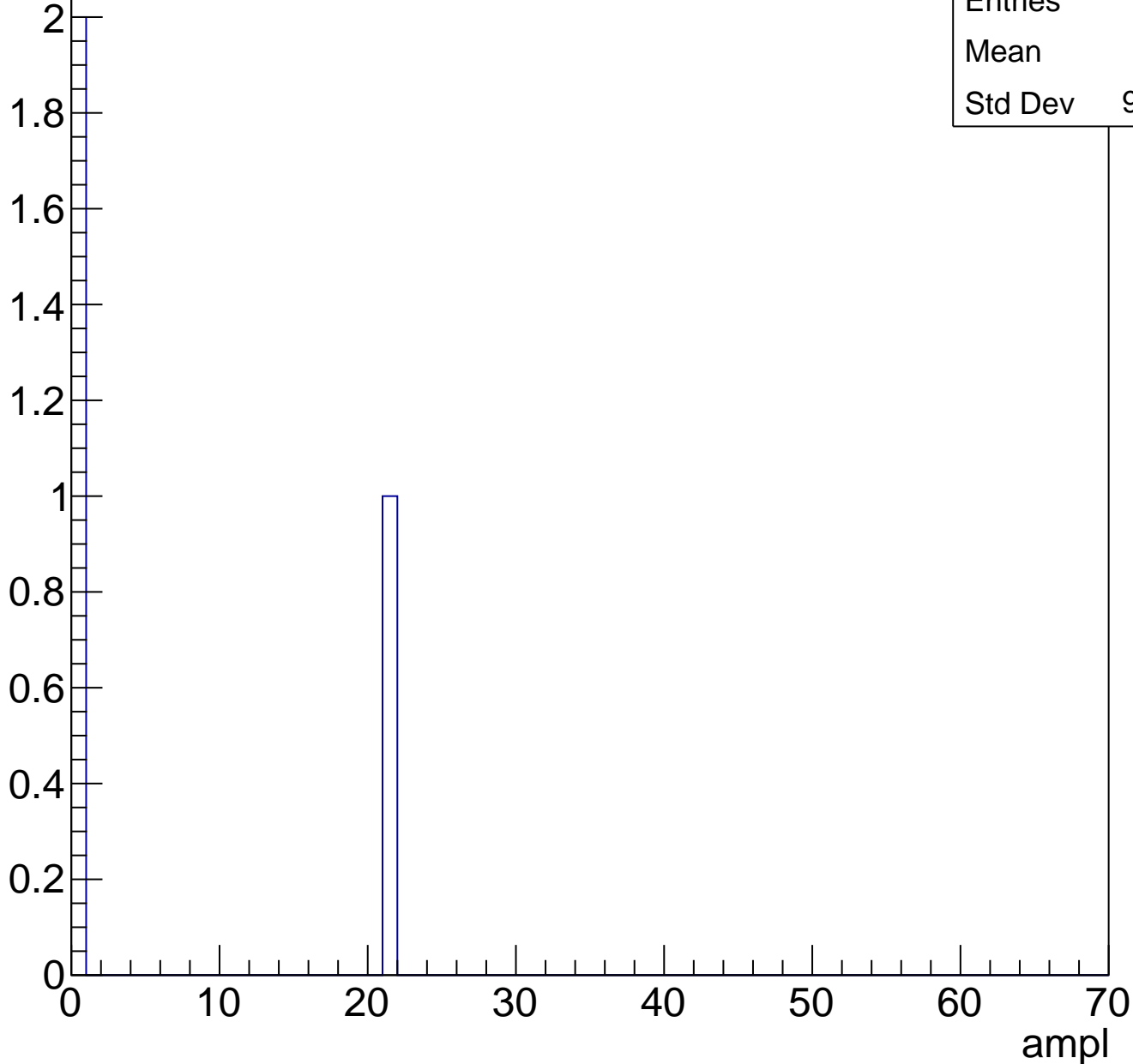
Entry



B1L101S, U26-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch69, adc0

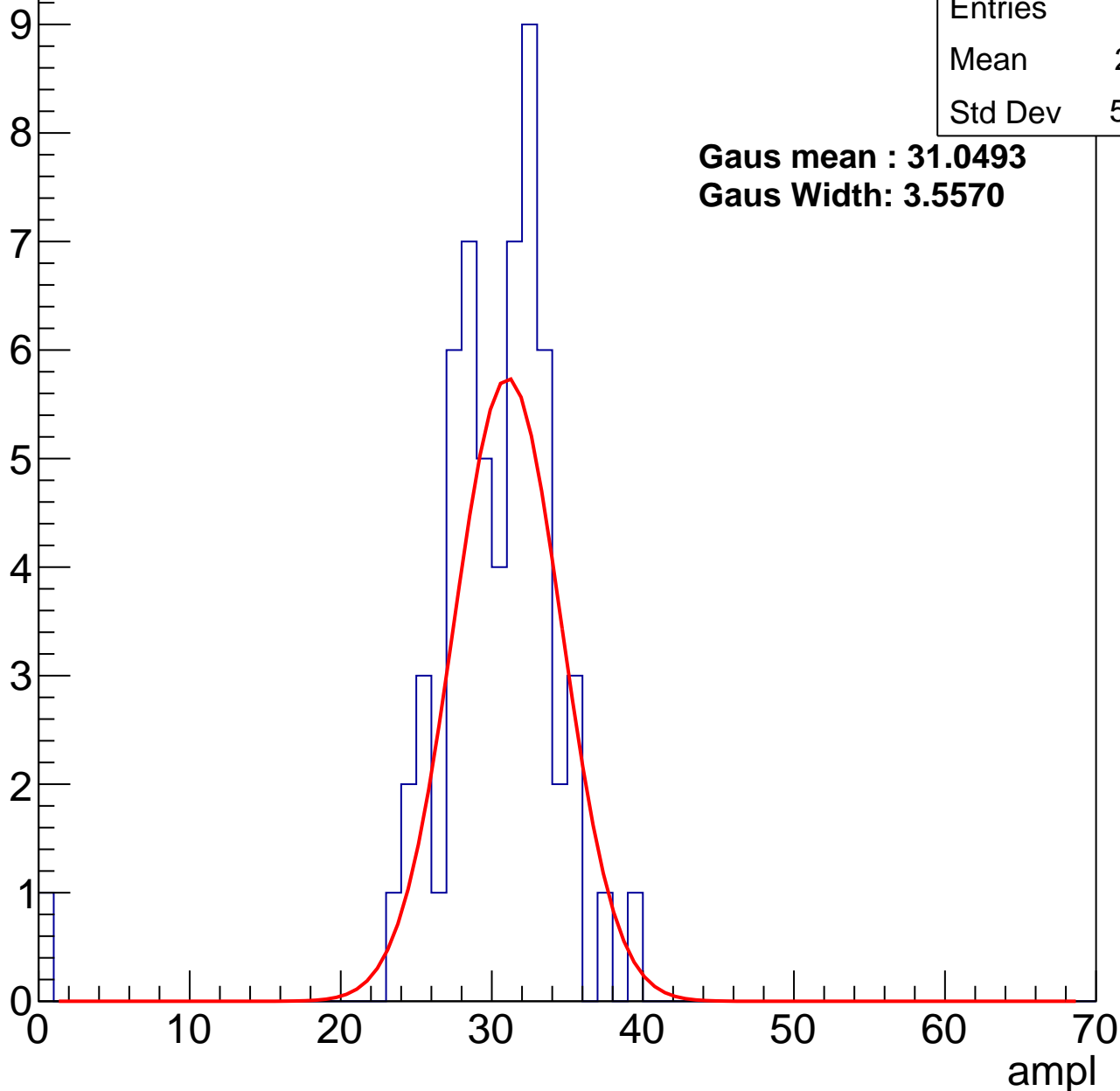
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.61
Std Dev	5.082

Gaus mean : 31.0493

Gaus Width: 3.5570



B1L101S, U26-ch69, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	36.75
Std Dev	3.427

Gaus mean : 37.2680

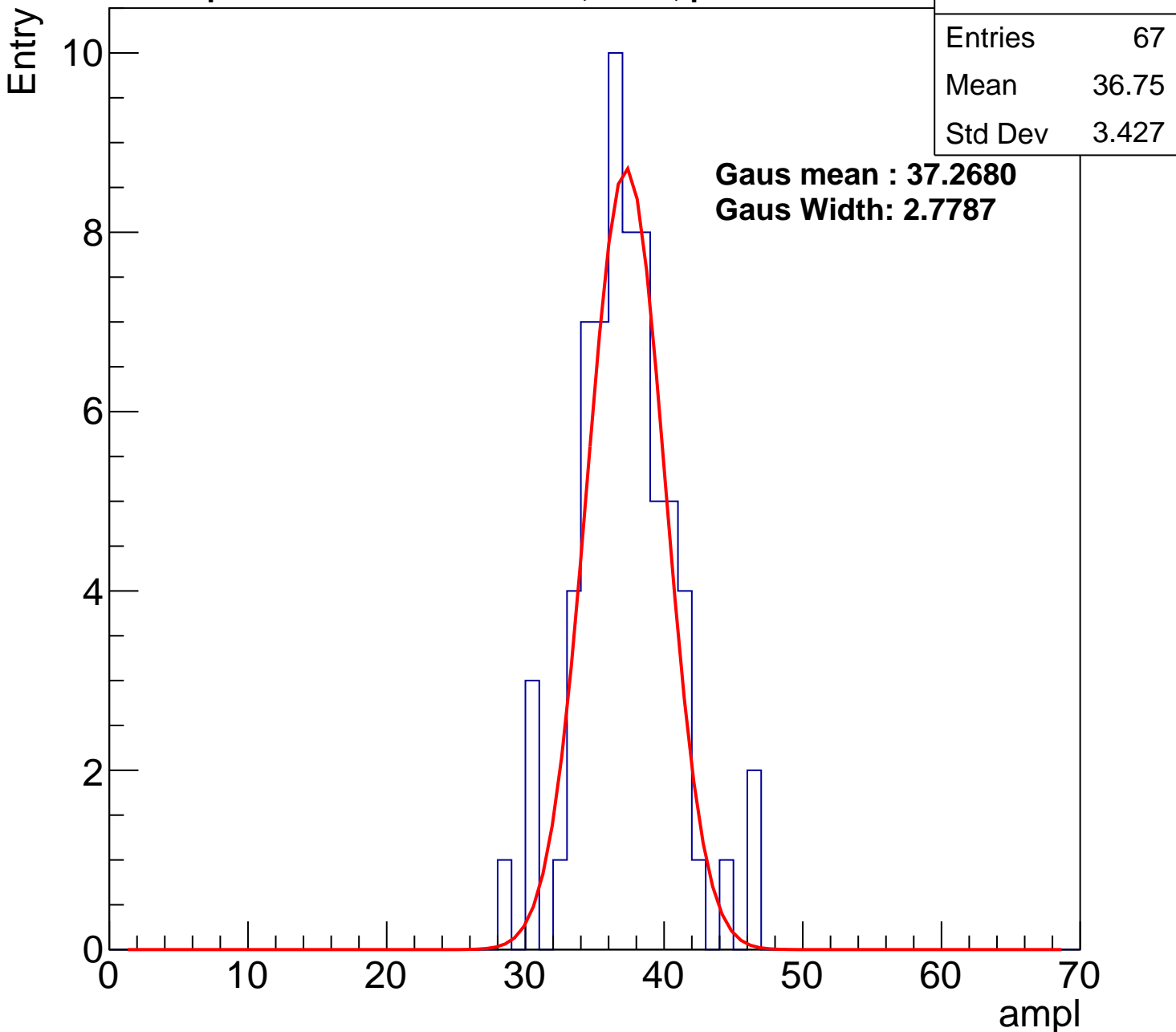
Gaus Width: 2.7787

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch69, adc2

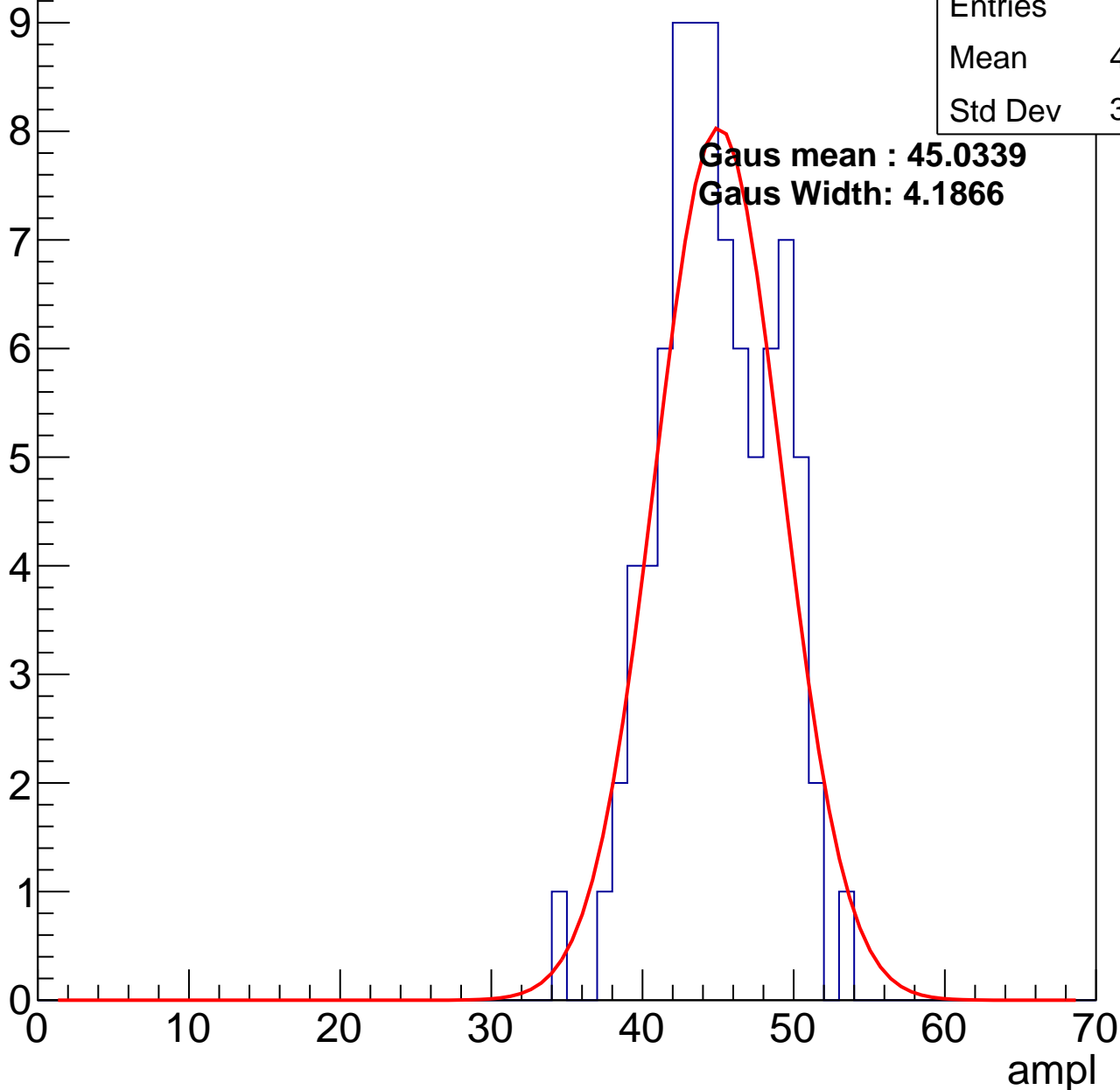
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	44.43
Std Dev	3.739

Gaus mean : 45.0339

Gaus Width: 4.1866

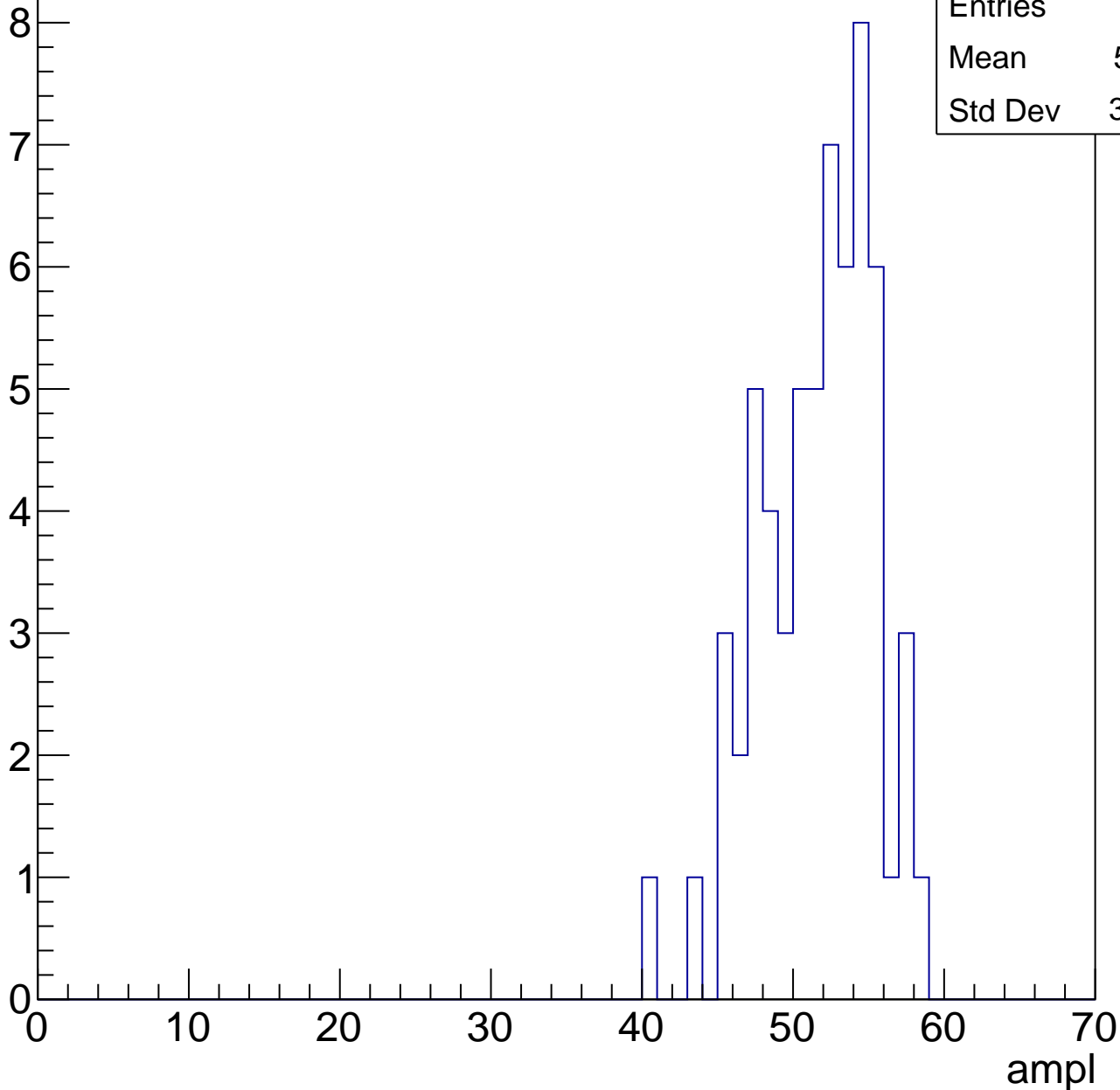


B1L101S, U26-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

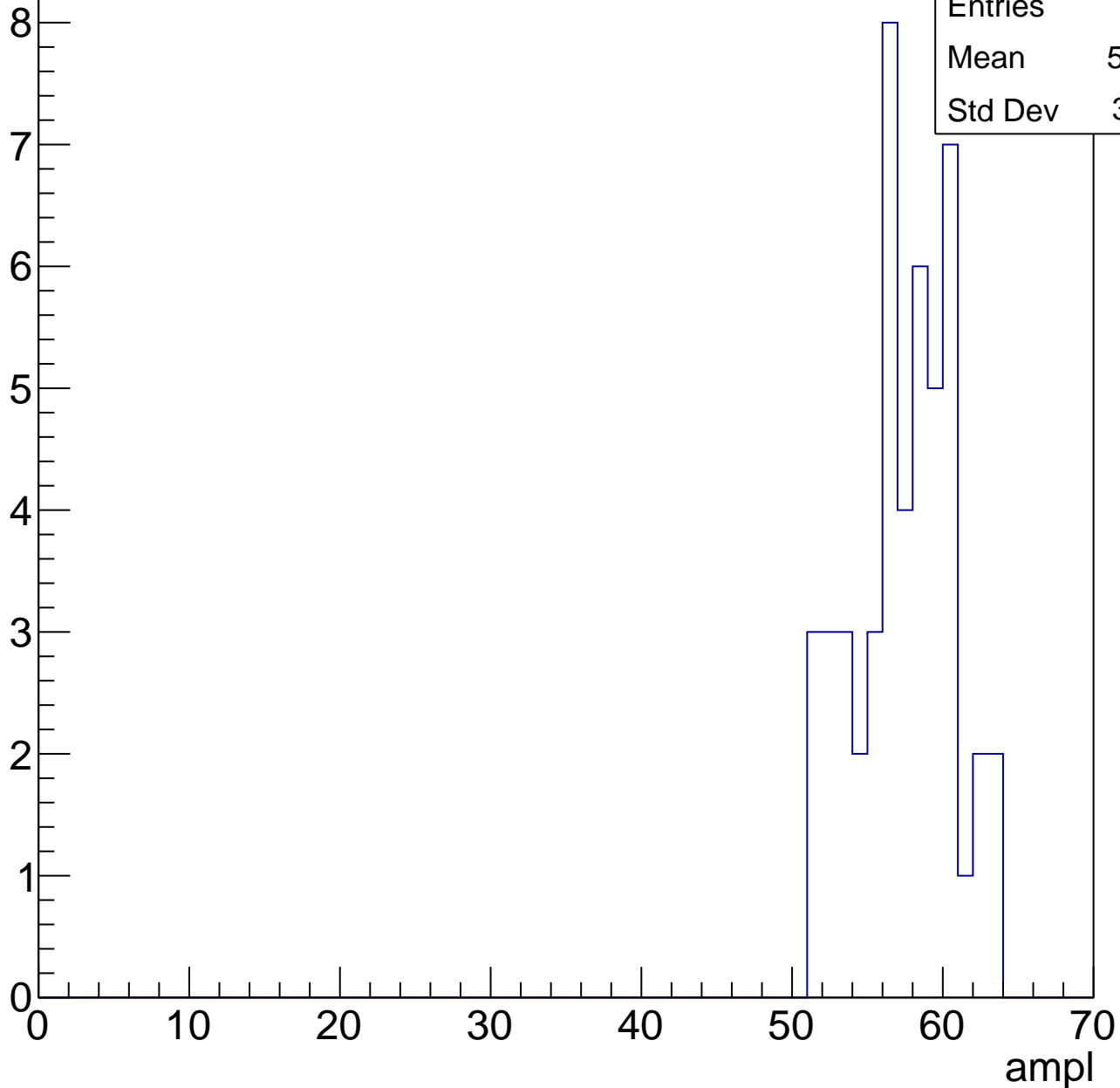
Entries	61
Mean	51.11
Std Dev	3.746



B1L101S, U26-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



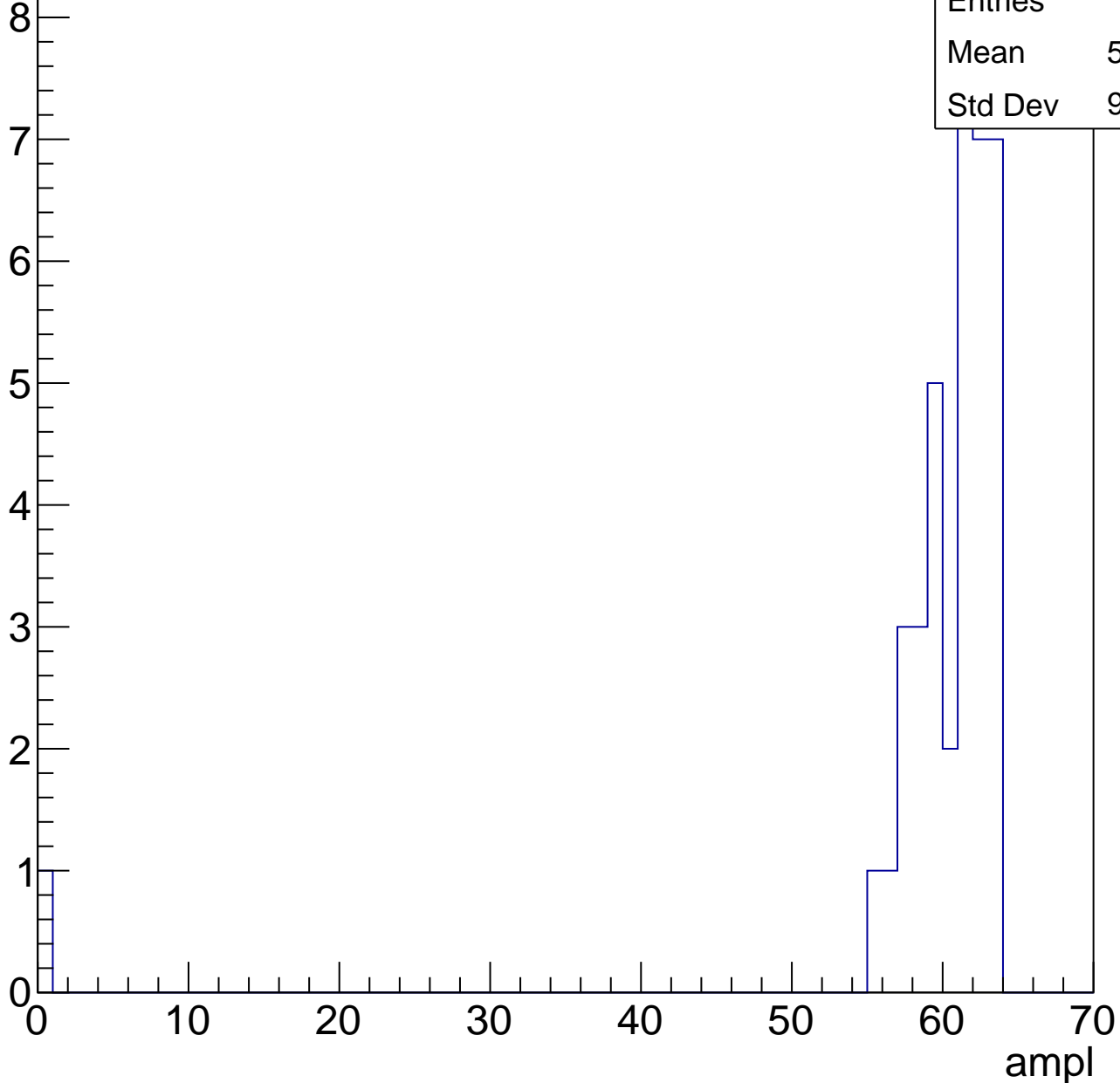
Entries	49
Mean	56.96
Std Dev	3.181

B1L101S, U26-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.79
Std Dev	9.905



B1L101S, U26-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch70, adc0

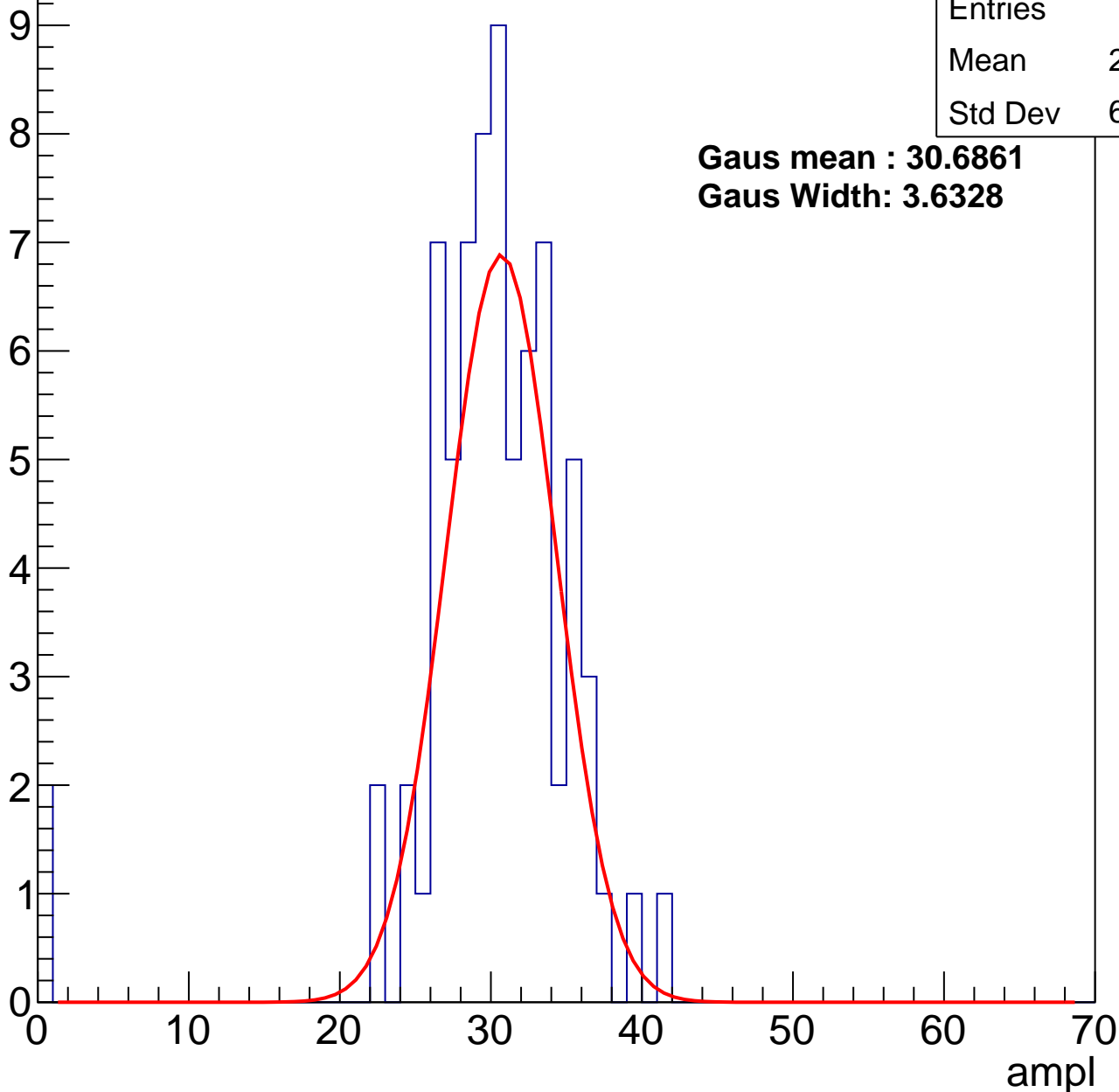
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.43
Std Dev	6.154

Gaus mean : 30.6861

Gaus Width: 3.6328



B1L101S, U26-ch70, adc1

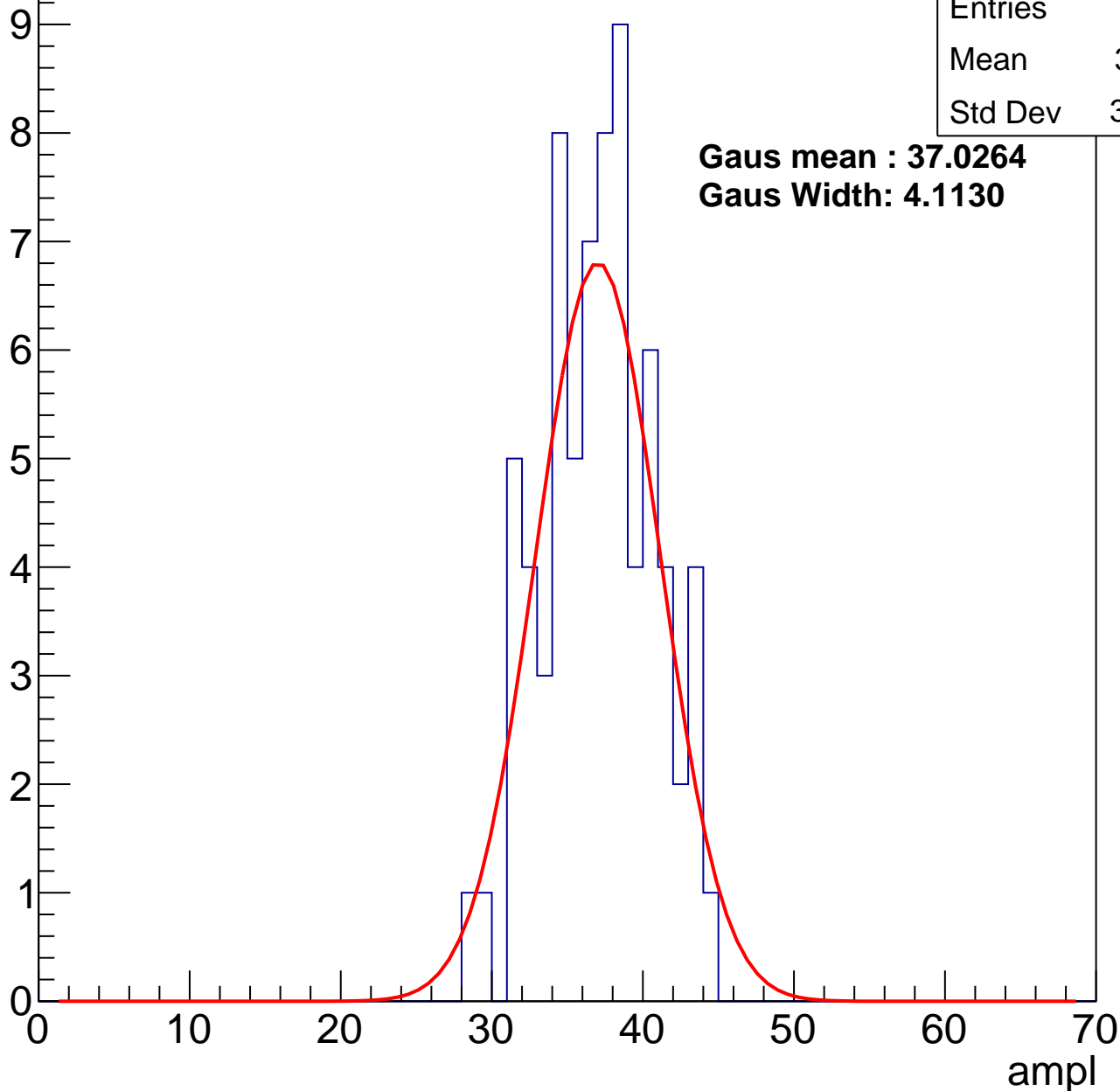
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.61
Std Dev	3.627

Gaus mean : 37.0264

Gaus Width: 4.1130



B1L101S, U26-ch70, adc2

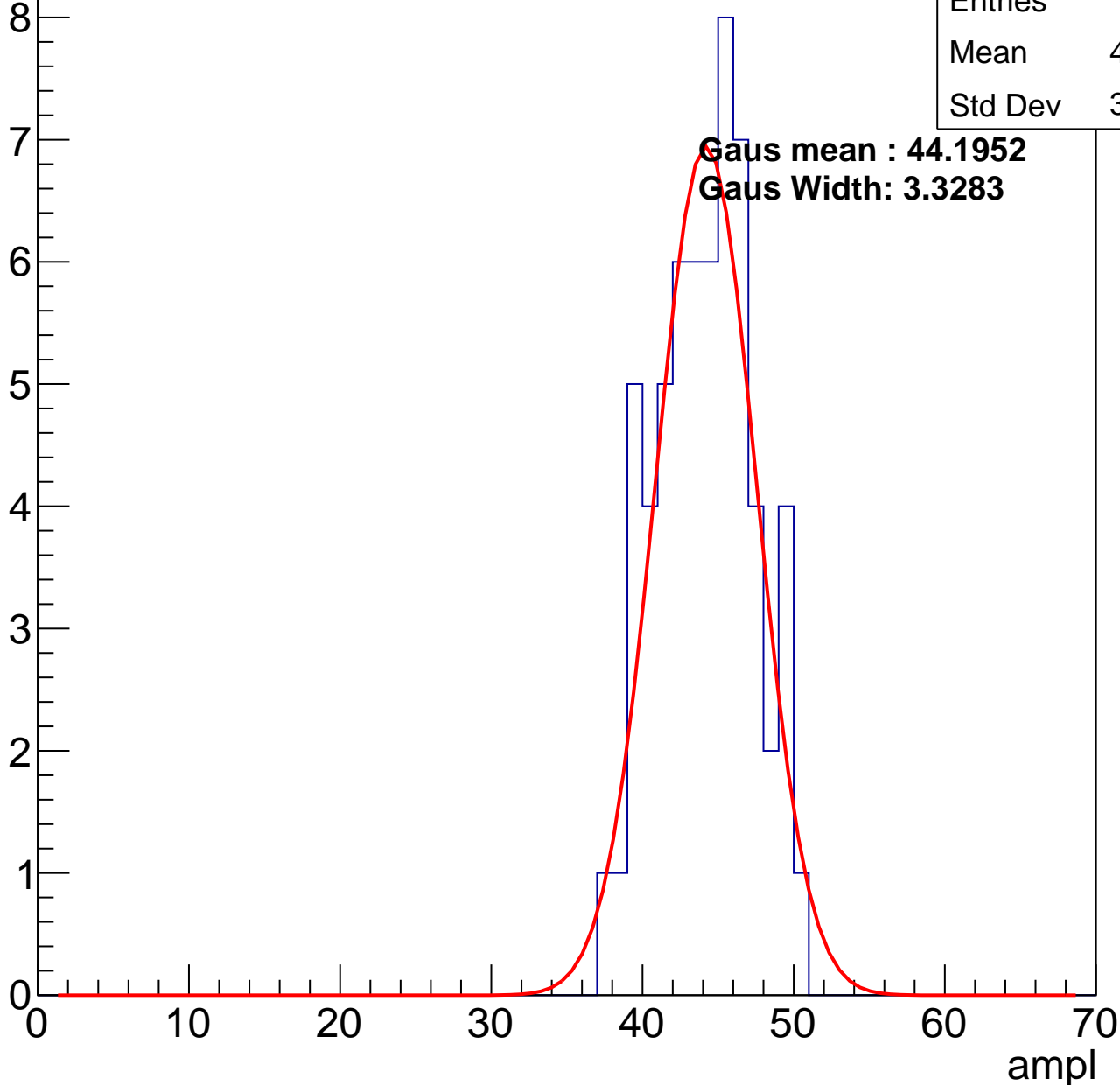
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.68
Std Dev	3.117

Gaus mean : 44.1952

Gaus Width: 3.3283

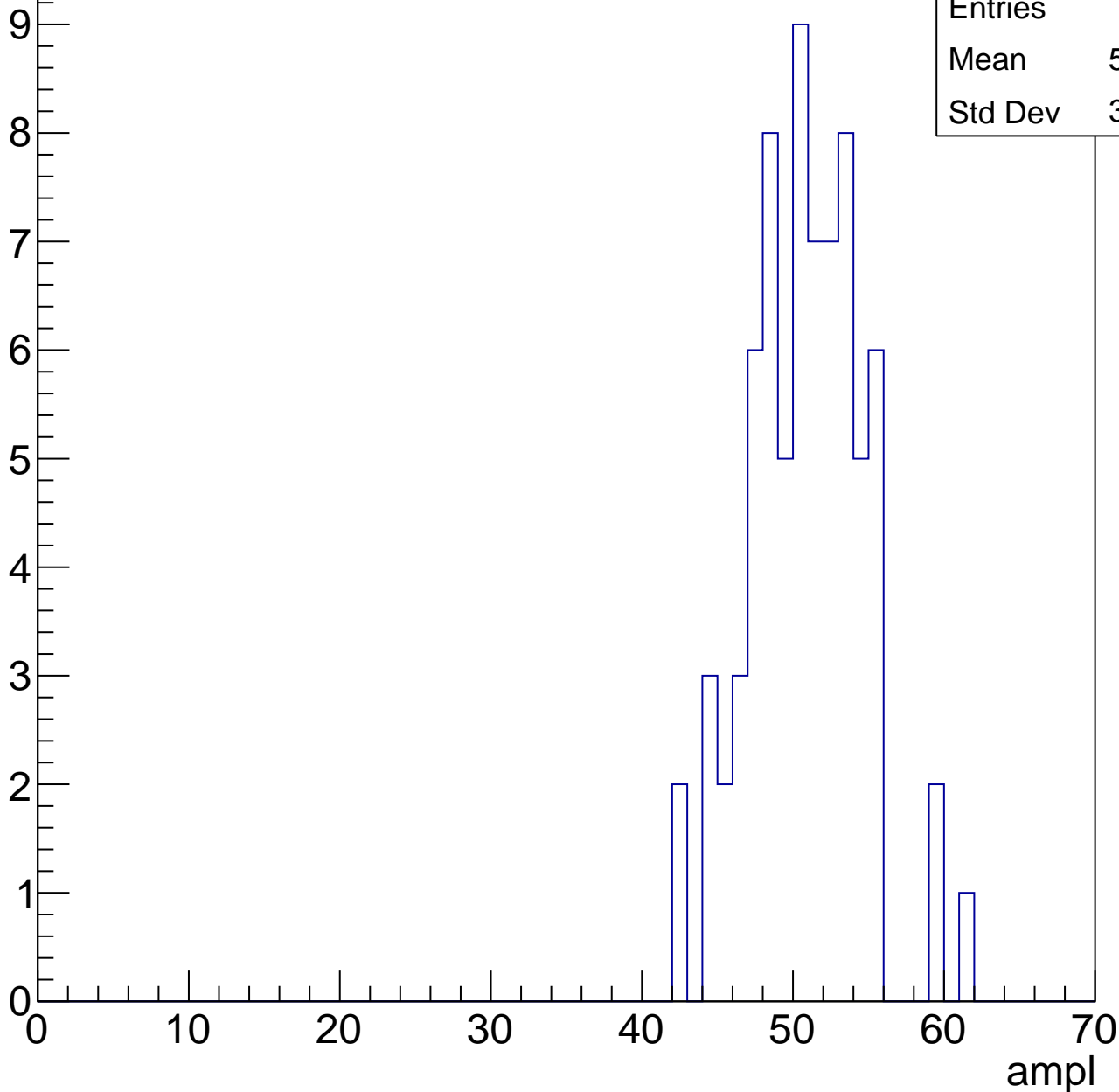


B1L101S, U26-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

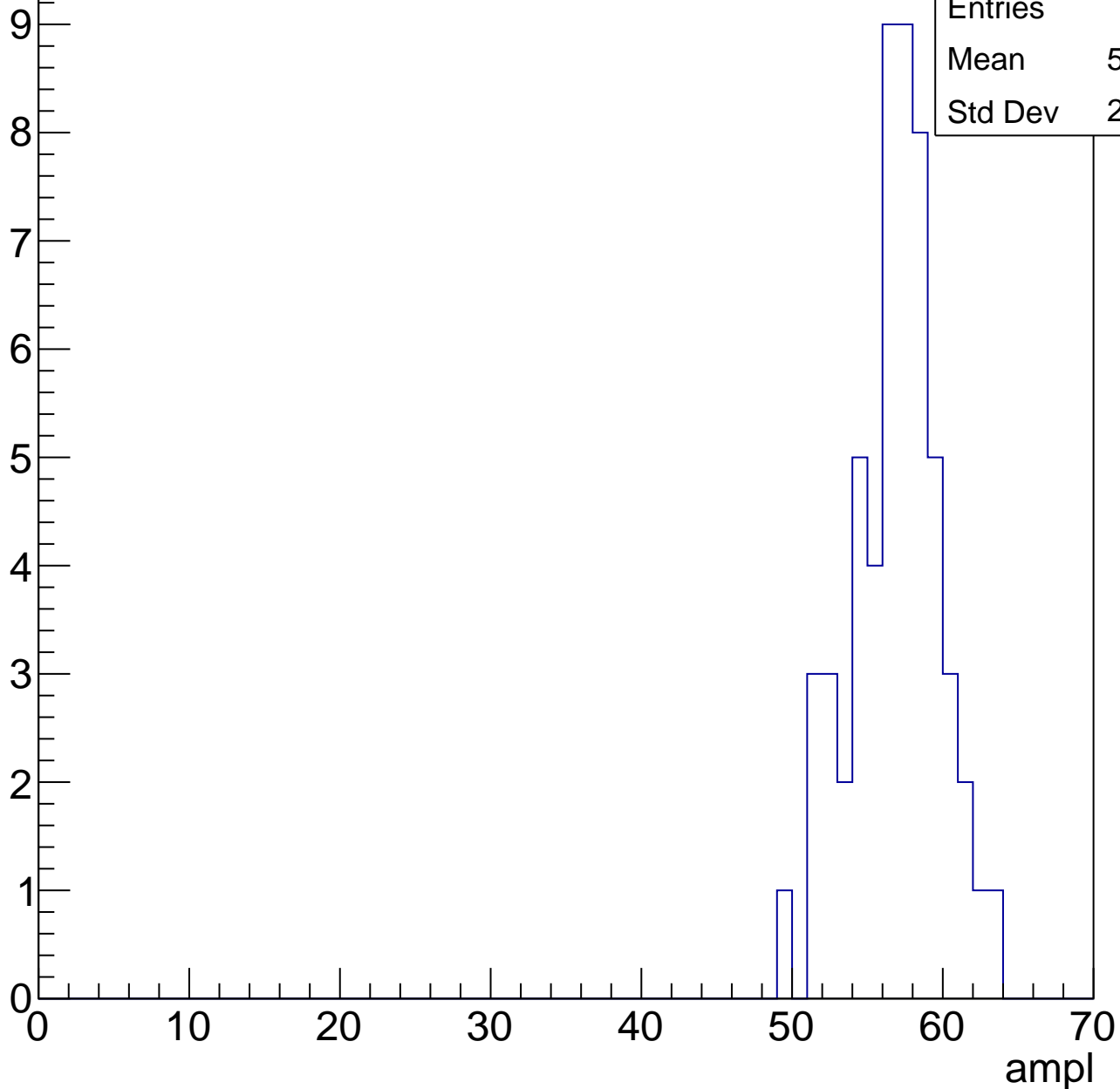
Entries	74
Mean	50.39
Std Dev	3.737



B1L101S, U26-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	56
Mean	56.38
Std Dev	2.913

B1L101S, U26-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

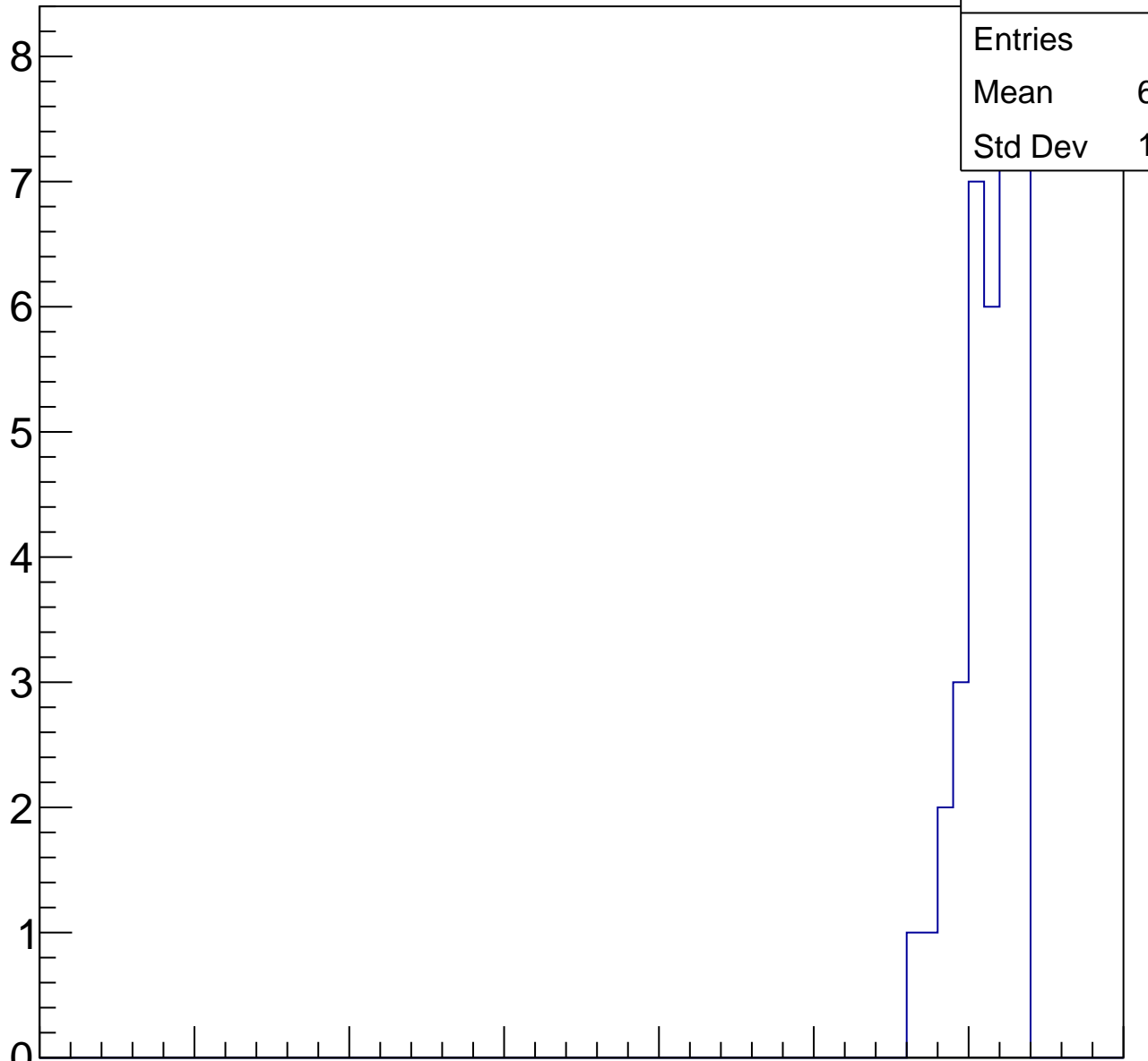
Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.89
Std Dev	1.807

ampl

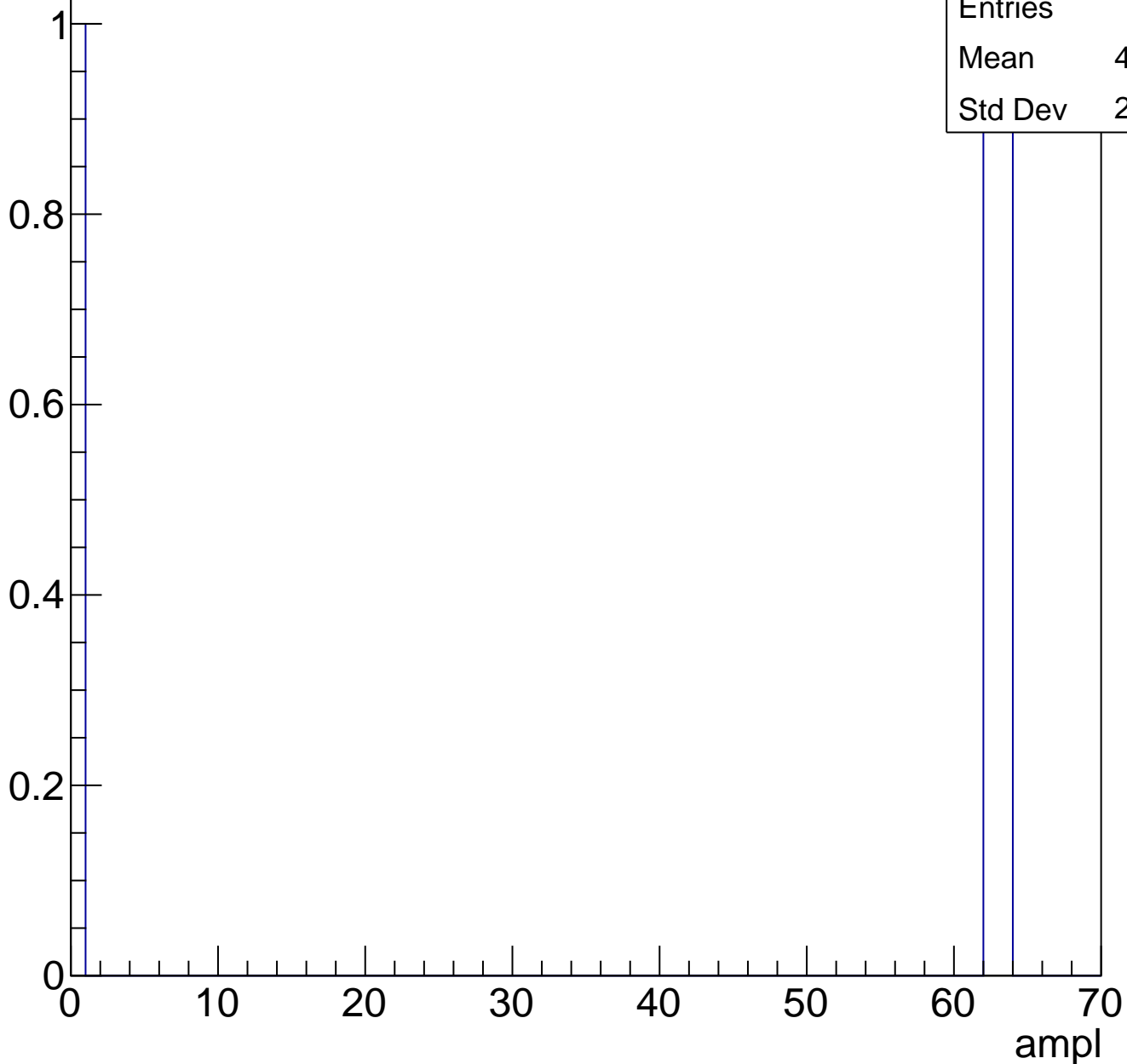
0 10 20 30 40 50 60 70



B1L101S, U26-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch71, adc0

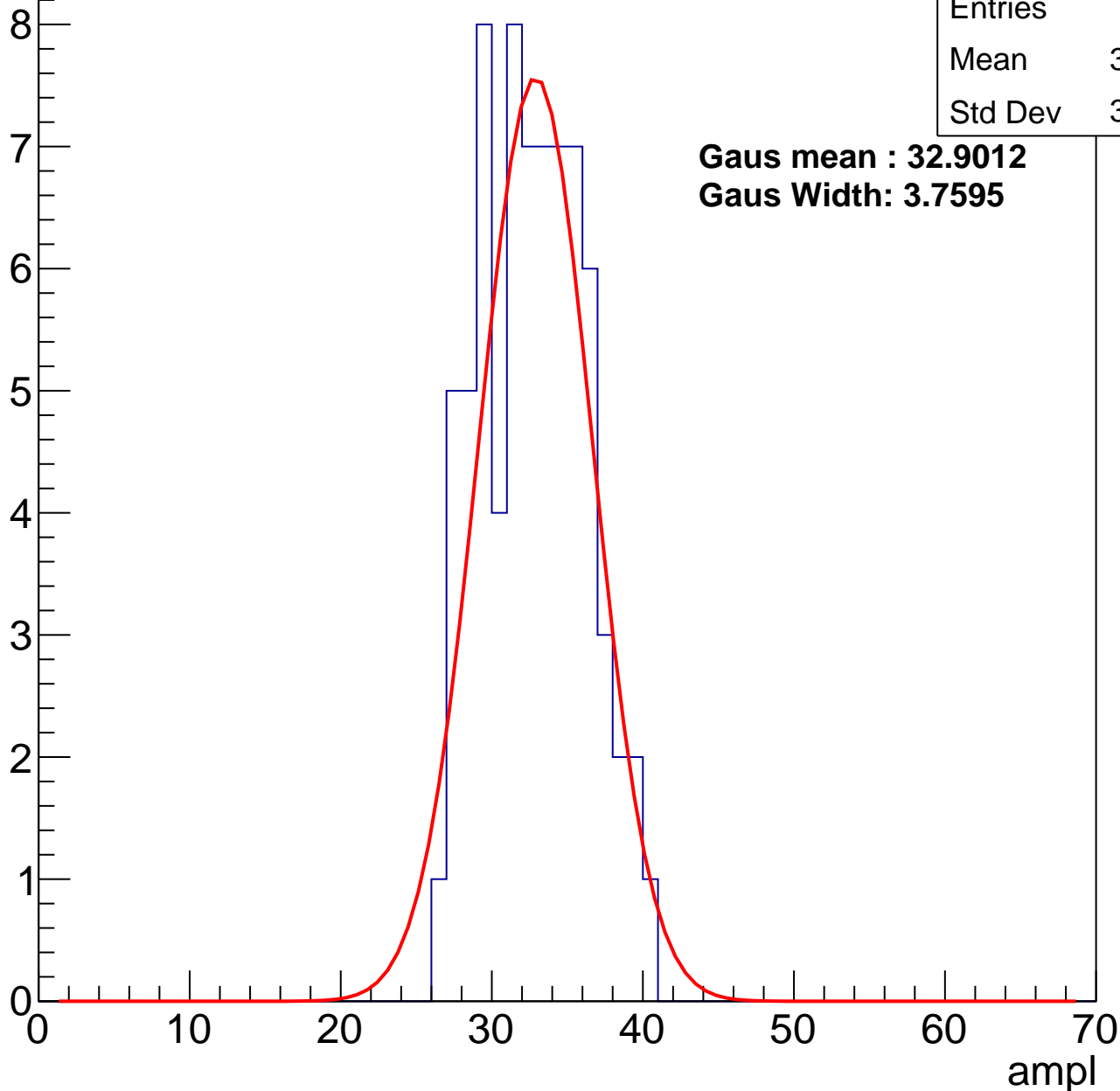
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	32.33
Std Dev	3.376

Gaus mean : 32.9012

Gaus Width: 3.7595



B1L101S, U26-ch71, adc1

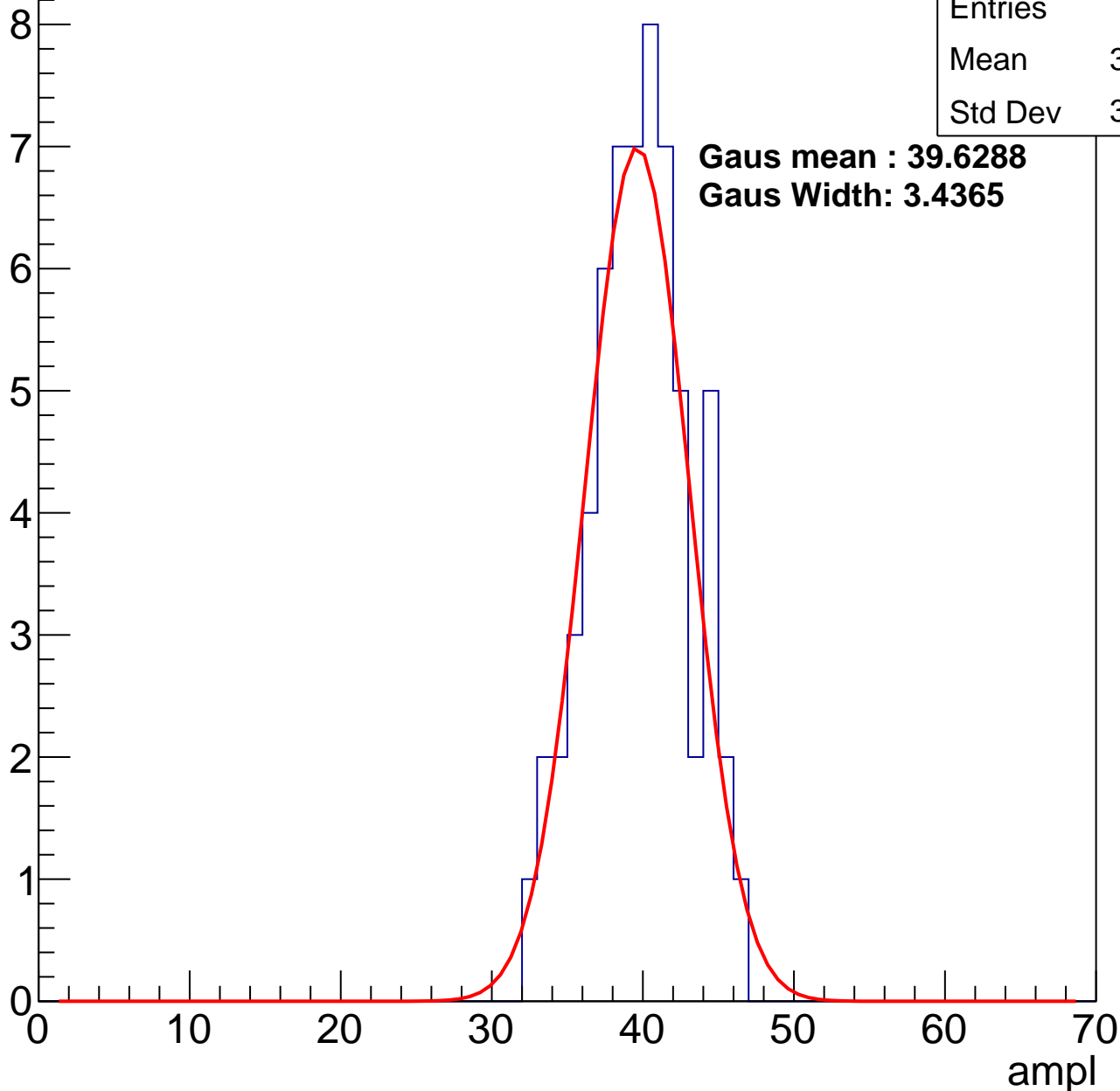
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	39.27
Std Dev	3.214

Gaus mean : 39.6288

Gaus Width: 3.4365



B1L101S, U26-ch71, adc2

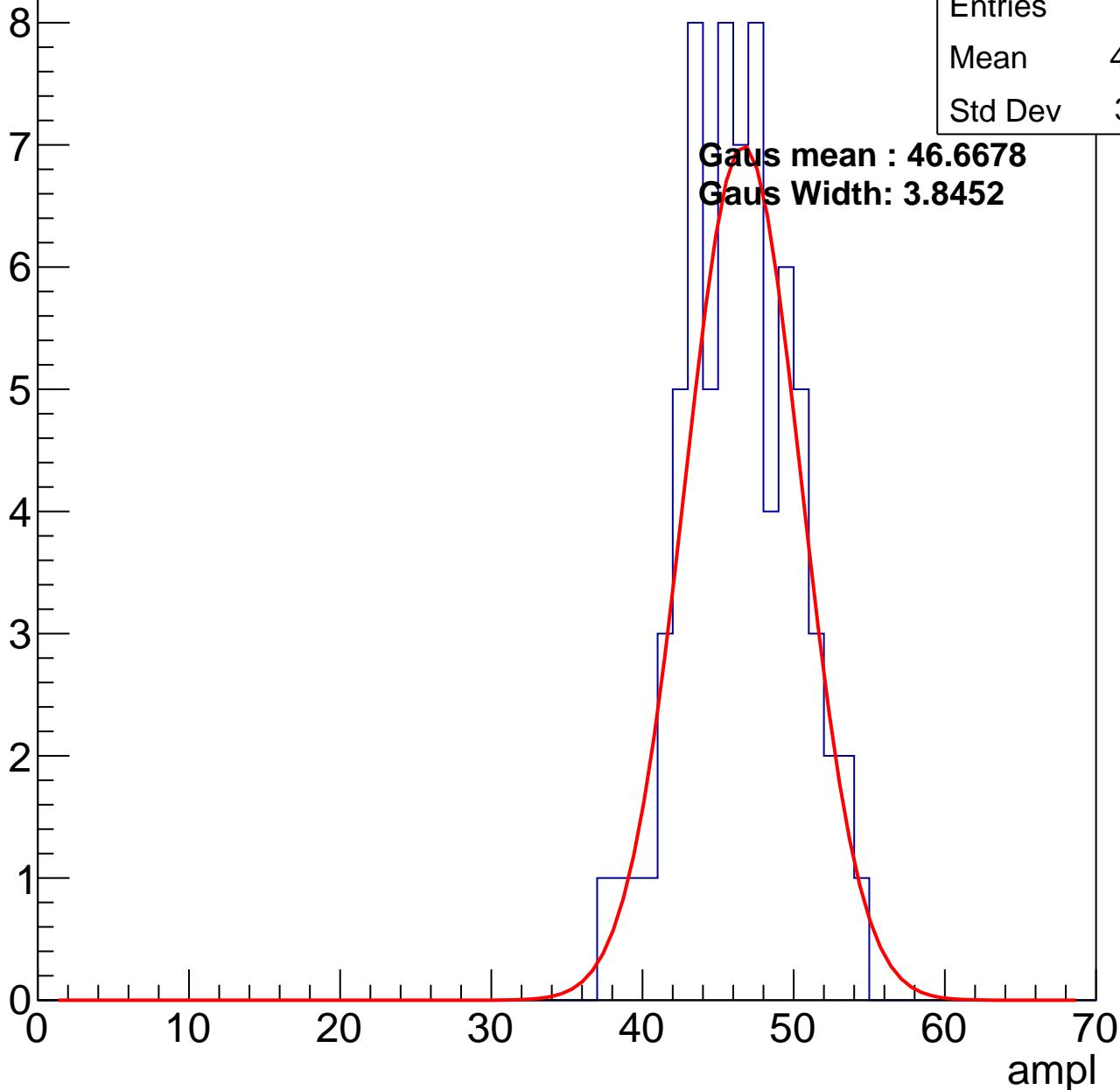
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	45.94
Std Dev	3.661

Gaus mean : 46.6678

Gaus Width: 3.8452

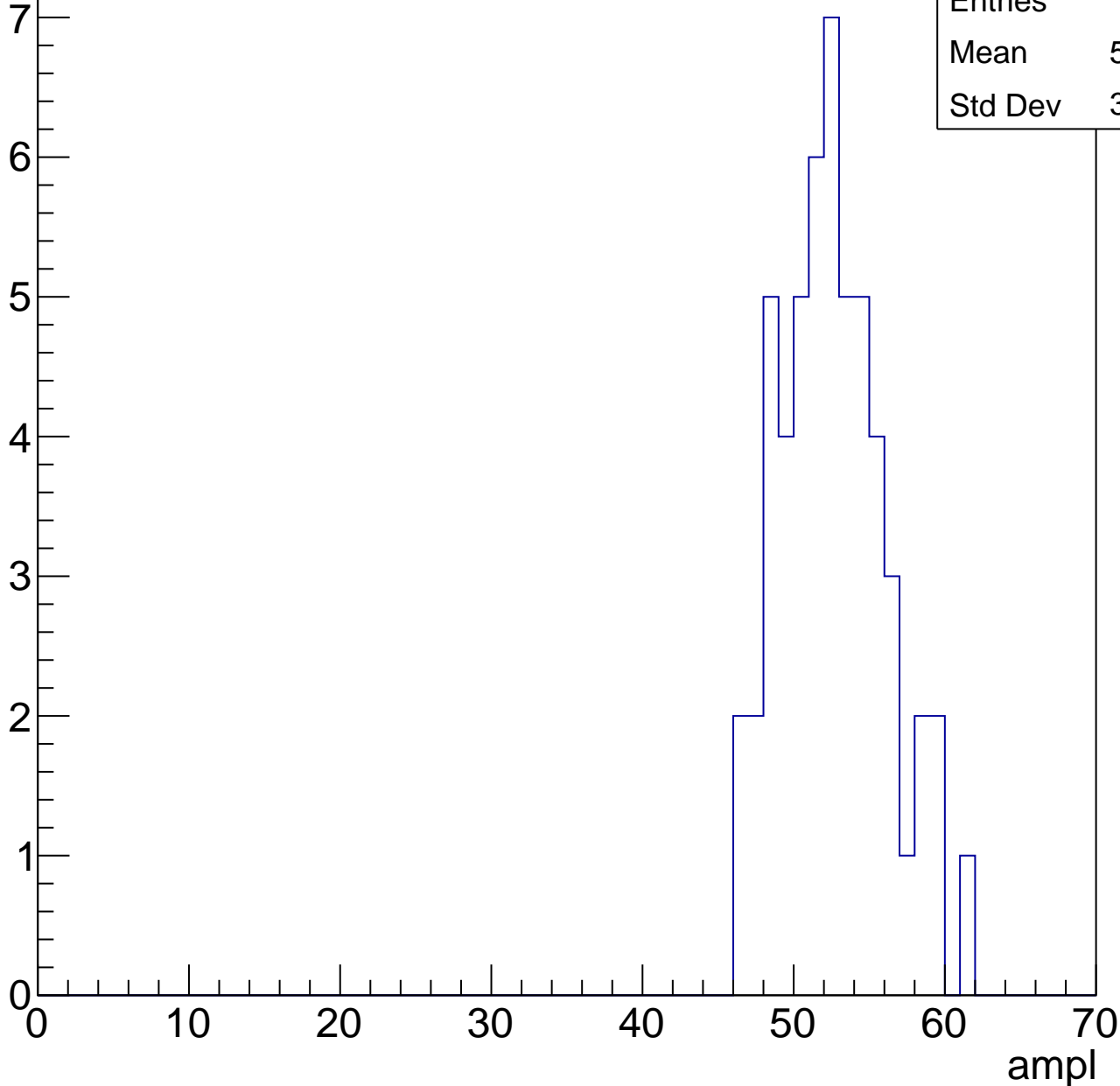


B1L101S, U26-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

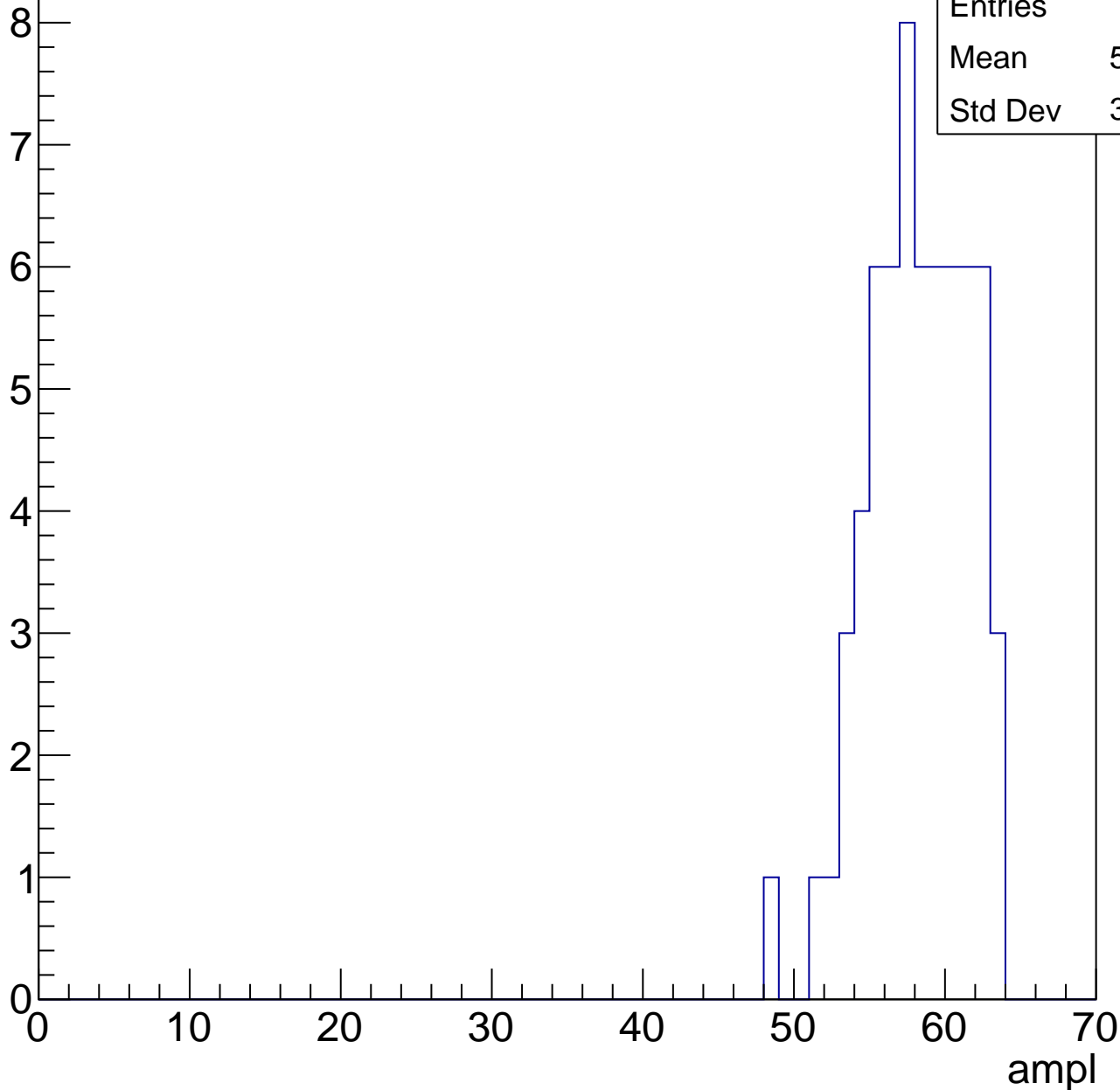
Entries	54
Mean	52.17
Std Dev	3.463



B1L101S, U26-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

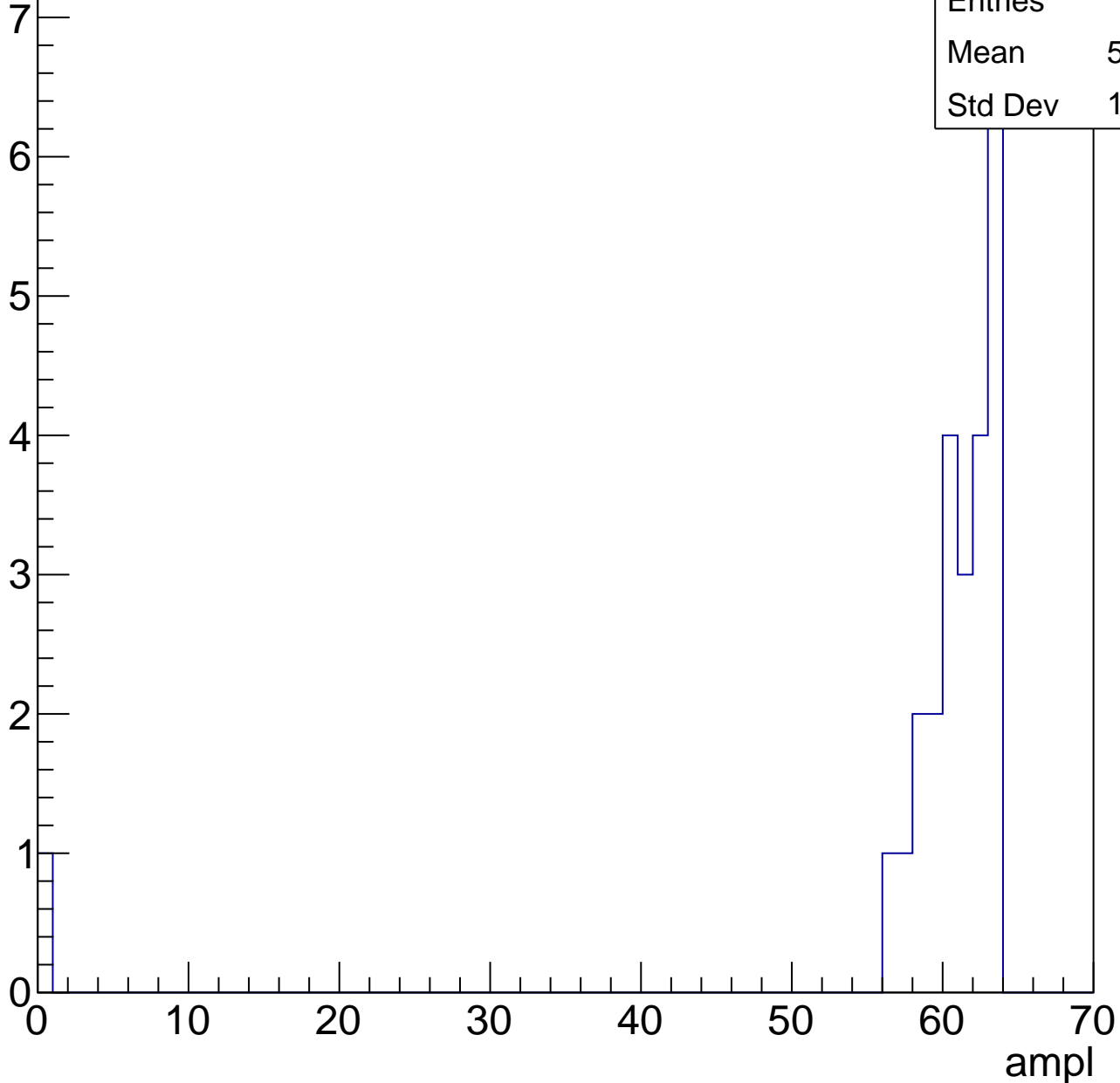


B1L101S, U26-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.36
Std Dev	12.08



B1L101S, U26-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L101S, U26-ch72, adc0

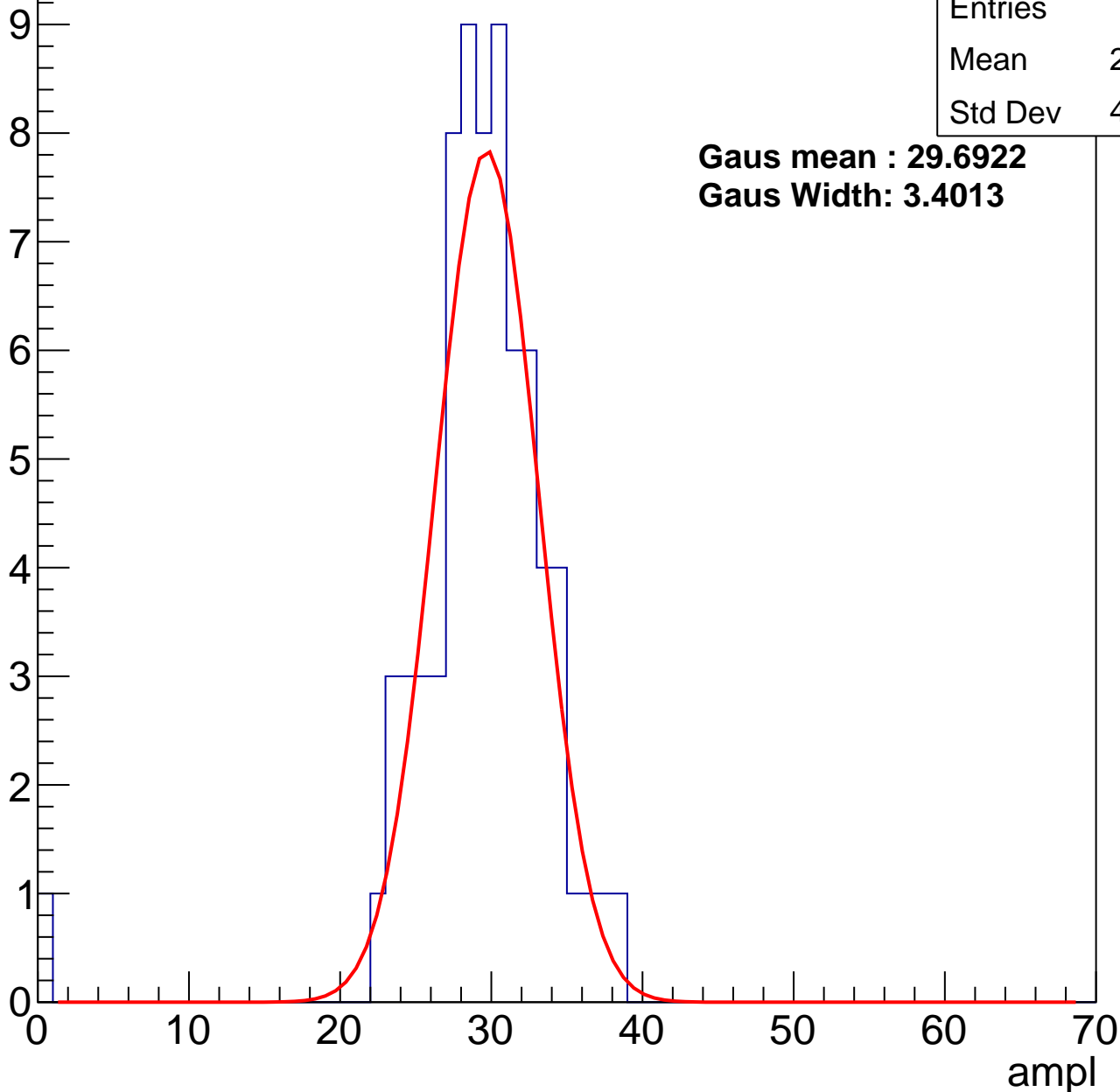
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.86
Std Dev	4.814

Gaus mean : 29.6922

Gaus Width: 3.4013



B1L101S, U26-ch72, adc1

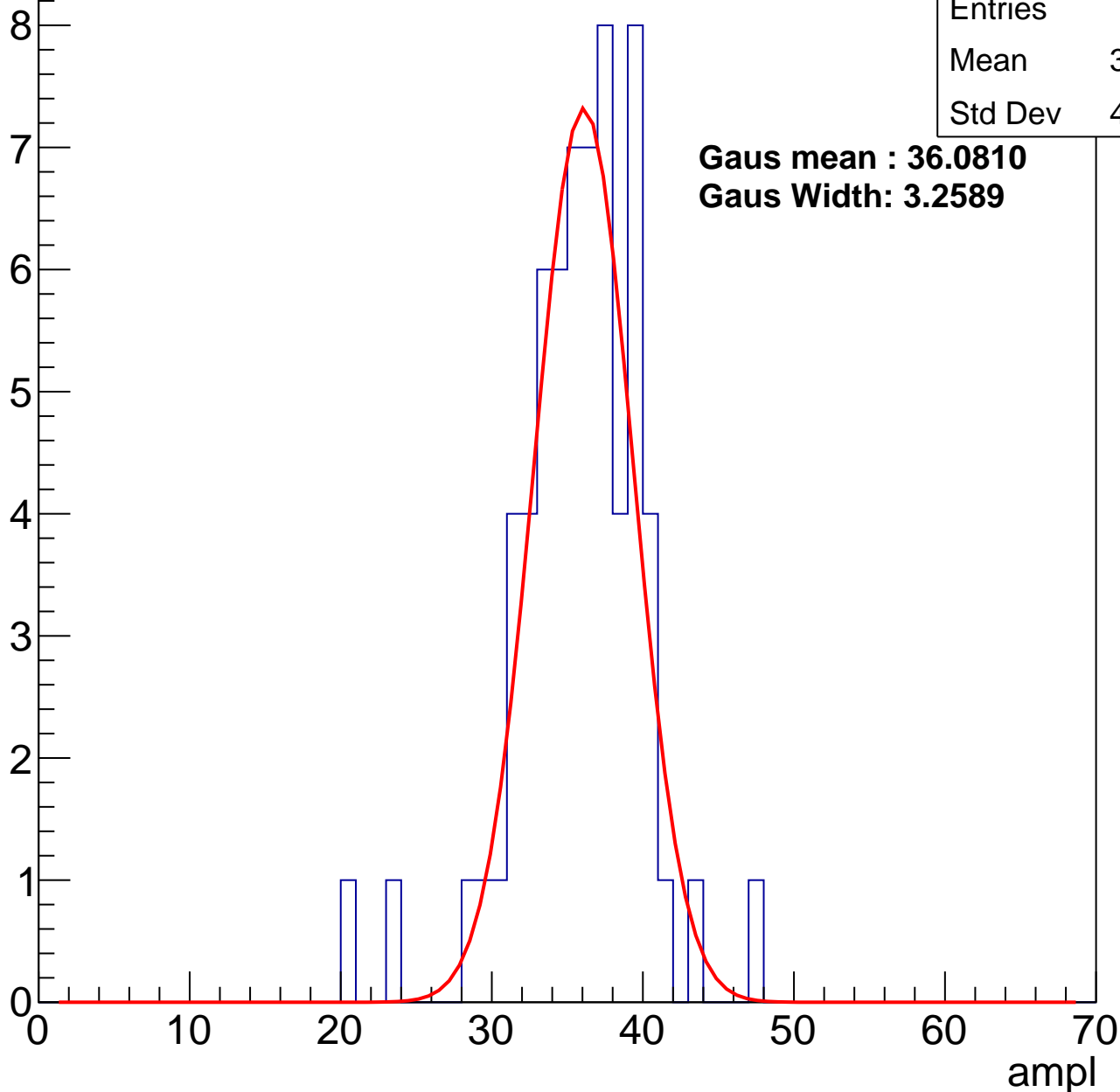
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.33
Std Dev	4.172

Gaus mean : 36.0810

Gaus Width: 3.2589



B1L101S, U26-ch72, adc2

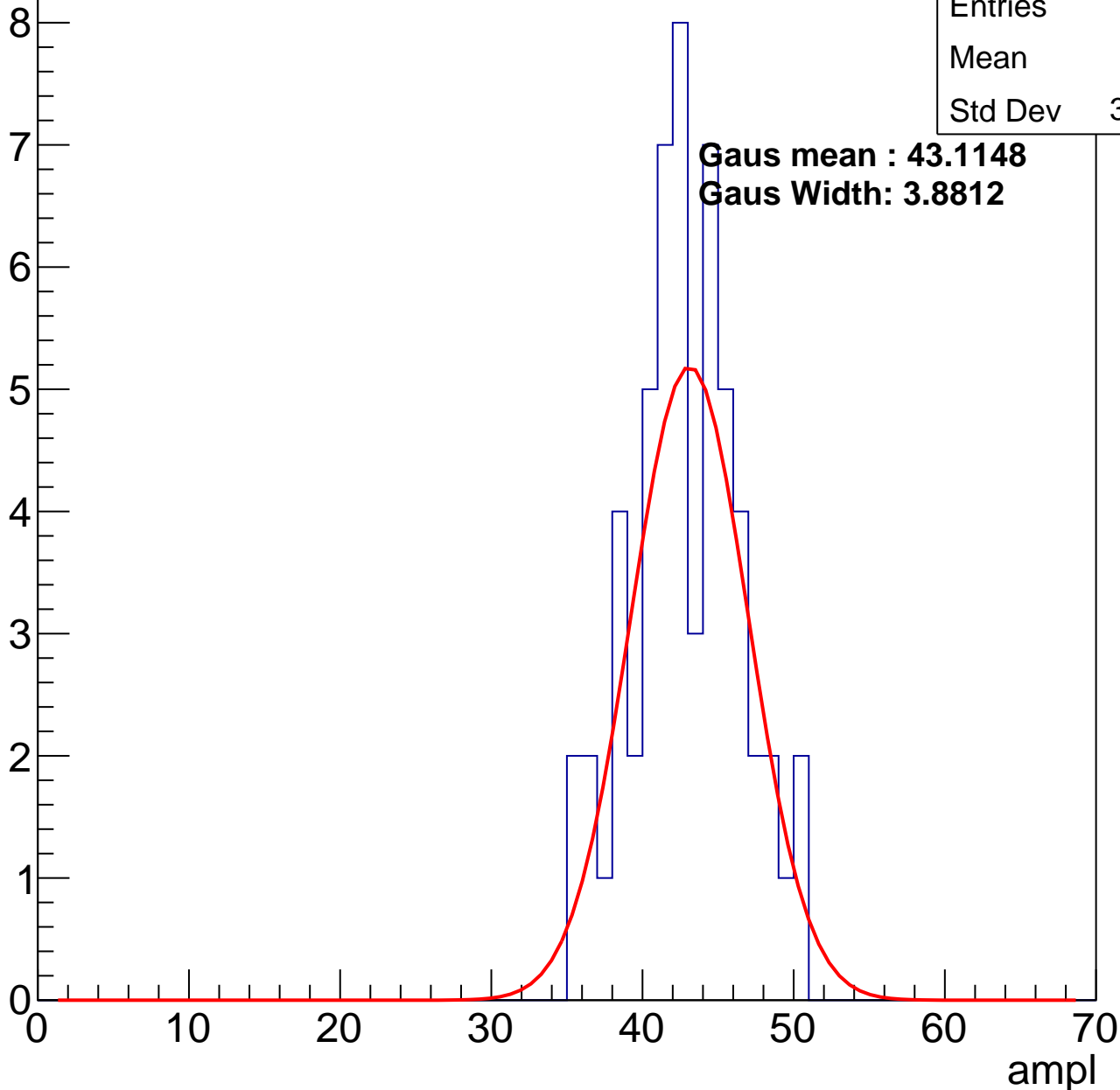
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.4
Std Dev	3.588

Gaus mean : 43.1148

Gaus Width: 3.8812

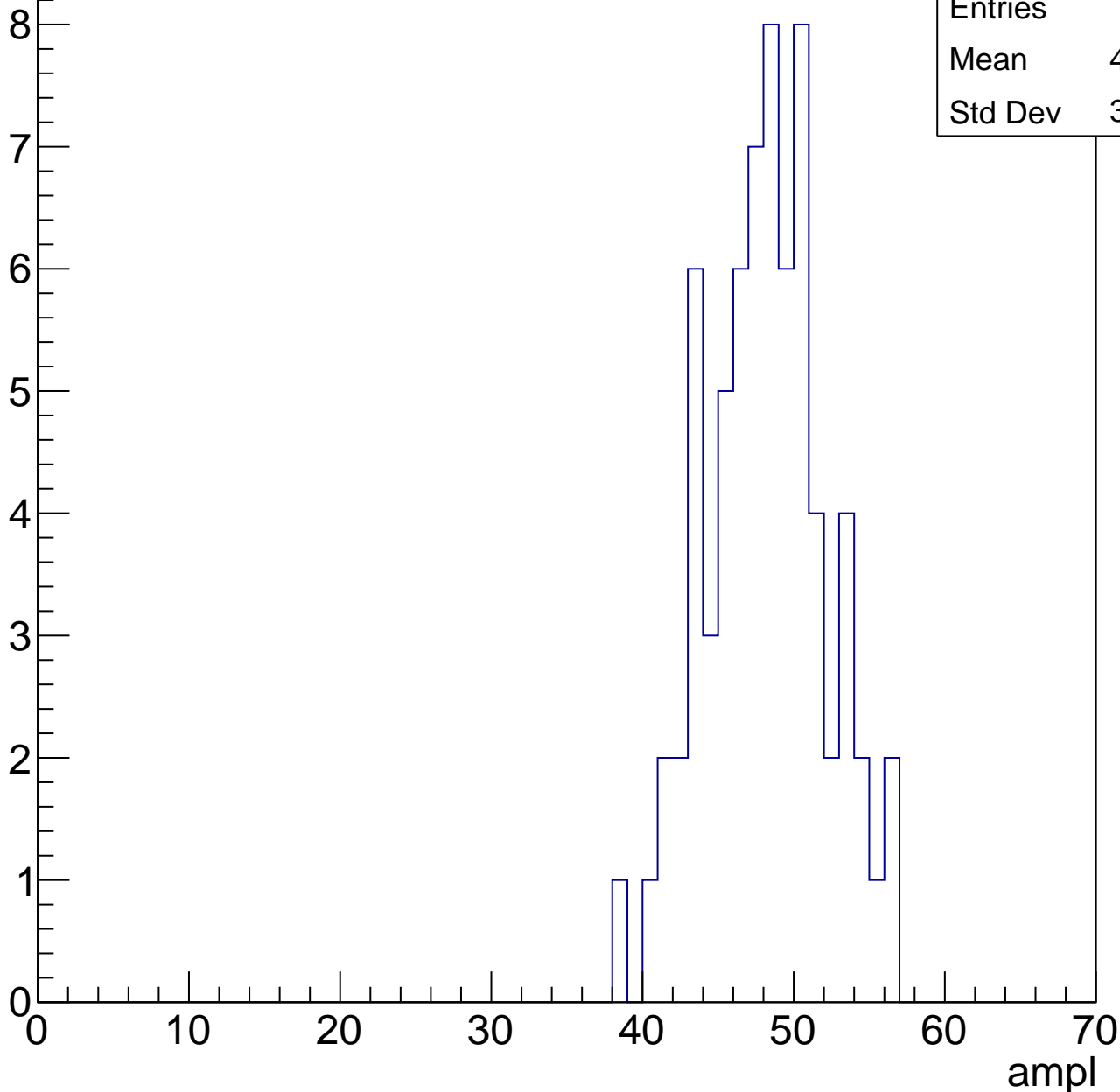


B1L101S, U26-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	47.67
Std Dev	3.883

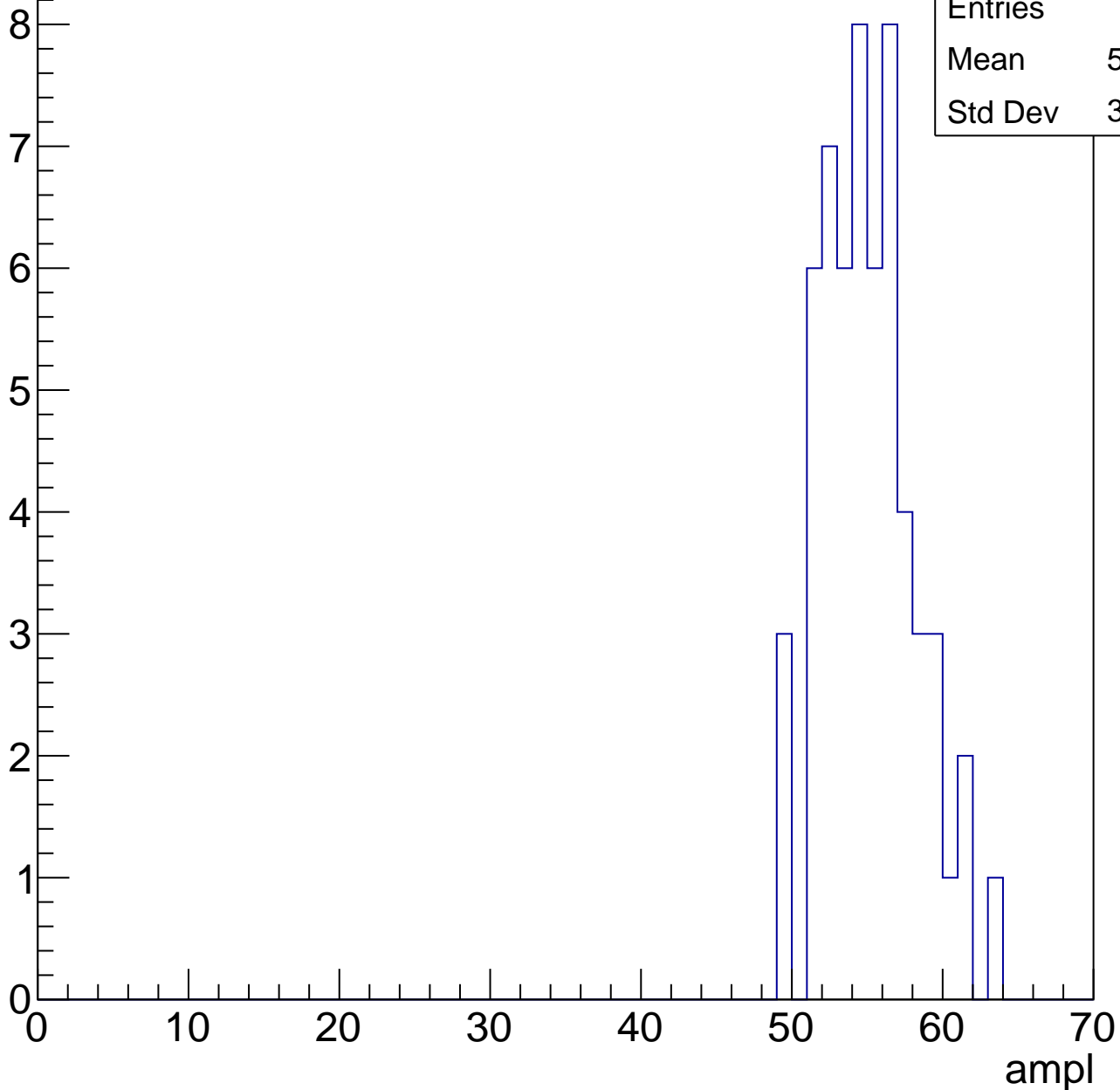


B1L101S, U26-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

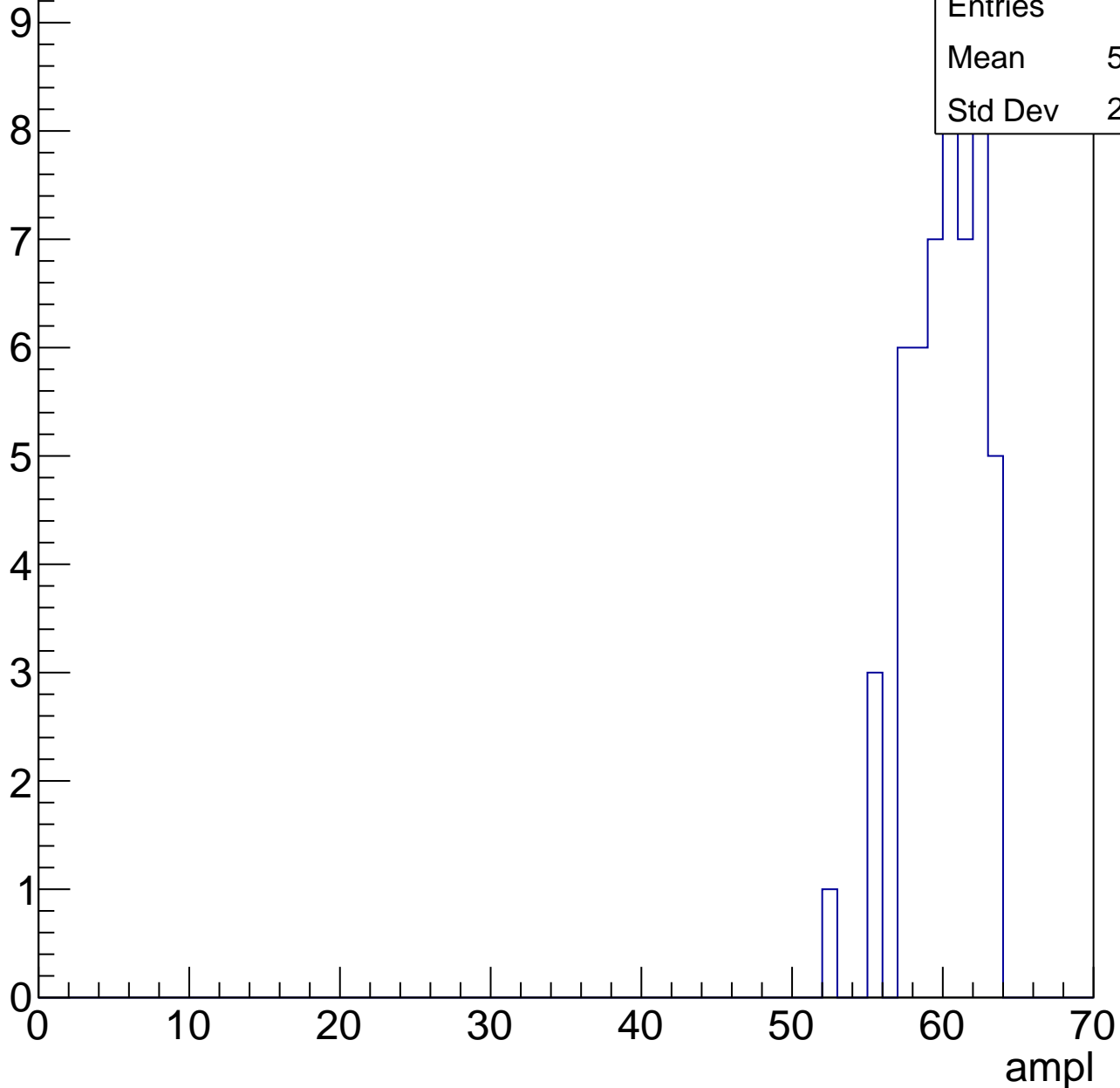
Entries	58
Mean	54.64
Std Dev	3.083



B1L101S, U26-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

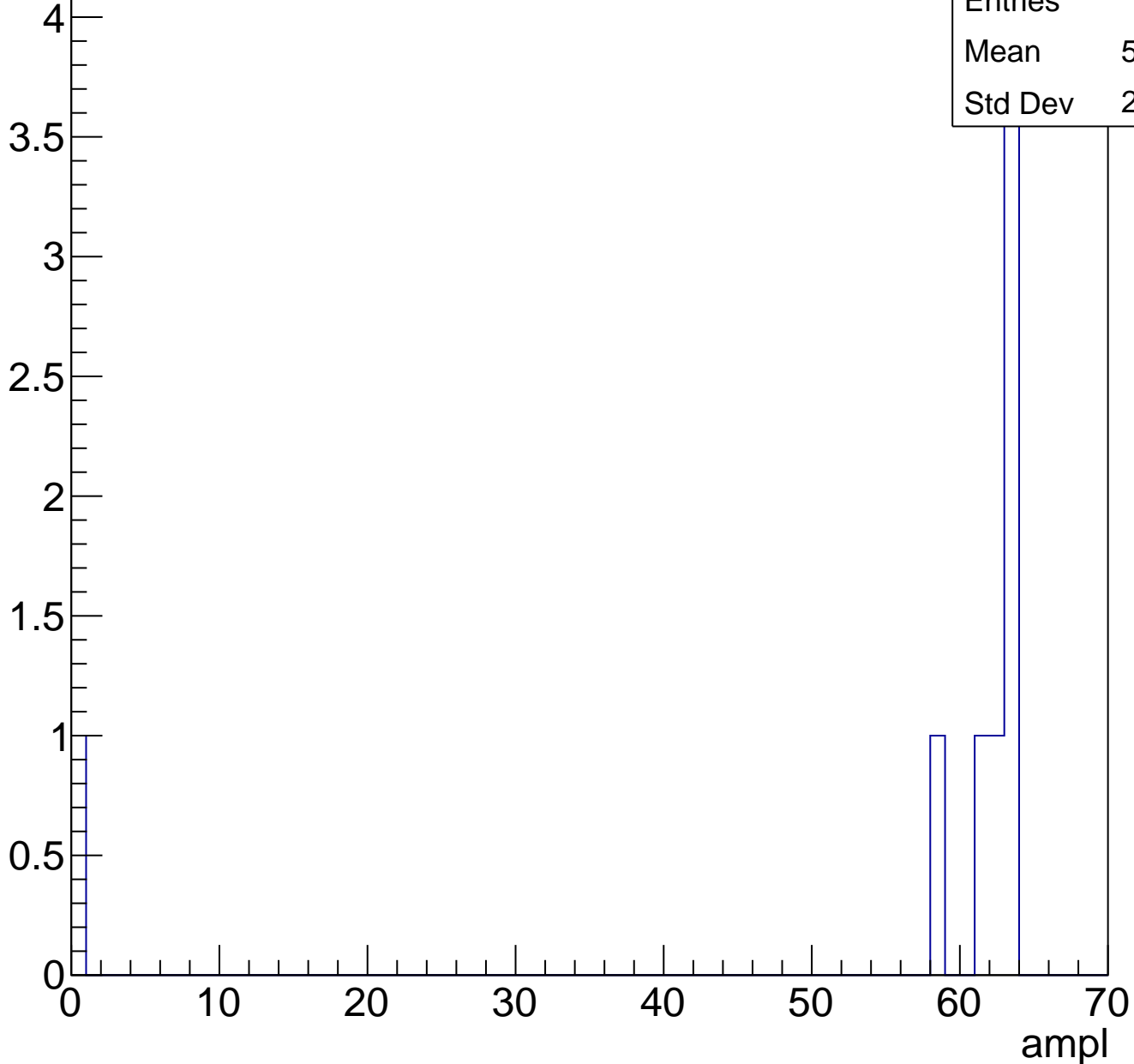
Entry



B1L101S, U26-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch73, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	31.94
Std Dev	5.233

Gaus mean : 33.0729

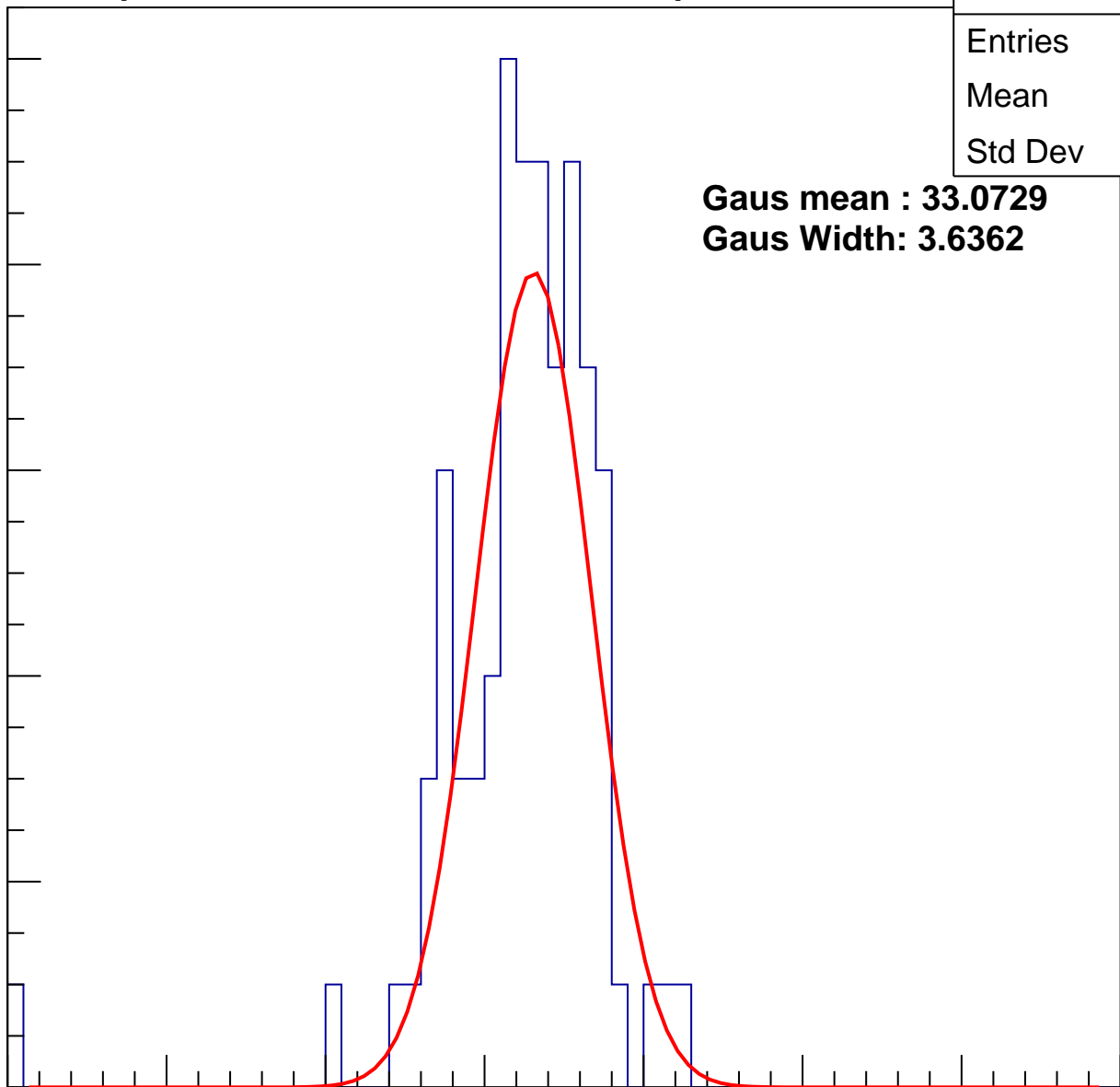
Gaus Width: 3.6362

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch73, adc1

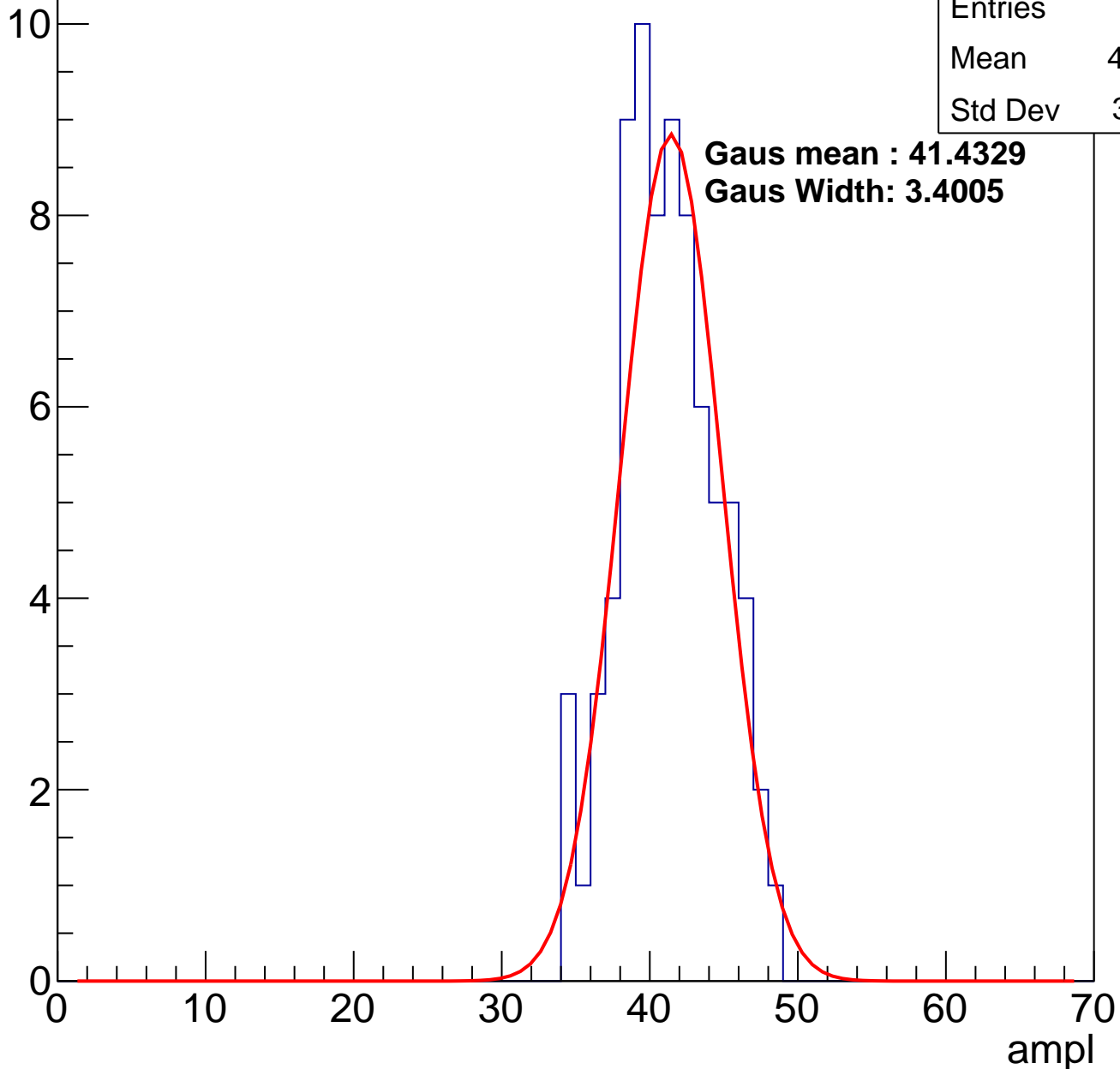
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	40.76
Std Dev	3.251

Gaus mean : 41.4329

Gaus Width: 3.4005

Entry



B1L101S, U26-ch73, adc2

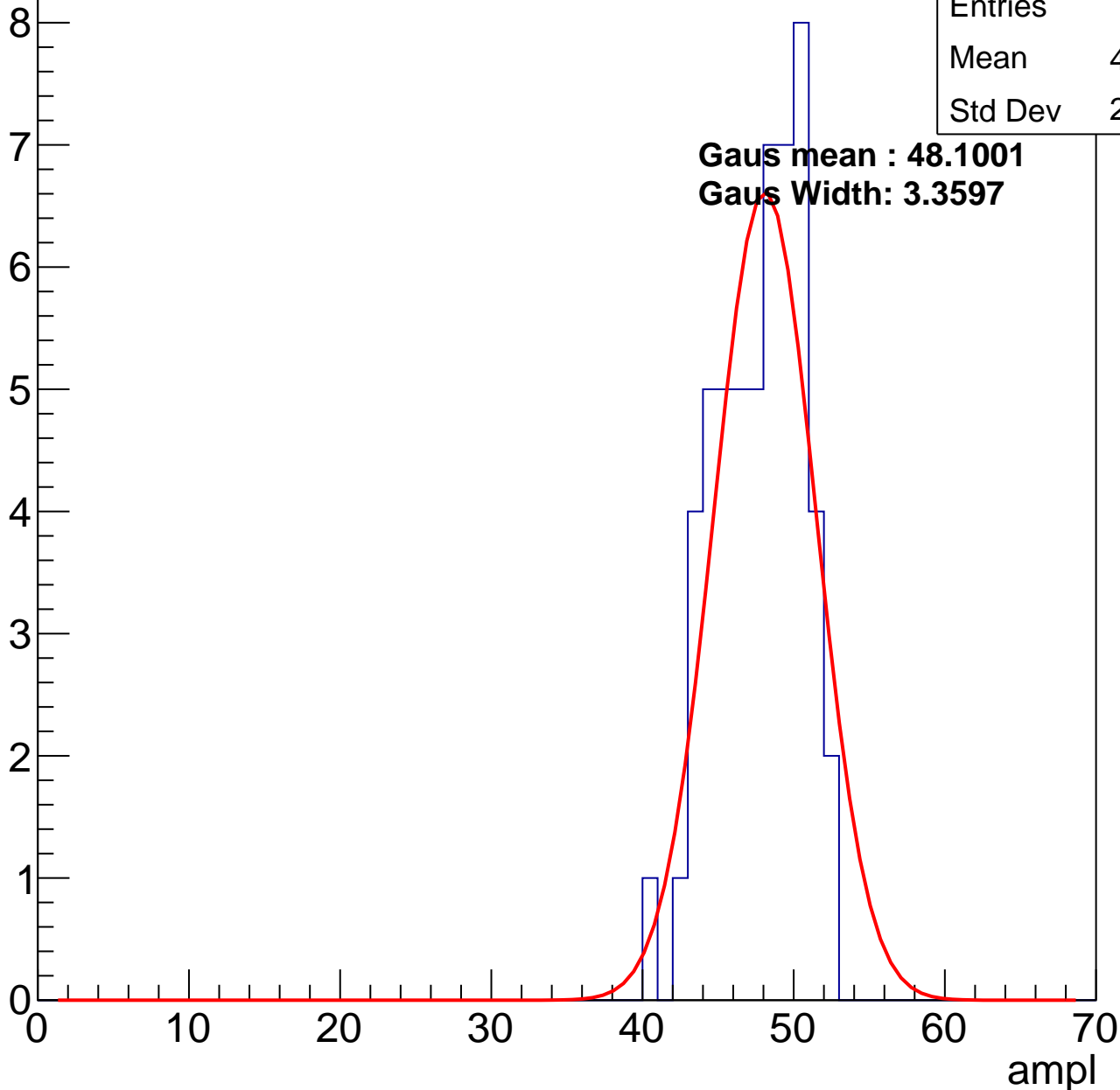
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	47.24
Std Dev	2.808

Gaus mean : 48.1001

Gaus Width: 3.3597



B1L101S, U26-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

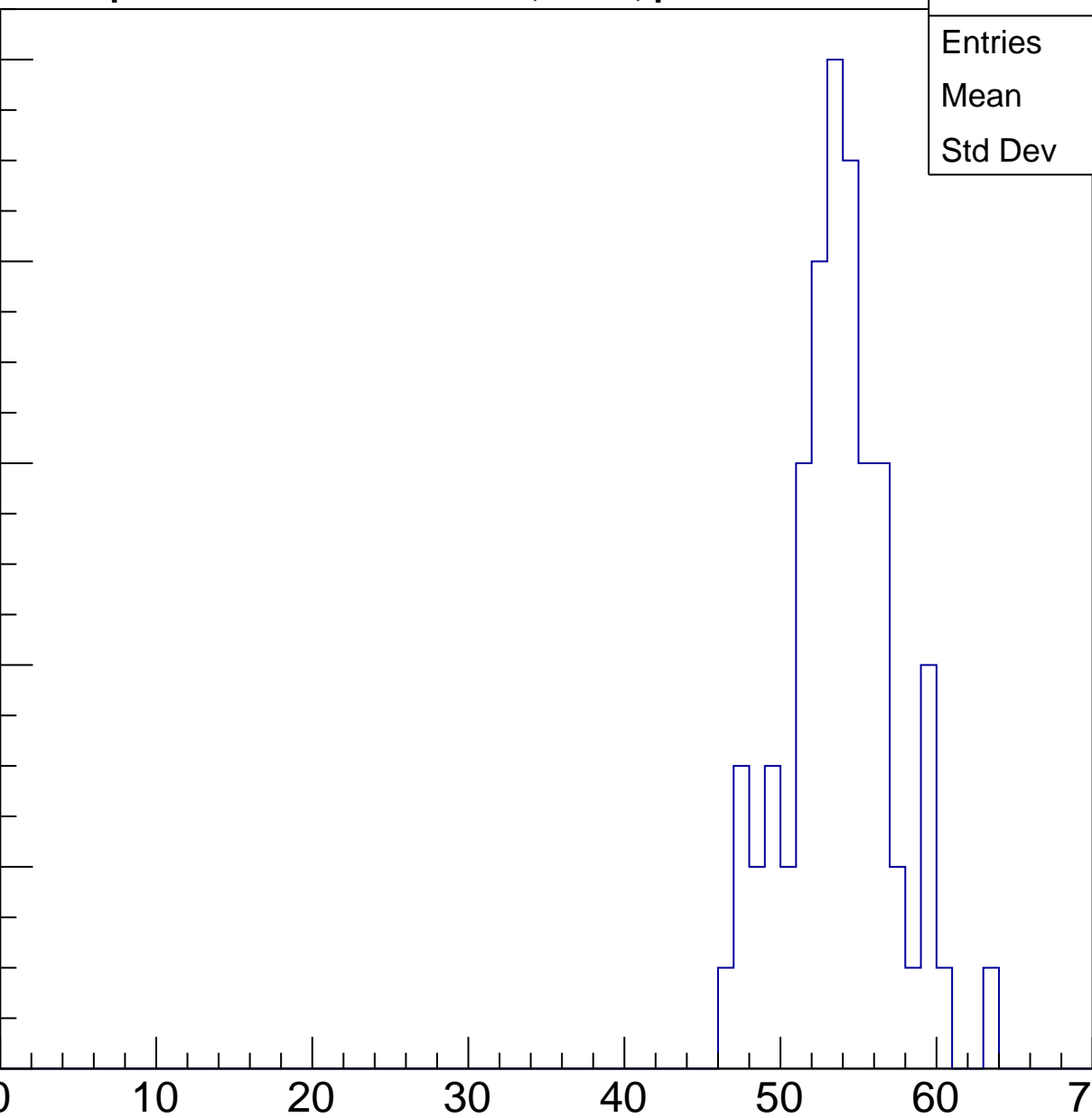
Entries	65
Mean	53.31
Std Dev	3.365

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch73, adc4

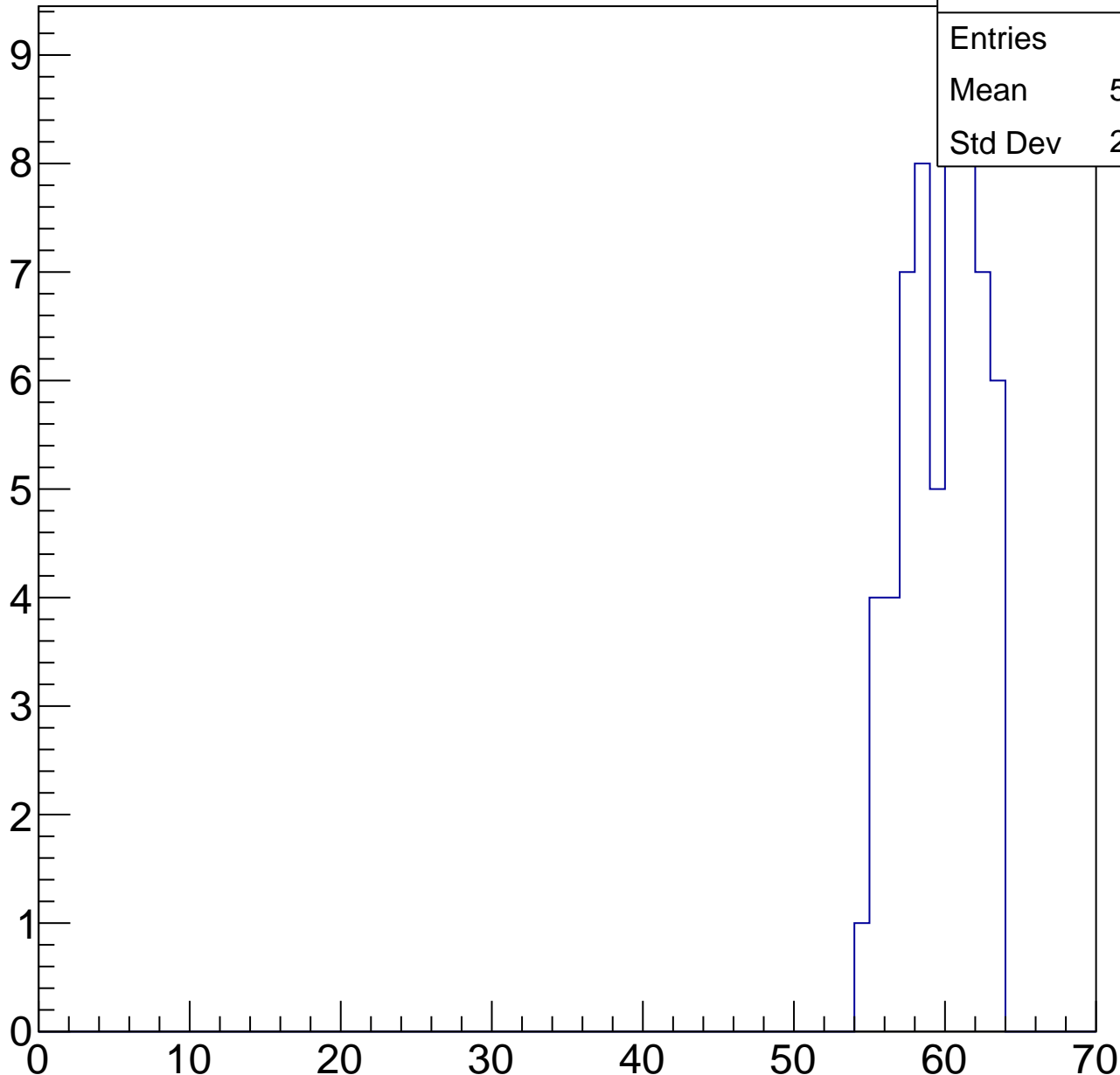
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	59.27
Std Dev	2.469

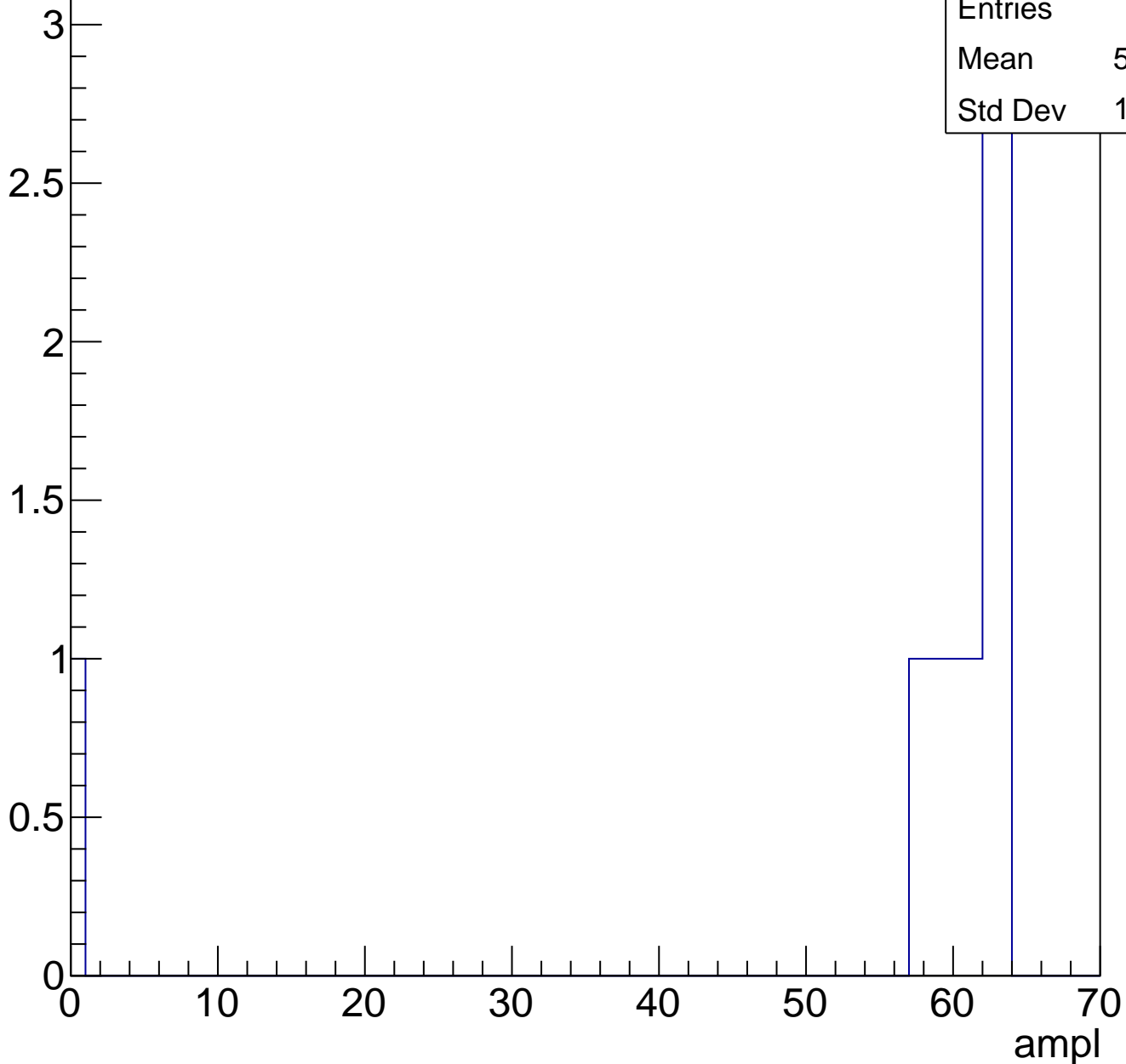
ampl



B1L101S, U26-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch74, adc0

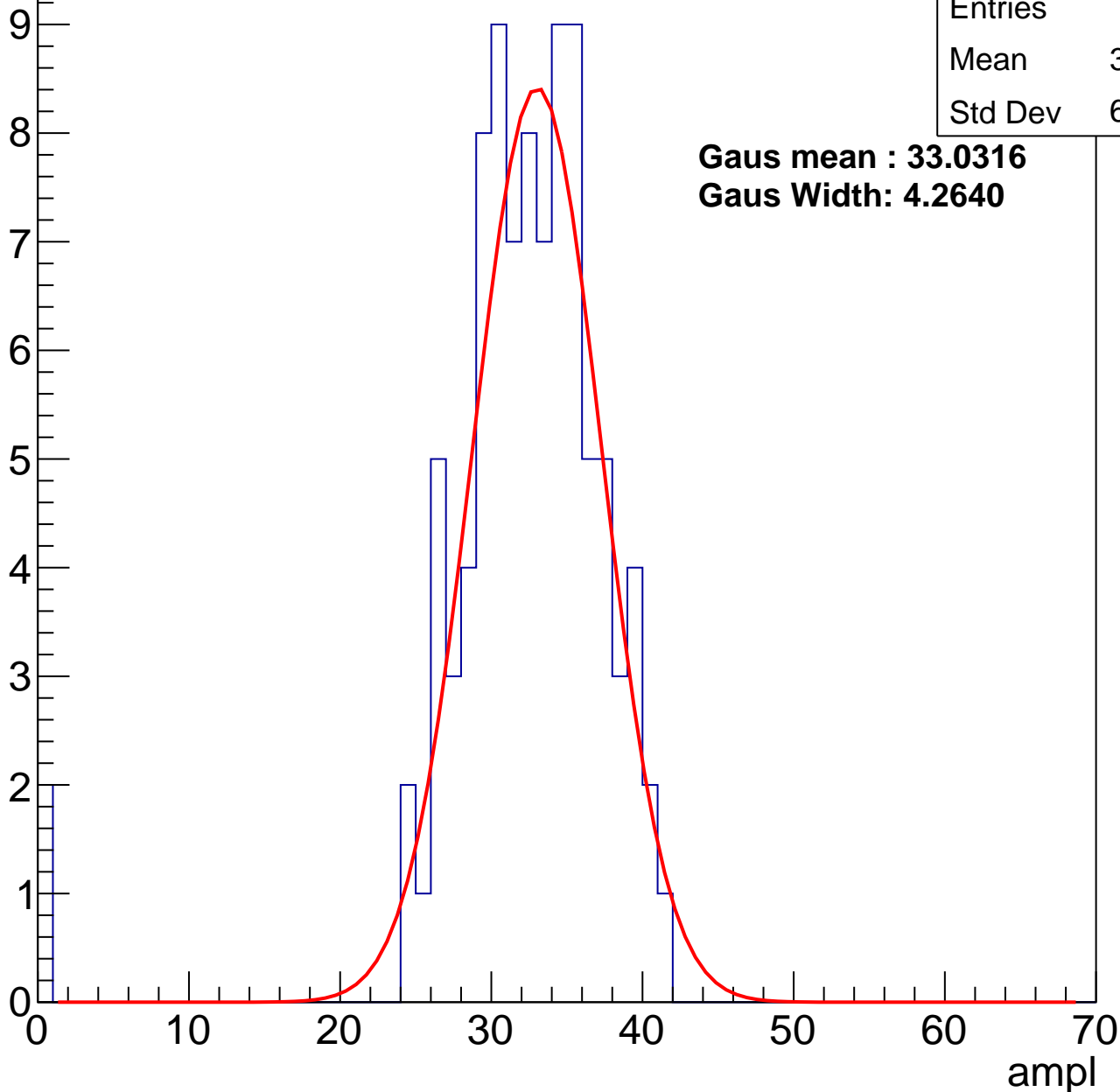
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	94
Mean	31.69
Std Dev	6.095

Gaus mean : 33.0316

Gaus Width: 4.2640



B1L101S, U26-ch74, adc1

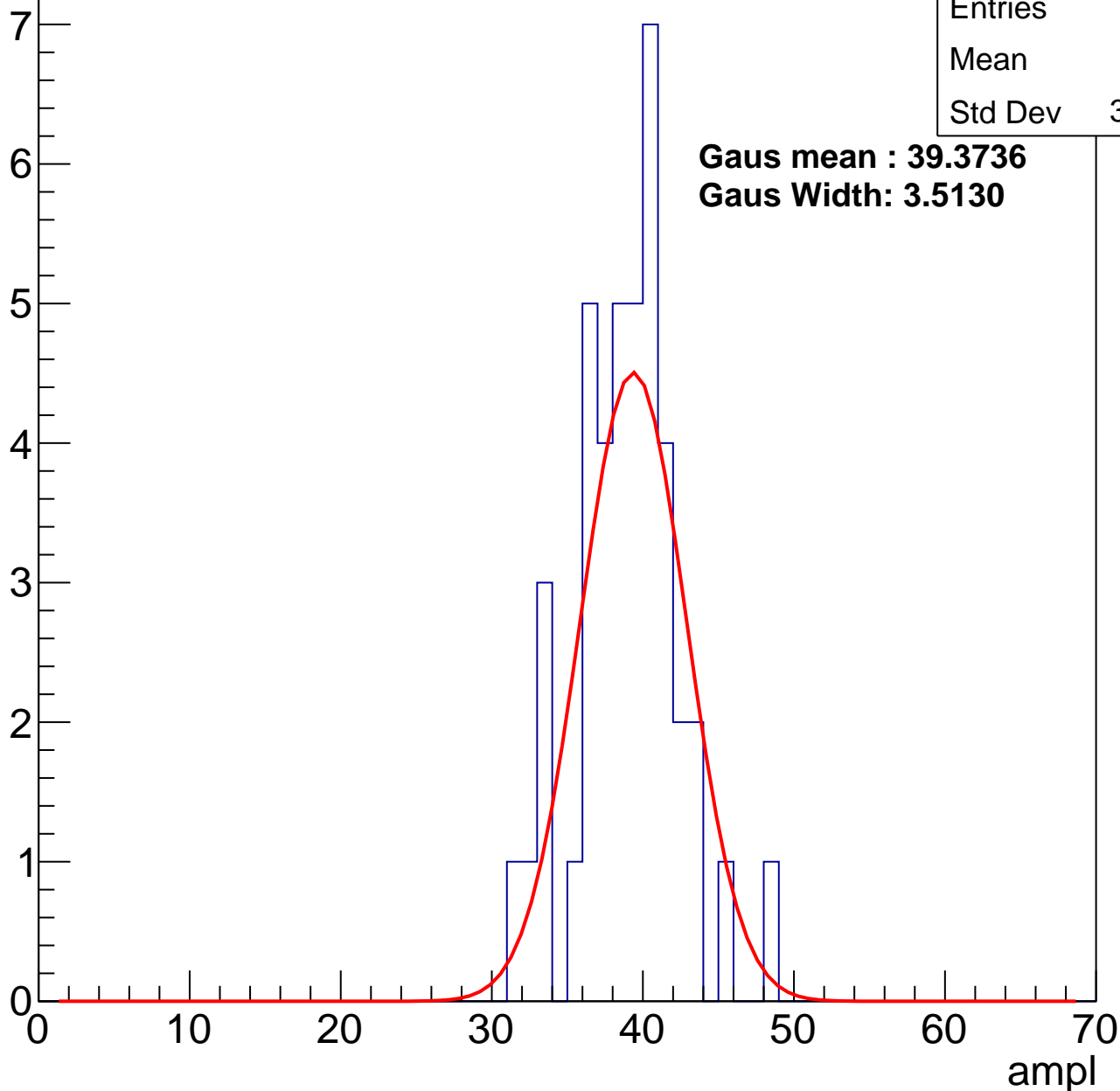
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	38.5
Std Dev	3.389

Gaus mean : 39.3736

Gaus Width: 3.5130



B1L101S, U26-ch74, adc2

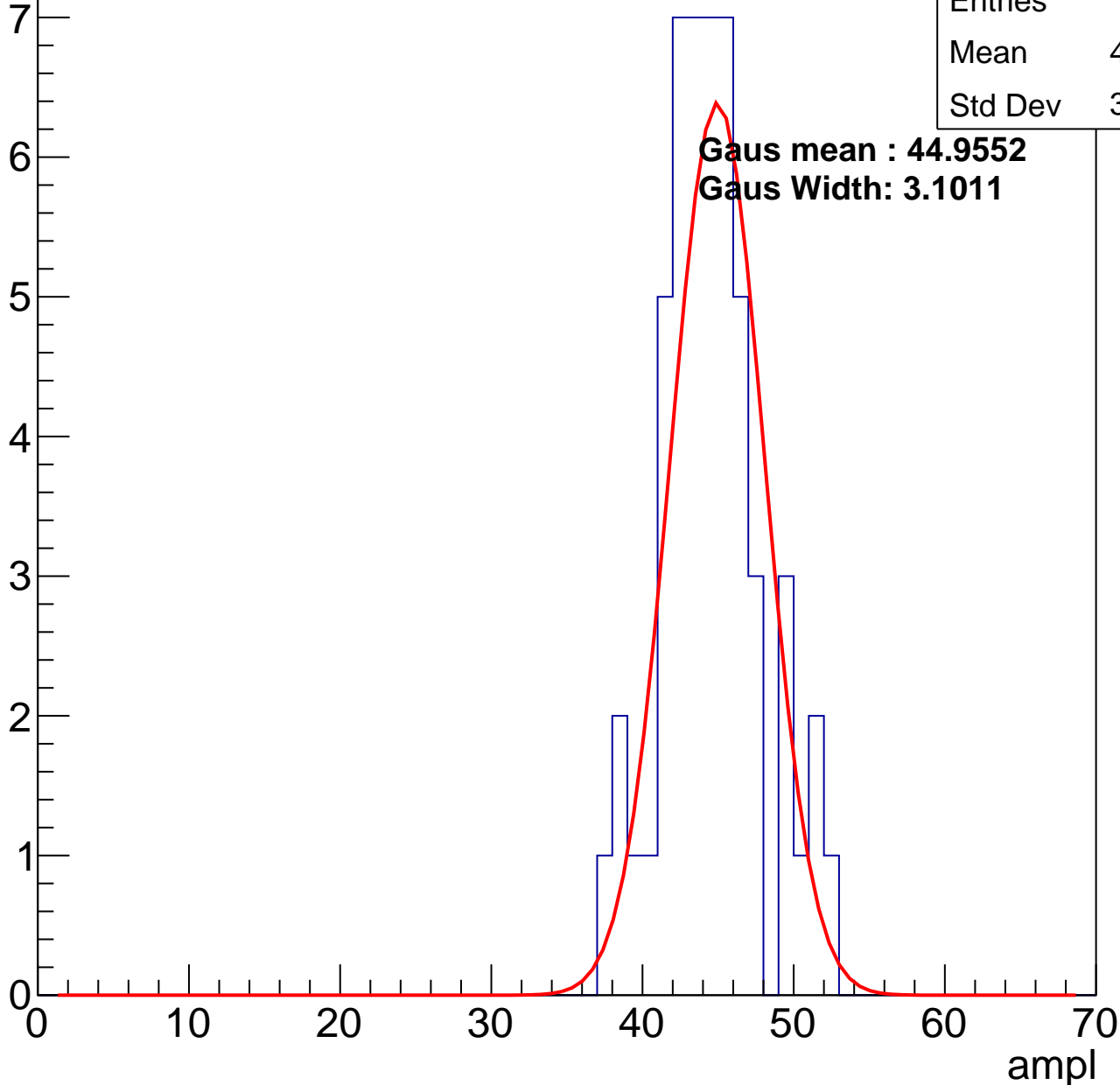
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	44.09
Std Dev	3.275

Gaus mean : 44.9552

Gaus Width: 3.1011

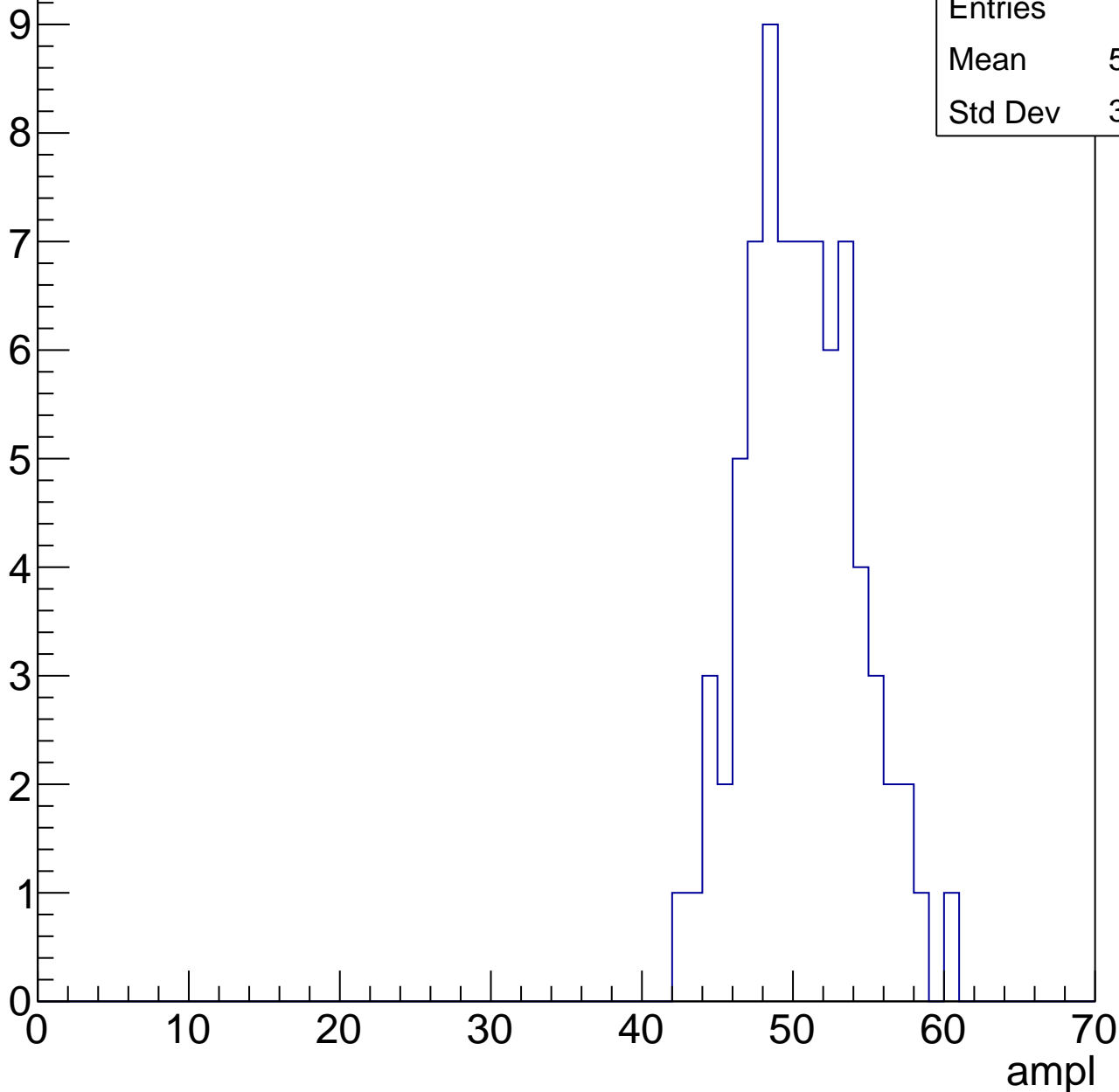


B1L101S, U26-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

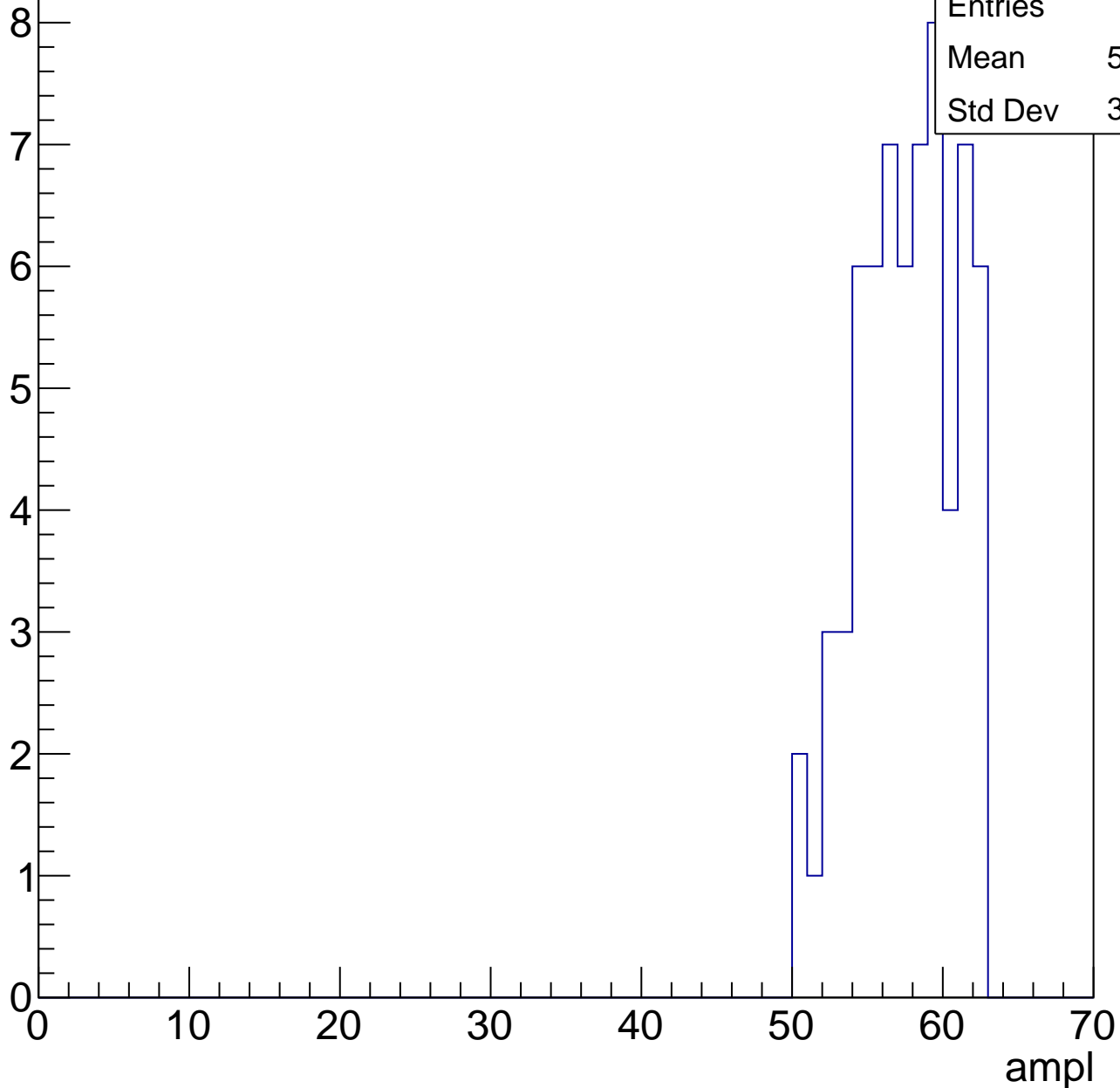
Entries	75
Mean	50.08
Std Dev	3.694



B1L101S, U26-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

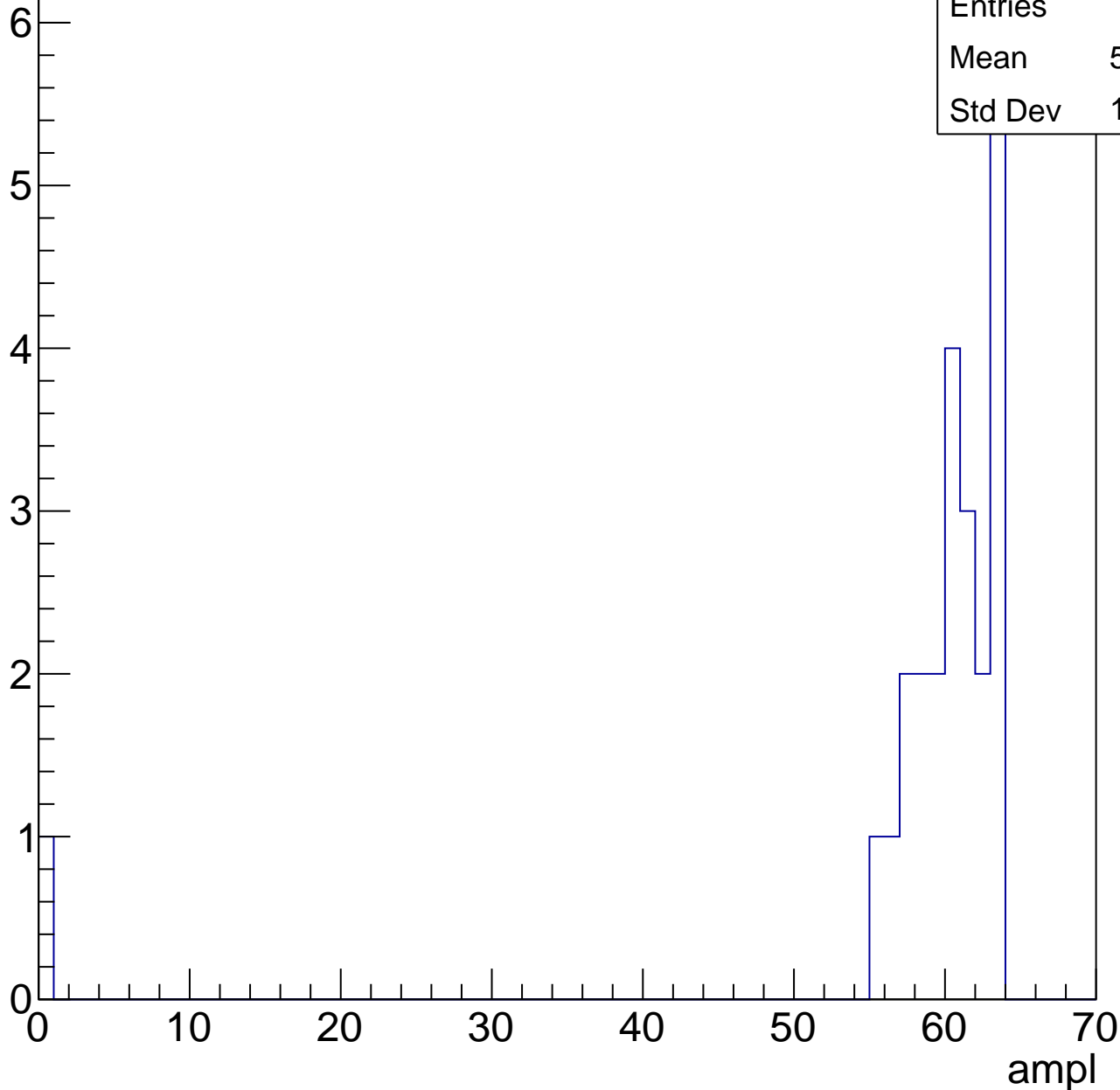


B1L101S, U26-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	57.67
Std Dev	12.25



B1L101S, U26-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch75, adc0

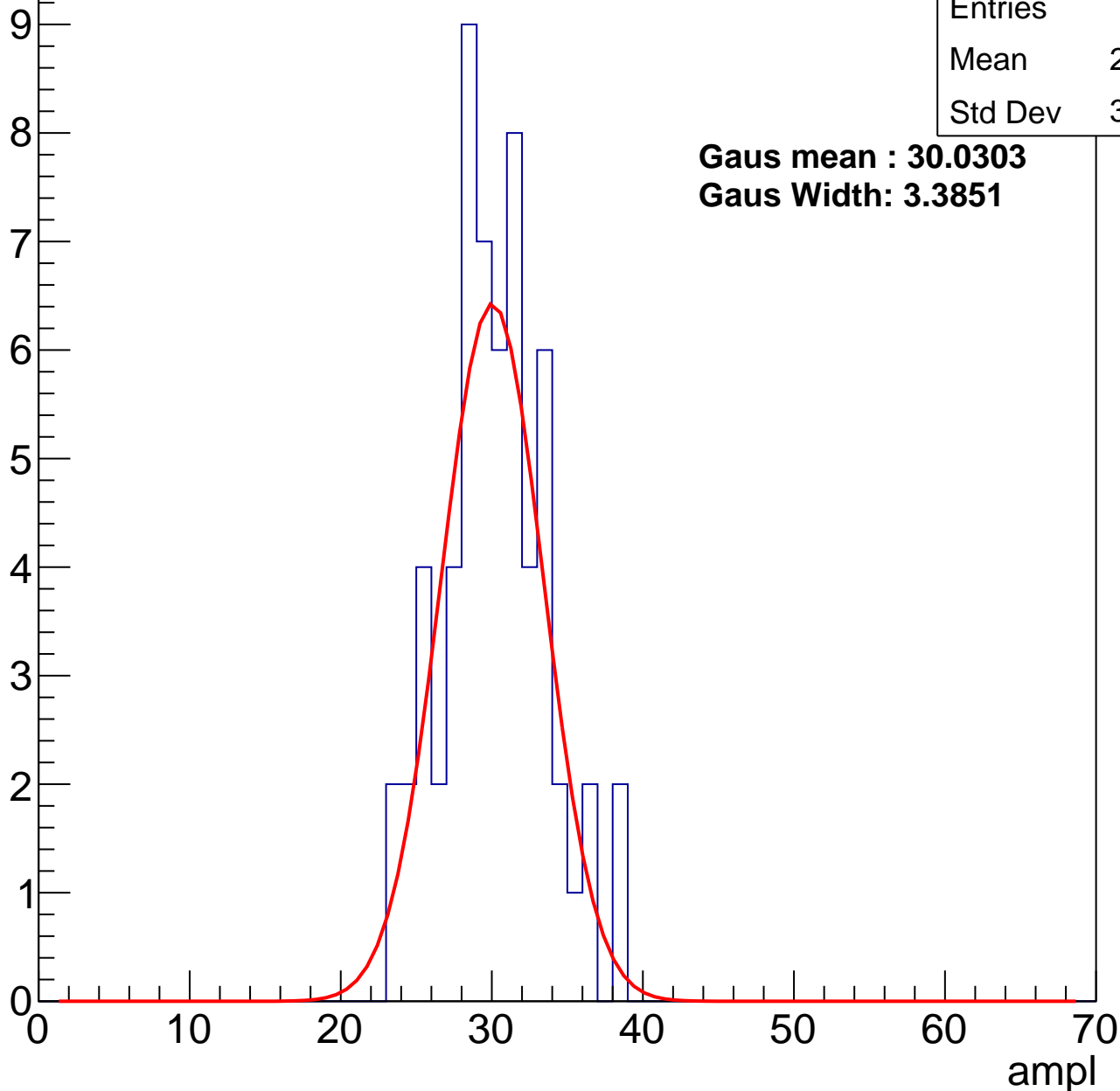
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.74
Std Dev	3.416

Gaus mean : 30.0303

Gaus Width: 3.3851



B1L101S, U26-ch75, adc1

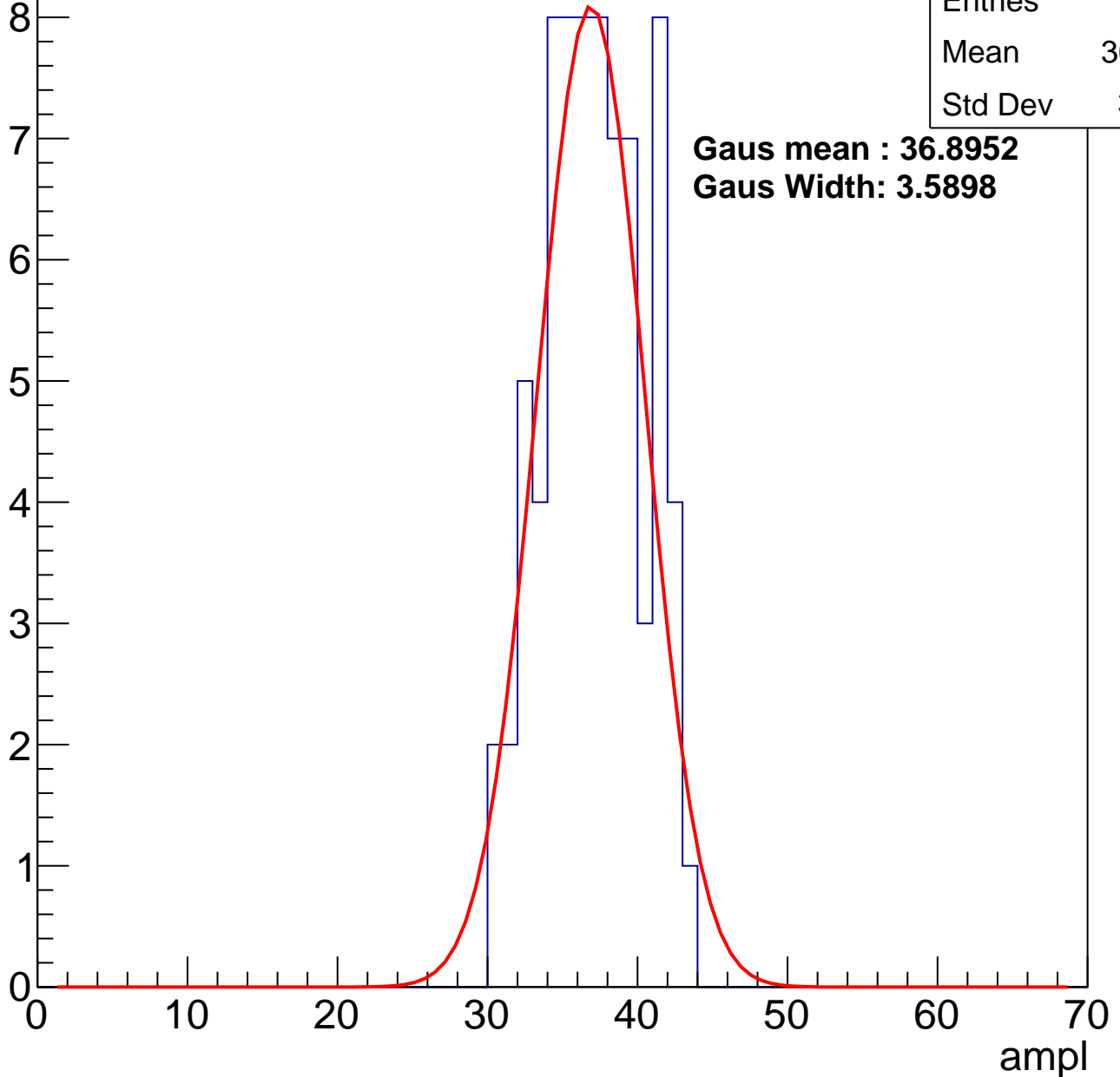
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.64
Std Dev	3.24

Gaus mean : 36.8952

Gaus Width: 3.5898



B1L101S, U26-ch75, adc2

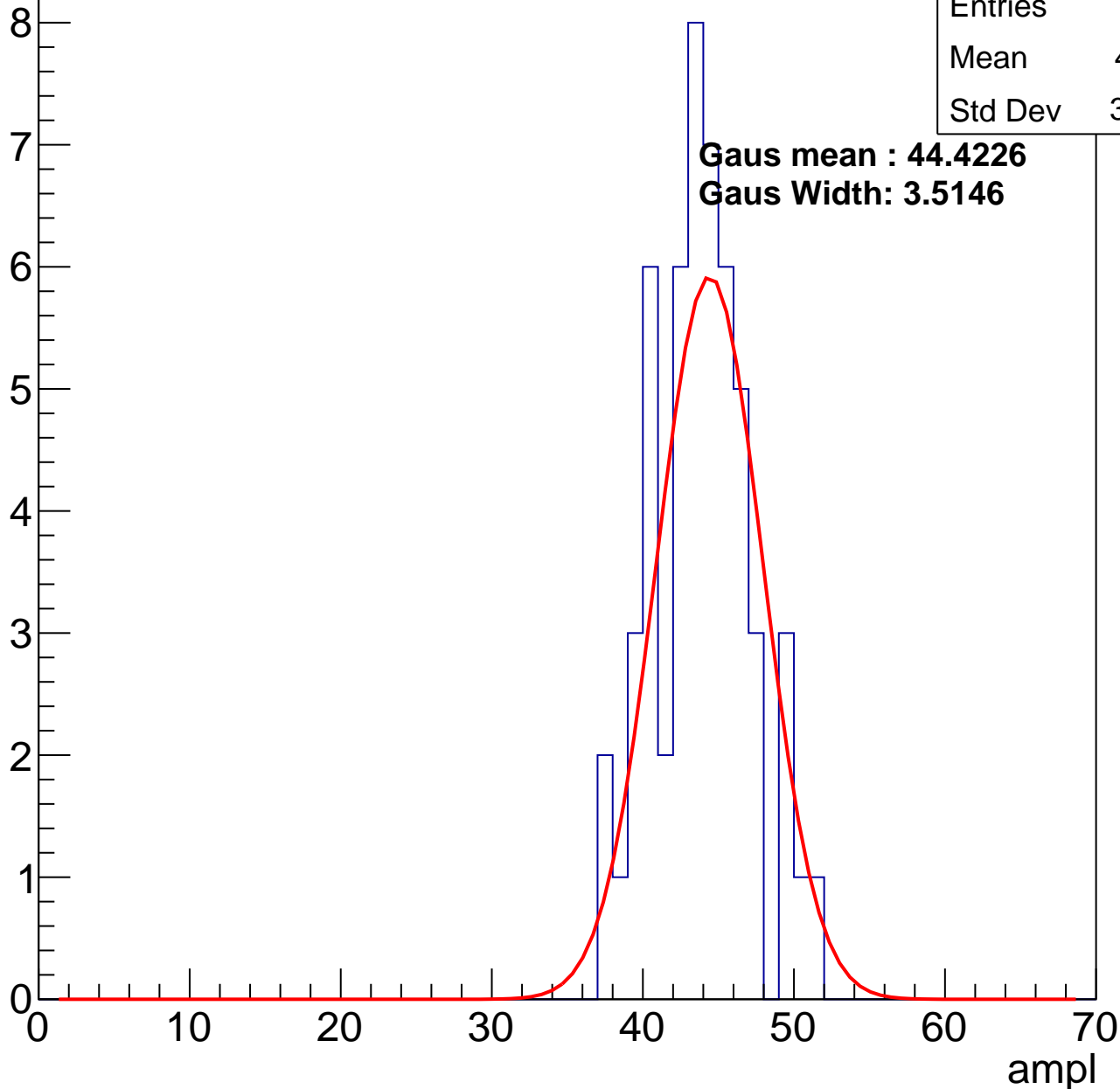
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.41
Std Dev	3.189

Gaus mean : 44.4226

Gaus Width: 3.5146



B1L101S, U26-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

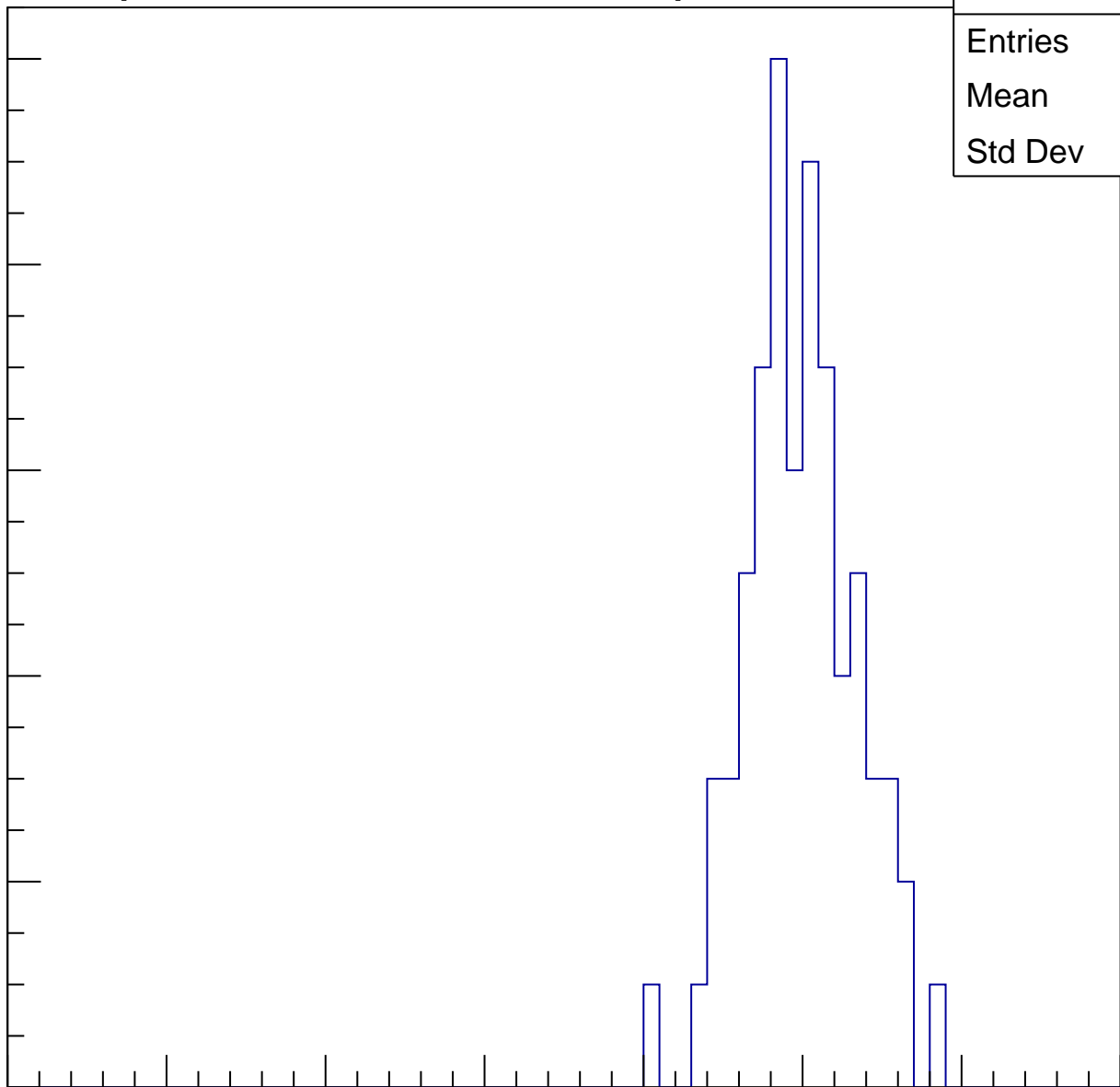
Entries	70
Mean	49.43
Std Dev	3.433

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

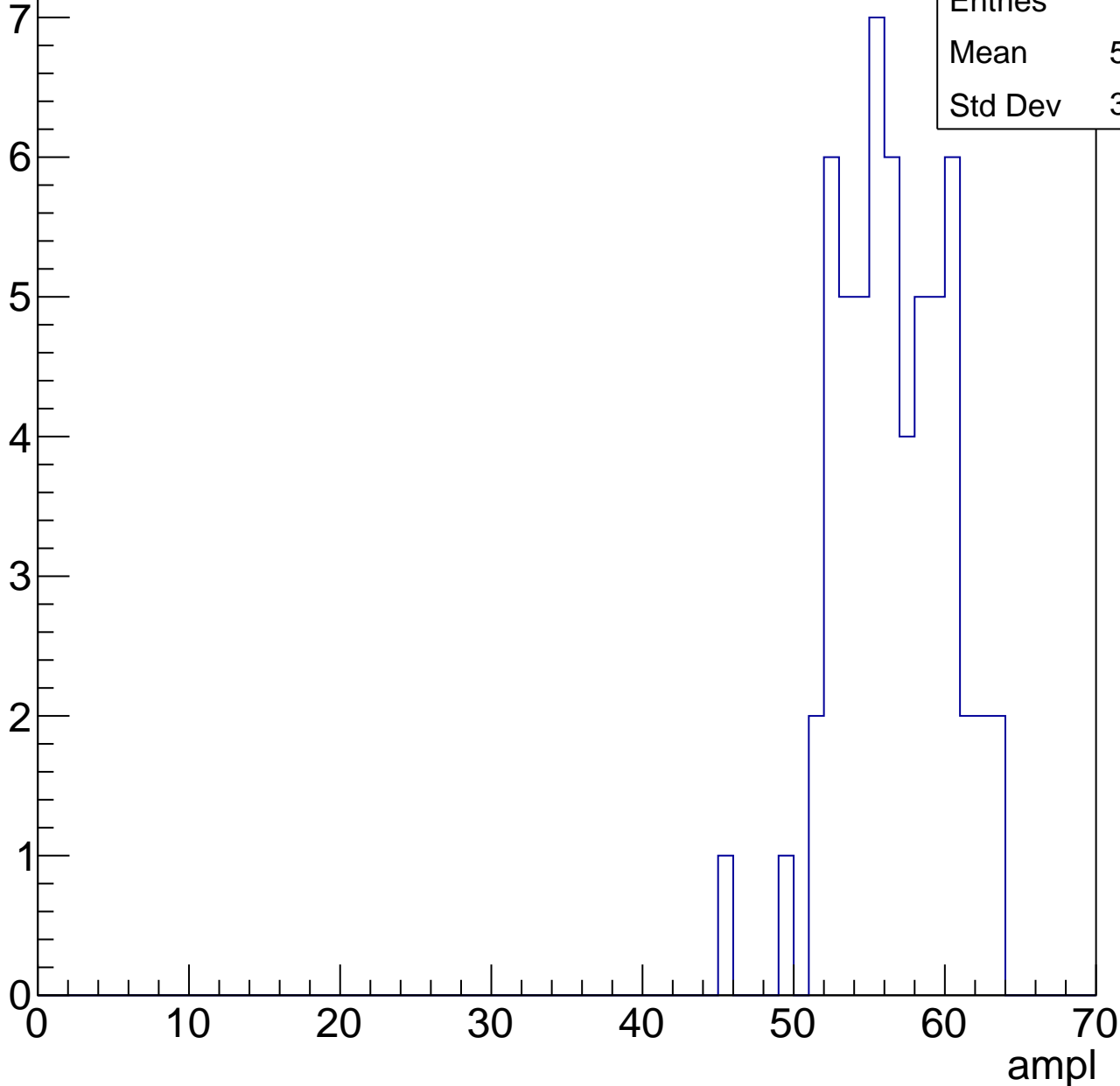


B1L101S, U26-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.08
Std Dev	3.619

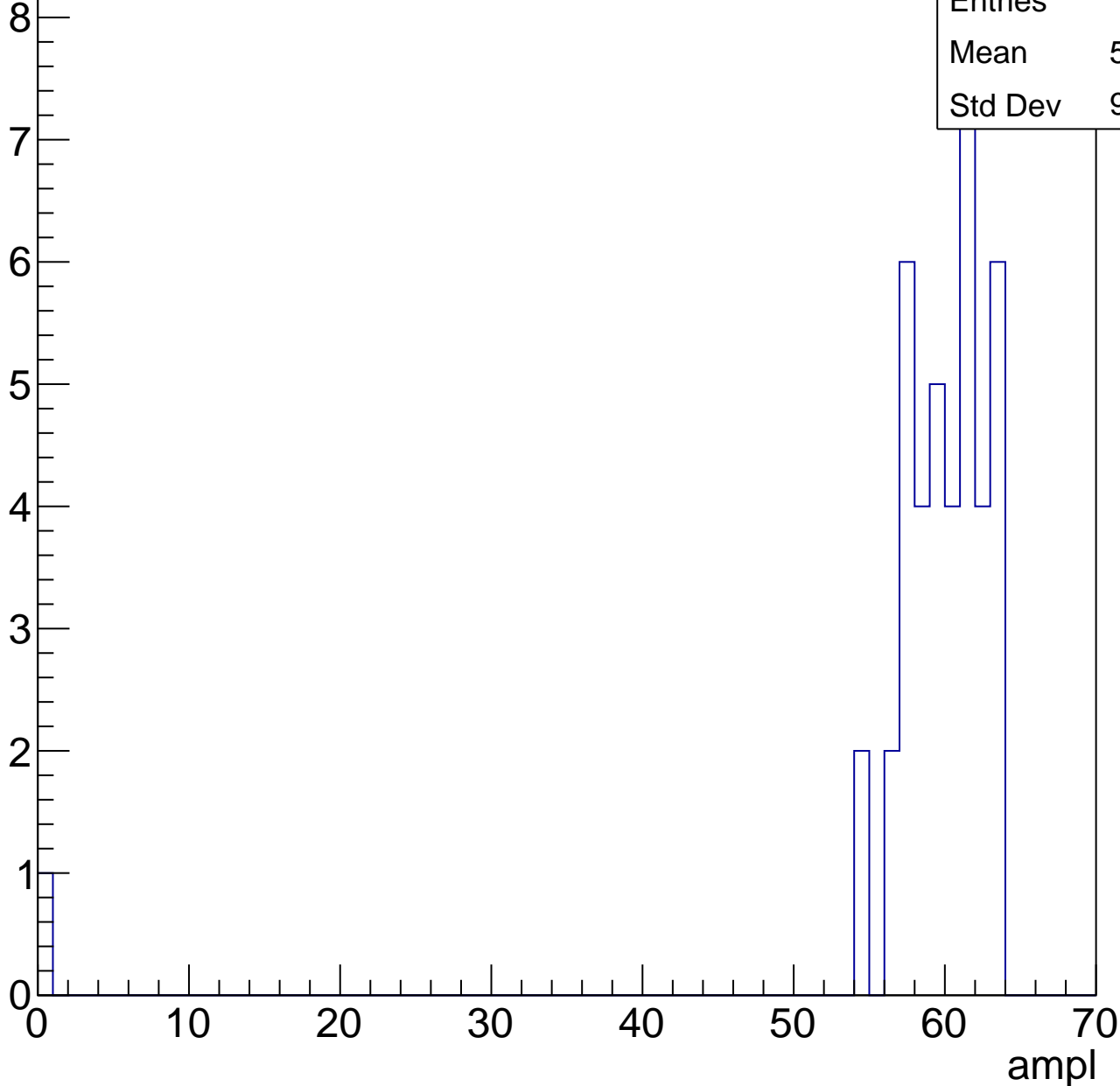


B1L101S, U26-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

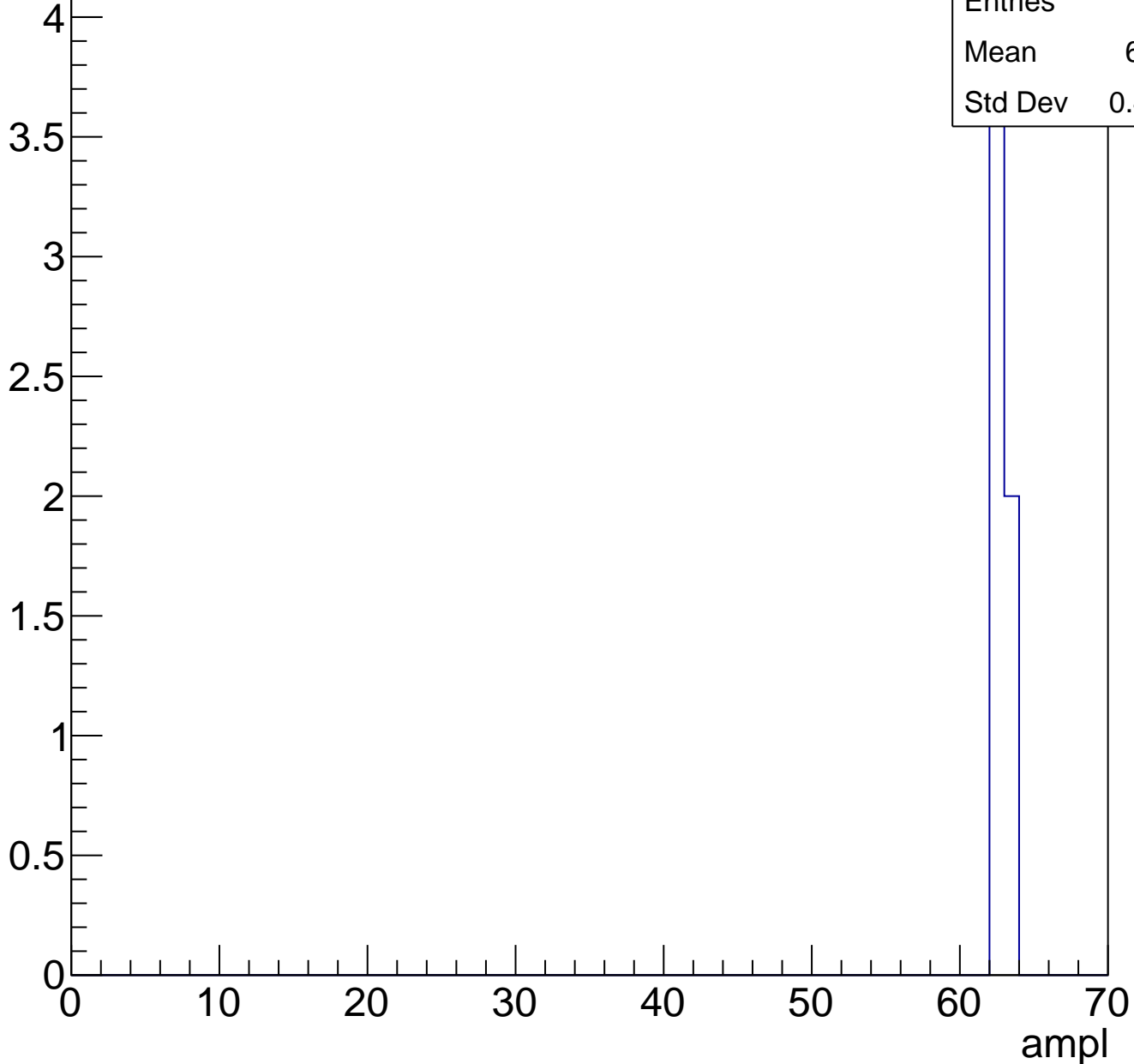
Entries	42
Mean	58.17
Std Dev	9.406



B1L101S, U26-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch76, adc0

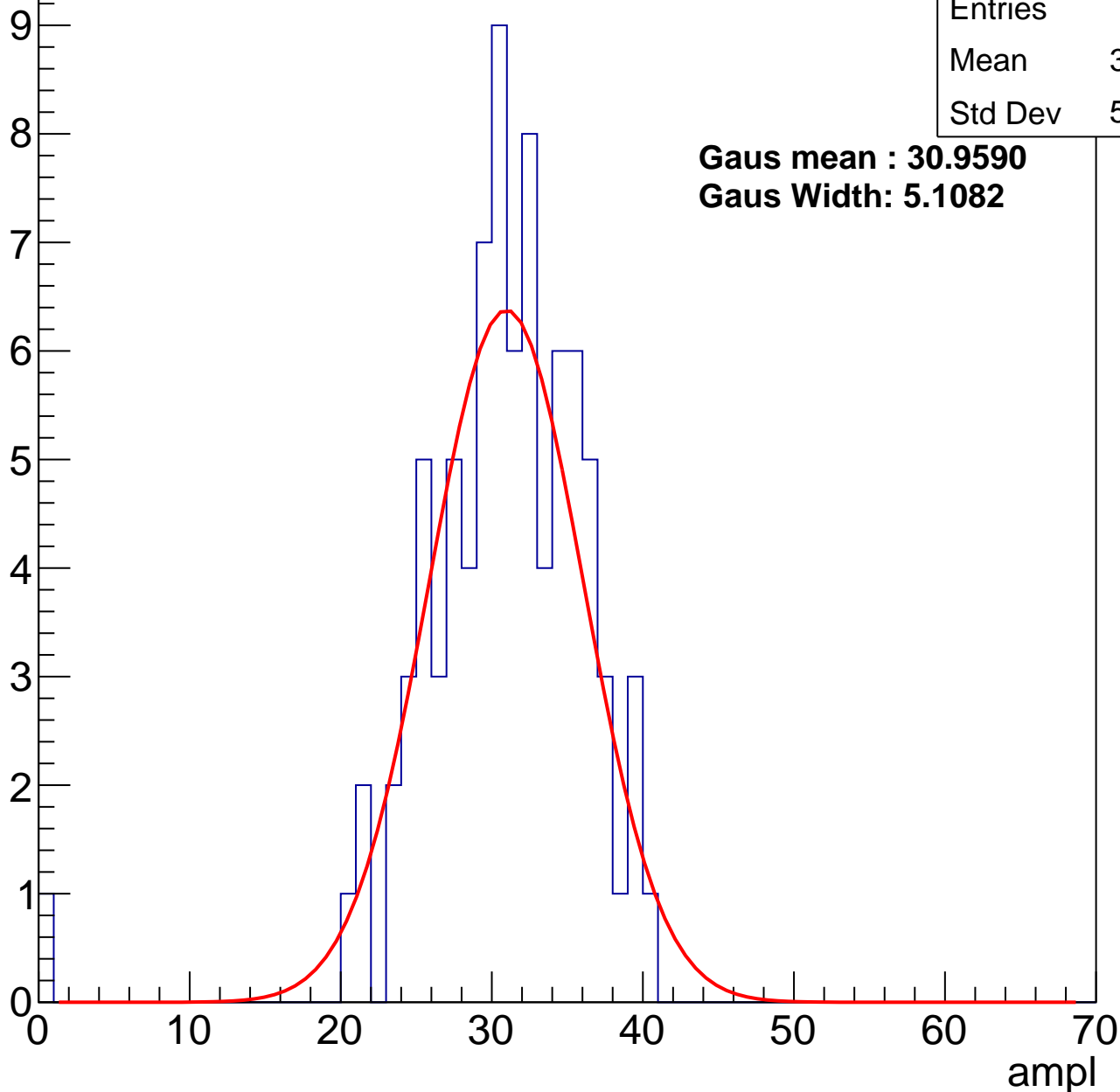
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	30.32
Std Dev	5.588

Gaus mean : 30.9590

Gaus Width: 5.1082



B1L101S, U26-ch76, adc1

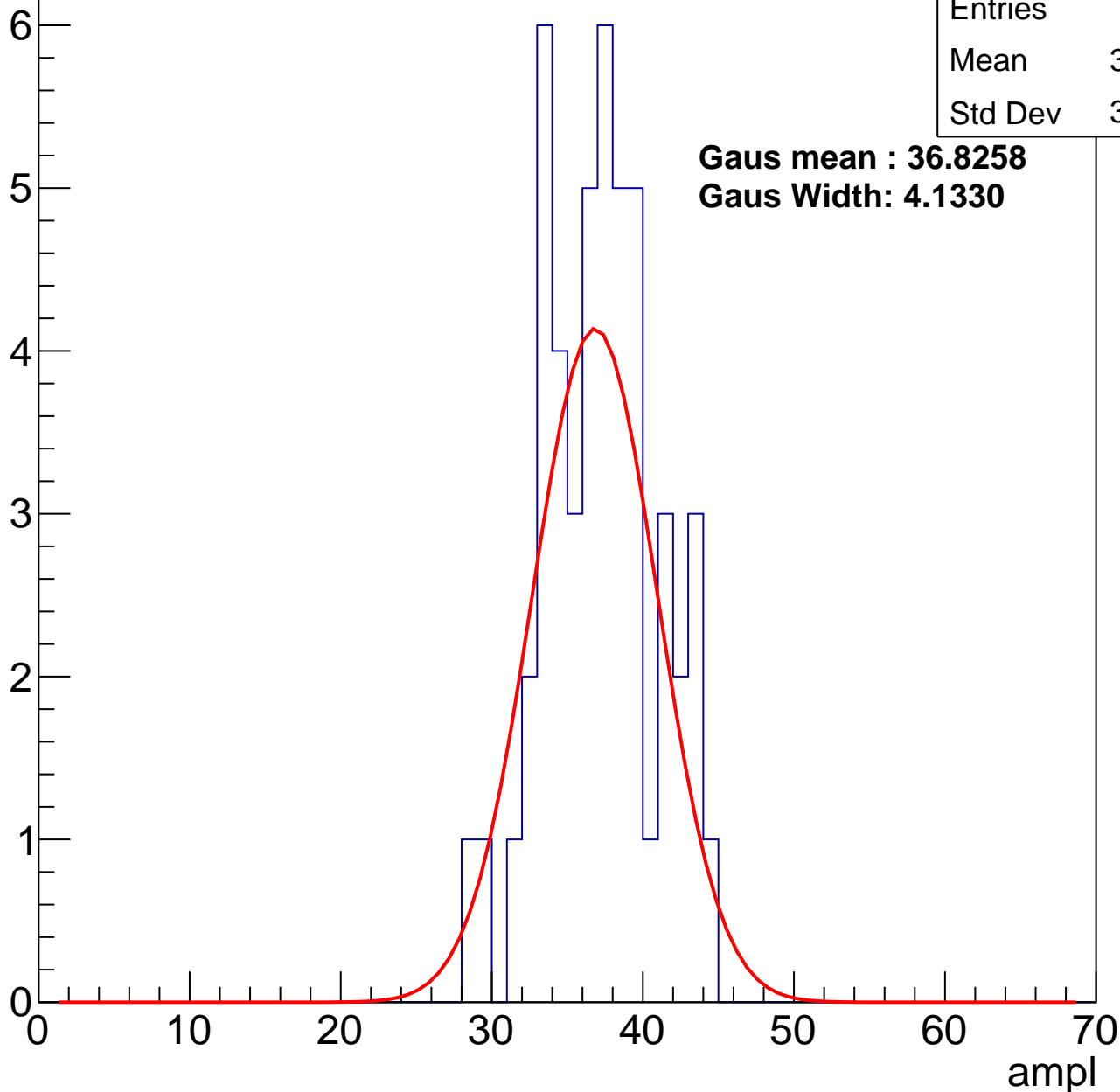
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	36.69
Std Dev	3.682

Gaus mean : 36.8258

Gaus Width: 4.1330



B1L101S, U26-ch76, adc2

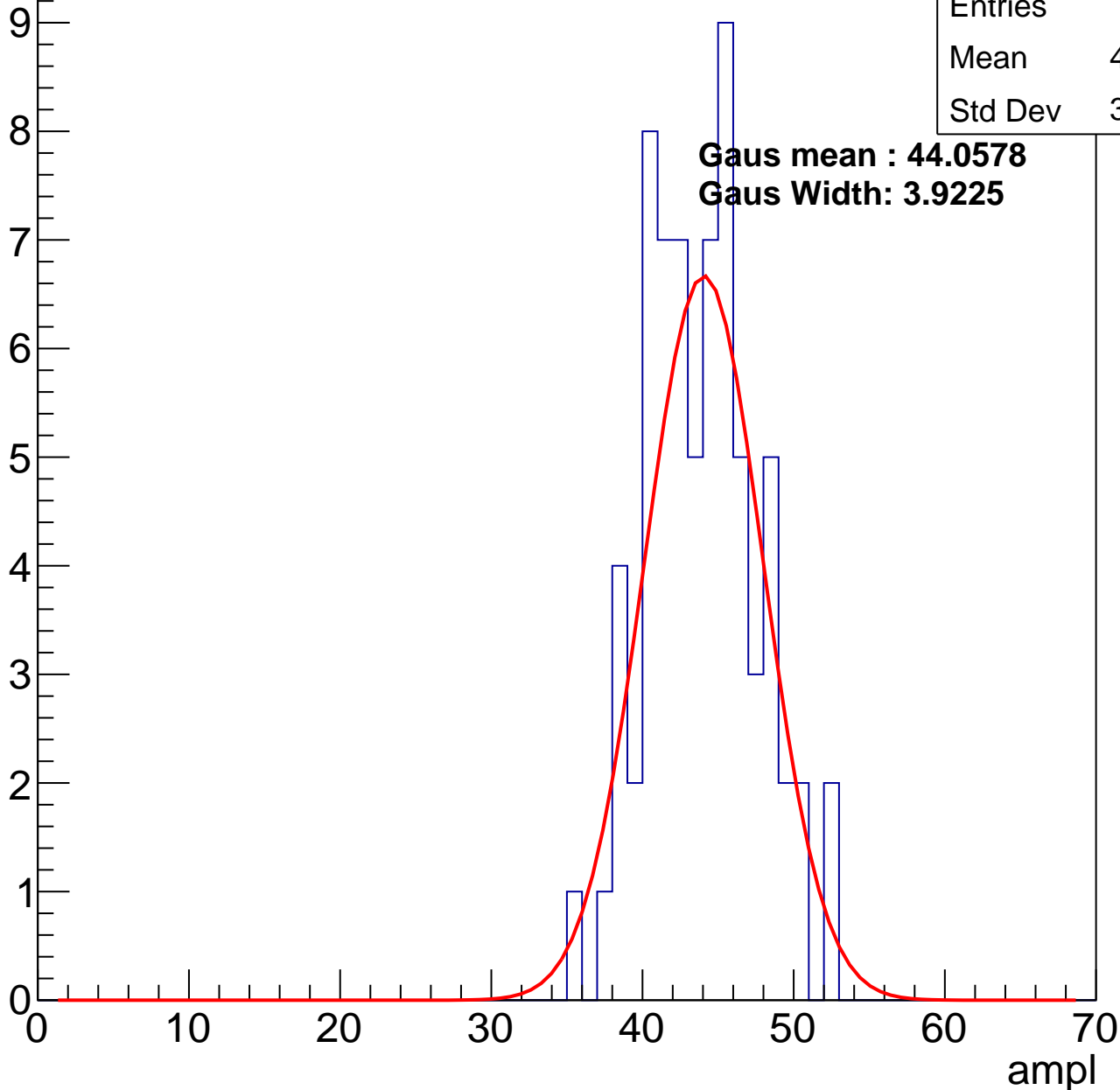
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.49
Std Dev	3.616

Gaus mean : 44.0578

Gaus Width: 3.9225

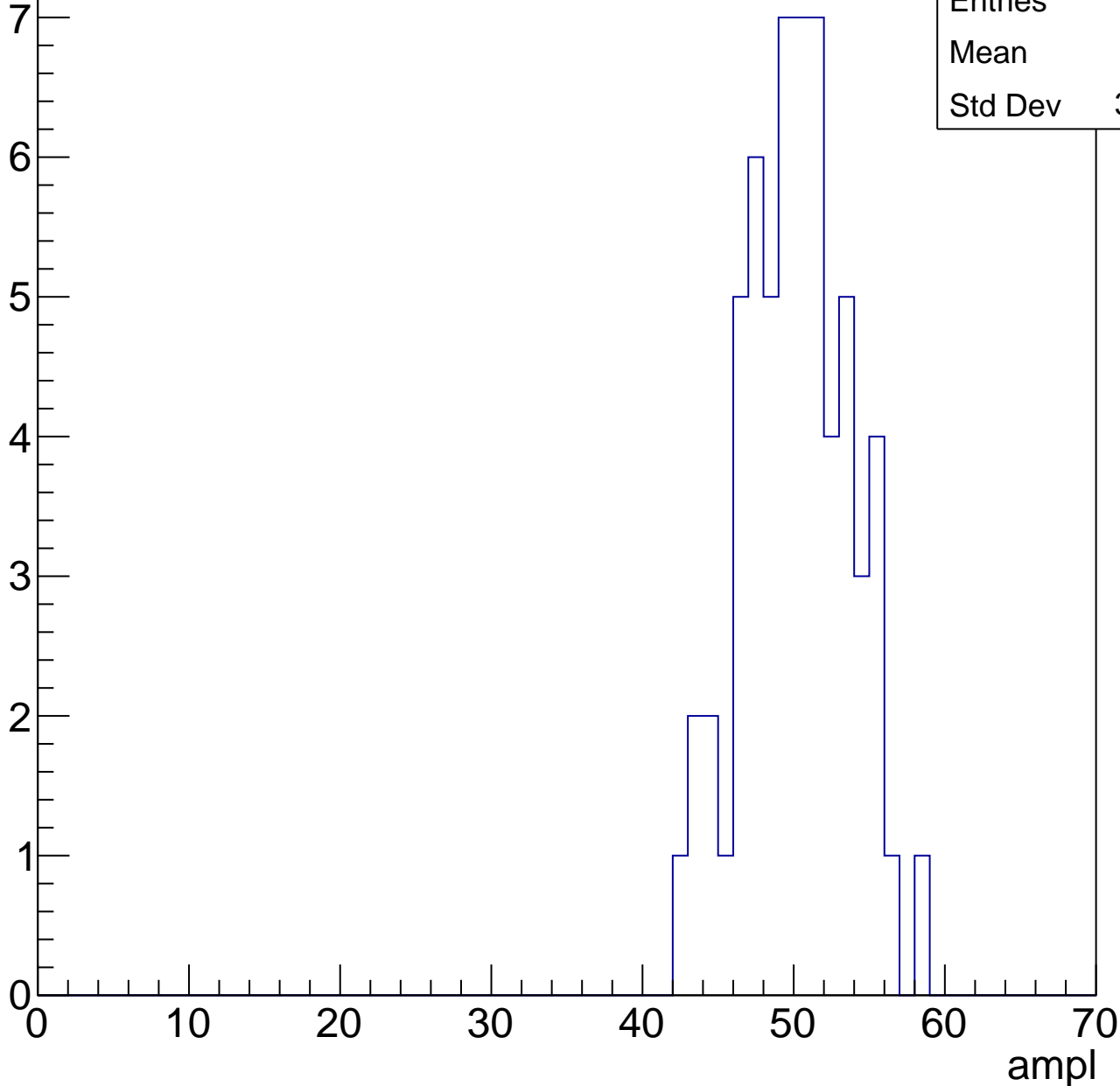


B1L101S, U26-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.7
Std Dev	3.461

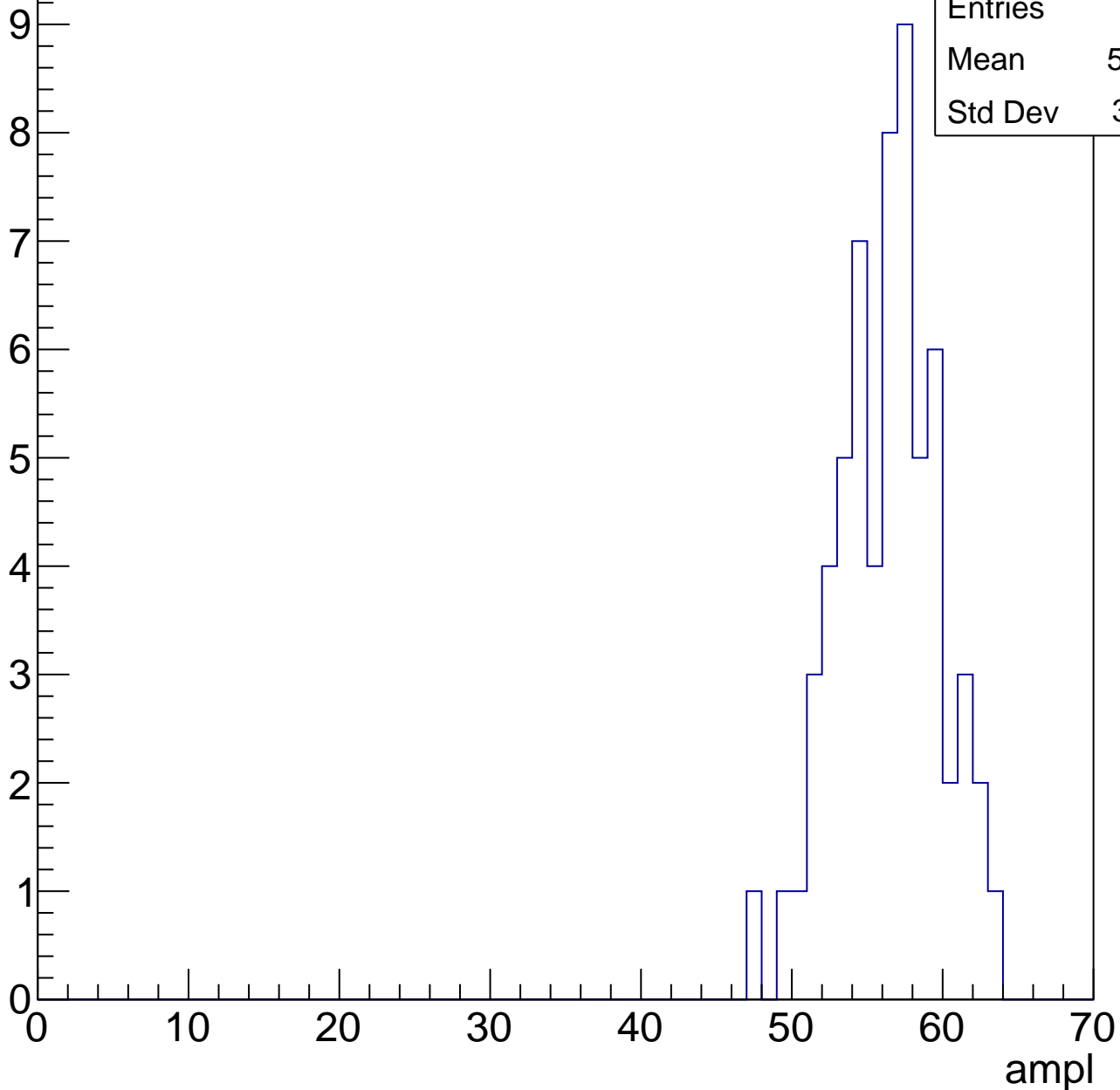


B1L101S, U26-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.89
Std Dev	3.341

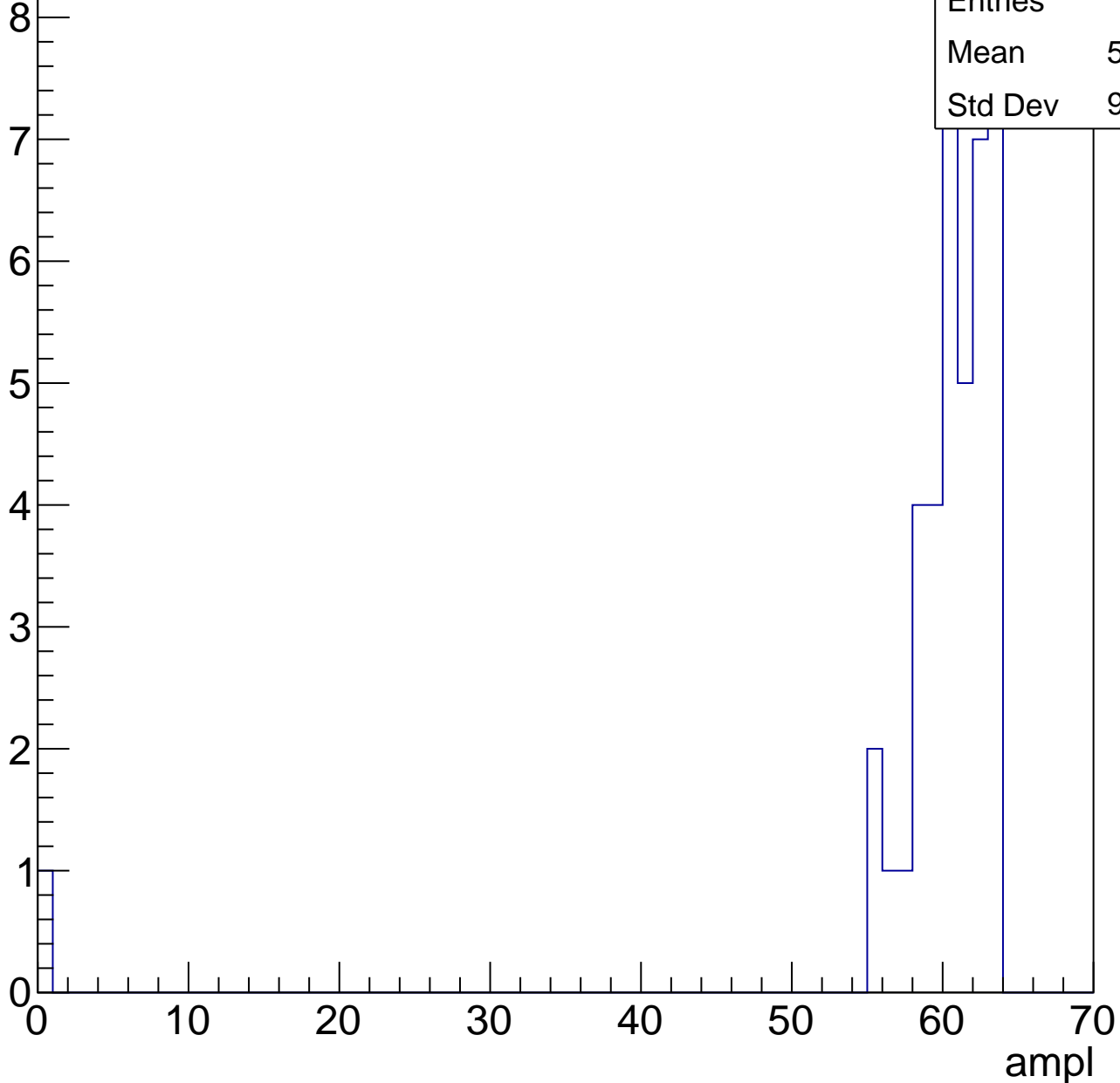


B1L101S, U26-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.88
Std Dev	9.562



B1L101S, U26-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch77, adc0

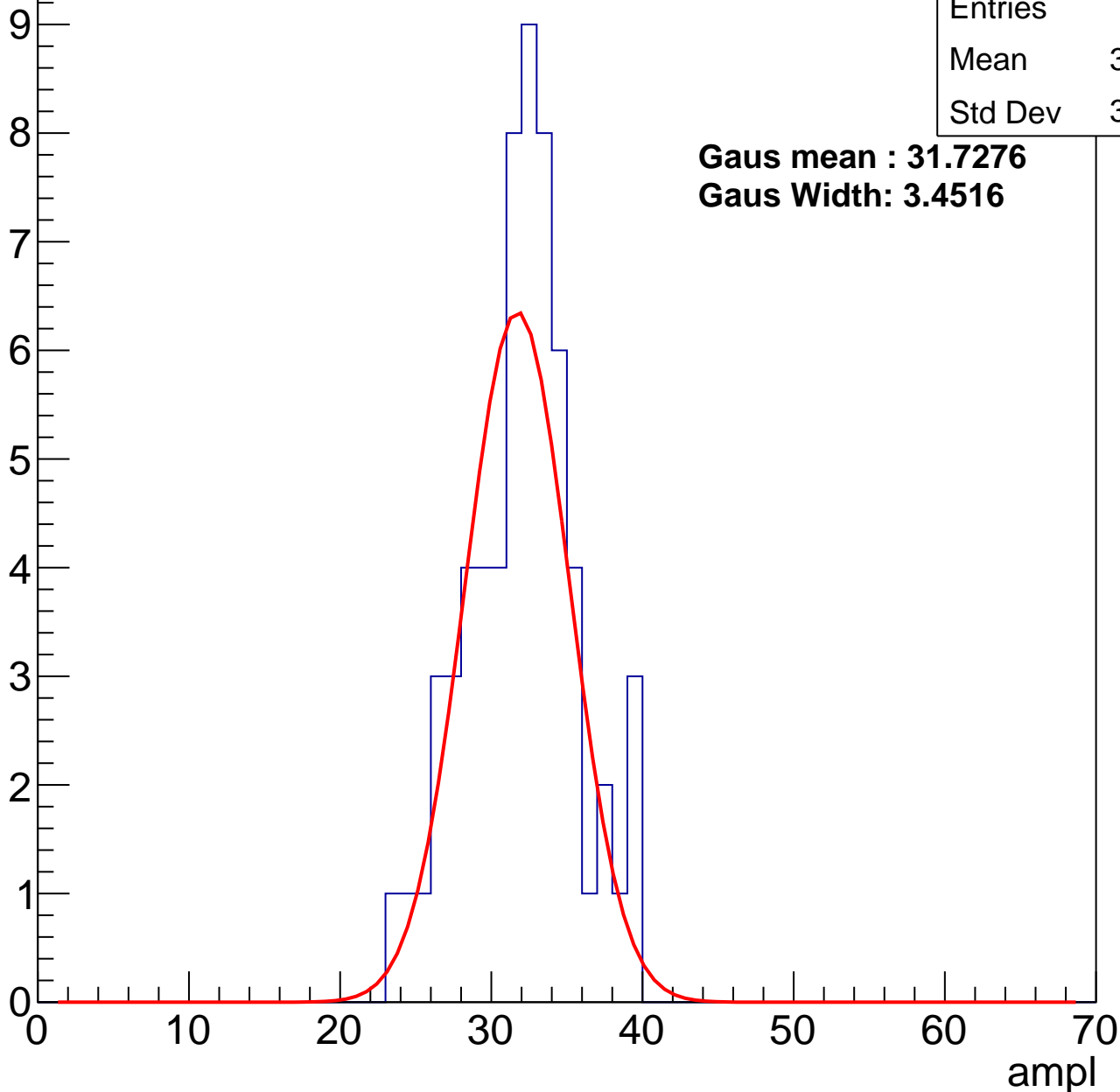
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.56
Std Dev	3.576

Gaus mean : 31.7276

Gaus Width: 3.4516



B1L101S, U26-ch77, adc1

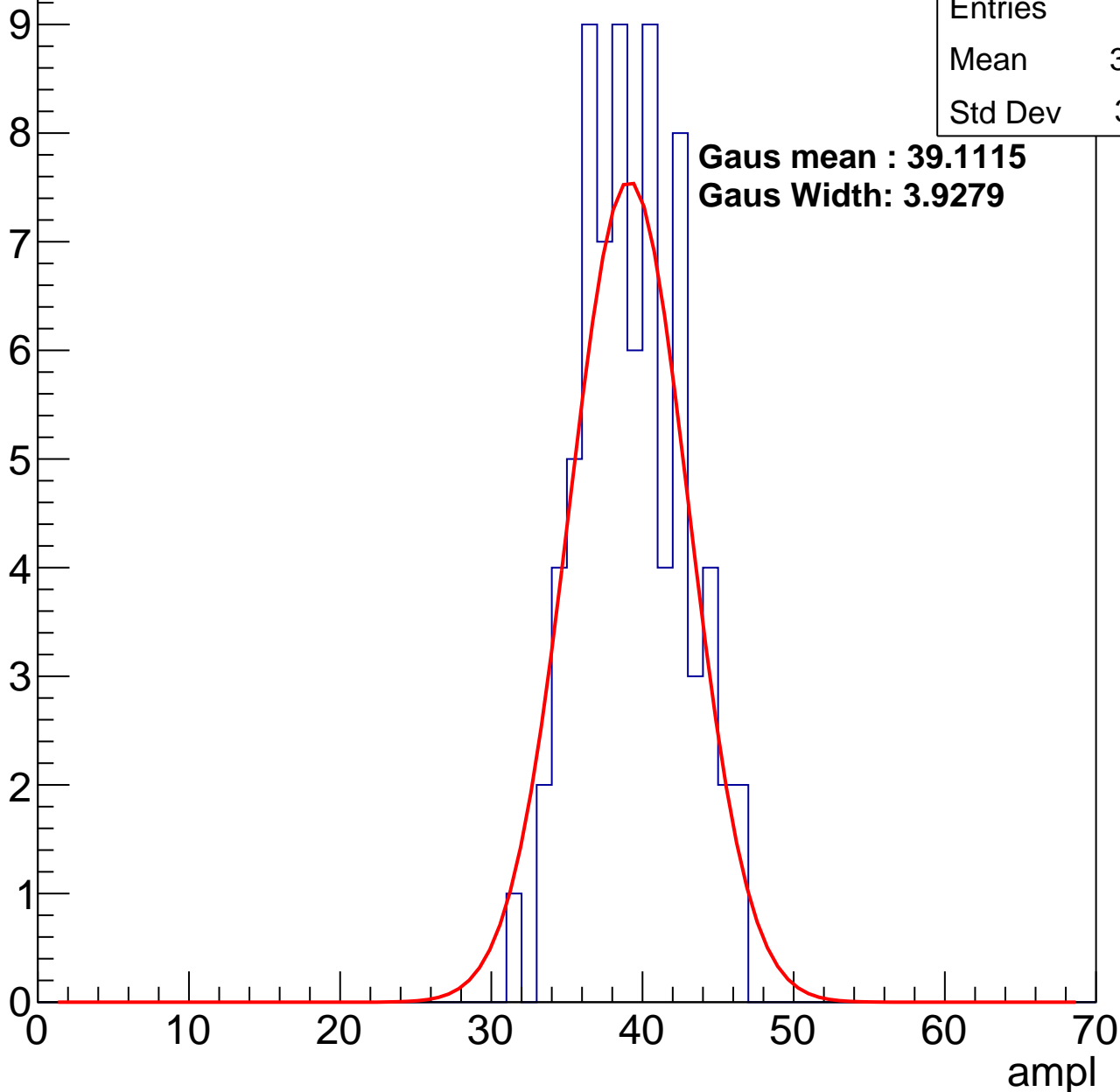
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	38.85
Std Dev	3.361

Gaus mean : 39.1115

Gaus Width: 3.9279



B1L101S, U26-ch77, adc2

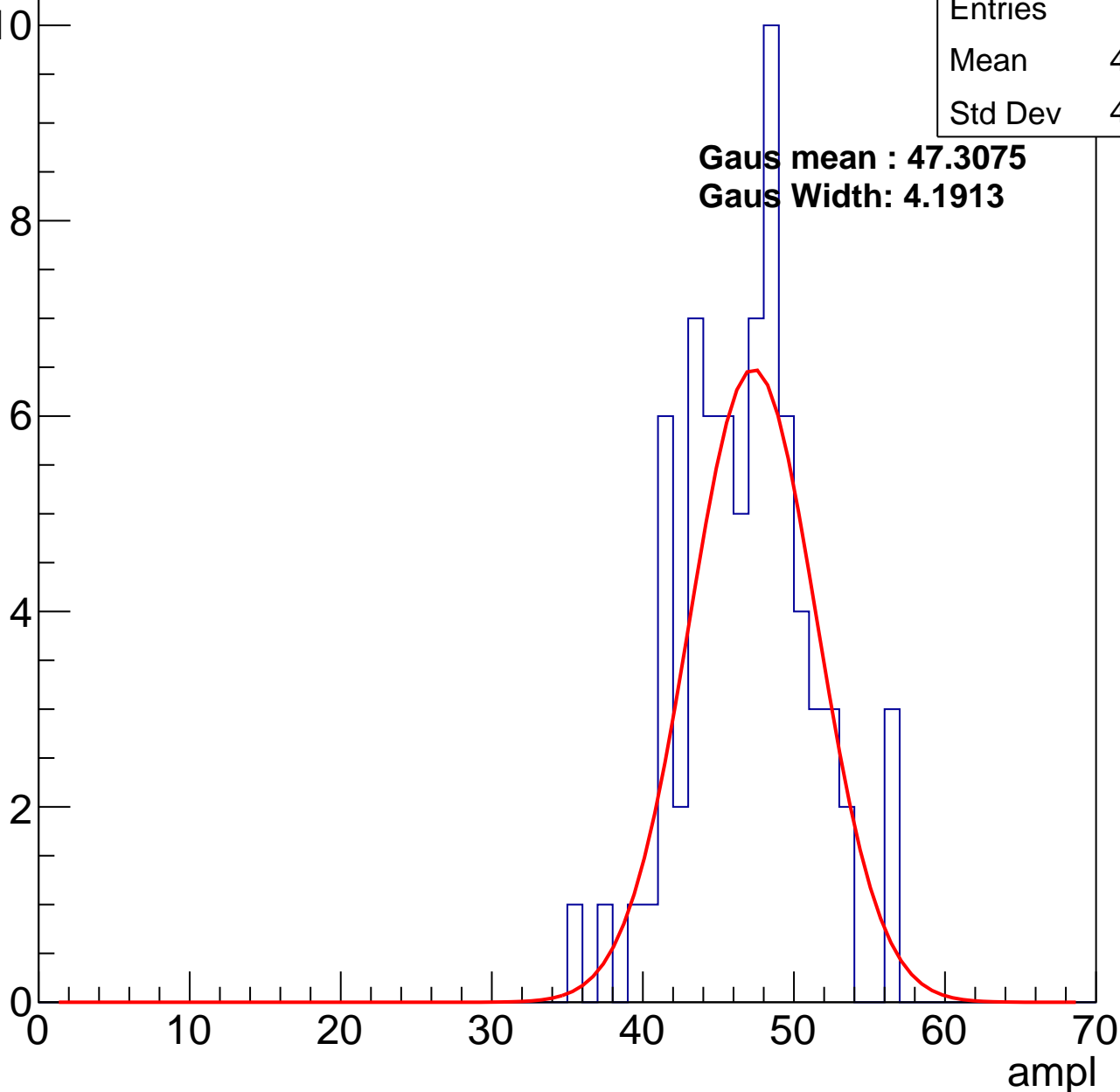
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	46.38
Std Dev	4.187

Gaus mean : 47.3075

Gaus Width: 4.1913

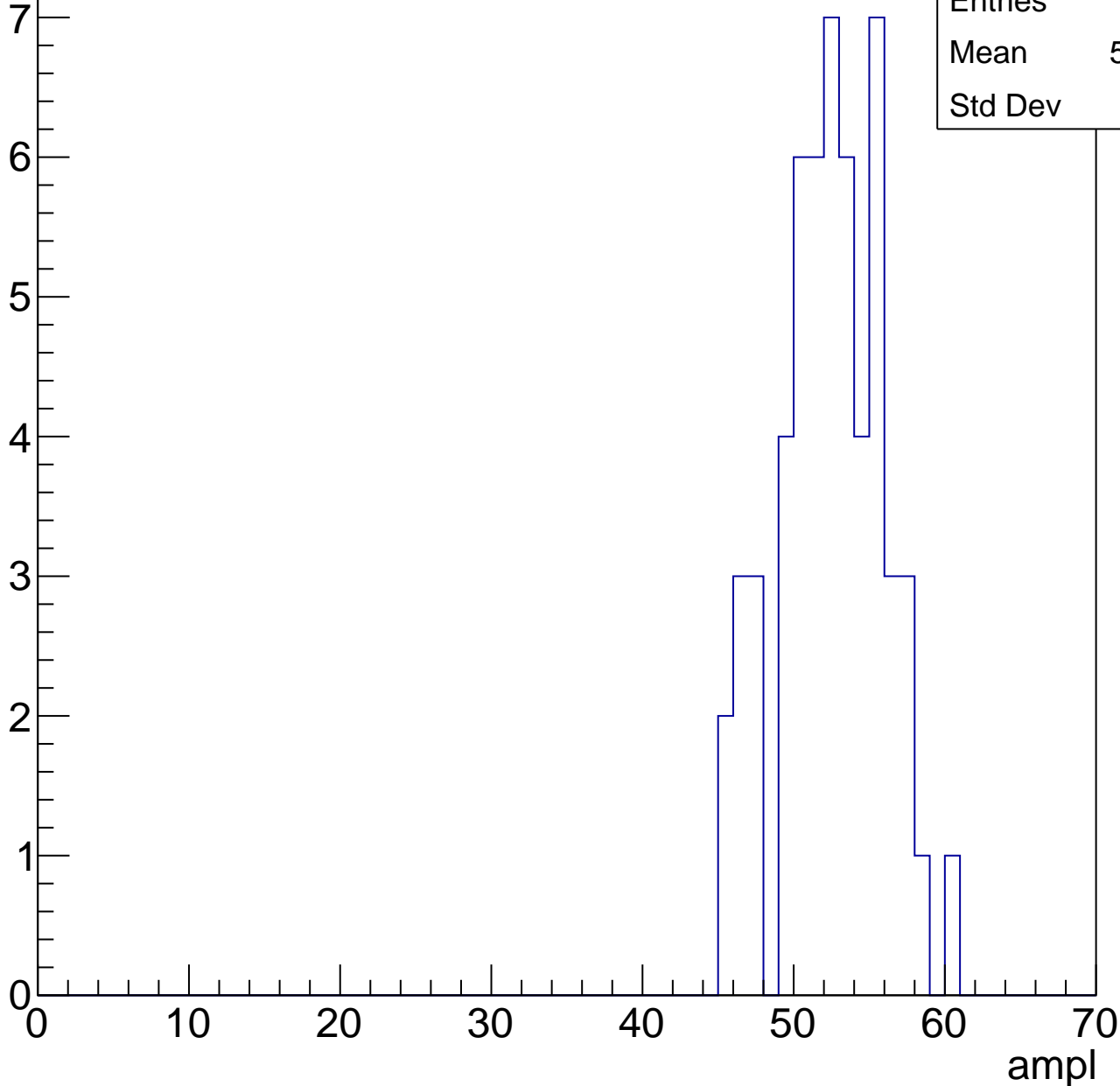


B1L101S, U26-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	51.98
Std Dev	3.42

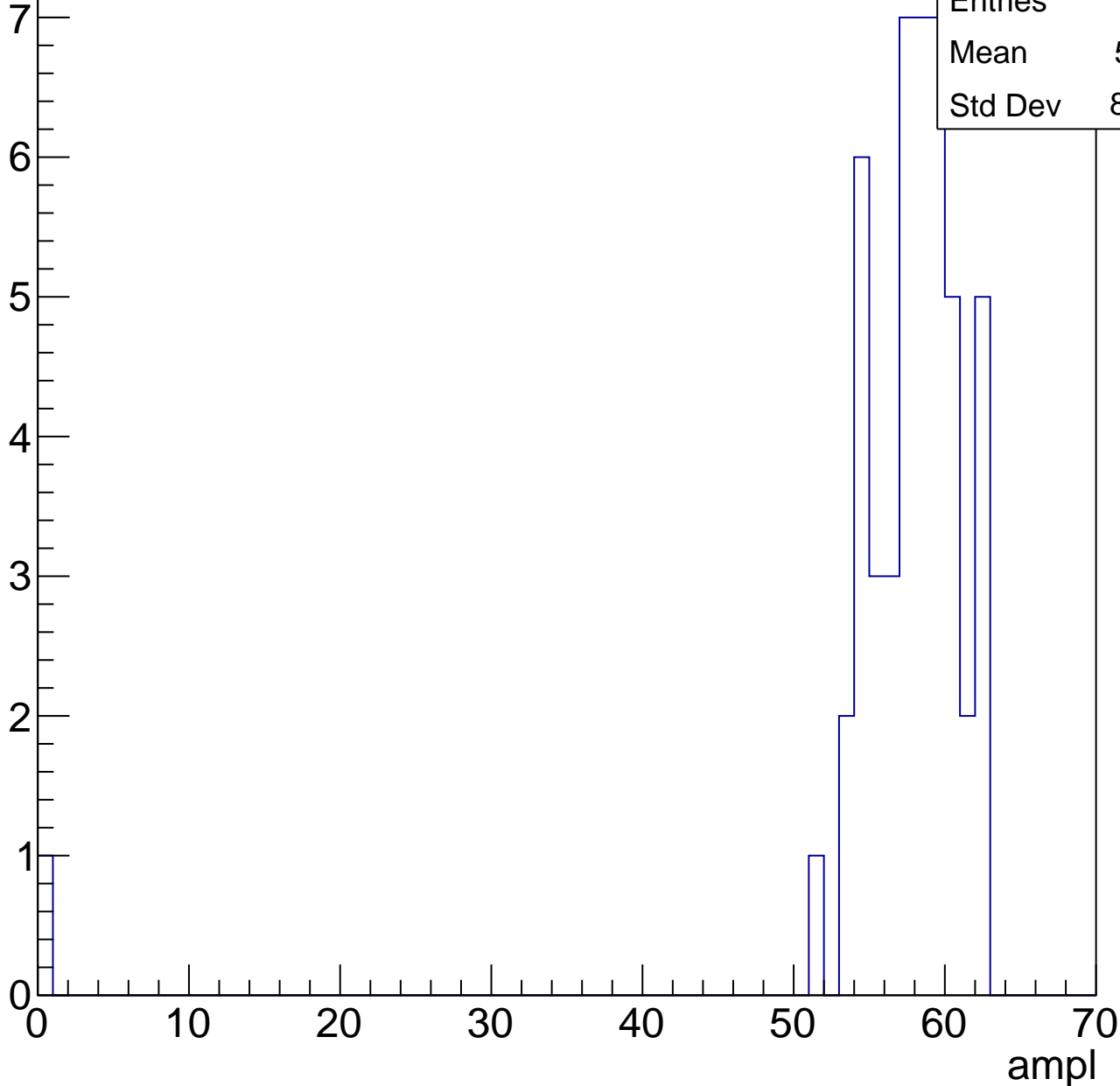


B1L101S, U26-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.41
Std Dev	8.578

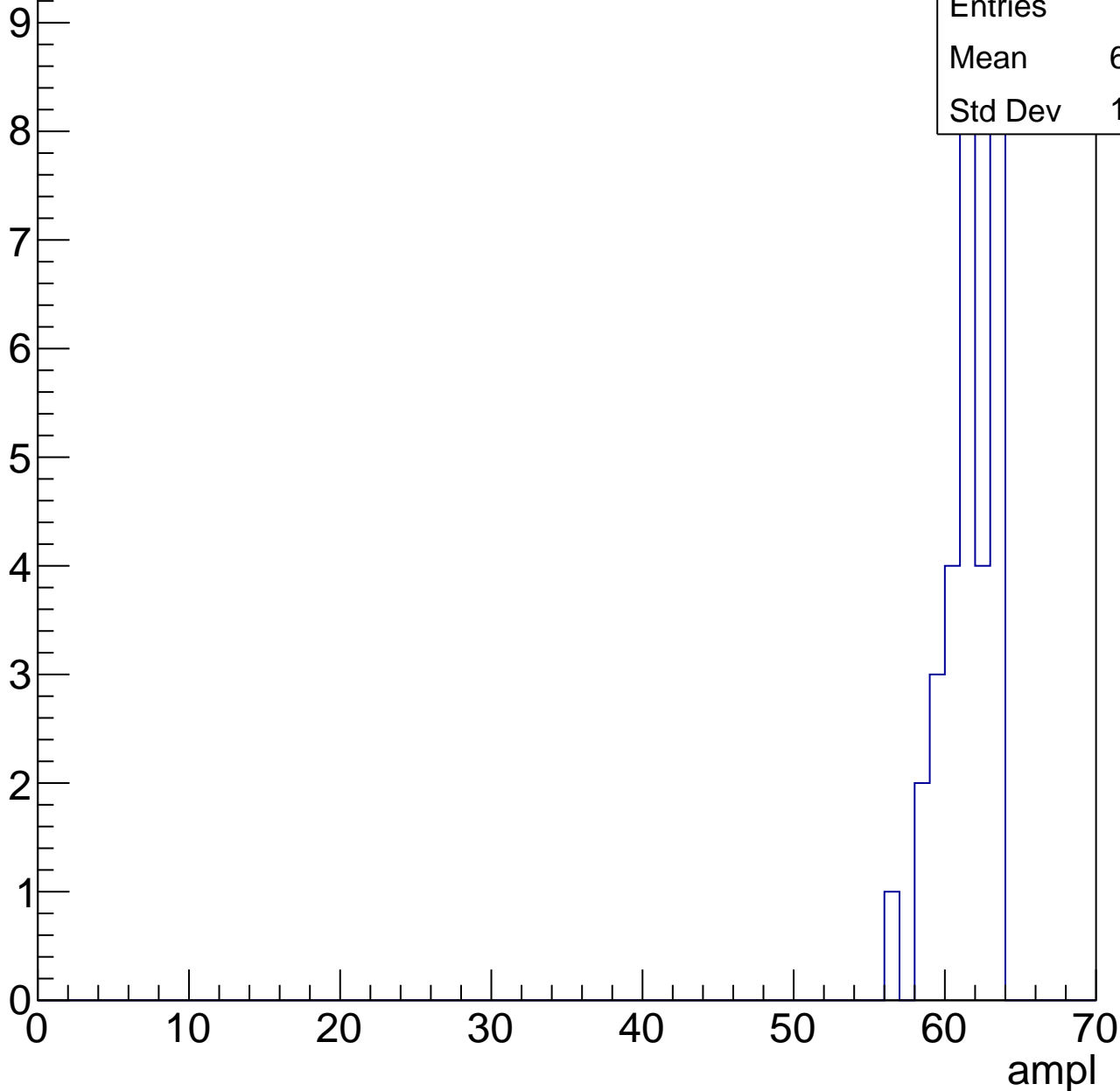


B1L101S, U26-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	61.03
Std Dev	1.787



B1L101S, U26-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch78, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	30.8
Std Dev	3.565

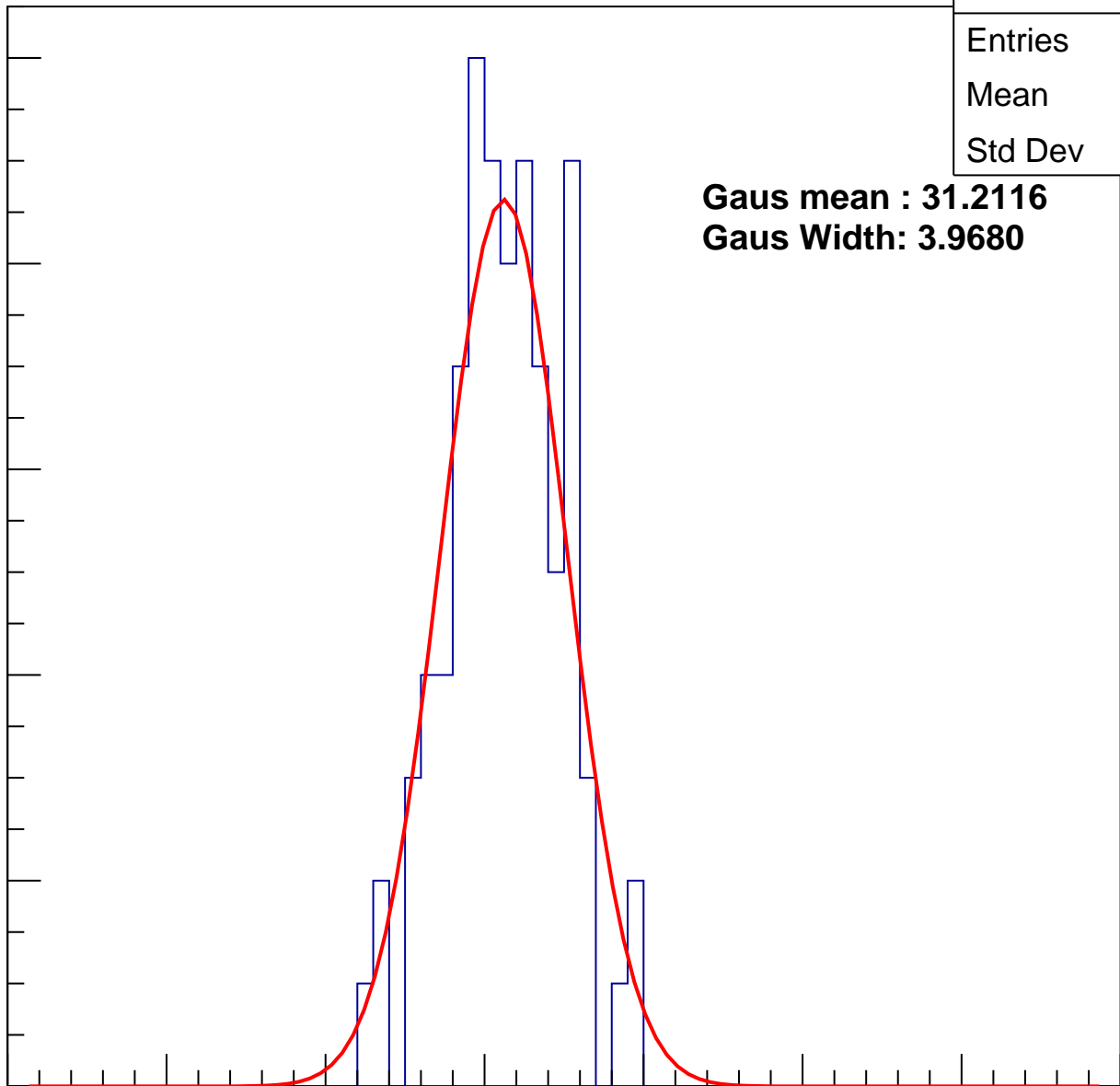
Gaus mean : 31.2116
Gaus Width: 3.9680

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch78, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	38.97
Std Dev	3.181

Gaus mean : 39.7336

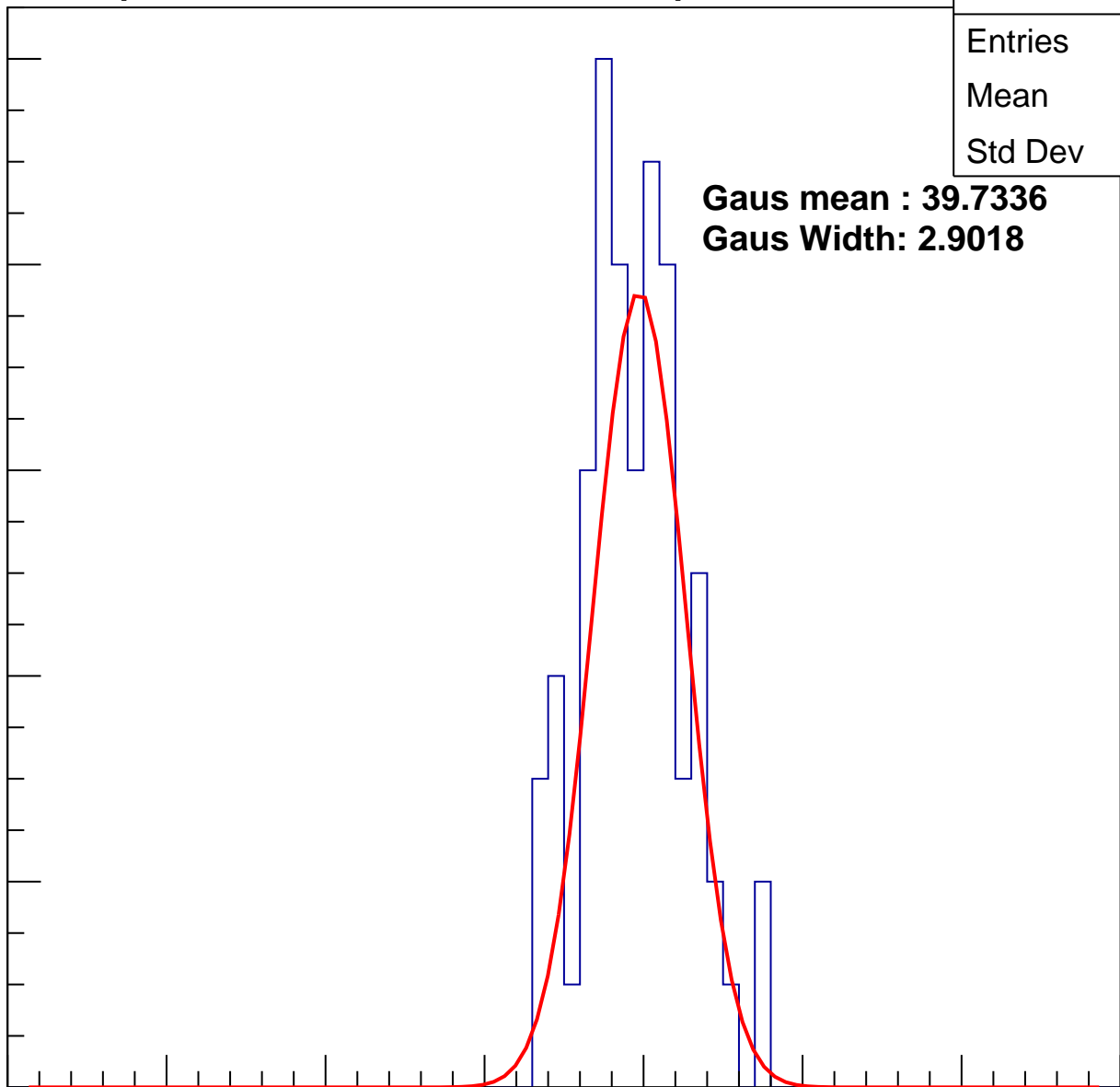
Gaus Width: 2.9018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch78, adc2

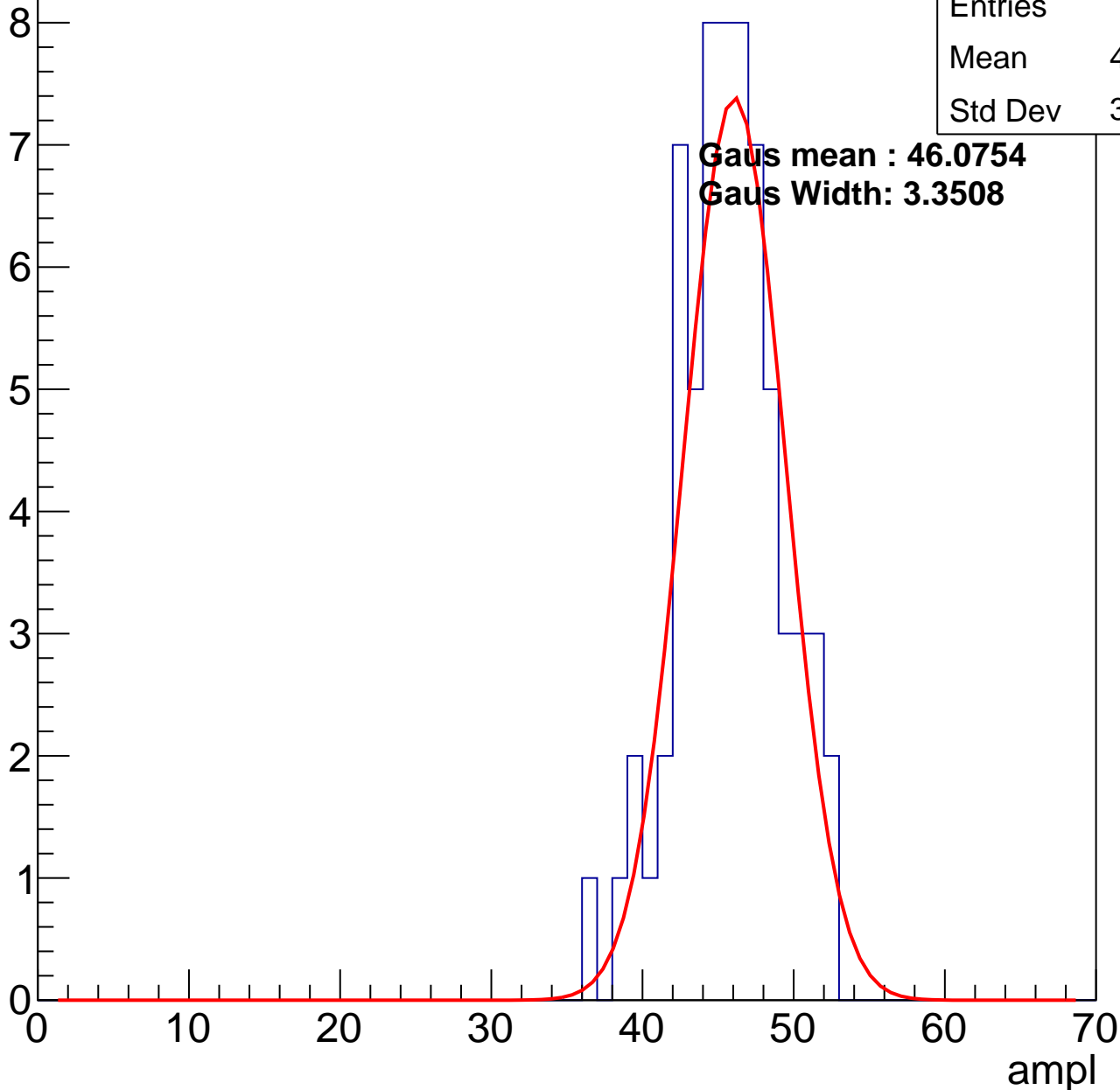
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.24
Std Dev	3.385

Gaus mean : 46.0754

Gaus Width: 3.3508

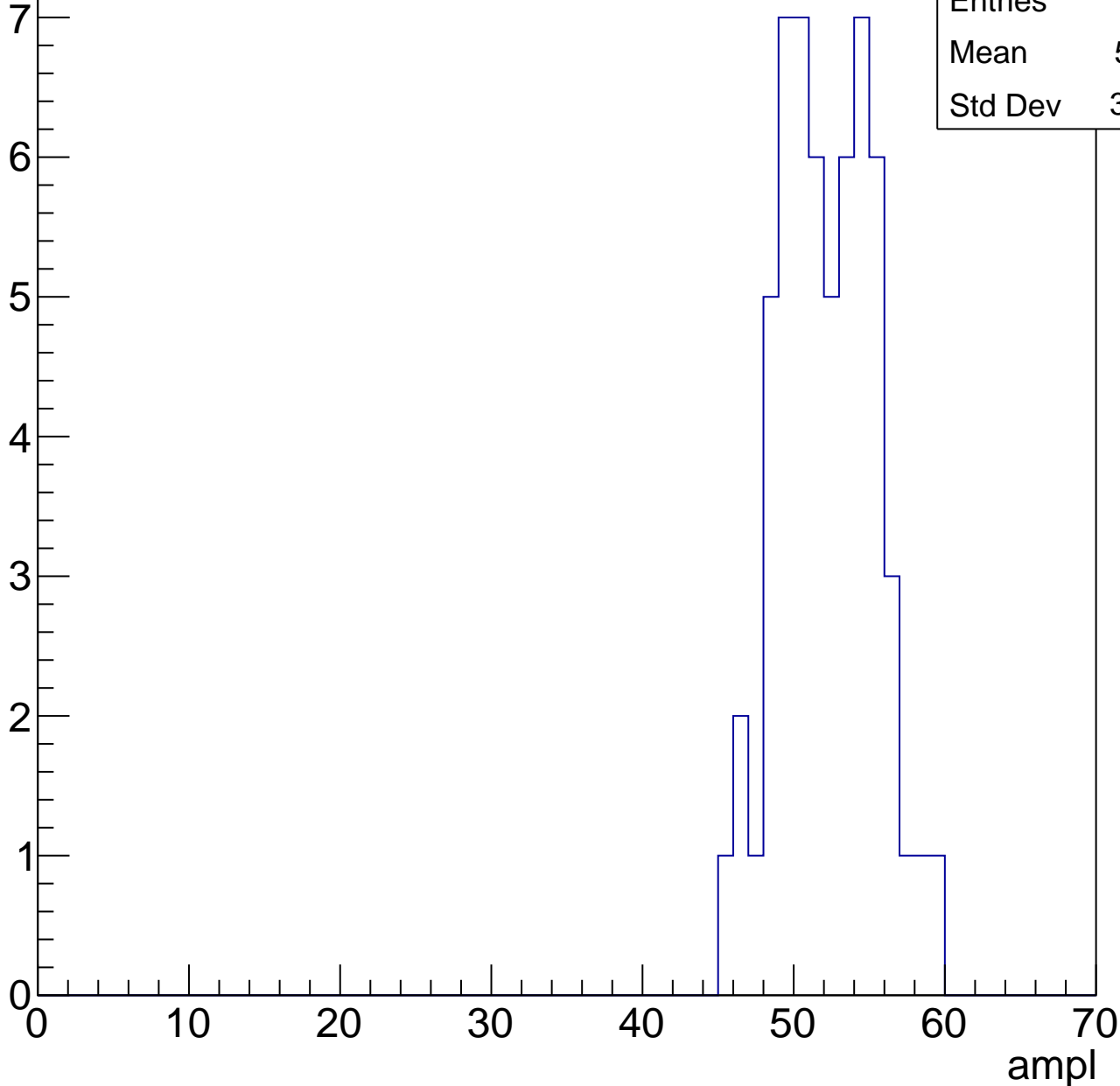


B1L101S, U26-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	51.71
Std Dev	3.092

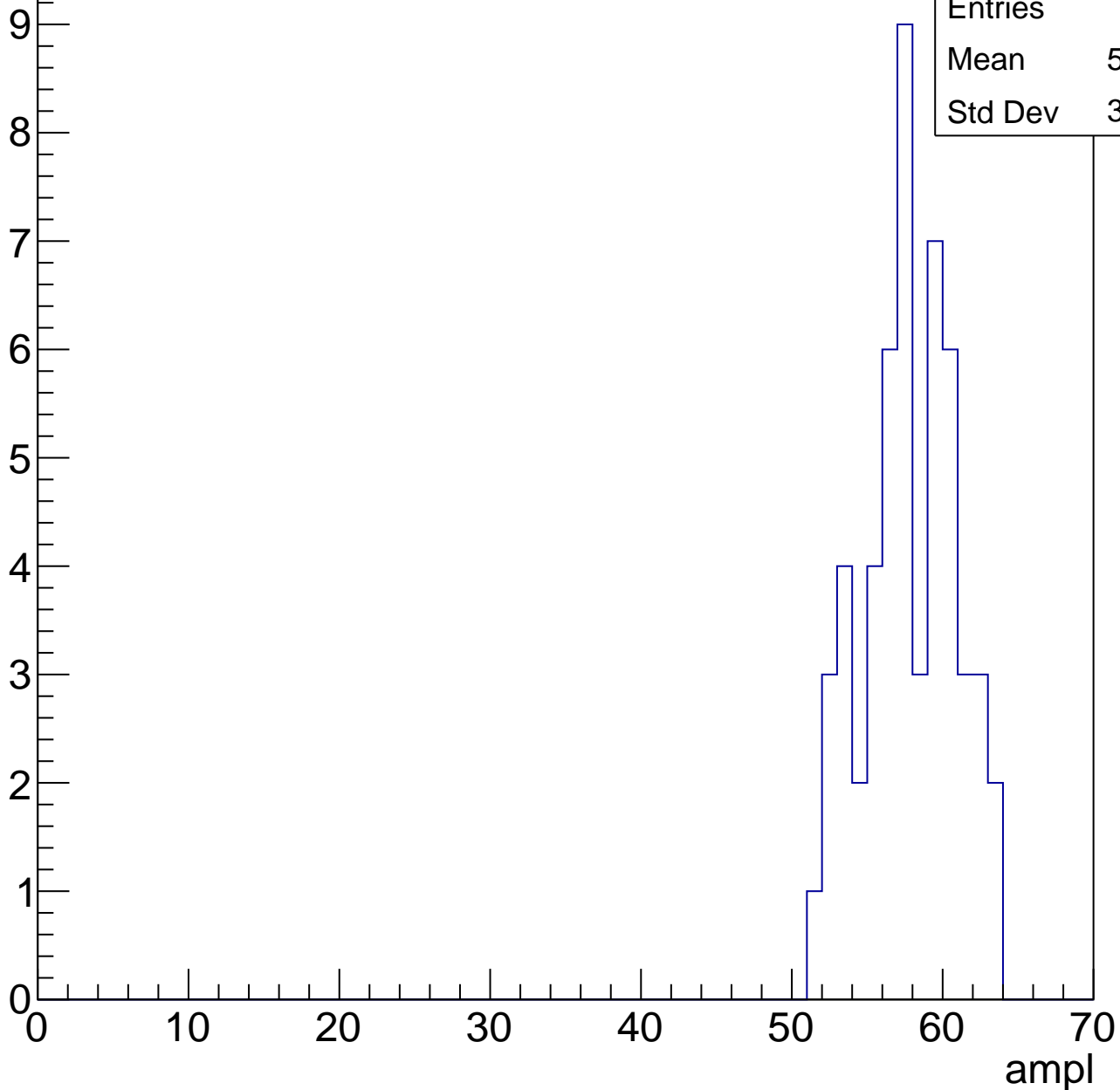


B1L101S, U26-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.32
Std Dev	3.039

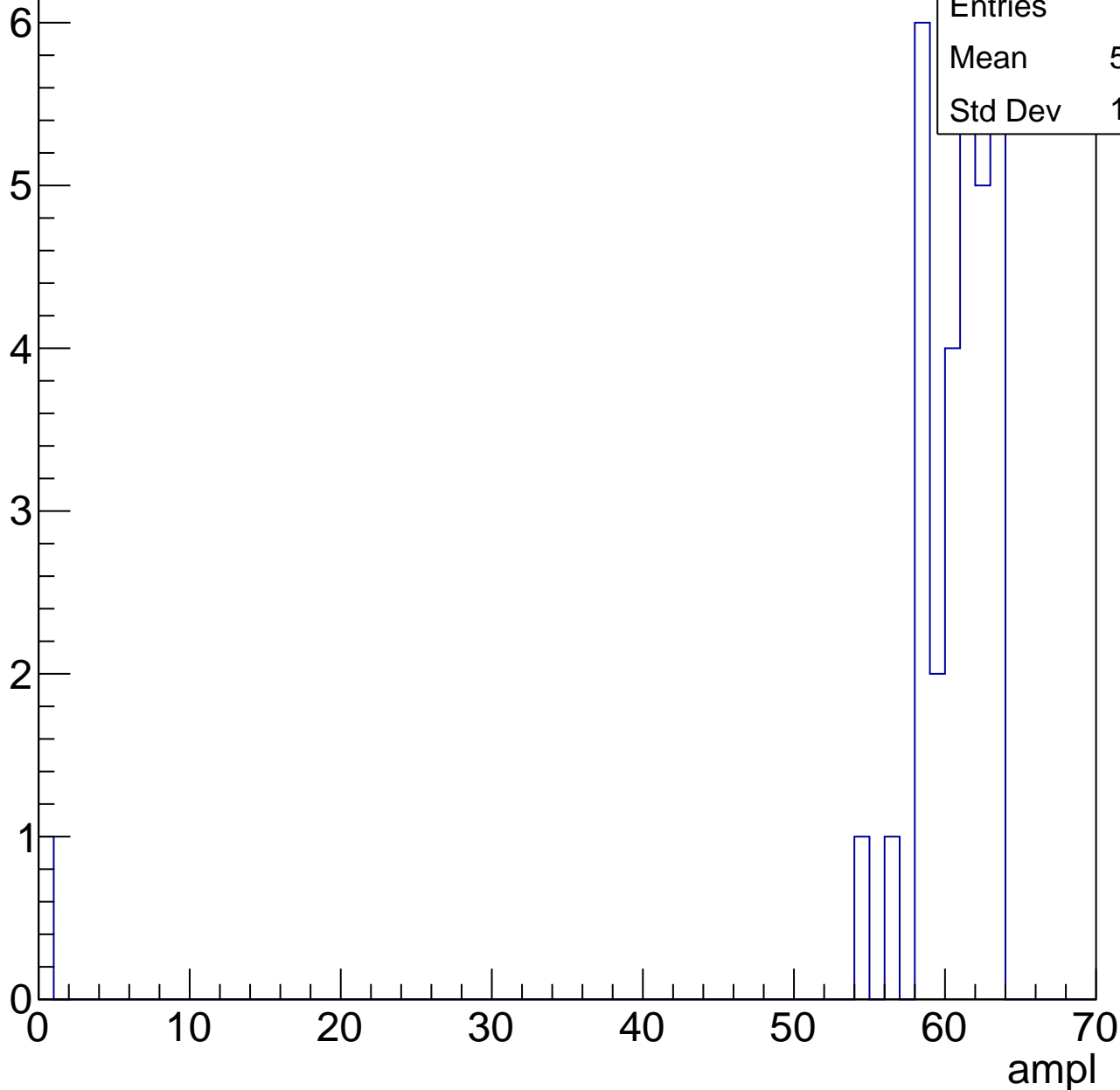


B1L101S, U26-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.44
Std Dev	10.72



B1L101S, U26-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch79, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	33.61
Std Dev	2.712

Gaus mean : 33.9256

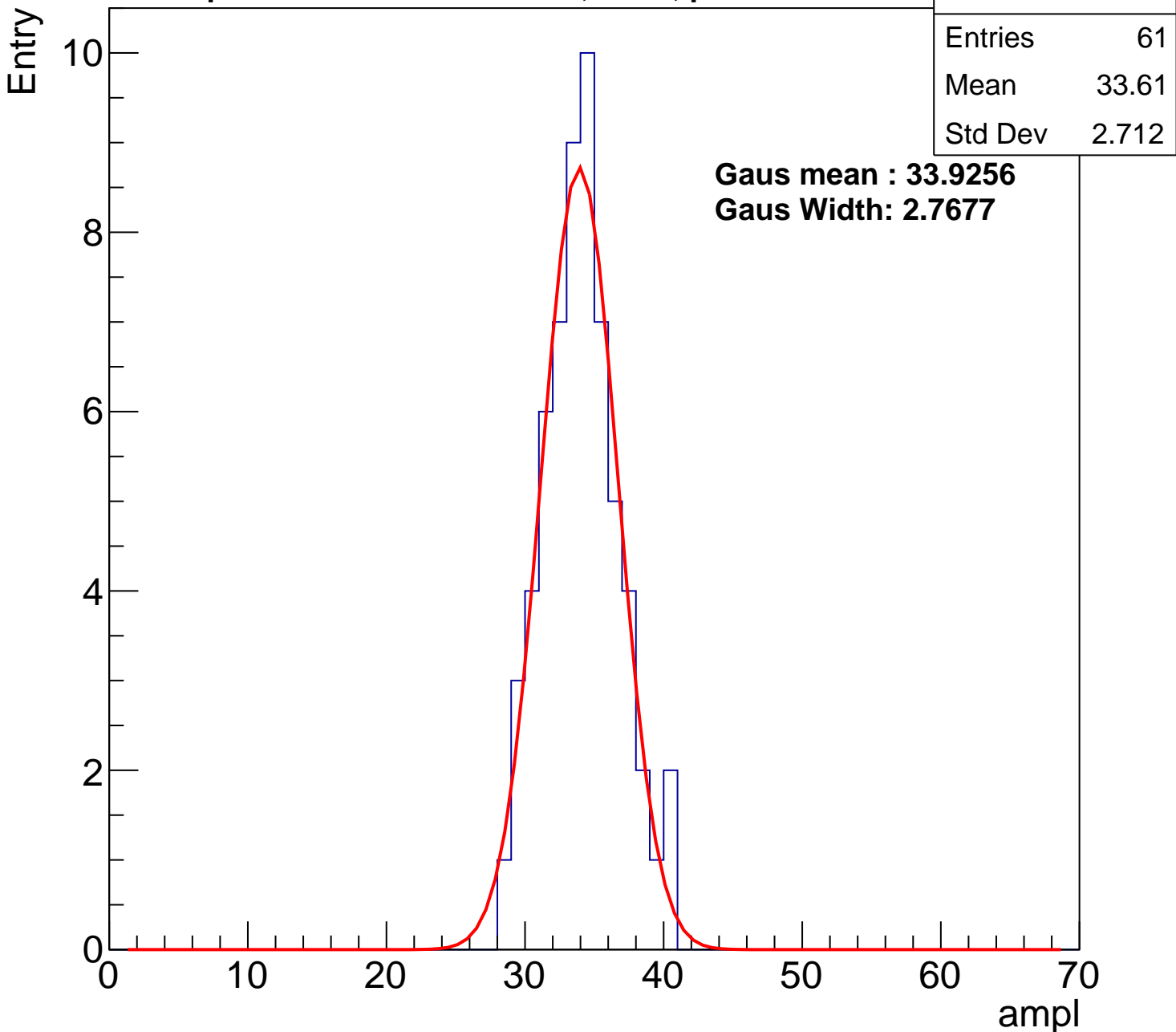
Gaus Width: 2.7677

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch79, adc1

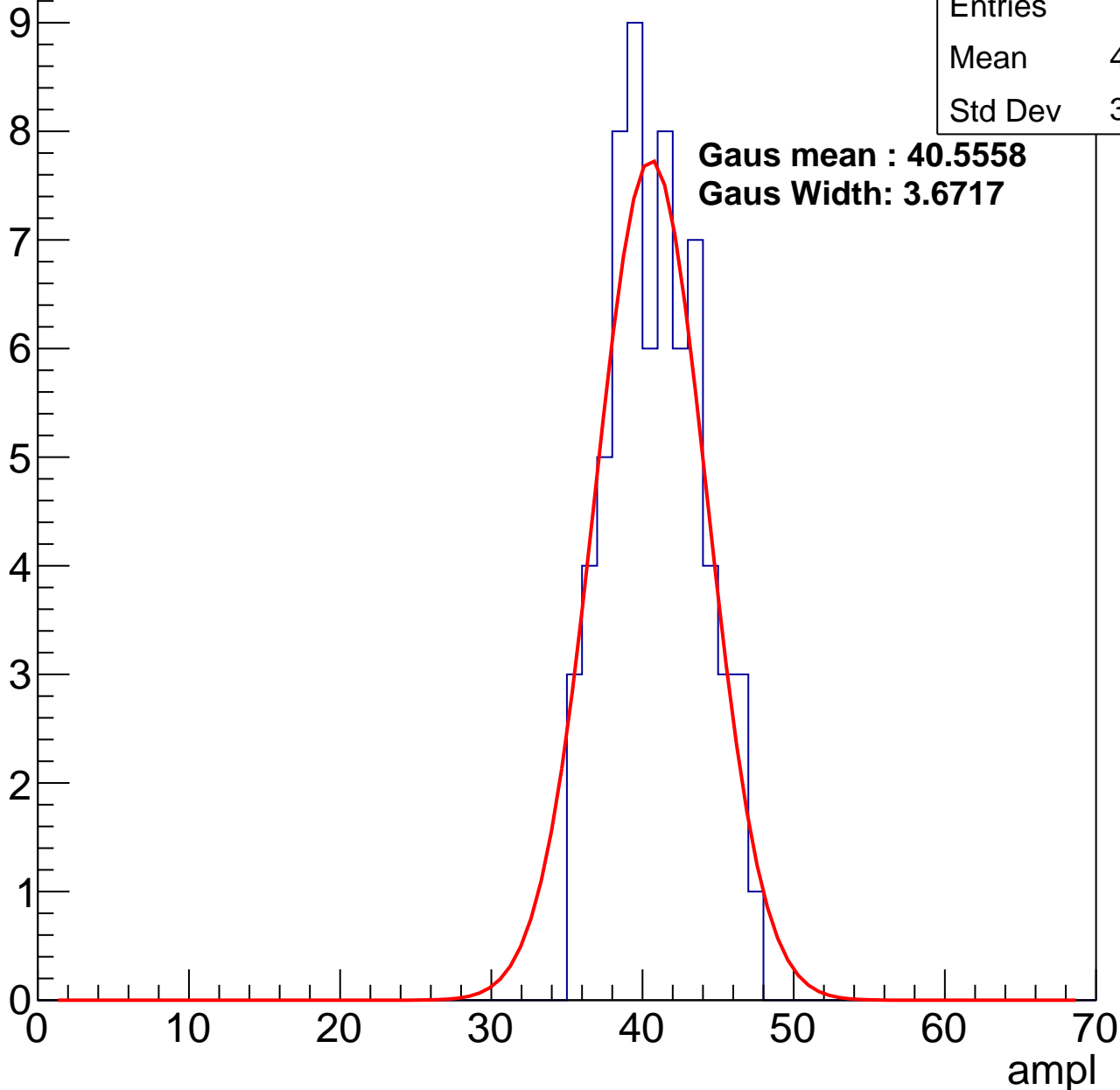
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	40.39
Std Dev	3.007

Gaus mean : 40.5558

Gaus Width: 3.6717



B1L101S, U26-ch79, adc2

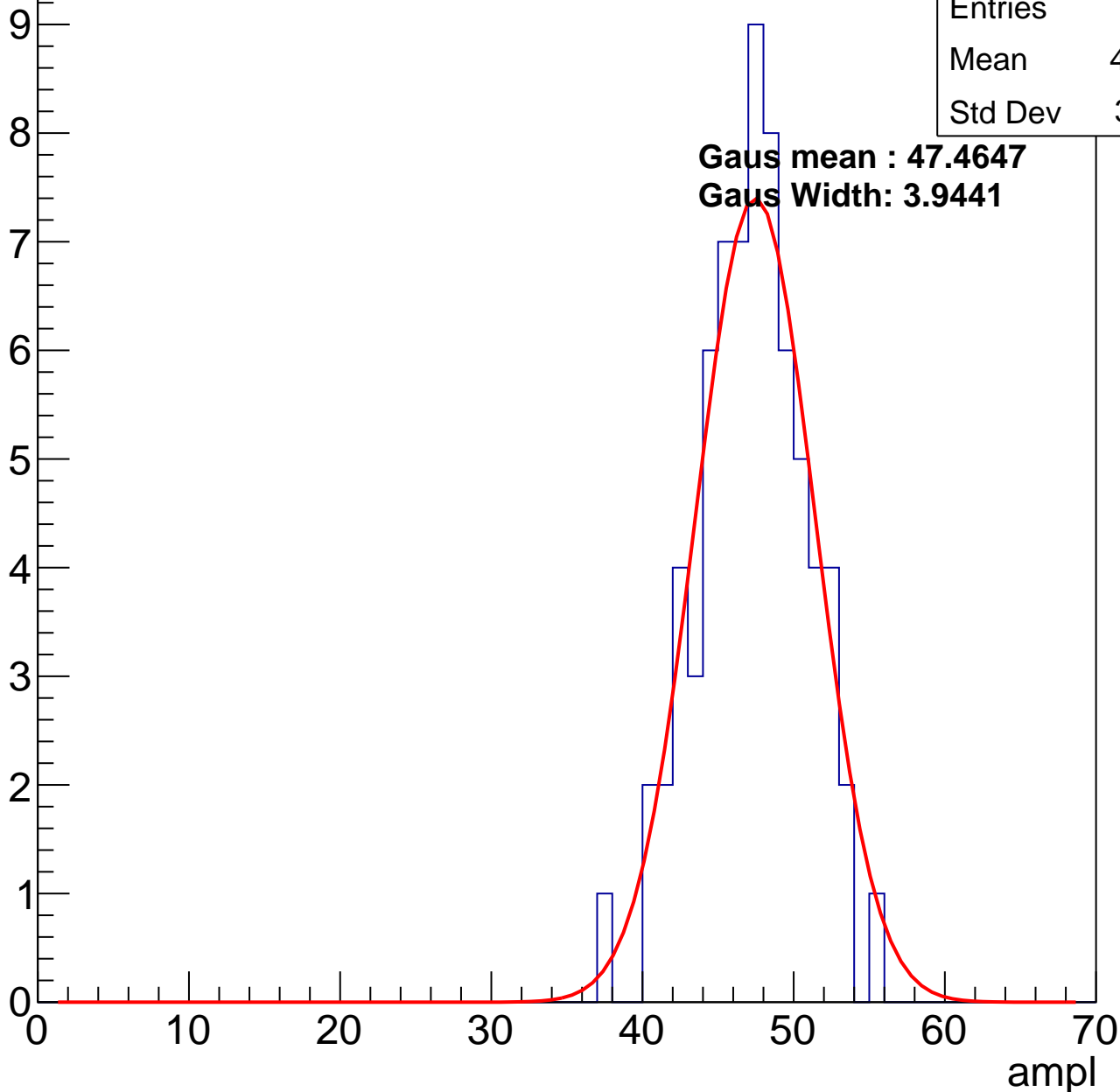
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	46.77
Std Dev	3.521

Gaus mean : 47.4647

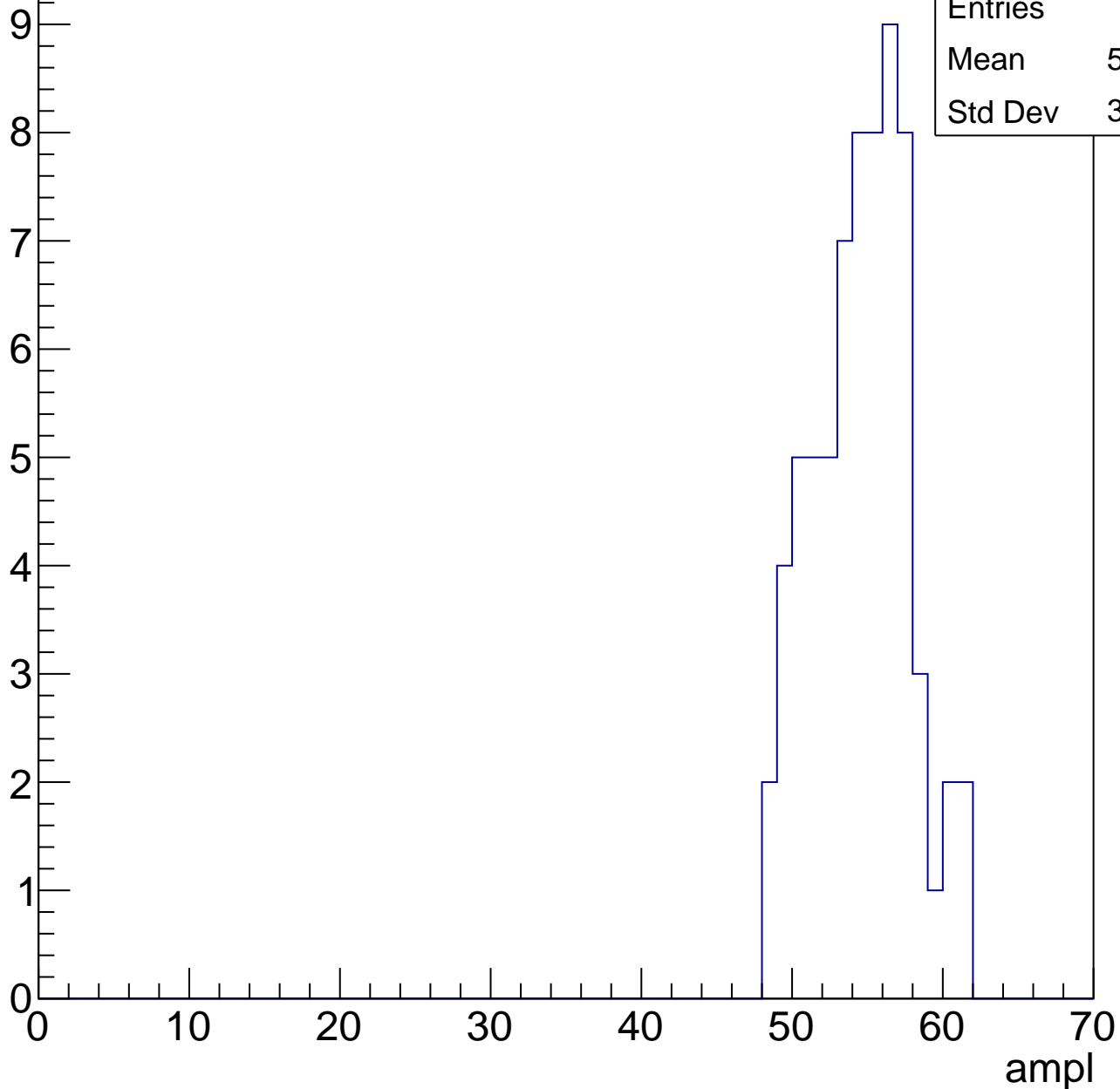
Gaus Width: 3.9441



B1L101S, U26-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

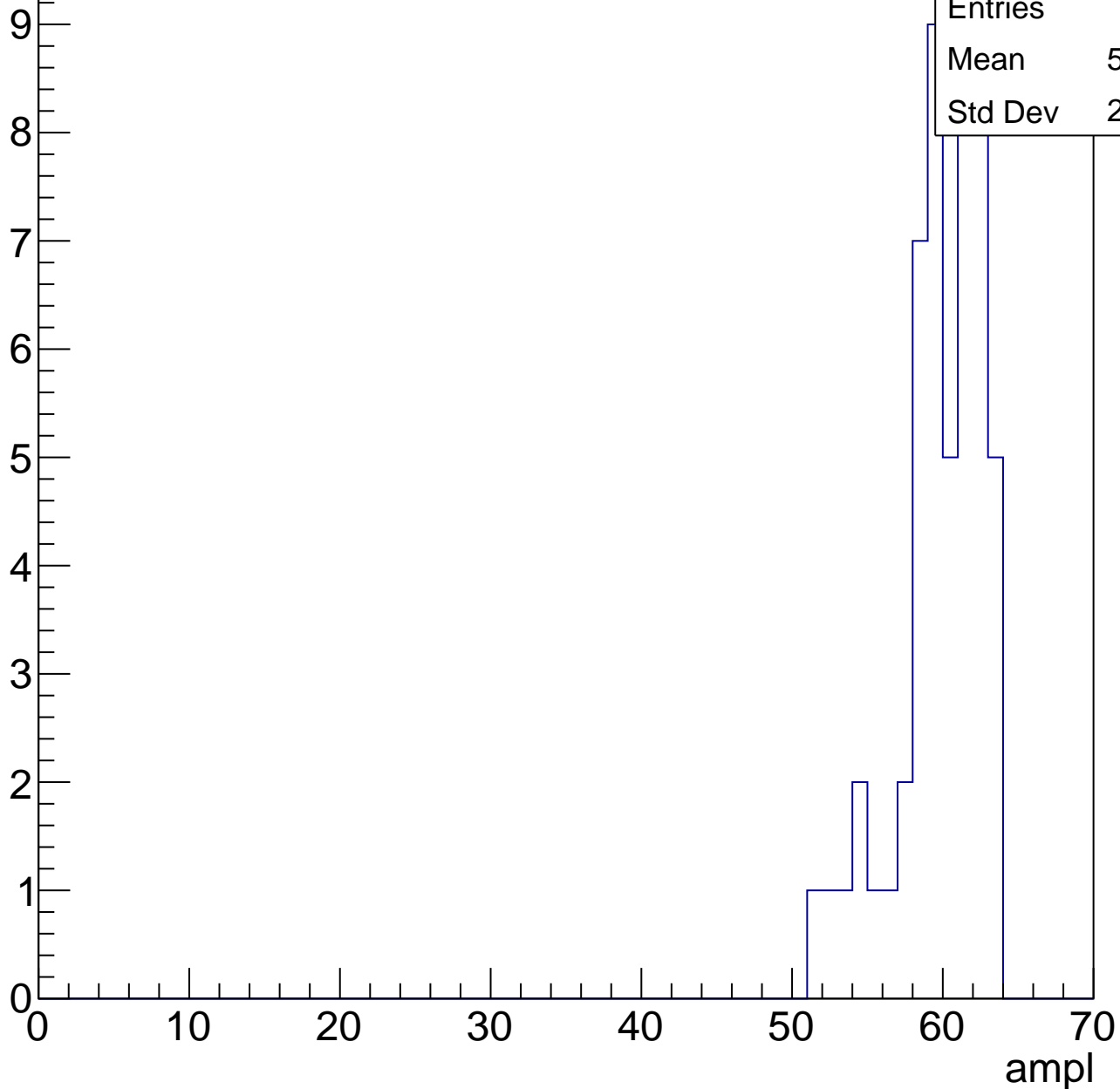
Entry



B1L101S, U26-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

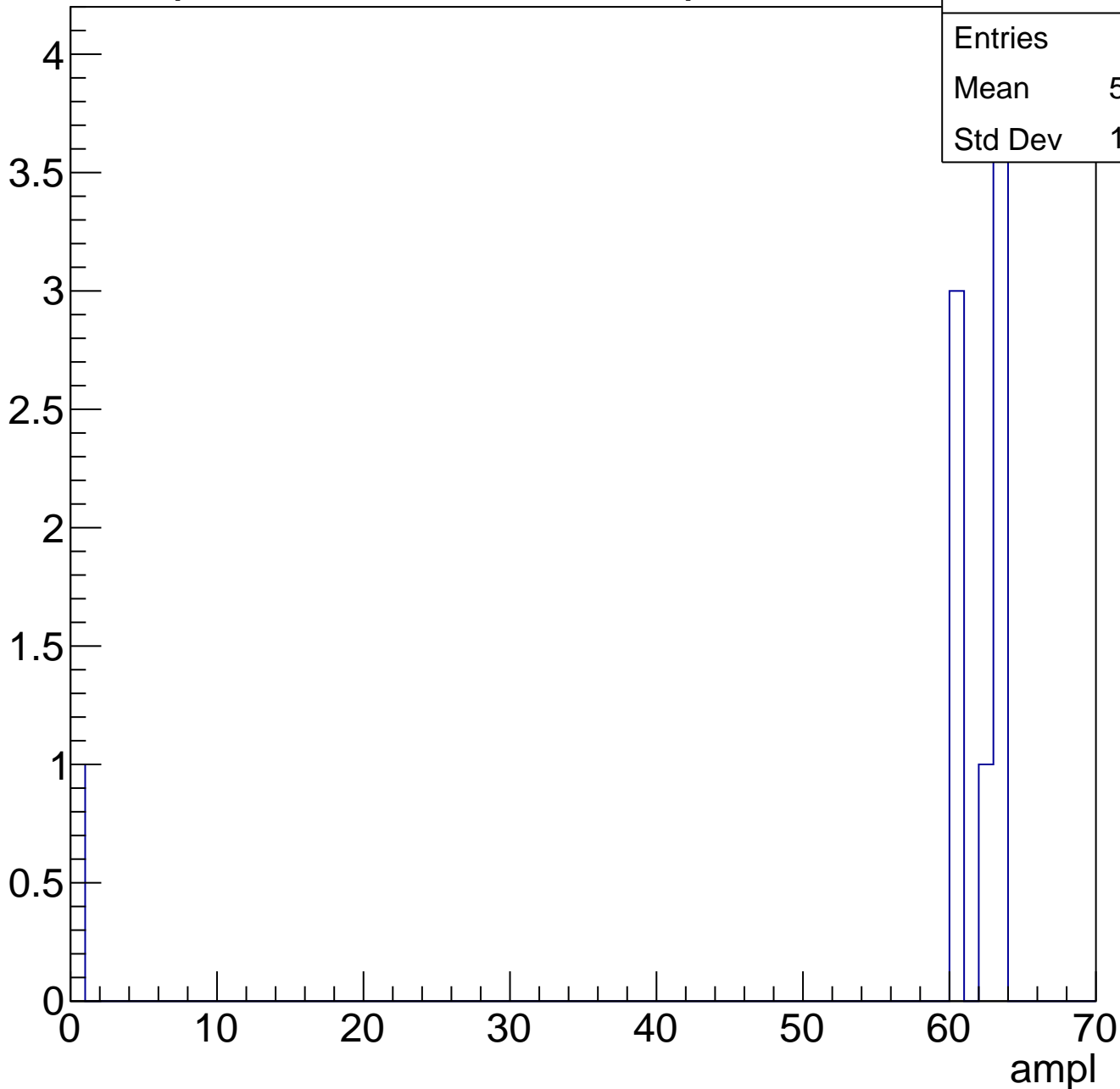
Entry



B1L101S, U26-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

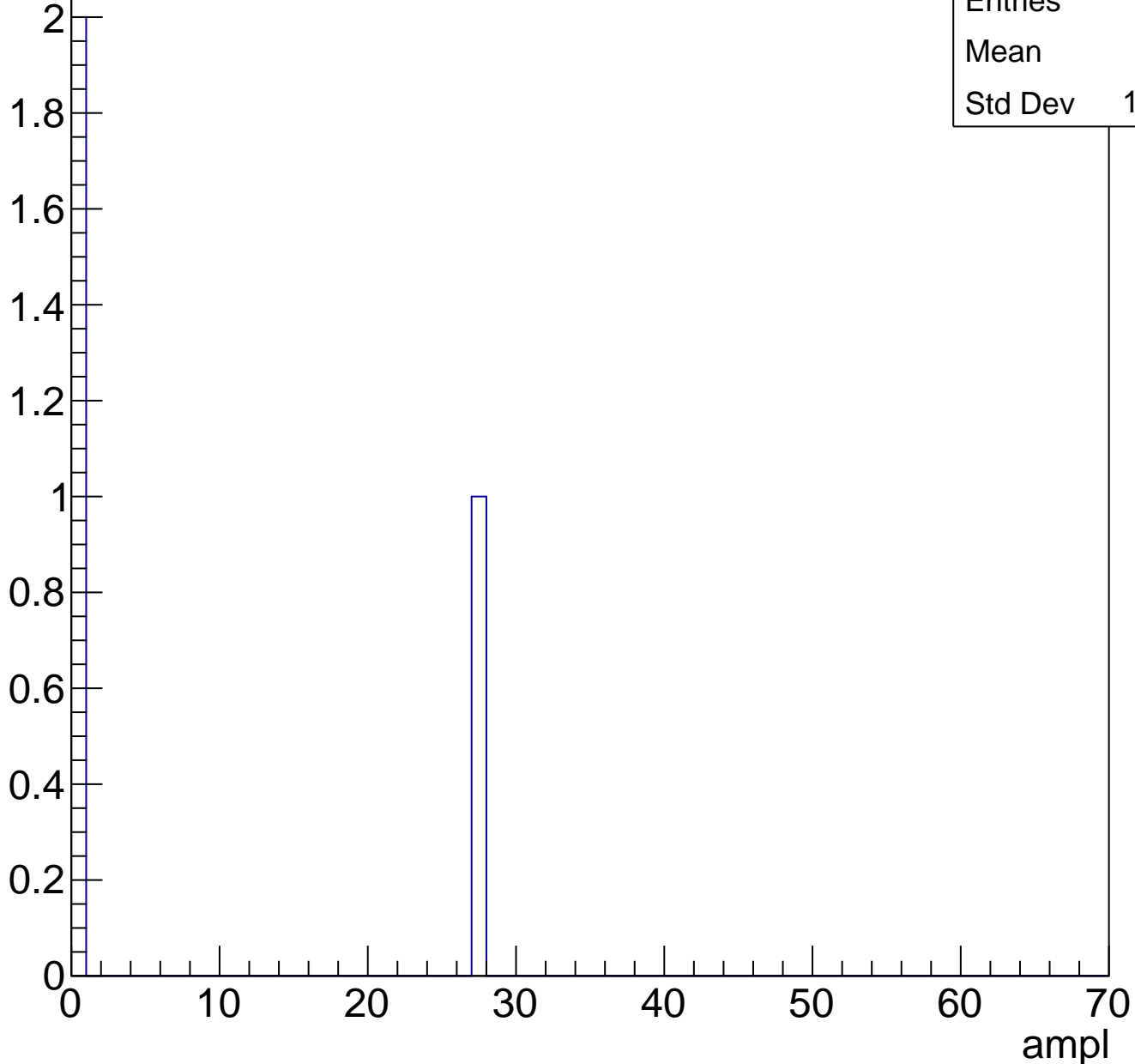


Entries	1
Mean	63
Std Dev	0

B1L101S, U26-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	9
Std Dev	12.73

B1L101S, U26-ch80, adc0

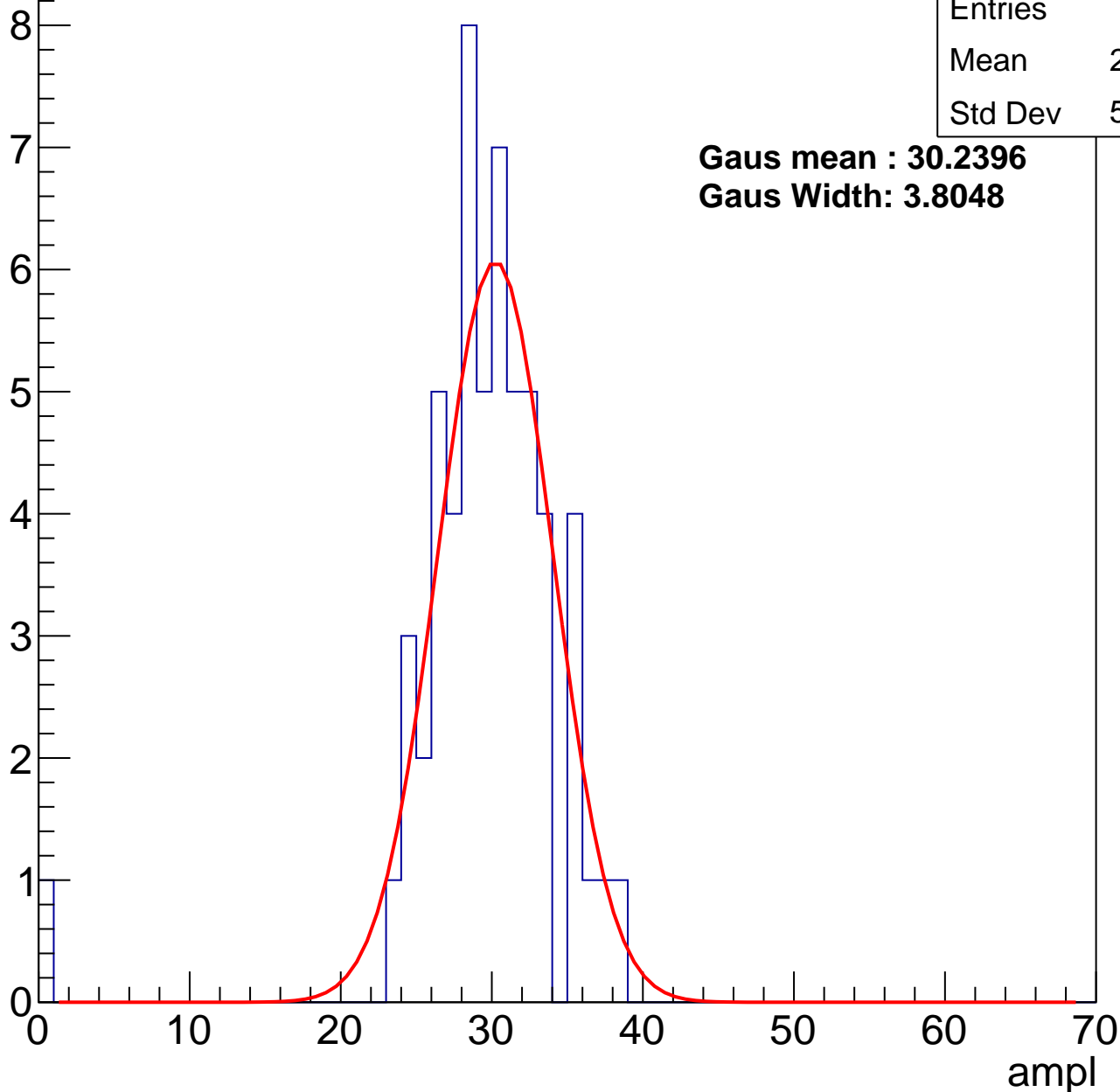
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	29.12
Std Dev	5.174

Gaus mean : 30.2396

Gaus Width: 3.8048



B1L101S, U26-ch80, adc1

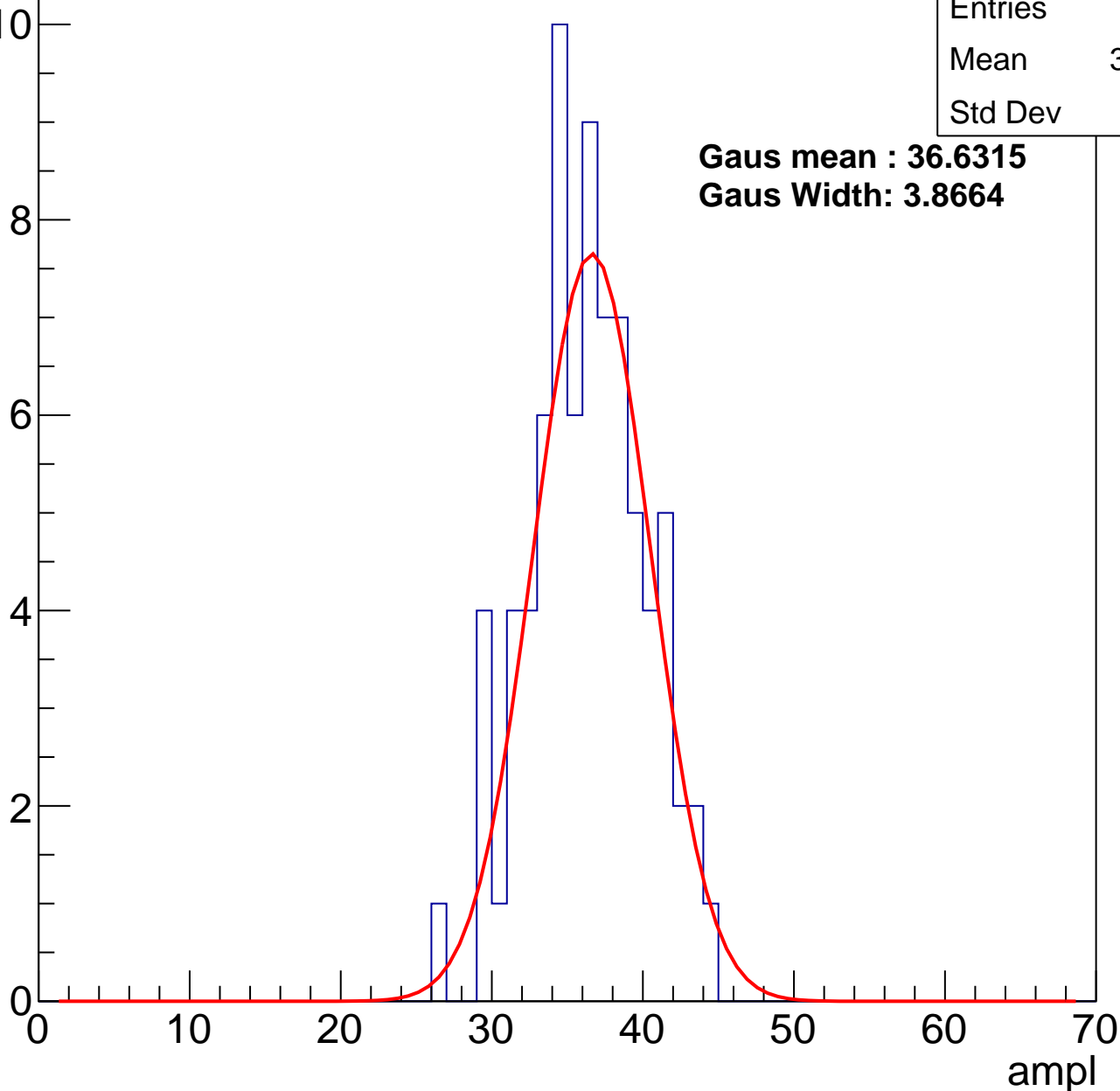
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.83
Std Dev	3.75

Gaus mean : 36.6315

Gaus Width: 3.8664



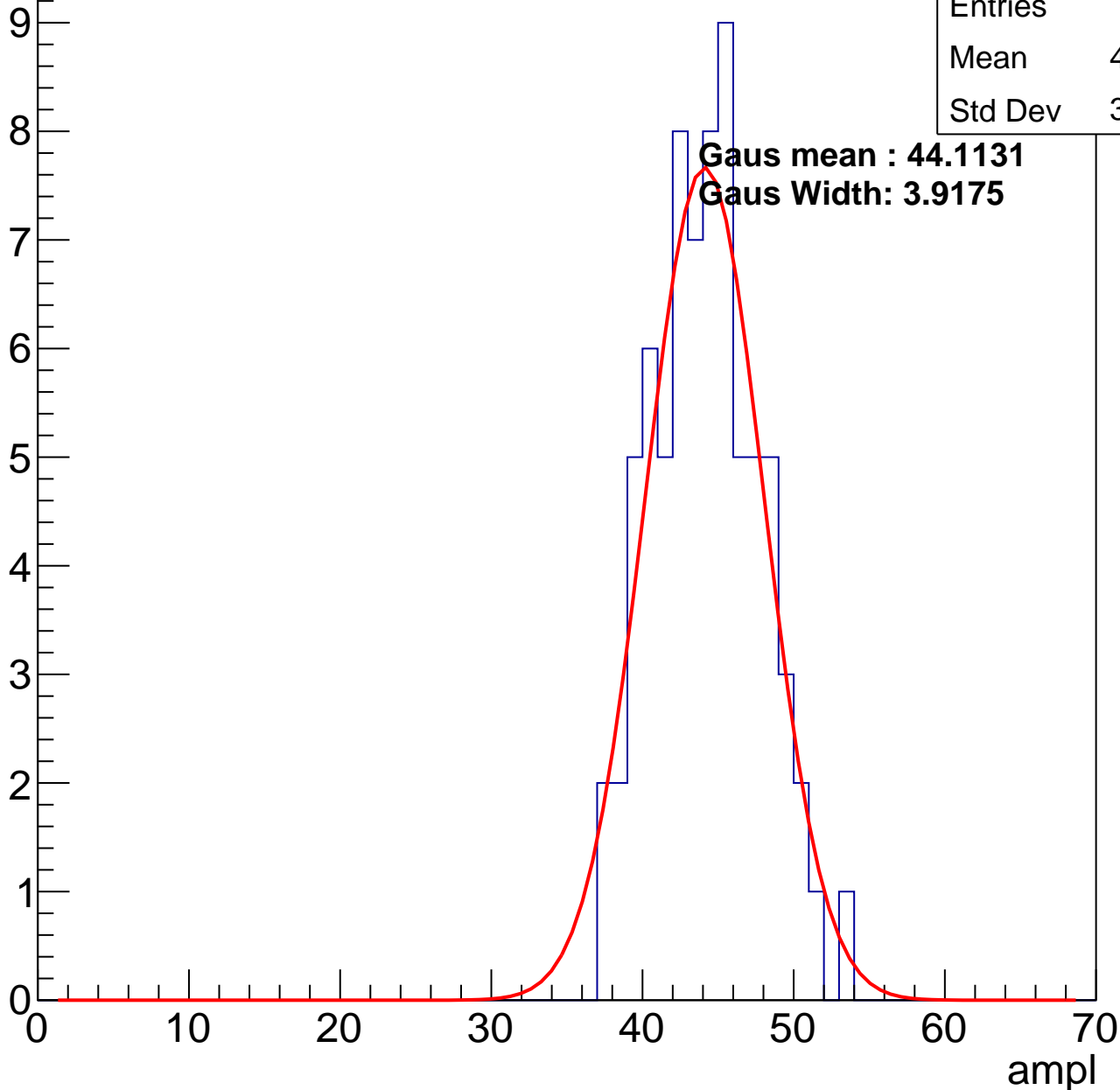
B1L101S, U26-ch80, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.78
Std Dev	3.492

Gaus mean : 44.1131
Gaus Width: 3.9175



B1L101S, U26-ch80, adc3

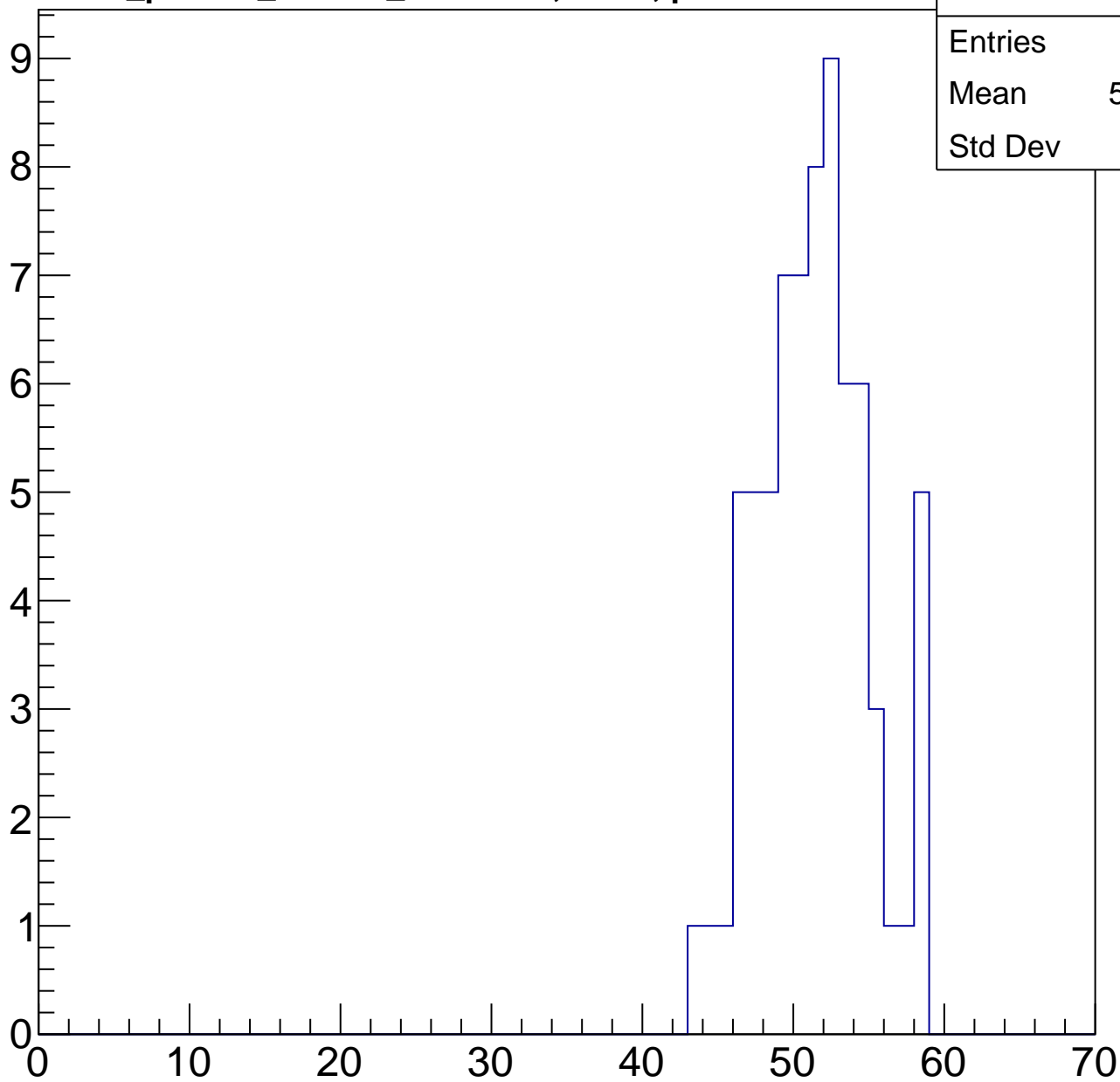
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	50.93
Std Dev	3.51

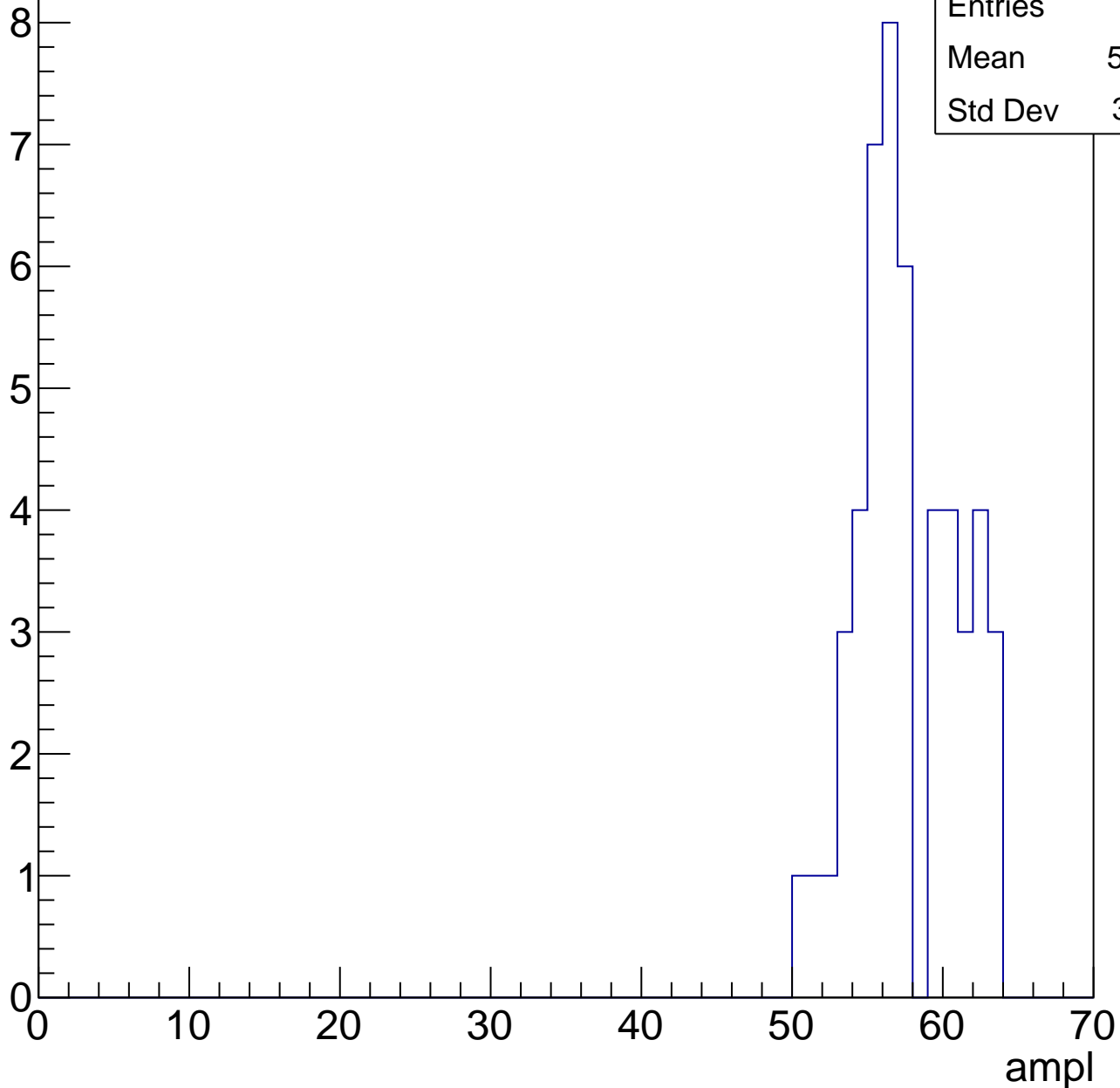
ampl



B1L101S, U26-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

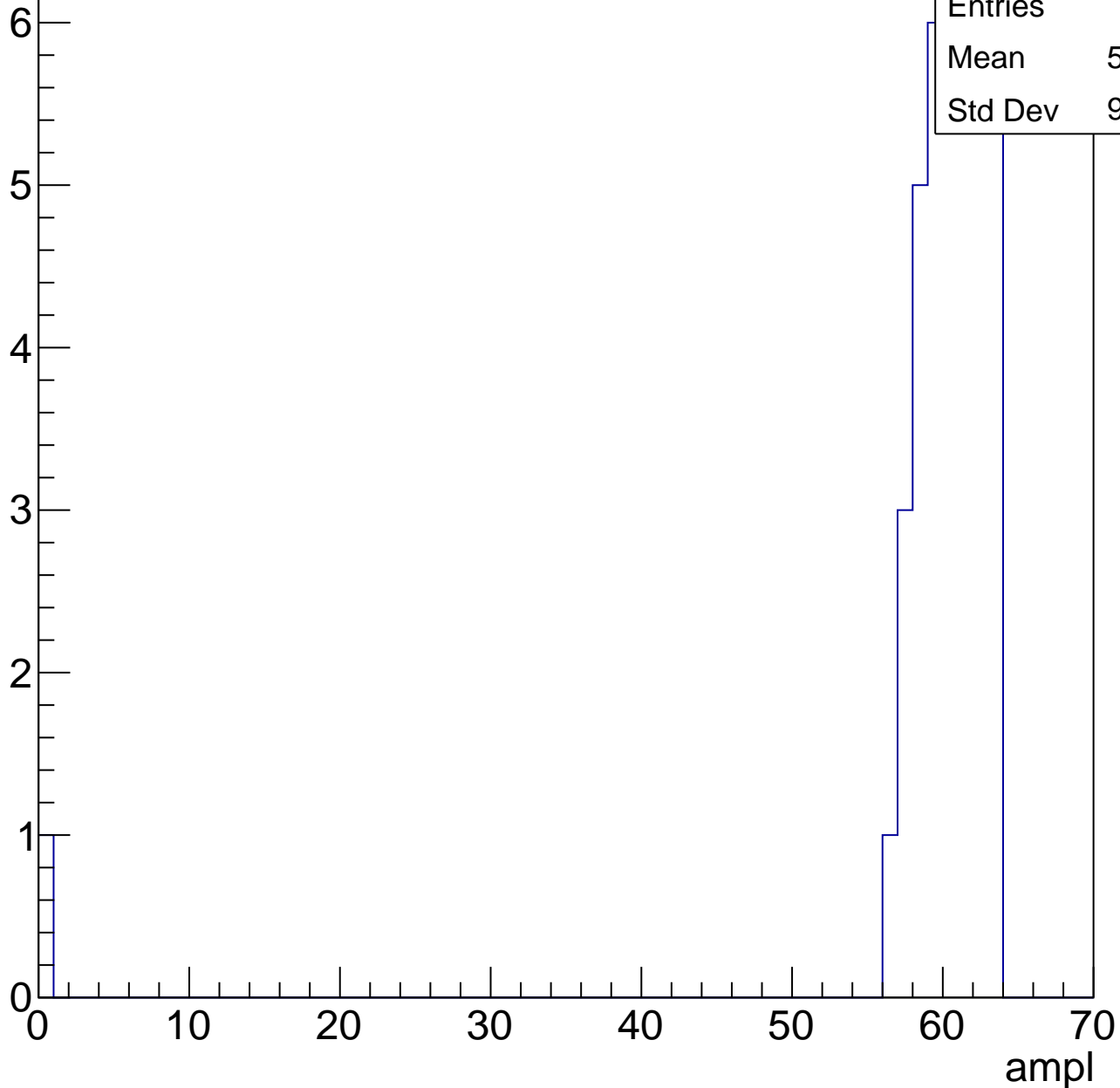
Entry



B1L101S, U26-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch81, adc0

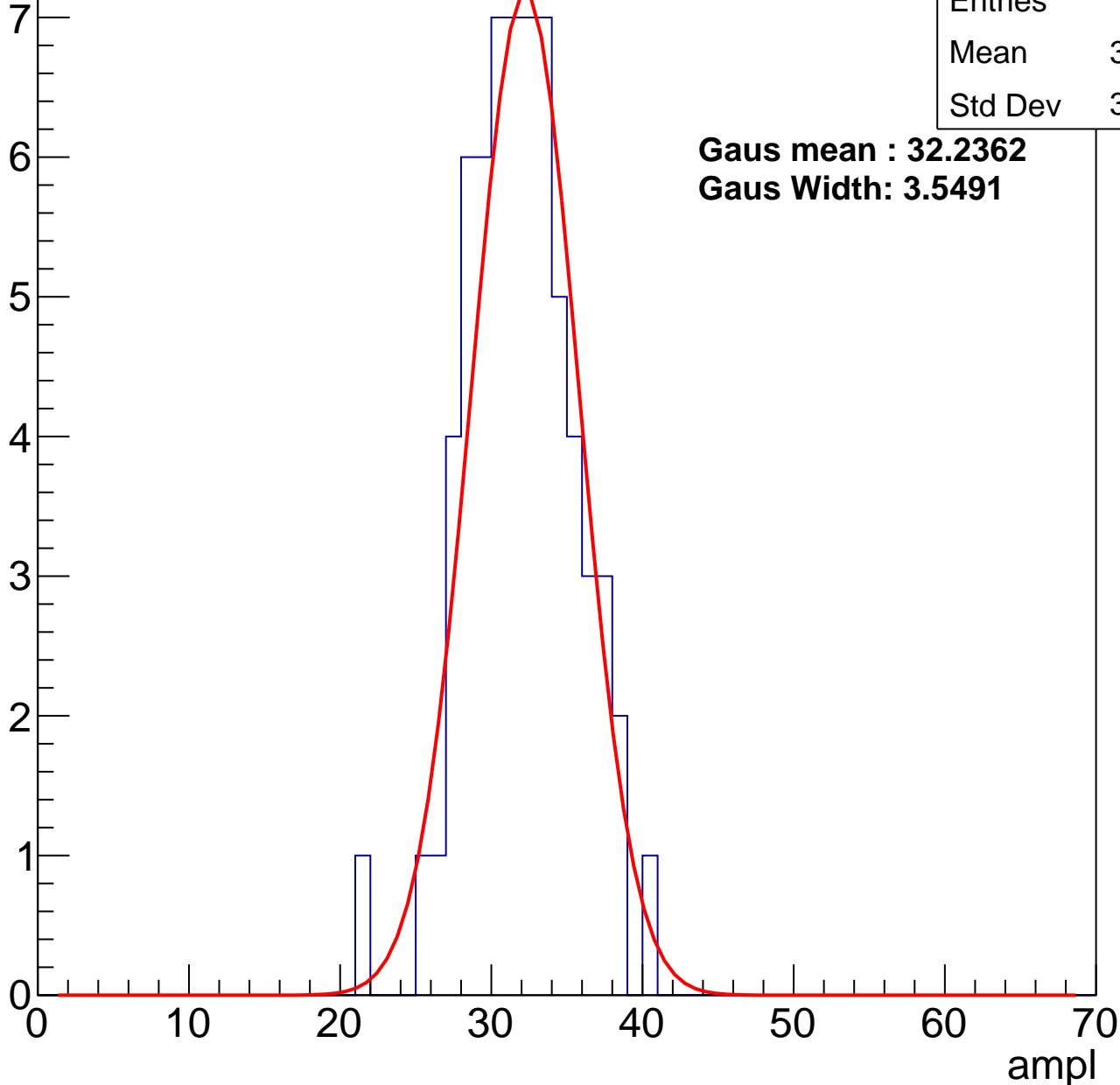
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	31.52
Std Dev	3.504

Gaus mean : 32.2362

Gaus Width: 3.5491



B1L101S, U26-ch81, adc1

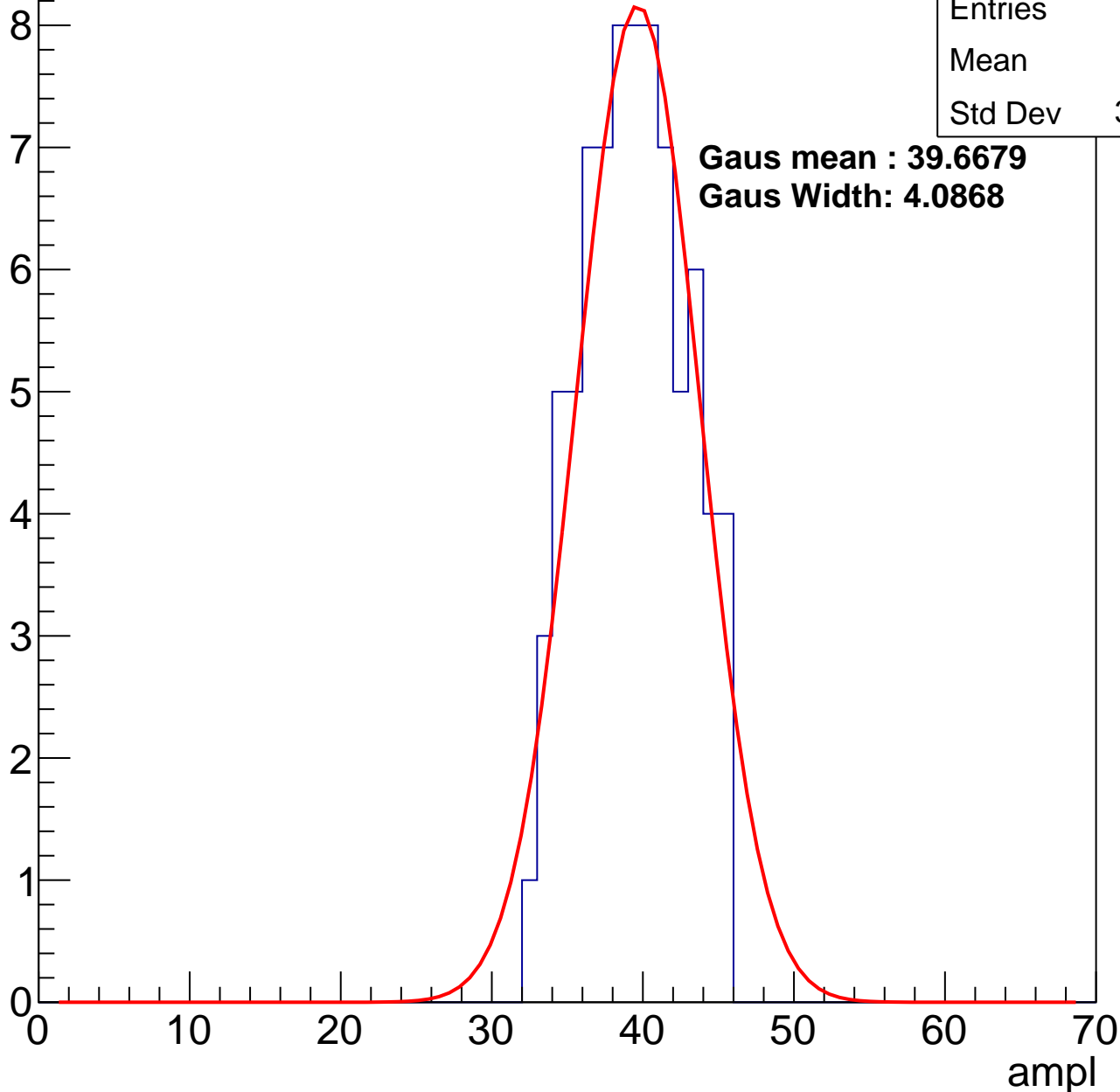
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	38.9
Std Dev	3.361

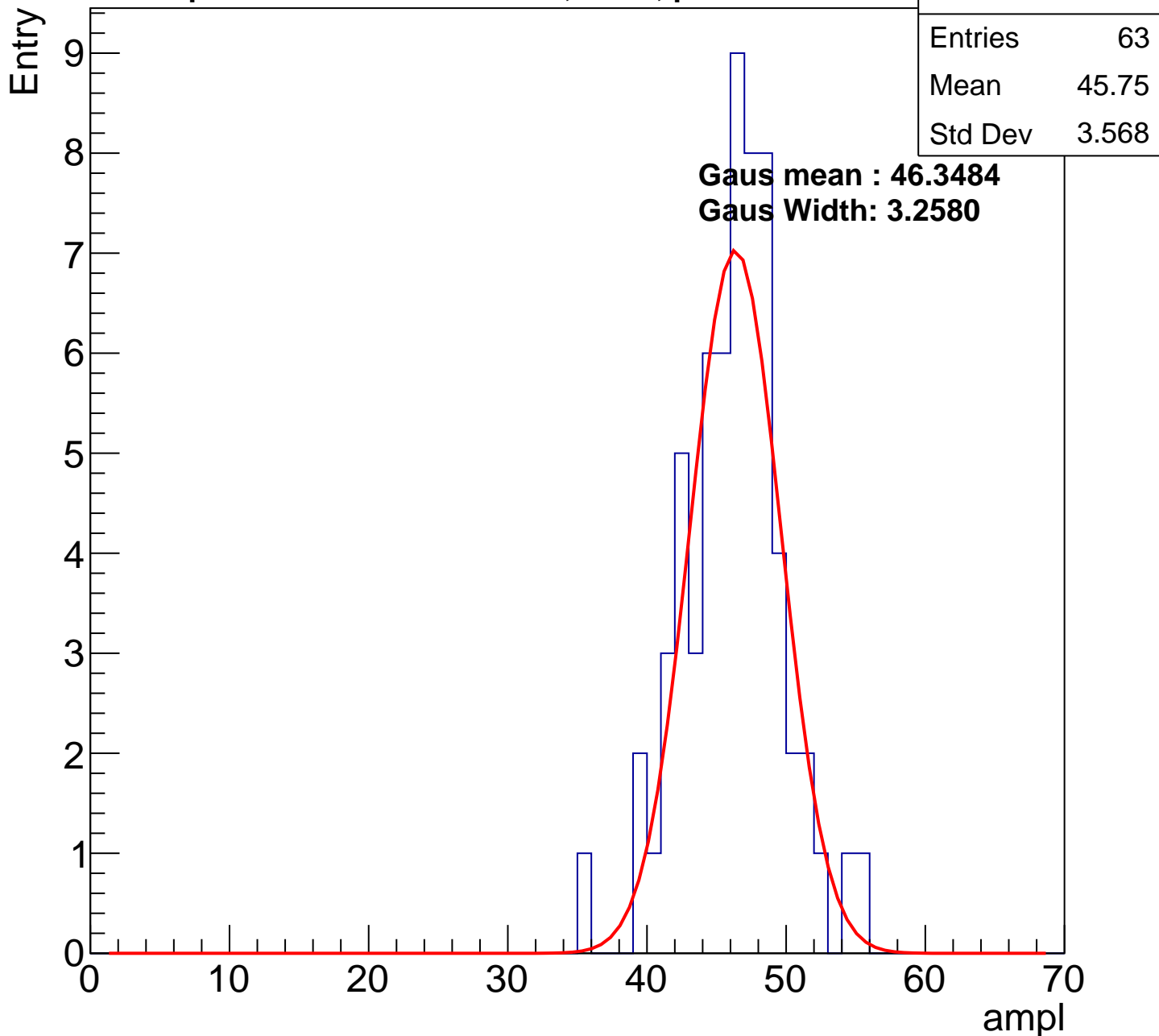
Gaus mean : 39.6679

Gaus Width: 4.0868



B1L101S, U26-ch81, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	52.68
Std Dev	3.322

Entry

10

8

6

4

2

0

0

10

20

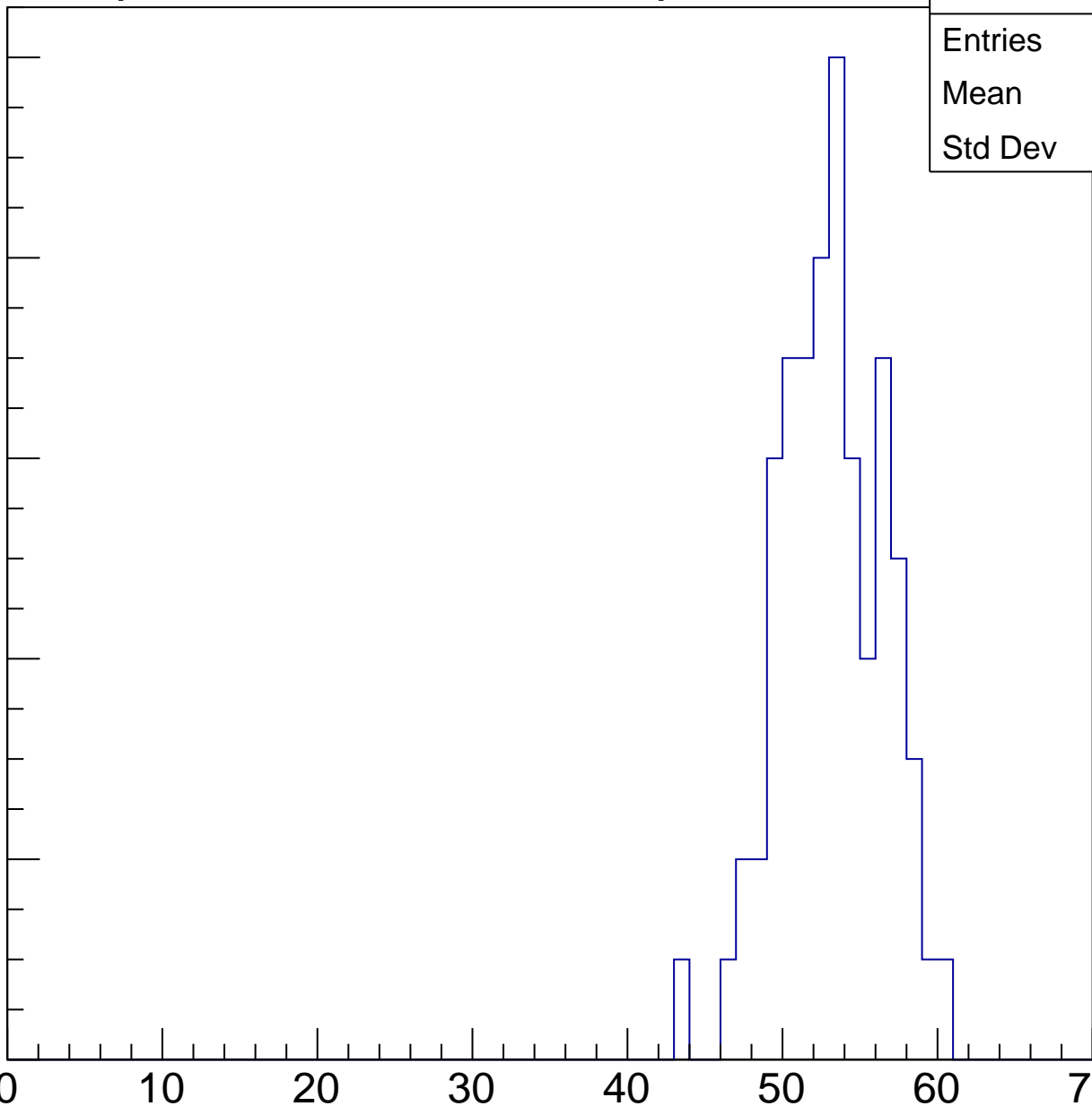
30

40

50

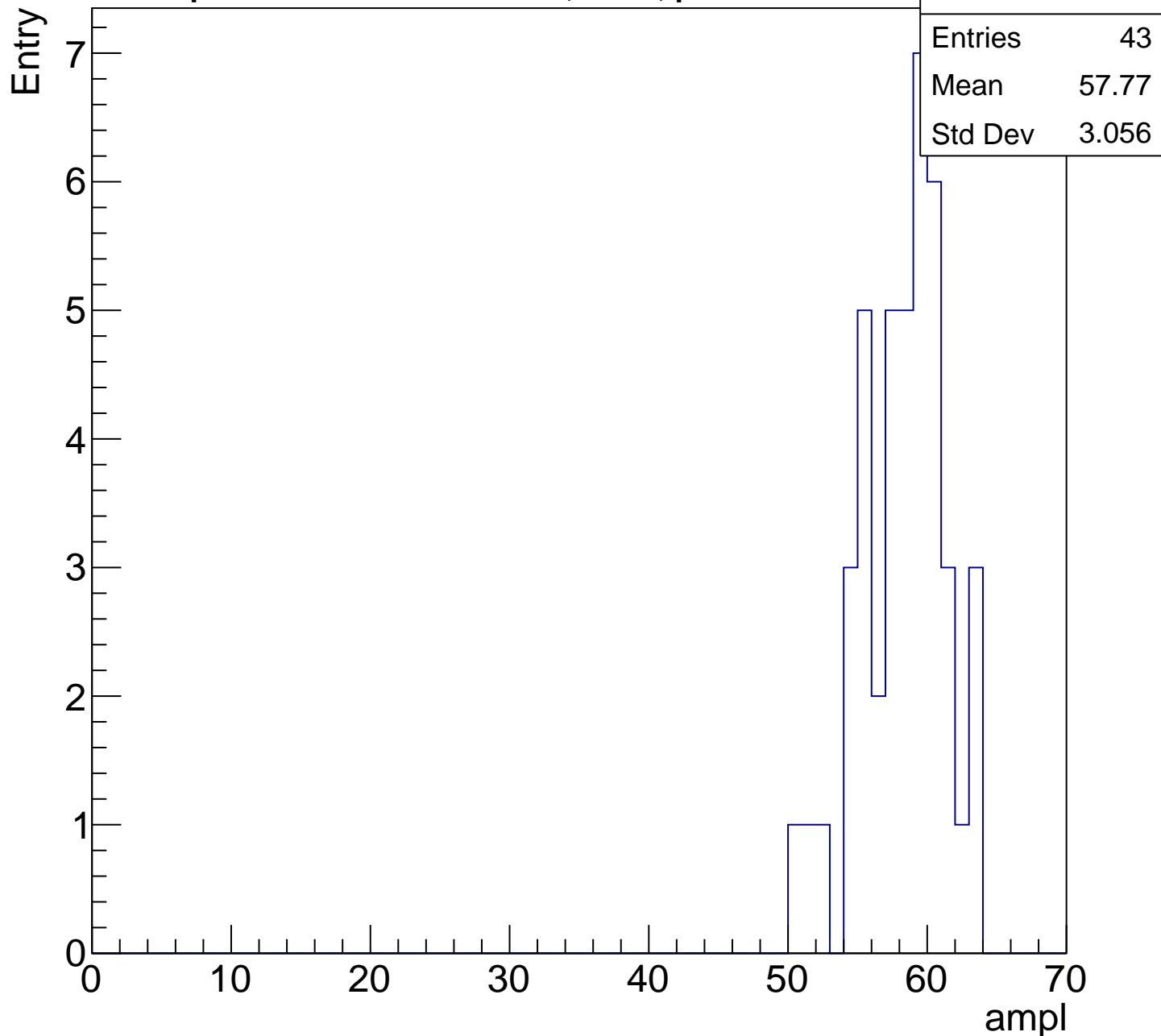
60

ampl



B1L101S, U26-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

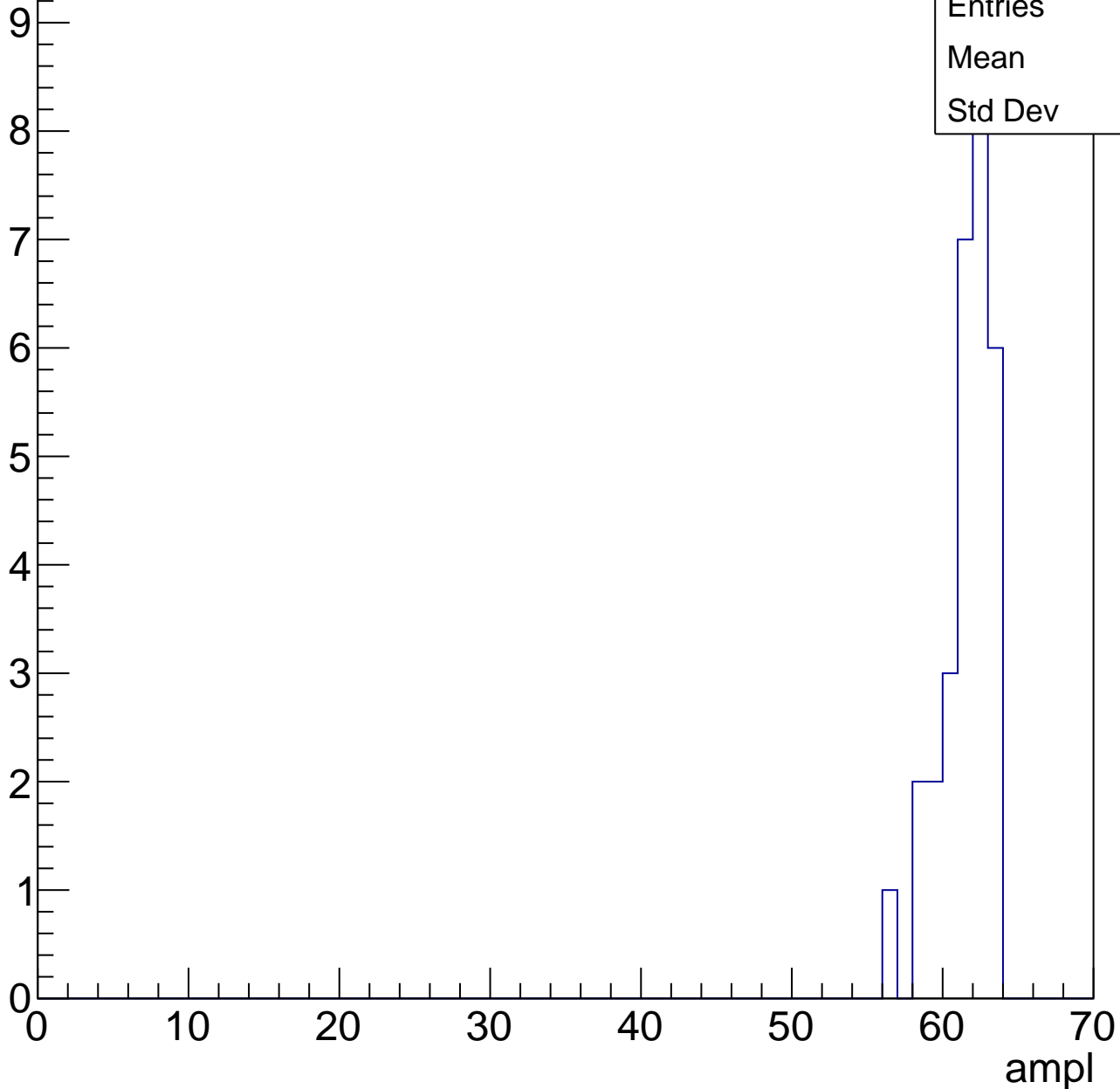


B1L101S, U26-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	61.1
Std Dev	1.7



B1L101S, U26-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch82, adc0

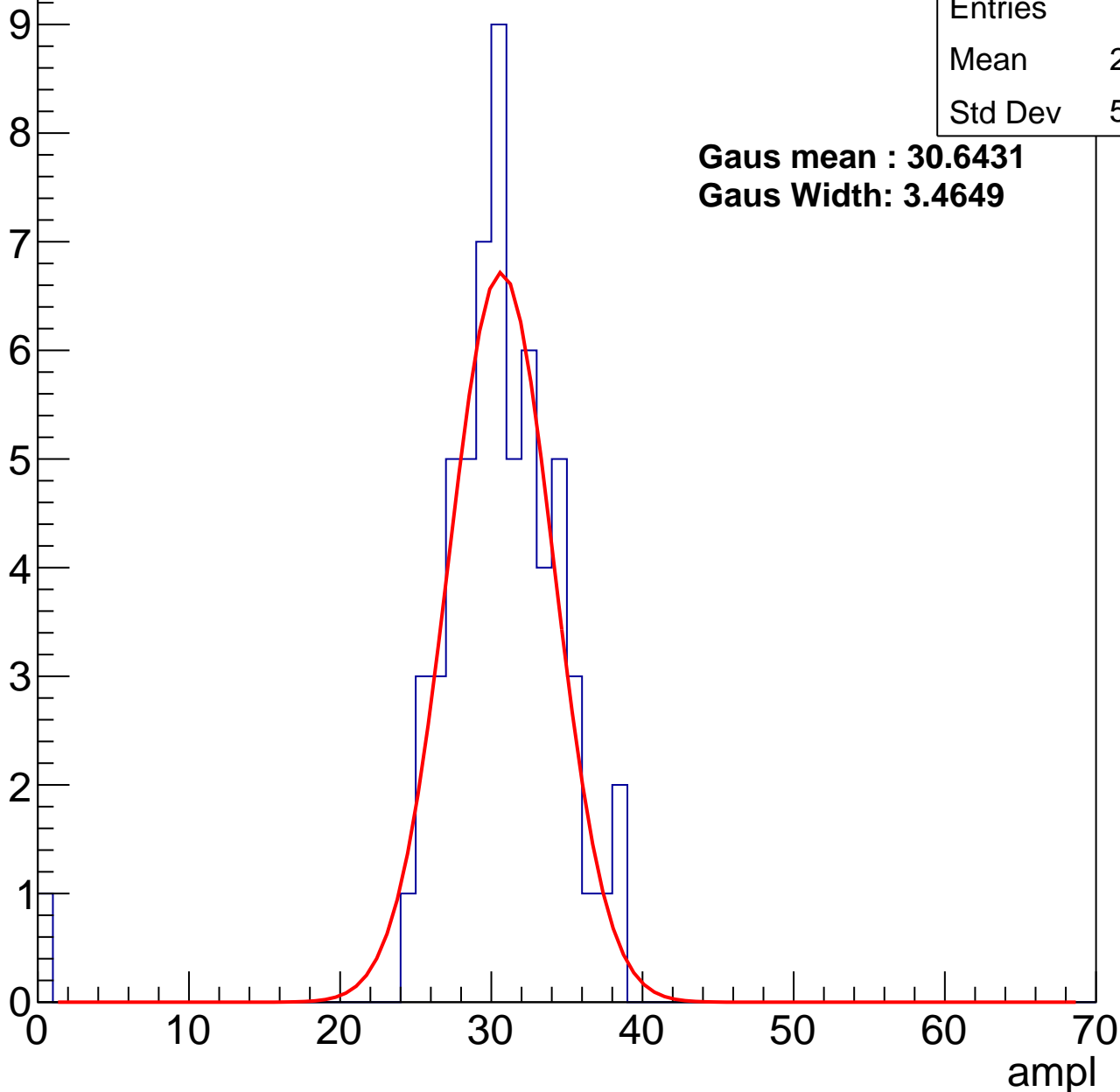
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.97
Std Dev	5.063

Gaus mean : 30.6431

Gaus Width: 3.4649



B1L101S, U26-ch82, adc1

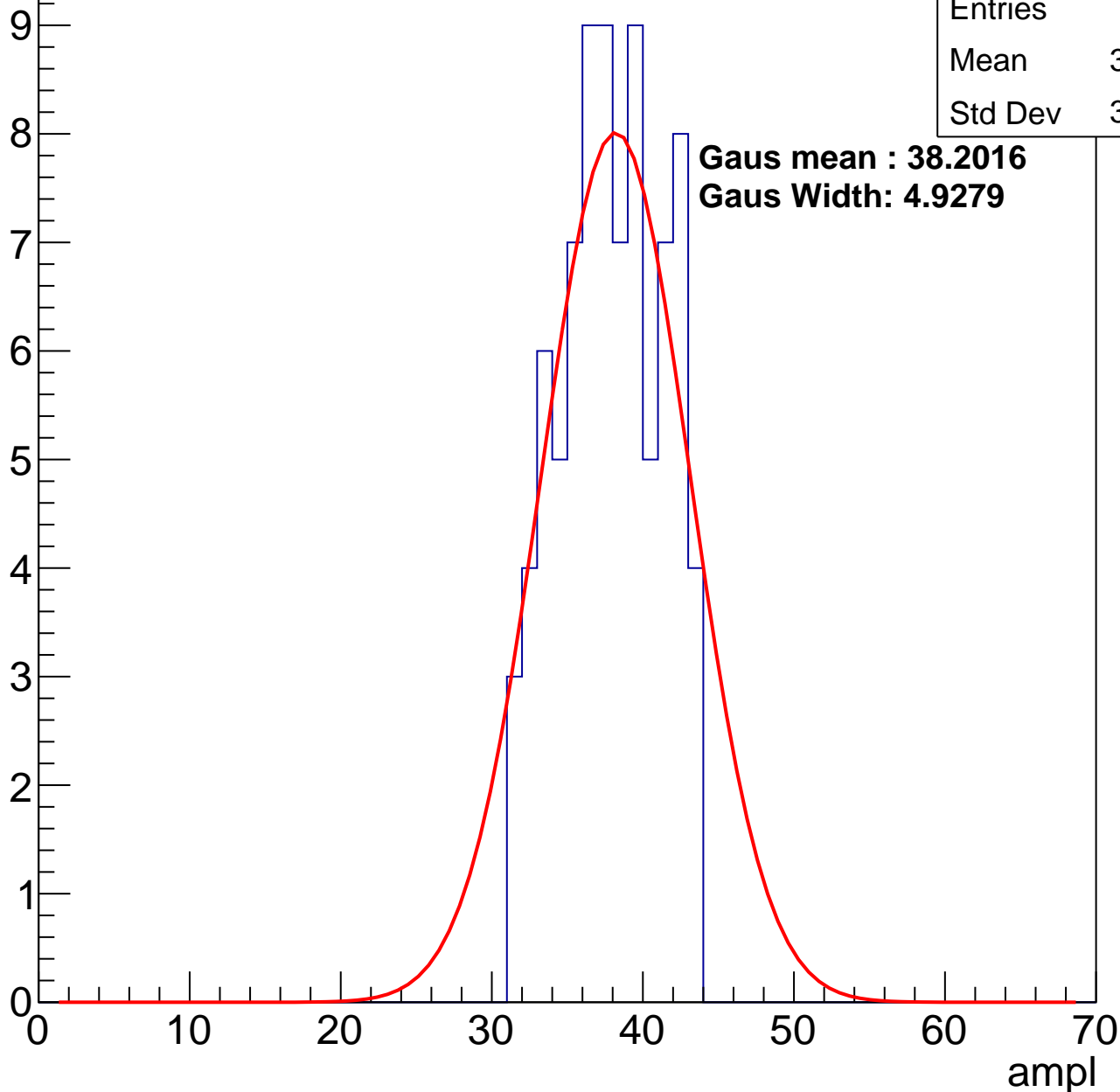
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	37.39
Std Dev	3.325

Gaus mean : 38.2016

Gaus Width: 4.9279



B1L101S, U26-ch82, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	45.66
Std Dev	3.636

Gaus mean : 46.0875

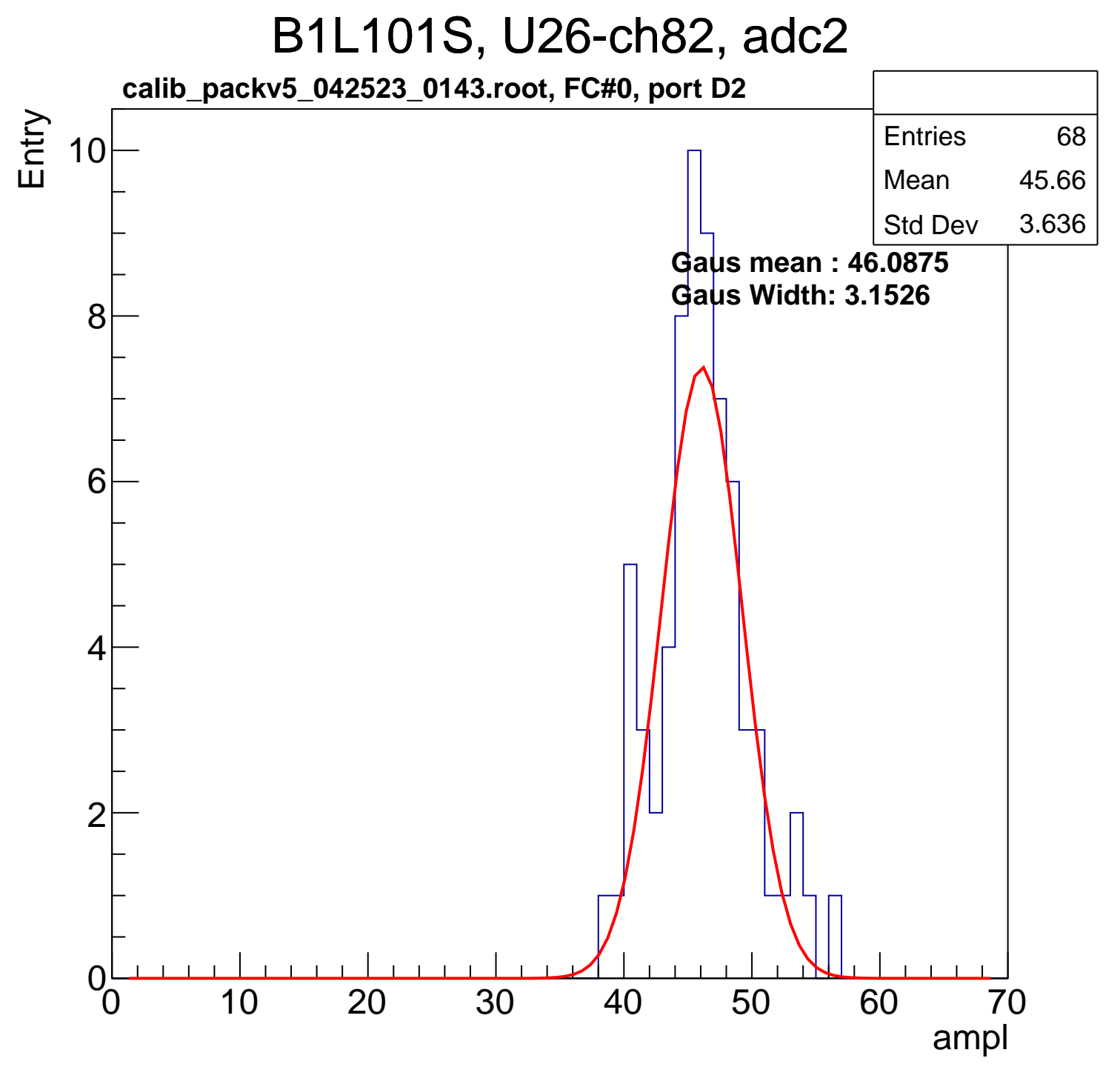
Gaus Width: 3.1526

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

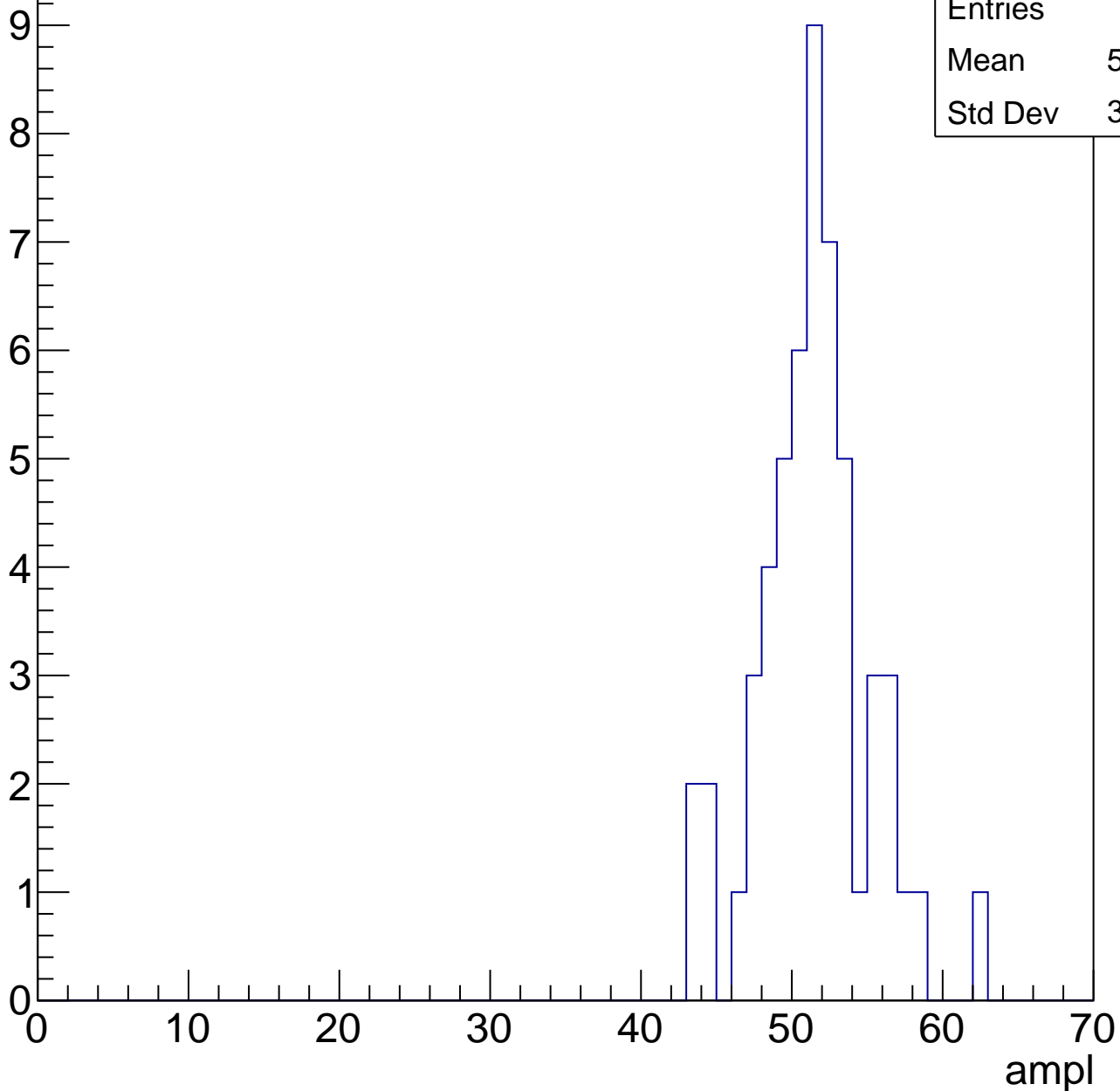


B1L101S, U26-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	50.93
Std Dev	3.666



B1L101S, U26-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 74

Mean 57.12

Std Dev 3.11

8

6

4

2

0

0

10

20

30

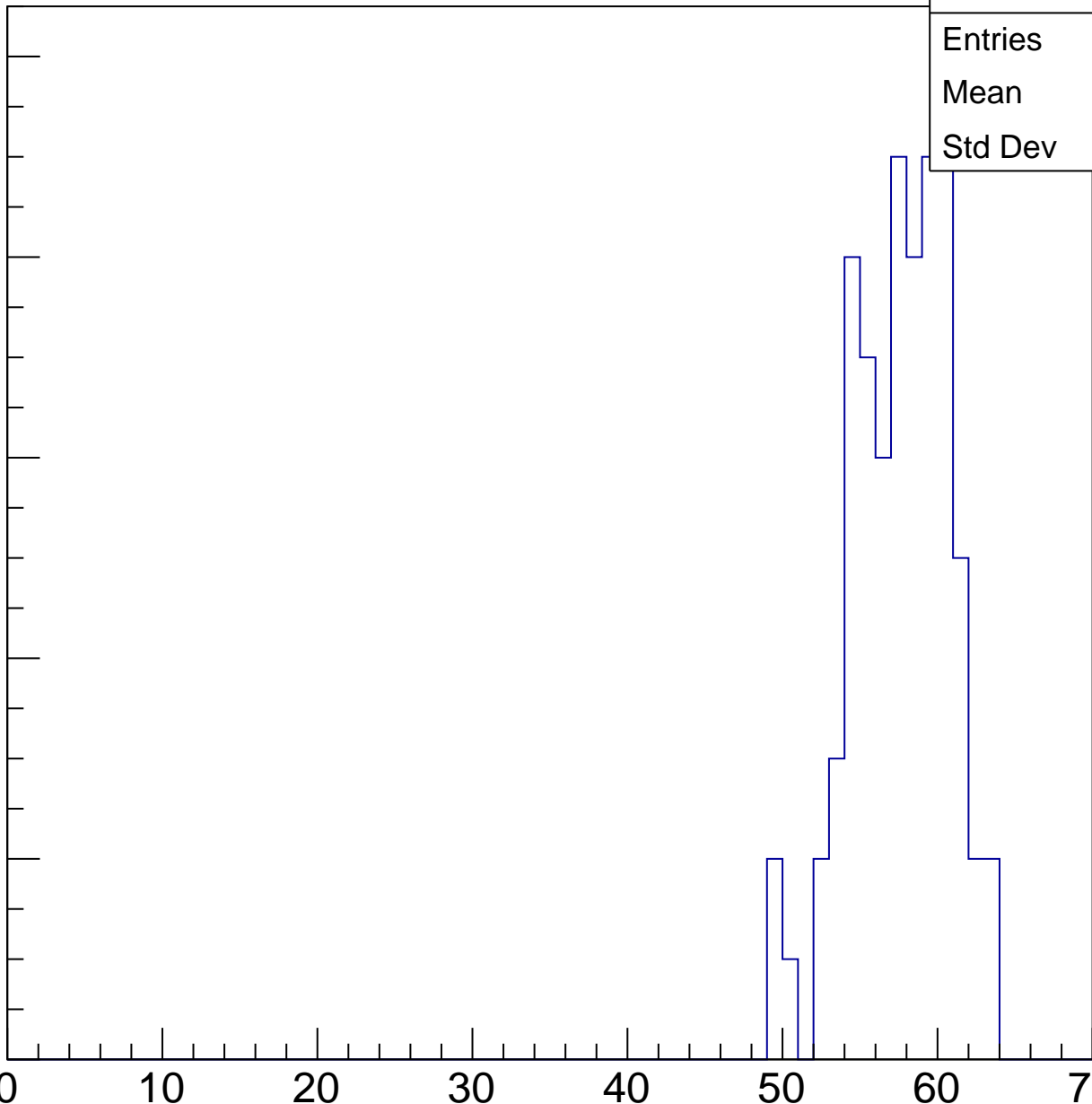
40

50

60

70

ampl

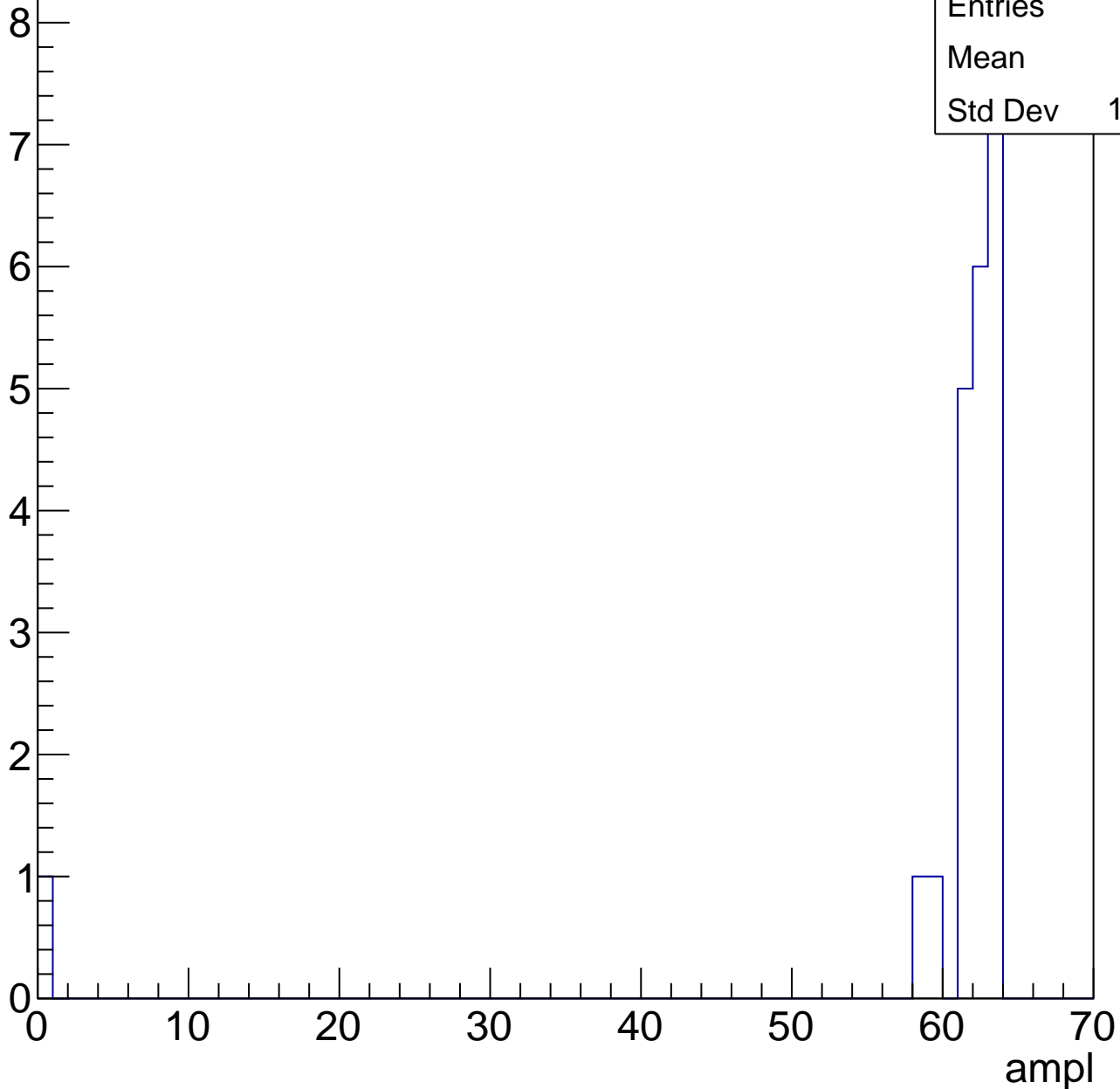


B1L101S, U26-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	59
Std Dev	12.94



B1L101S, U26-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U26-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch83, adc0

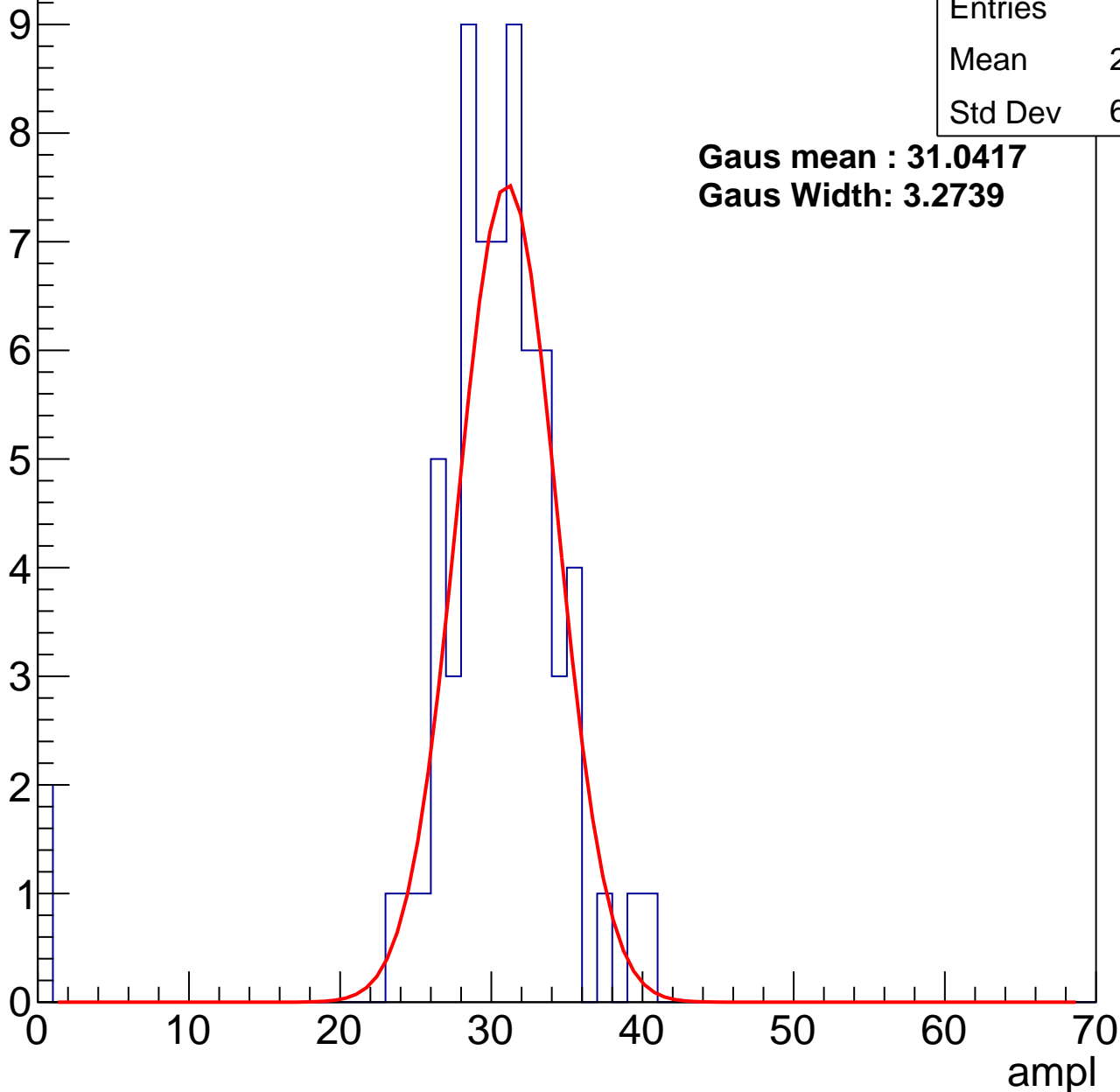
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.48
Std Dev	6.117

Gaus mean : 31.0417

Gaus Width: 3.2739



B1L101S, U26-ch83, adc1

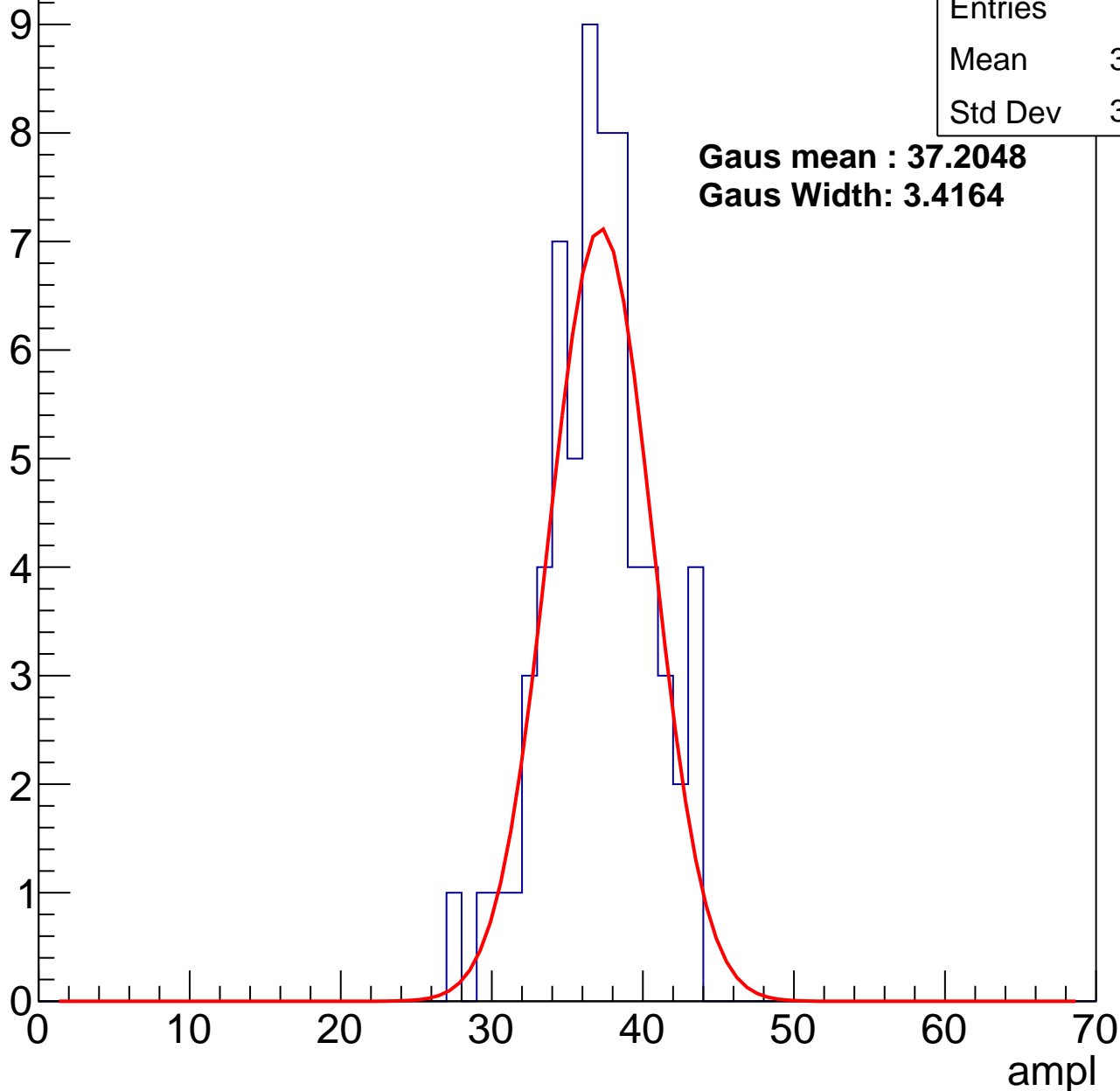
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.57
Std Dev	3.442

Gaus mean : 37.2048

Gaus Width: 3.4164



B1L101S, U26-ch83, adc2

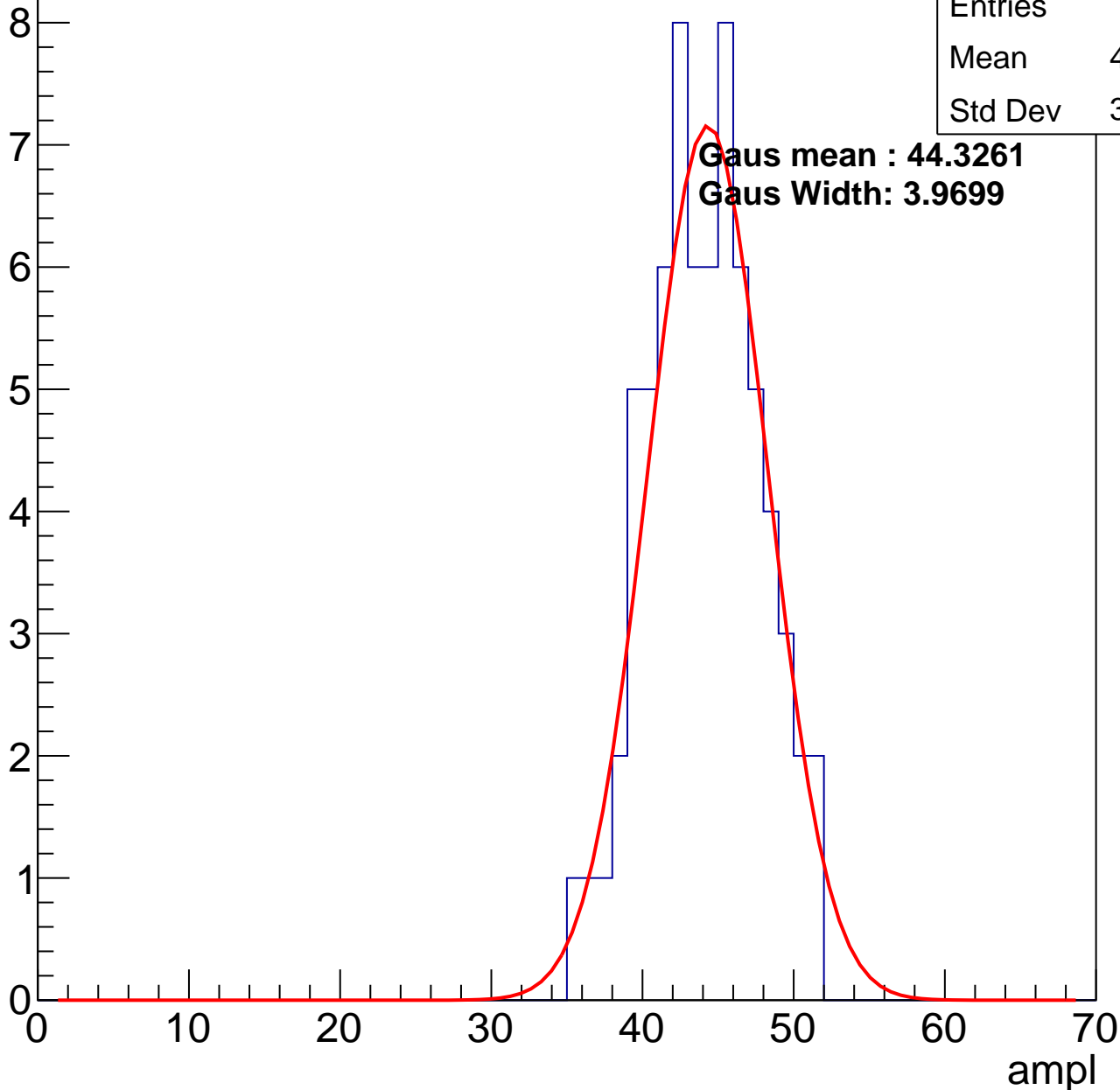
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.59
Std Dev	3.633

Gaus mean : 44.3261

Gaus Width: 3.9699

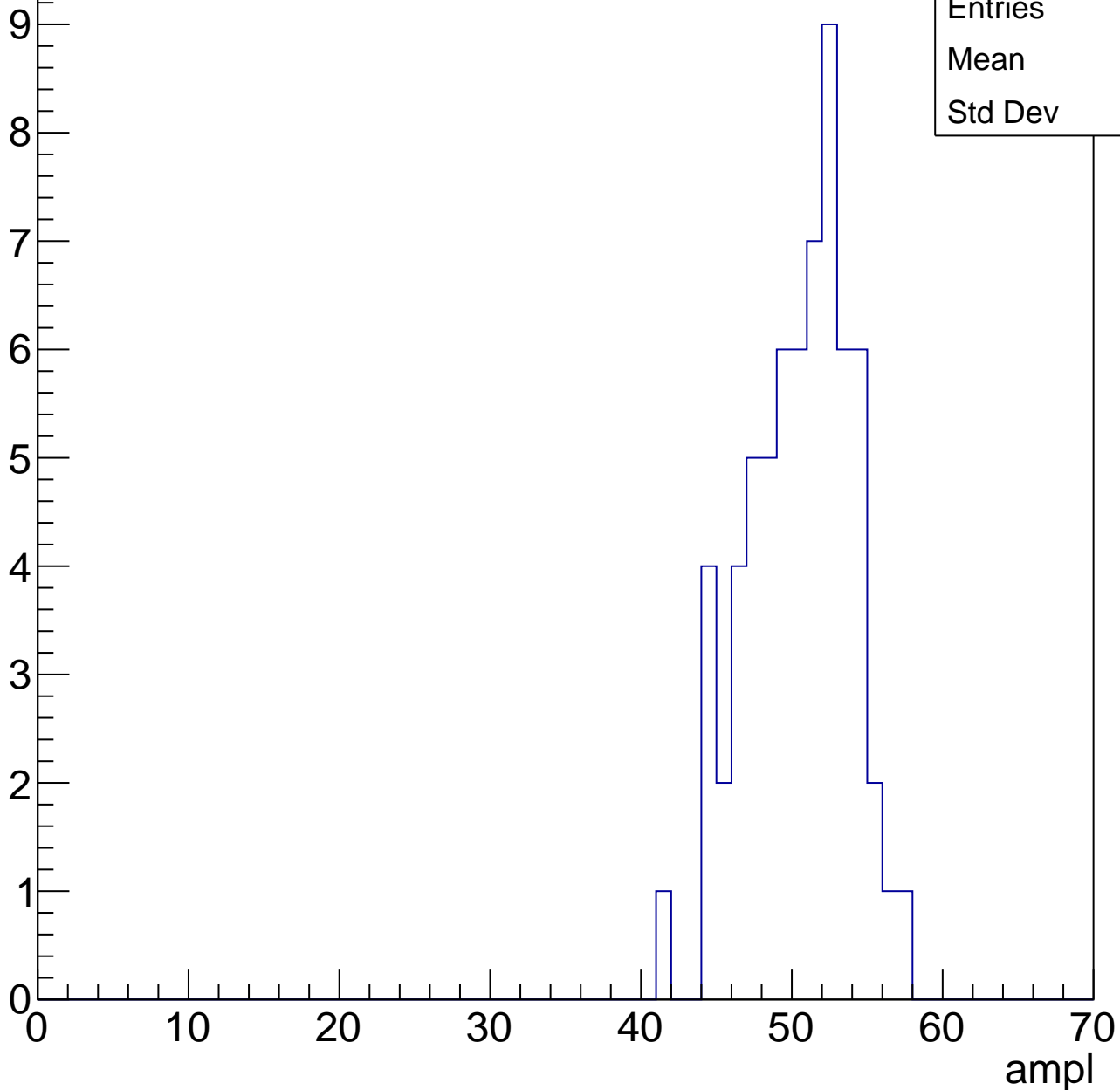


B1L101S, U26-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50
Std Dev	3.37

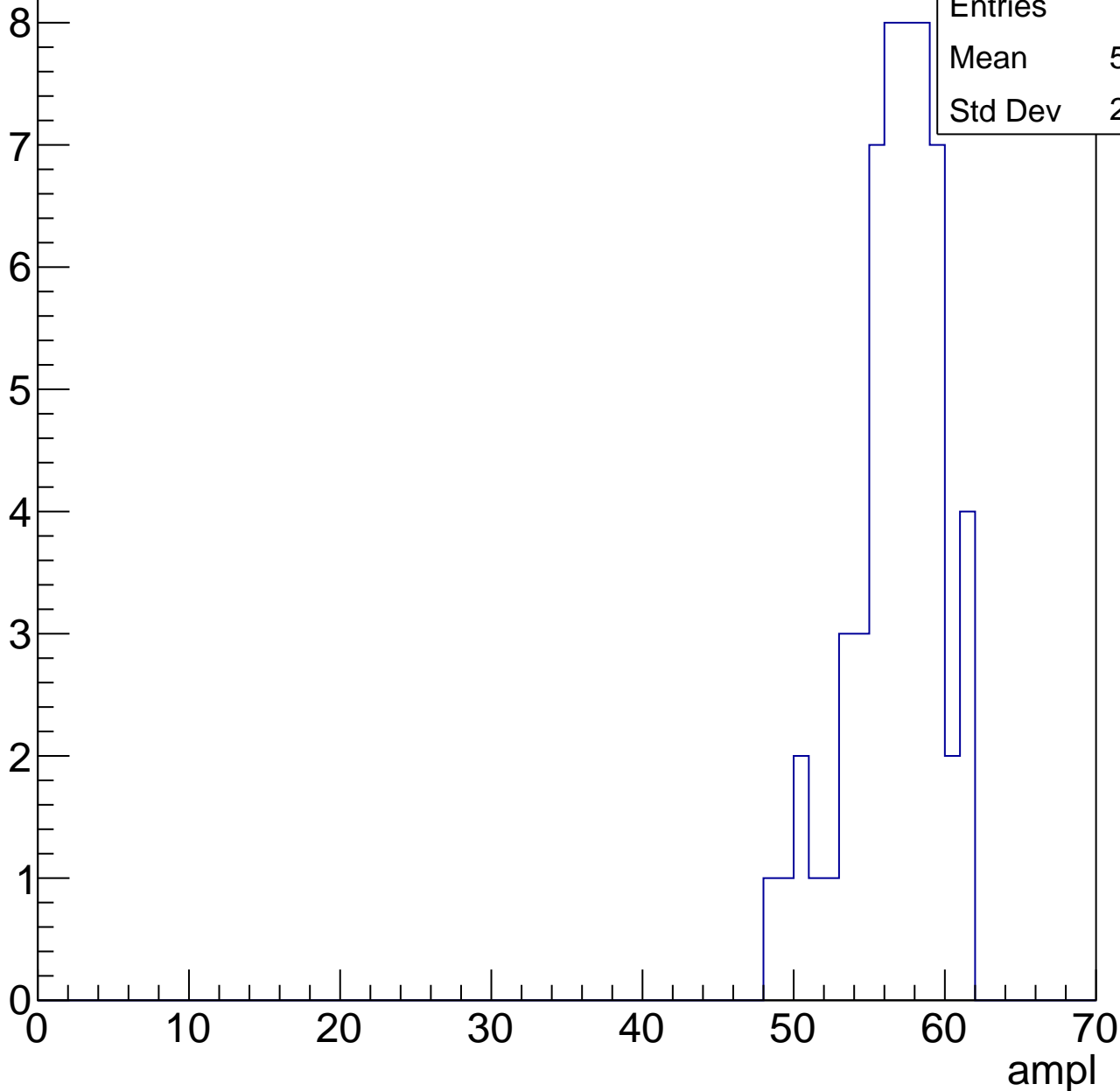


B1L101S, U26-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.27
Std Dev	2.997



B1L101S, U26-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

45

Mean

59.2

Std Dev

9.253

0

2

4

6

8

10

B1L101S, U26-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch84, adc0

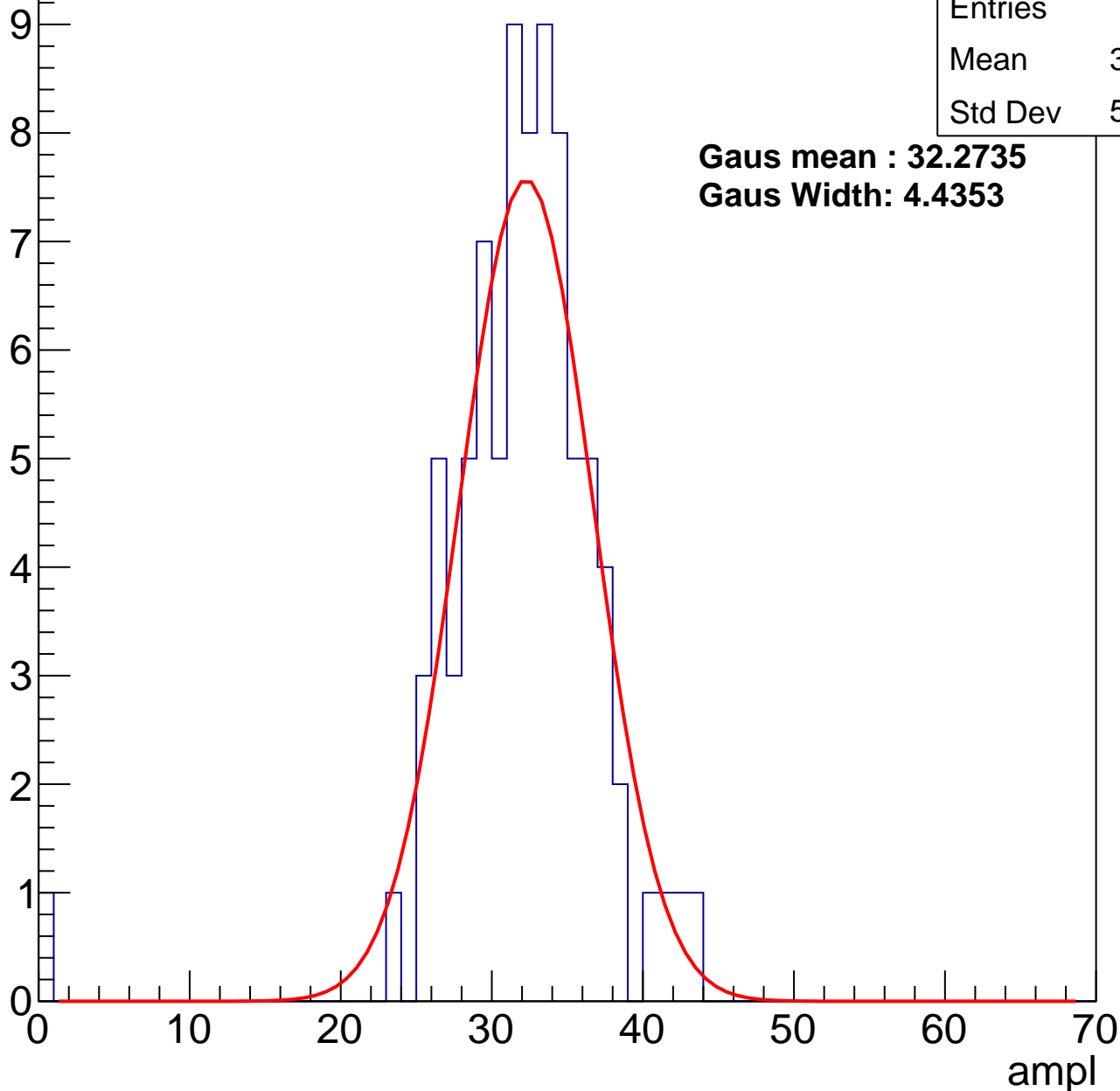
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	31.56
Std Dev	5.308

Gaus mean : 32.2735

Gaus Width: 4.4353



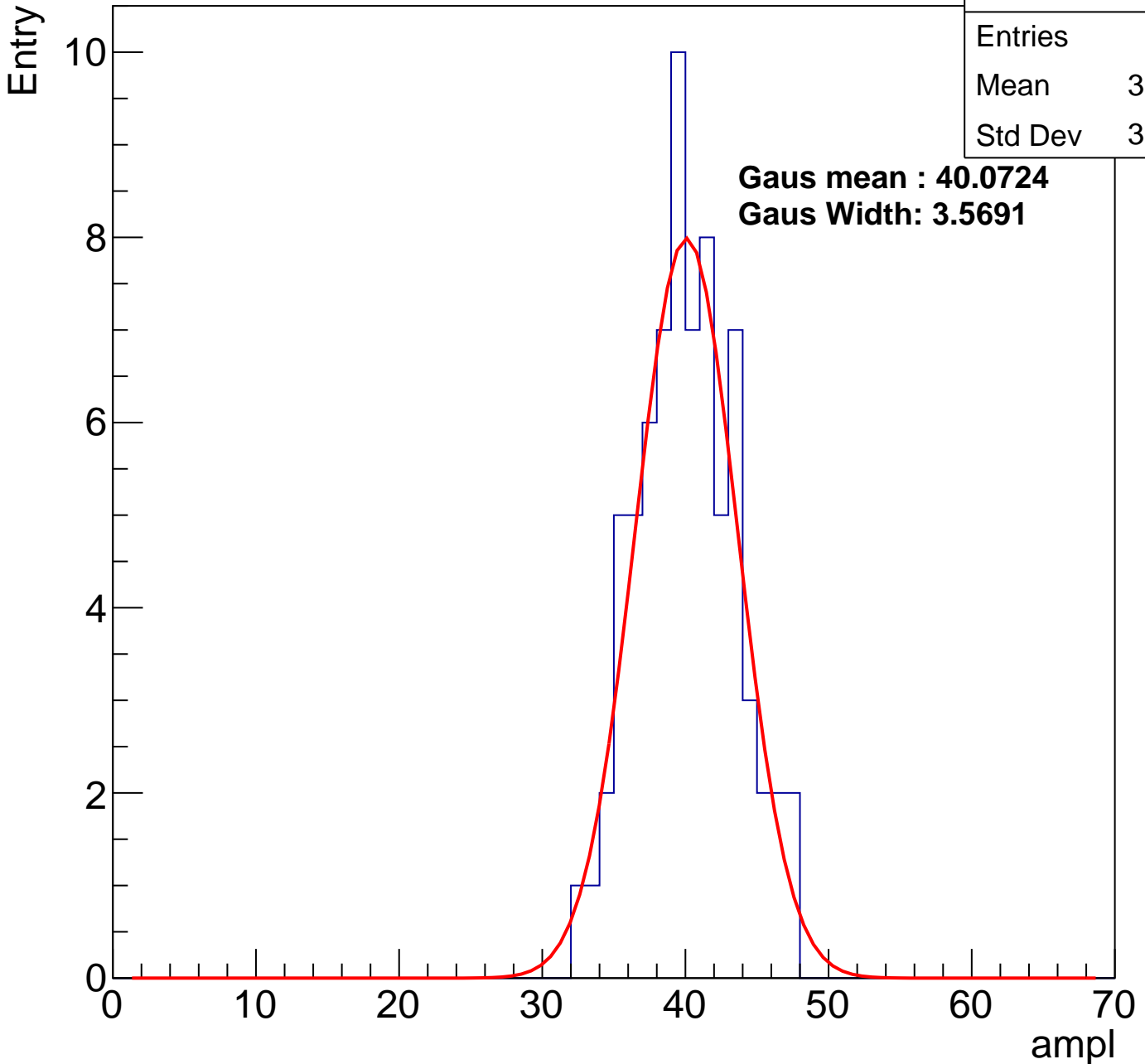
B1L101S, U26-ch84, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	39.63
Std Dev	3.374

Gaus mean : 40.0724

Gaus Width: 3.5691



B1L101S, U26-ch84, adc2

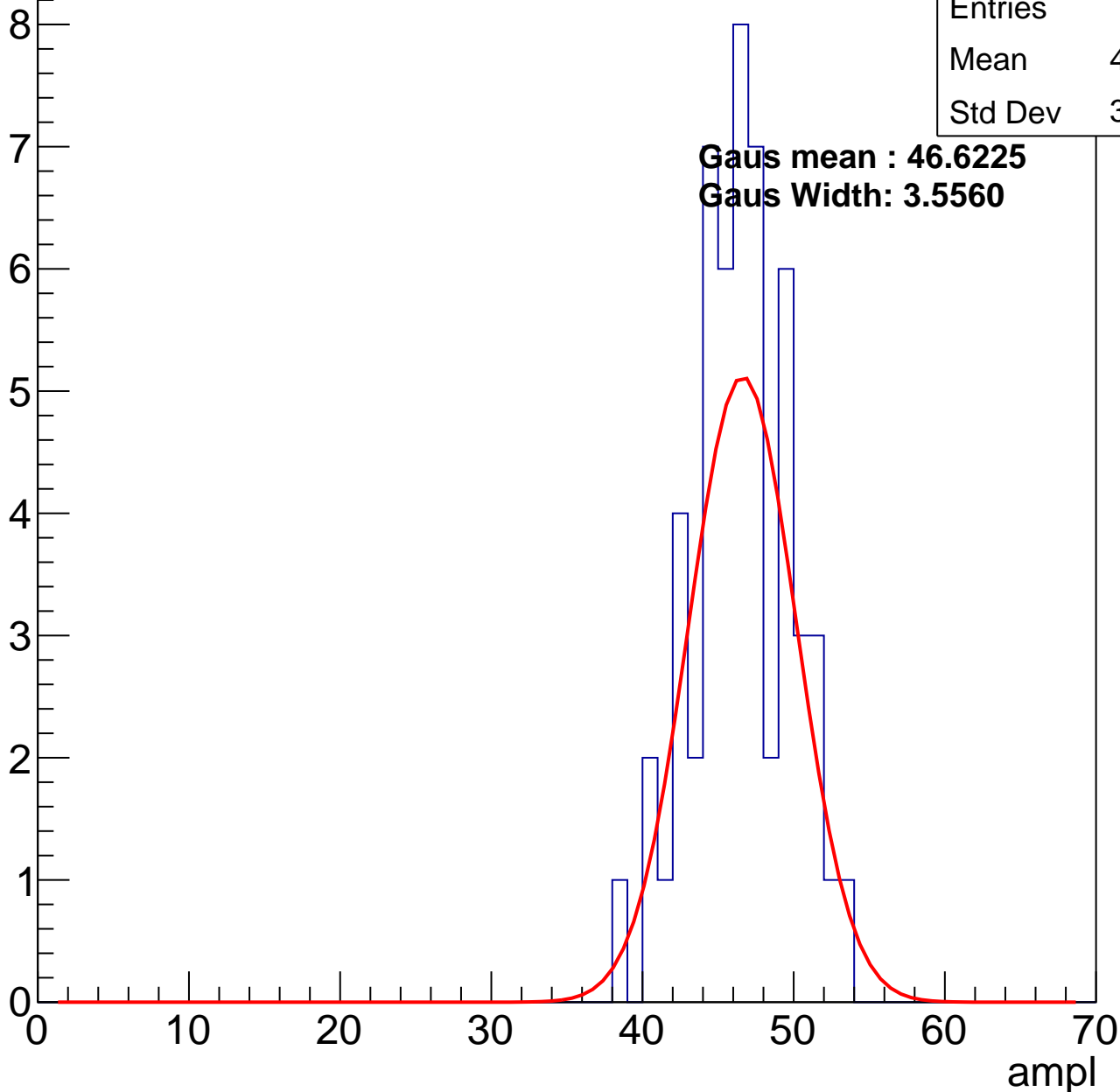
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	46.04
Std Dev	3.203

Gaus mean : 46.6225

Gaus Width: 3.5560



B1L101S, U26-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	52.41
Std Dev	3.238

Entry

10

8

6

4

2

0

0

10

20

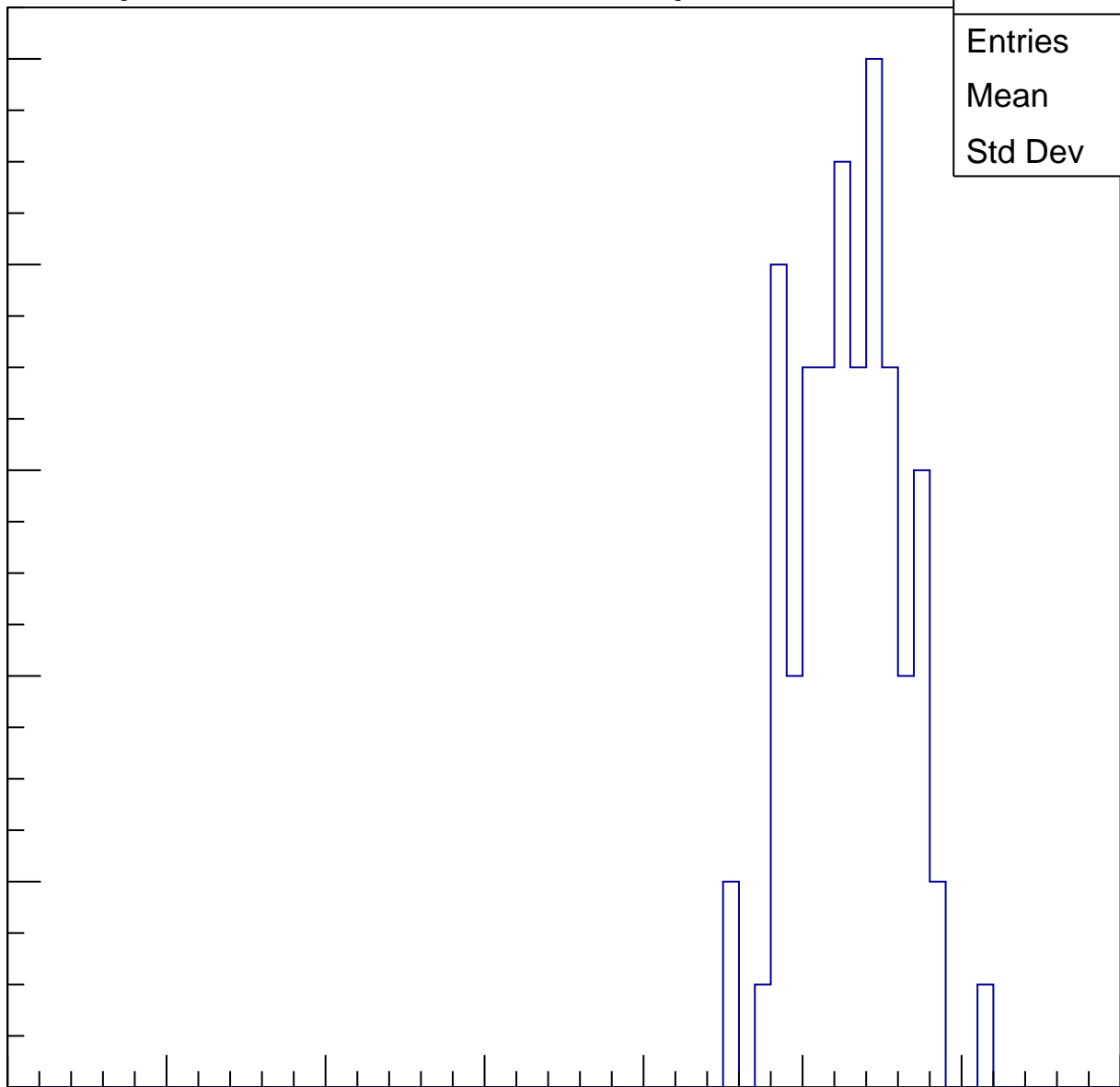
30

40

50

60

ampl

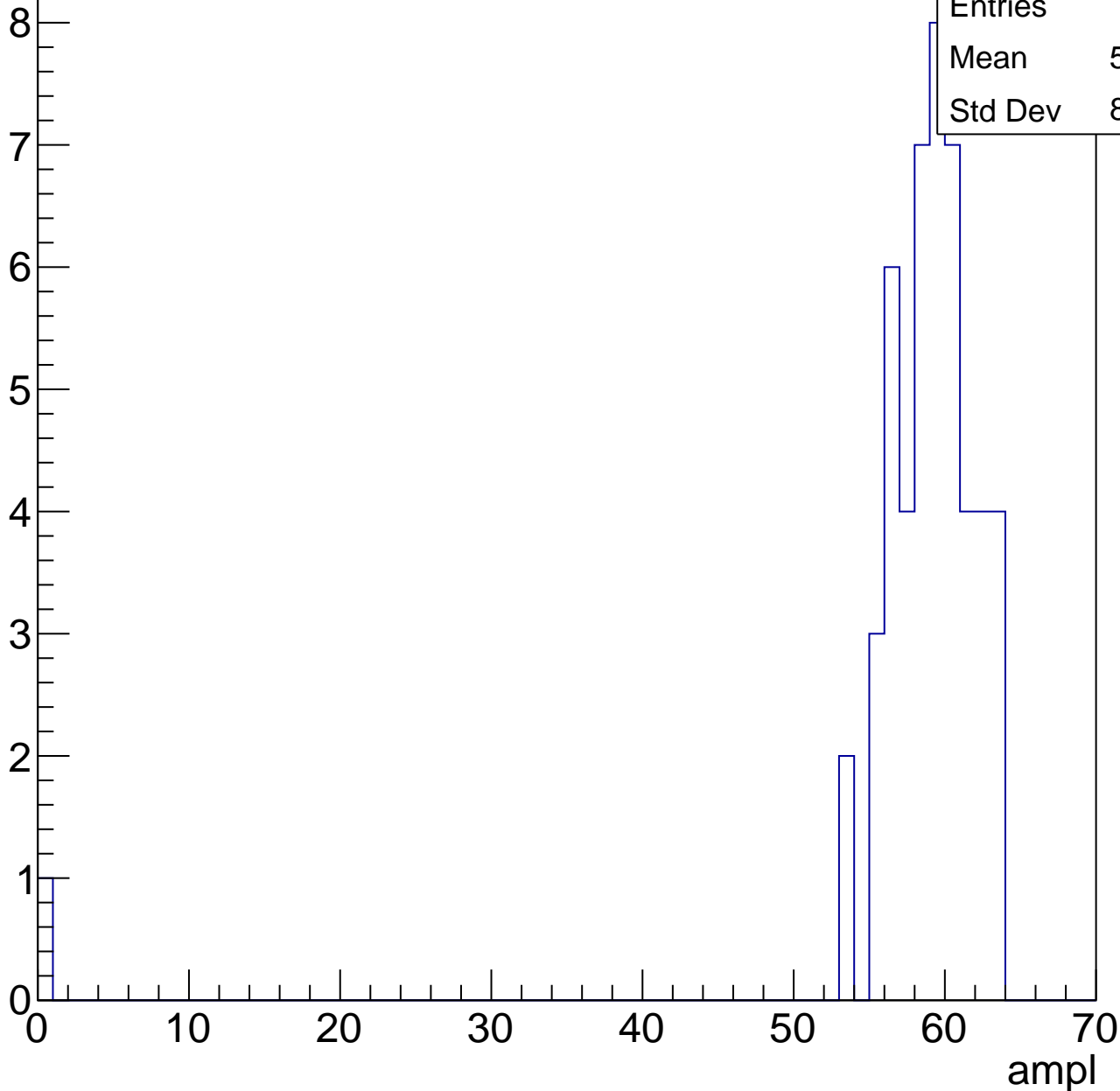


B1L101S, U26-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.54
Std Dev	8.596

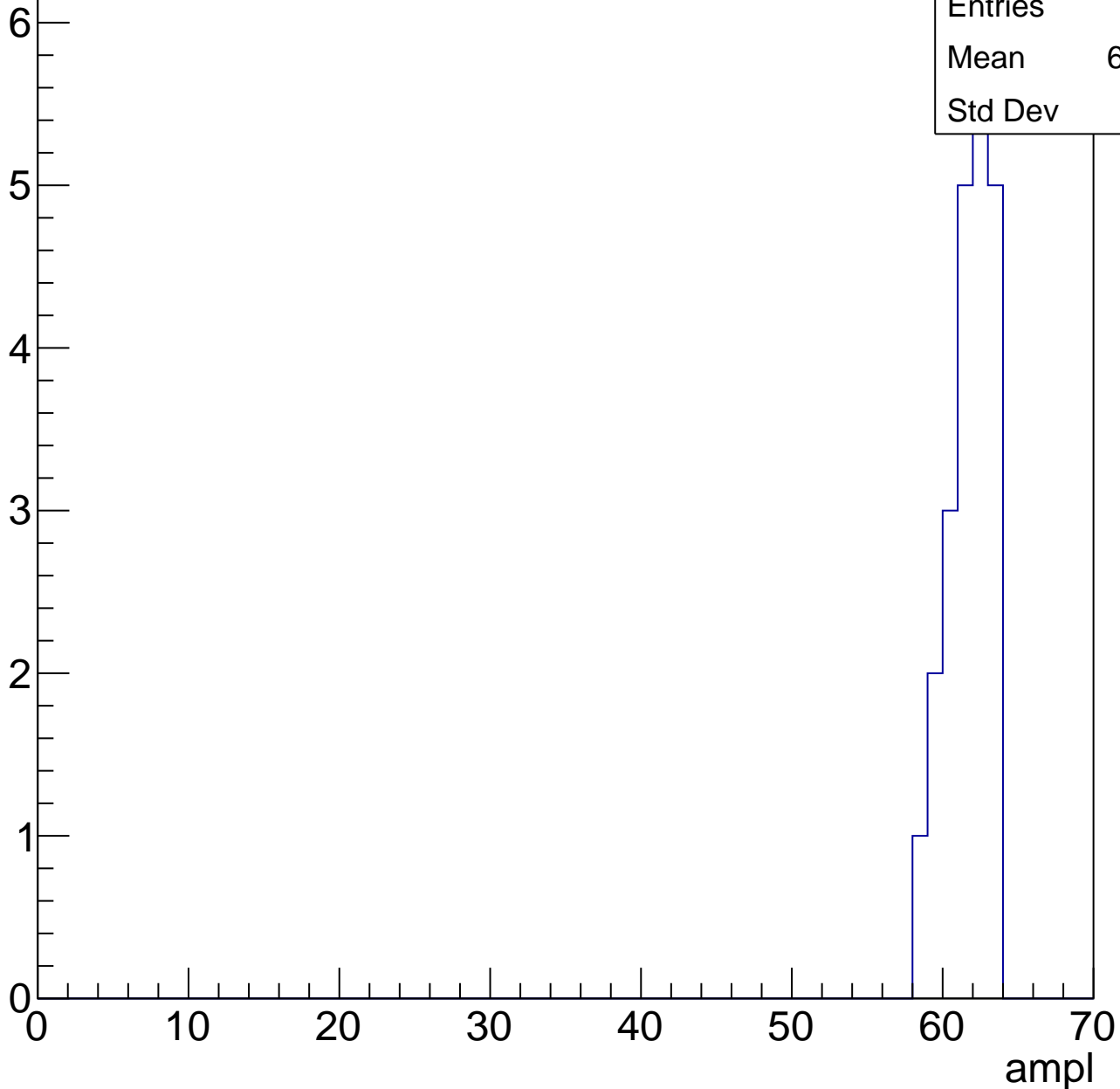


B1L101S, U26-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	61.27
Std Dev	1.42



B1L101S, U26-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch85, adc0

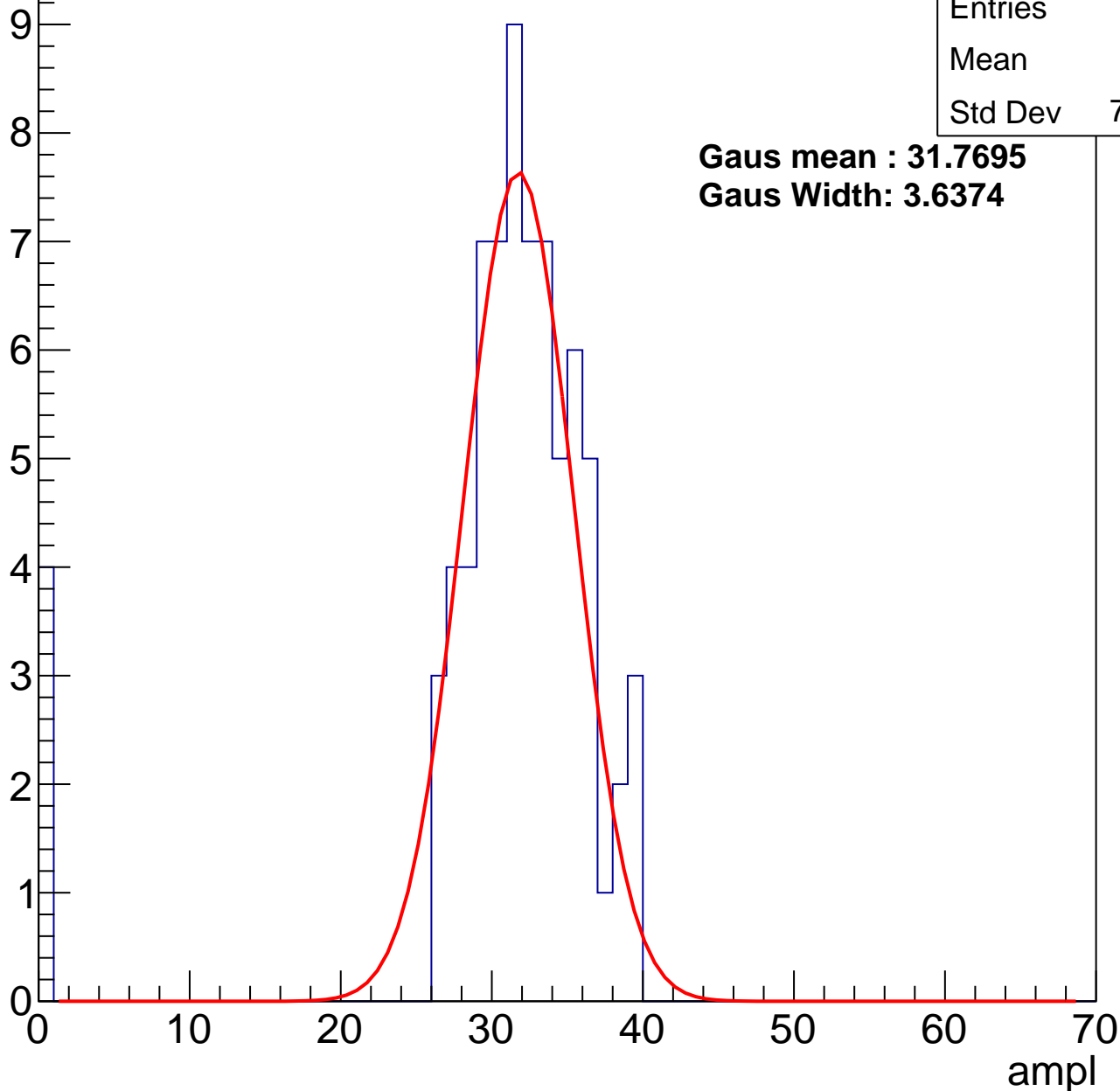
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.2
Std Dev	7.913

Gaus mean : 31.7695

Gaus Width: 3.6374



B1L101S, U26-ch85, adc1

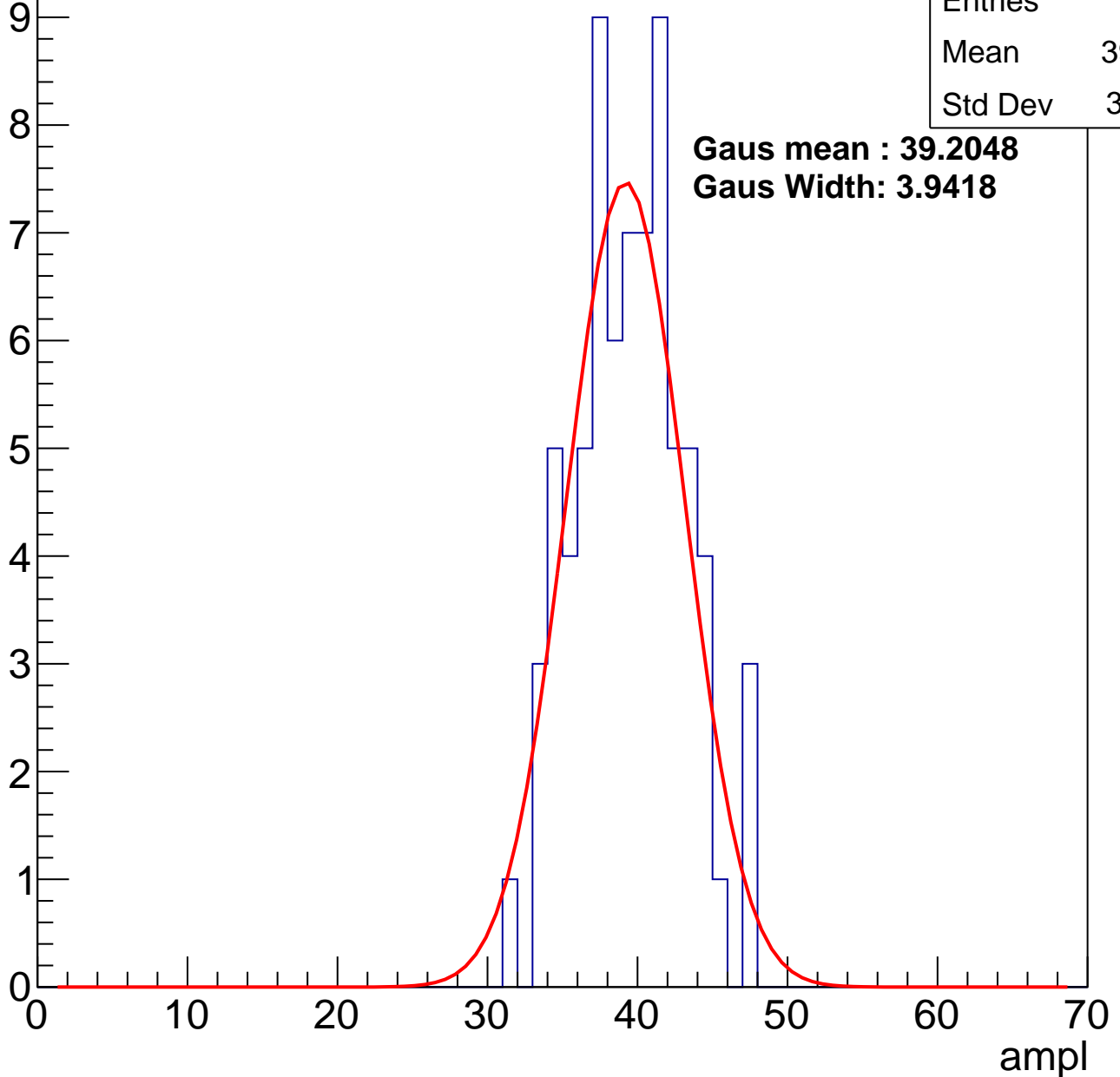
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	39.05
Std Dev	3.571

Gaus mean : 39.2048

Gaus Width: 3.9418



B1L101S, U26-ch85, adc2

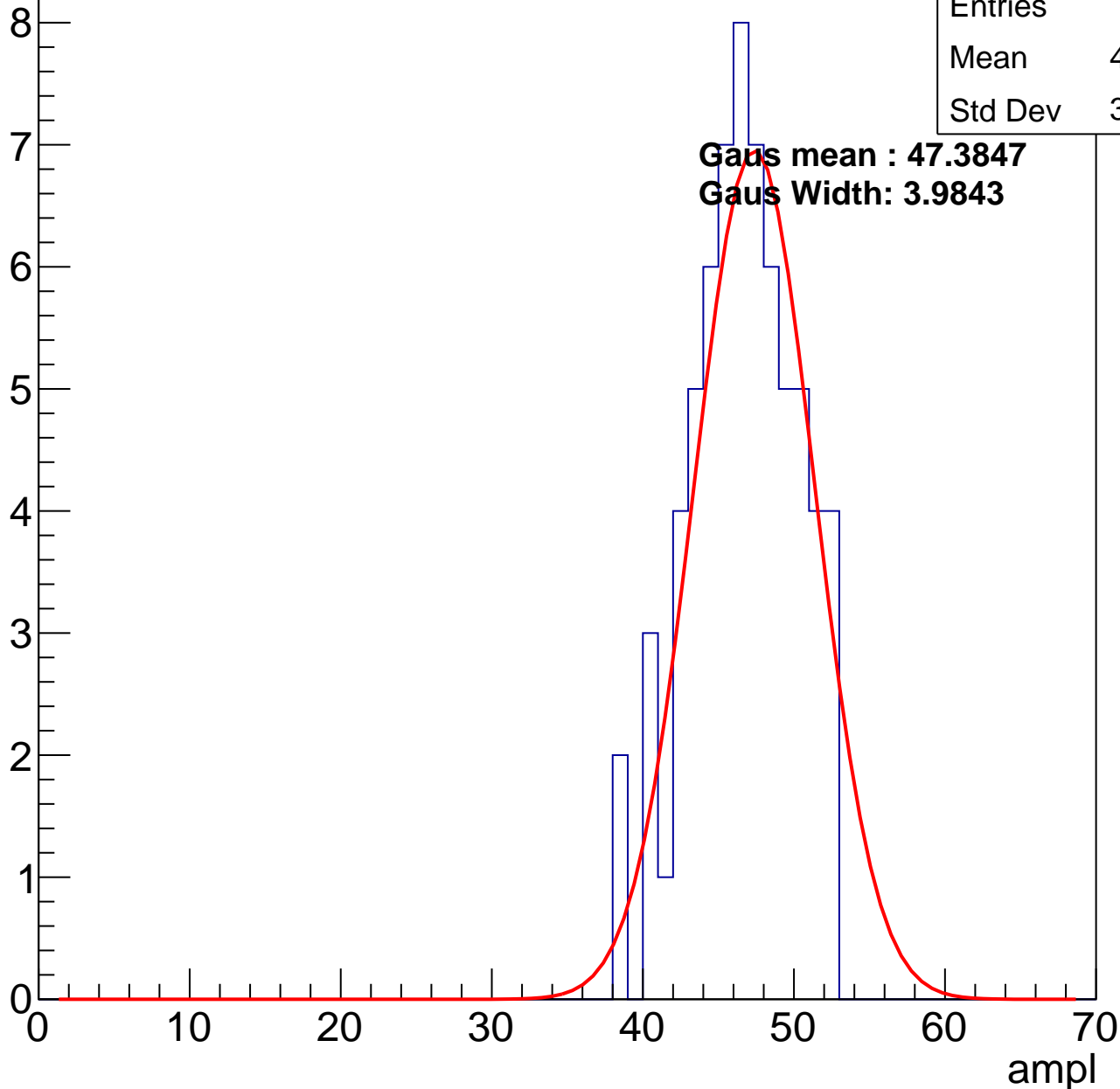
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	46.13
Std Dev	3.455

Gaus mean : 47.3847

Gaus Width: 3.9843



B1L101S, U26-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	52.89
Std Dev	3.893

Entry

10

8

6

4

2

0

0

10

20

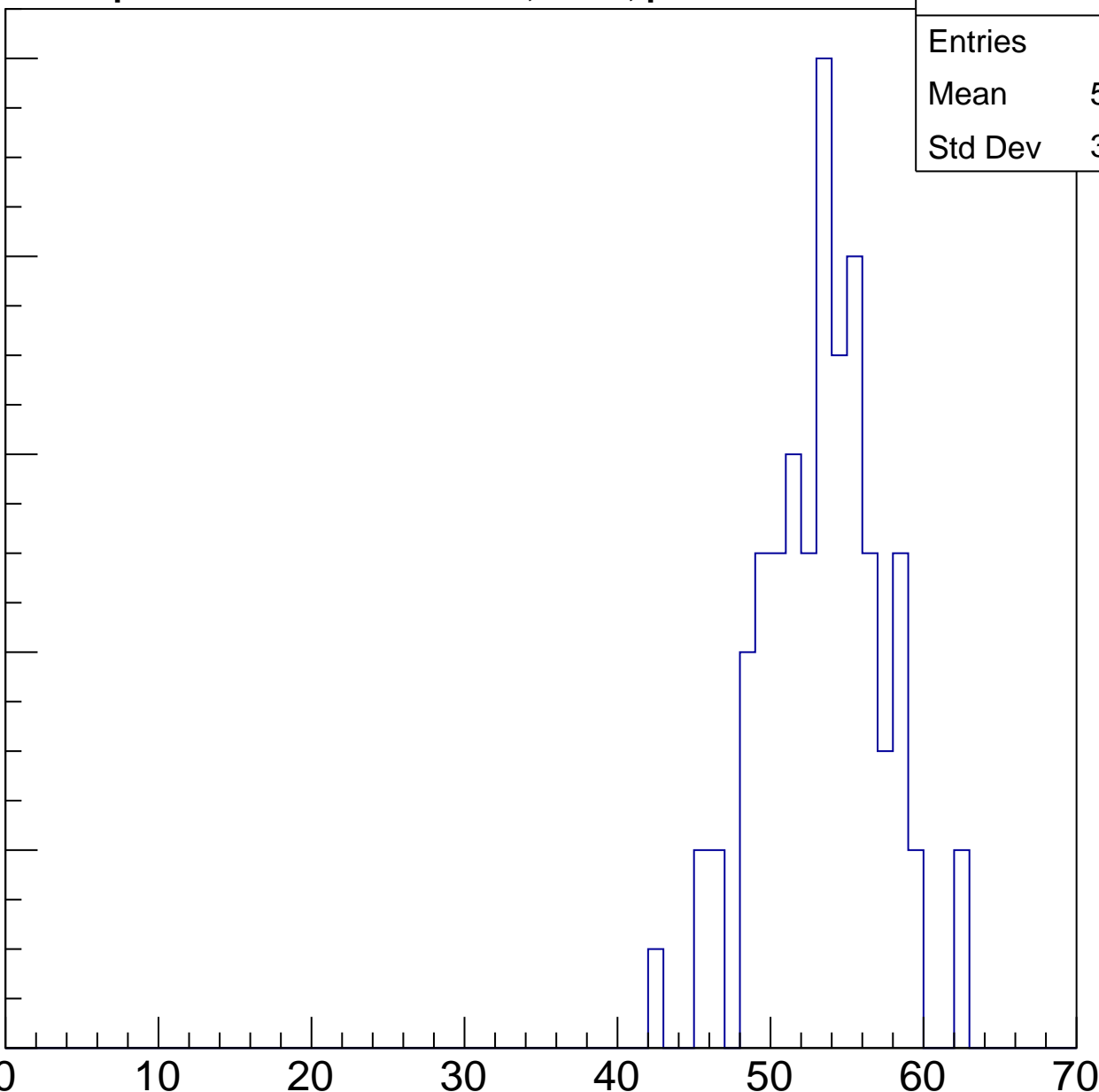
30

40

50

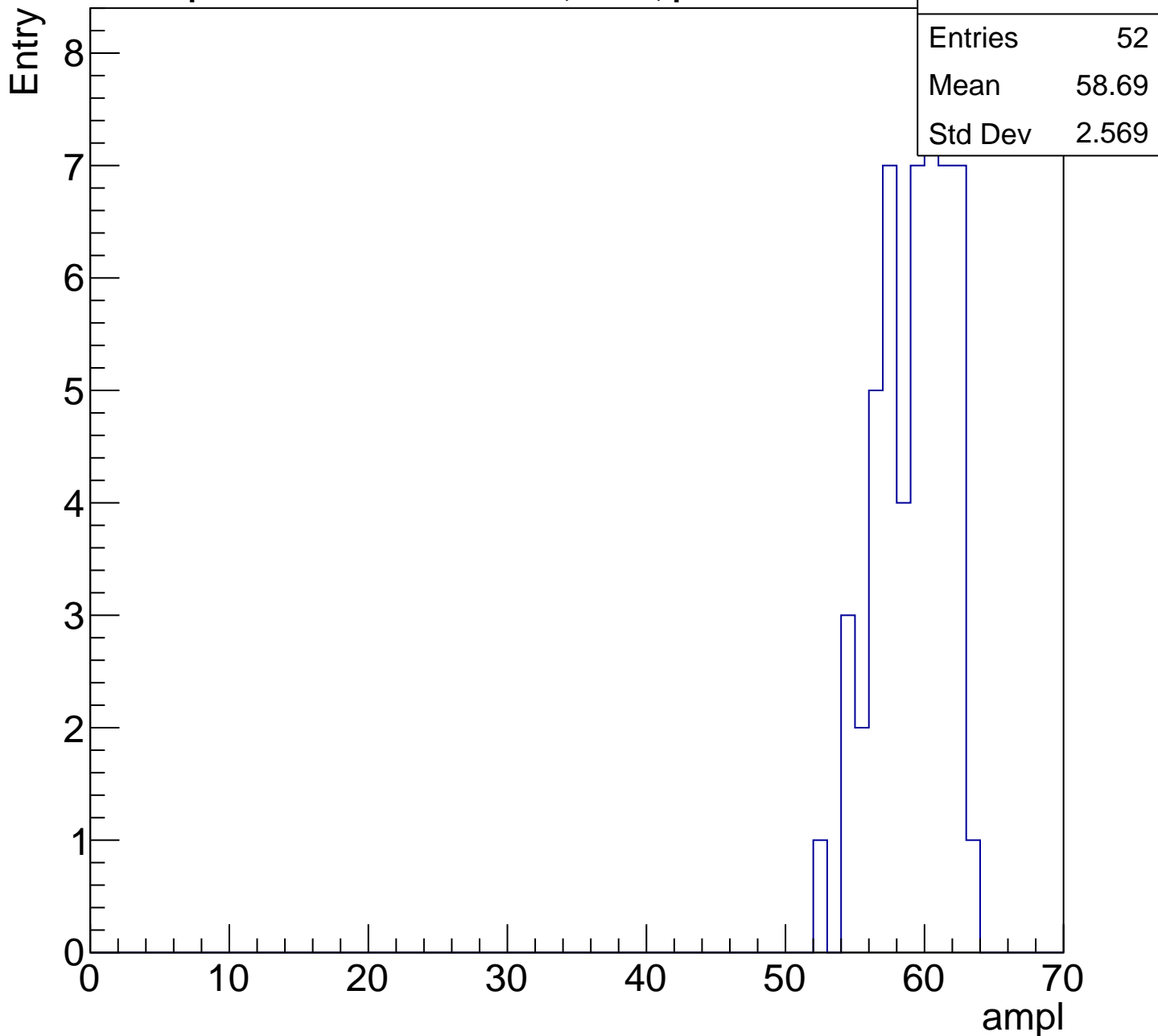
60

ampl



B1L101S, U26-ch85, adc4

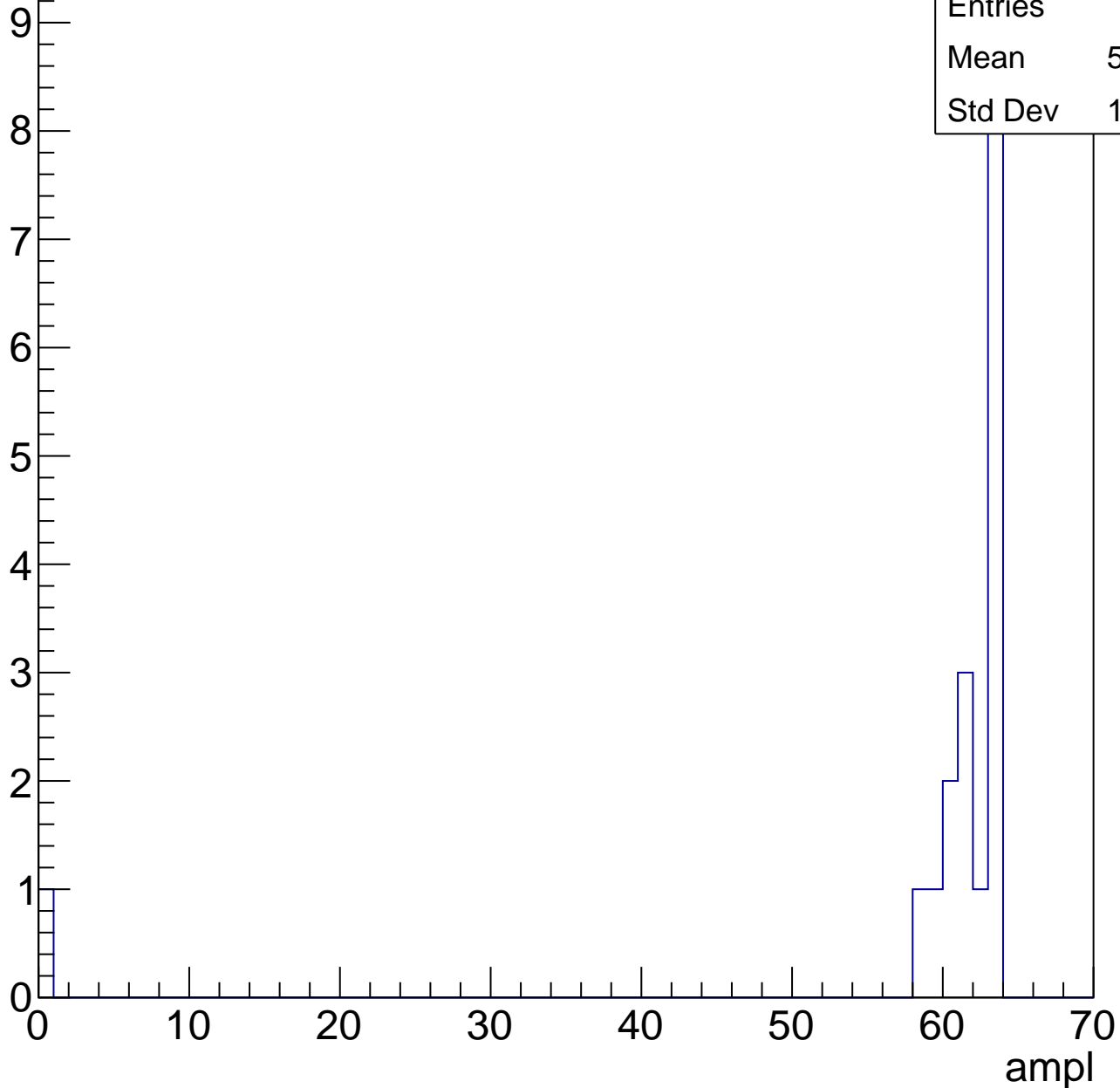
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch86, adc0

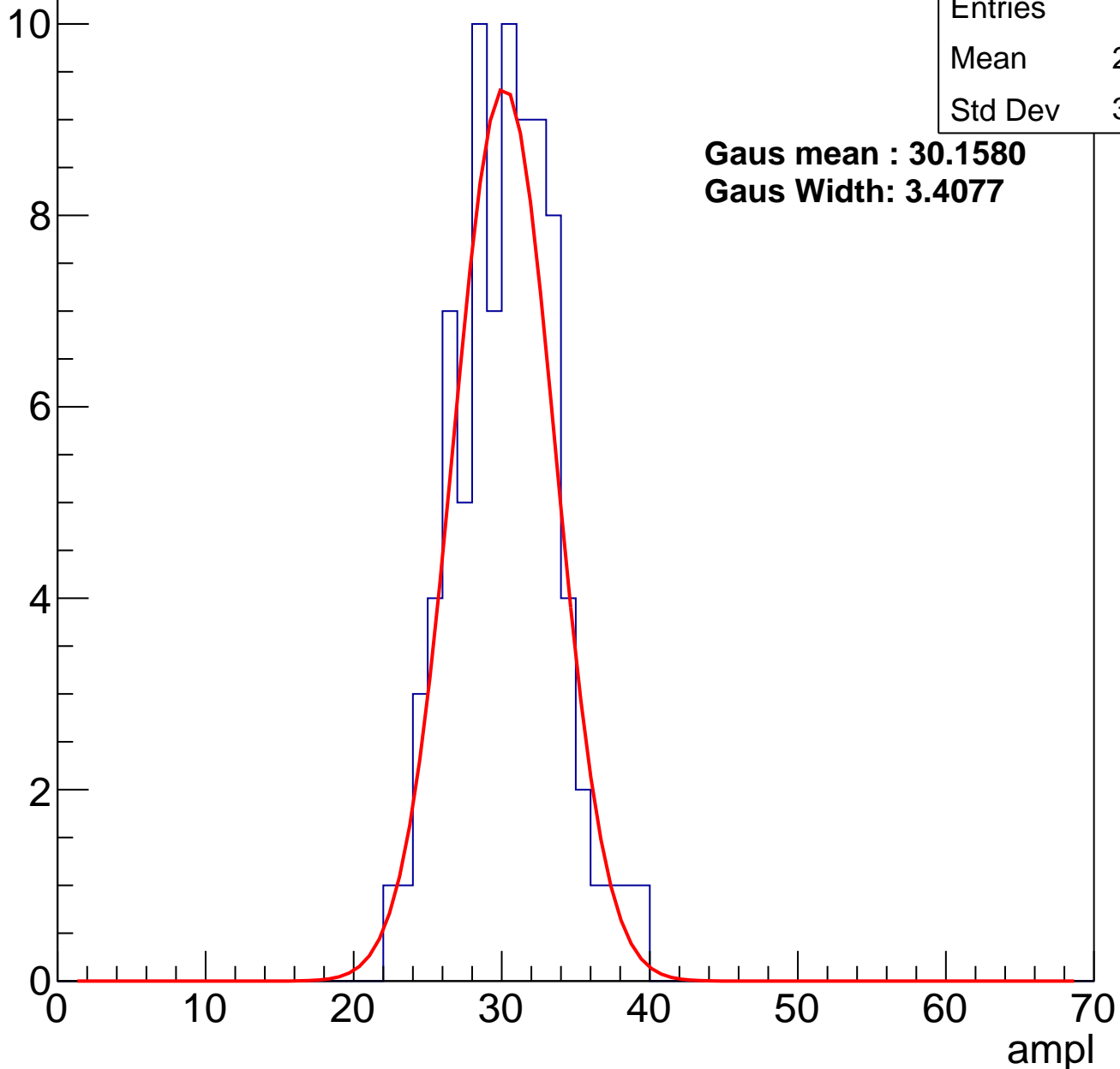
calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	29.81
Std Dev	3.421

Gaus mean : 30.1580

Gaus Width: 3.4077

Entry



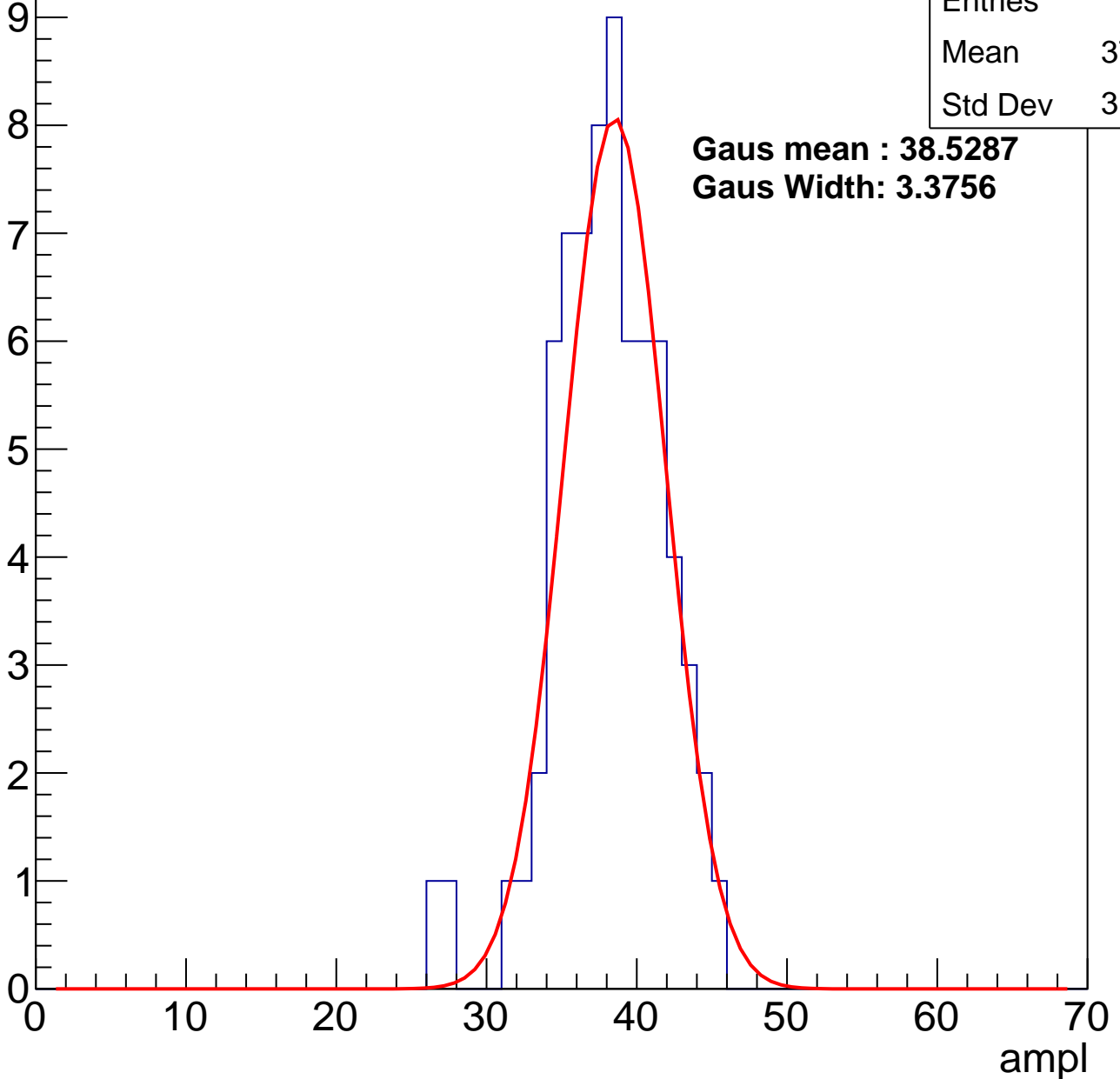
B1L101S, U26-ch86, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.62
Std Dev	3.617

Gaus mean : 38.5287
Gaus Width: 3.3756

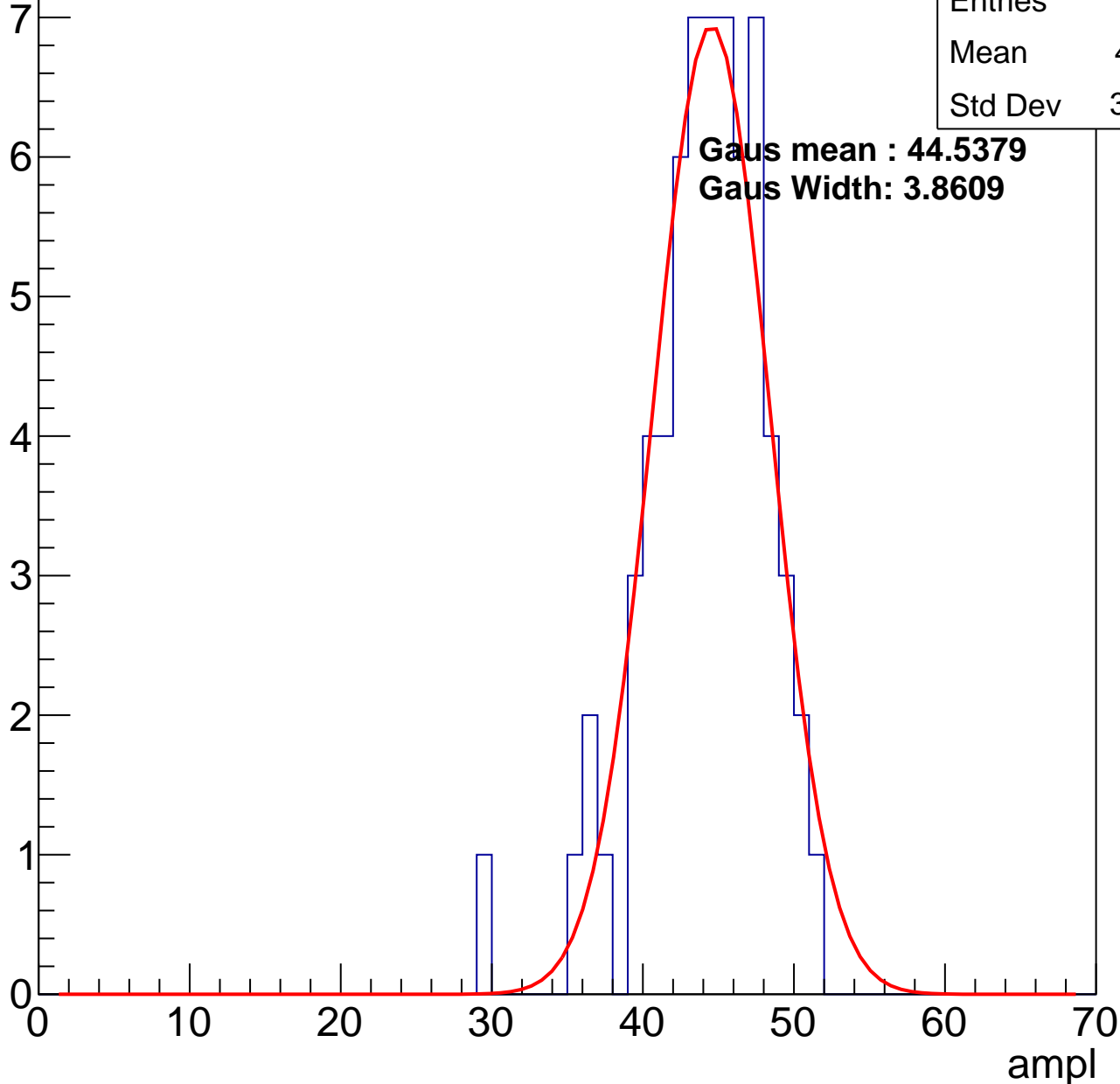


B1L101S, U26-ch86, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.71
Std Dev	3.965

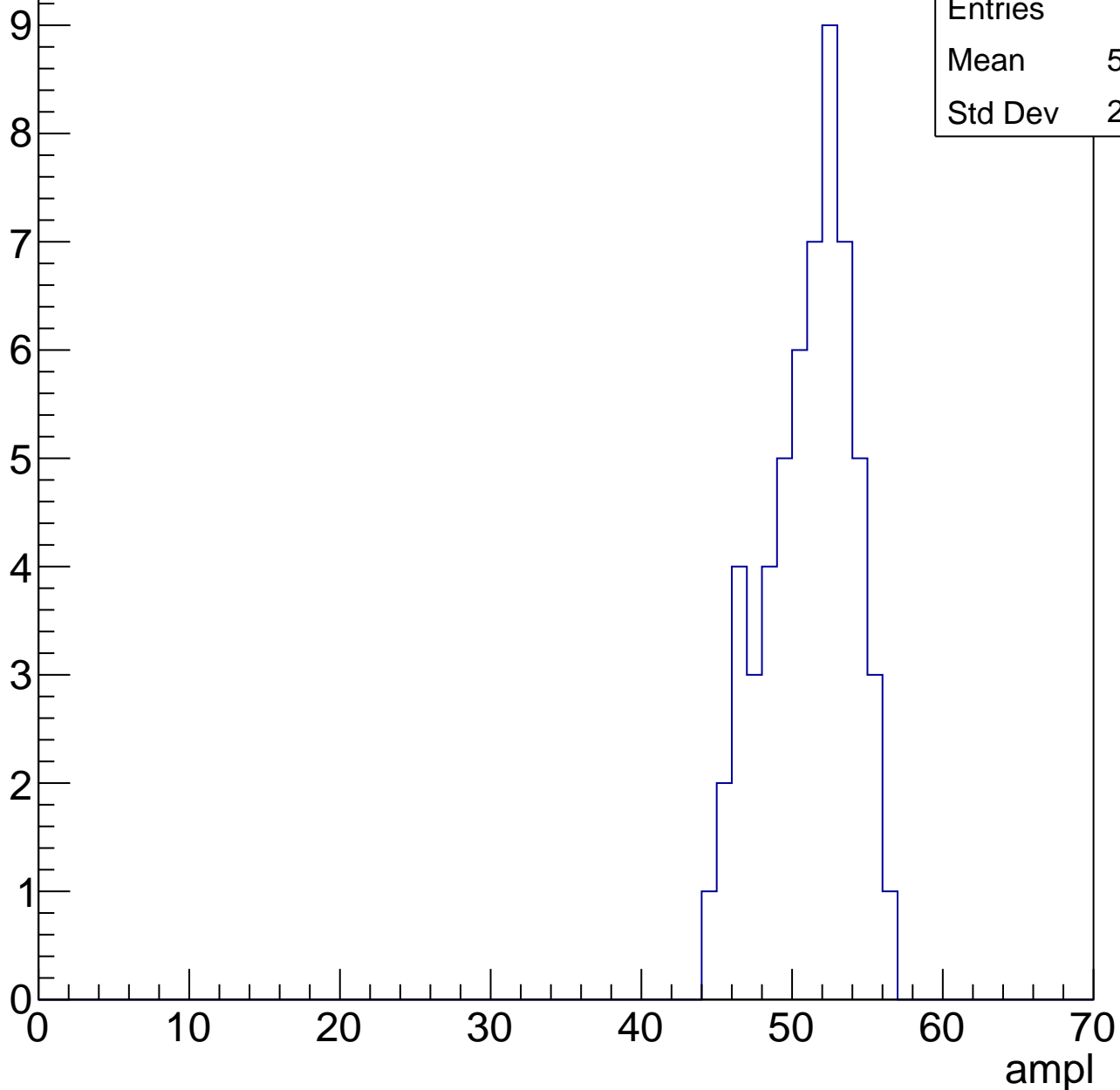


B1L101S, U26-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	50.58
Std Dev	2.889

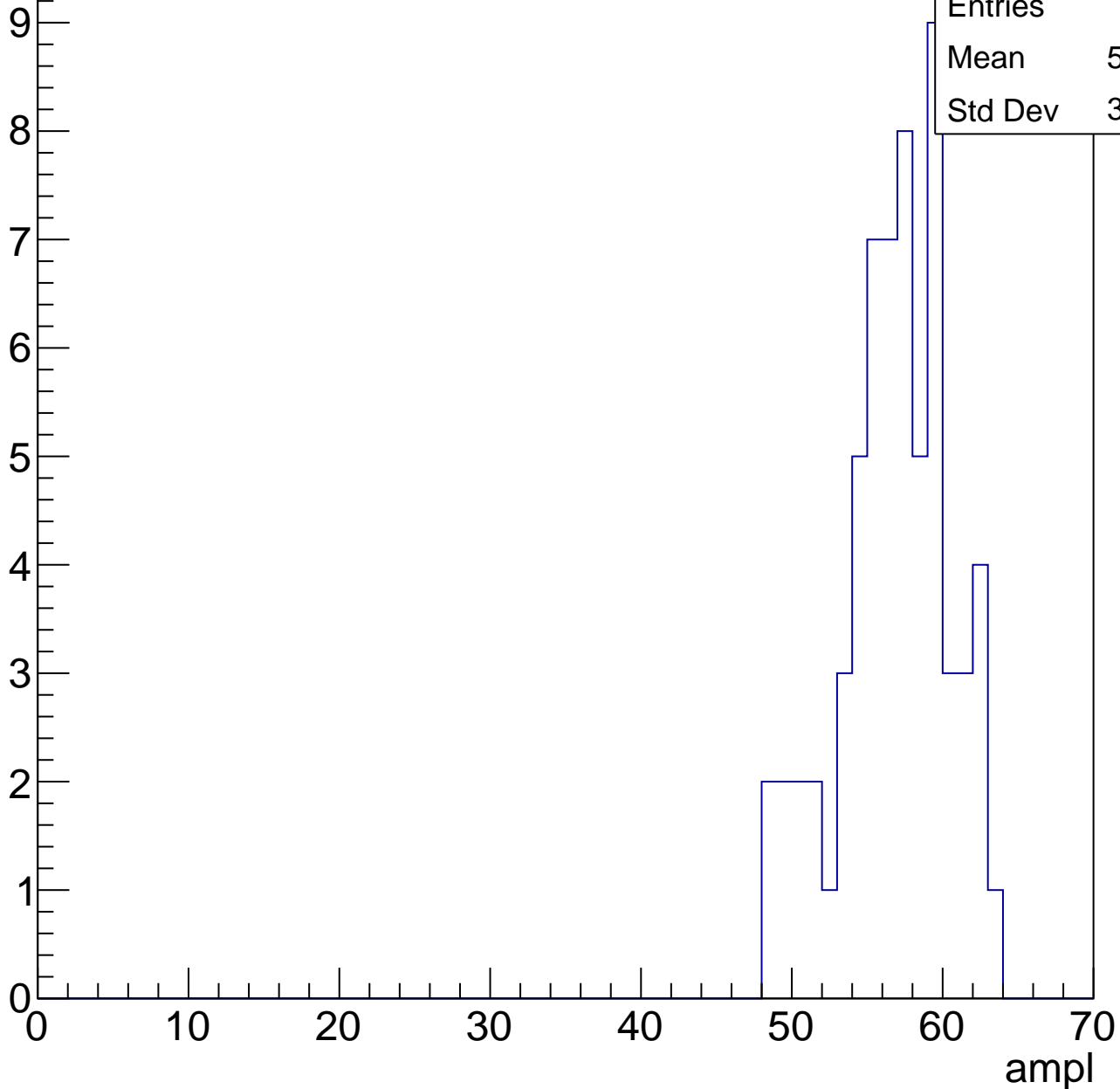


B1L101S, U26-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	56.33
Std Dev	3.614

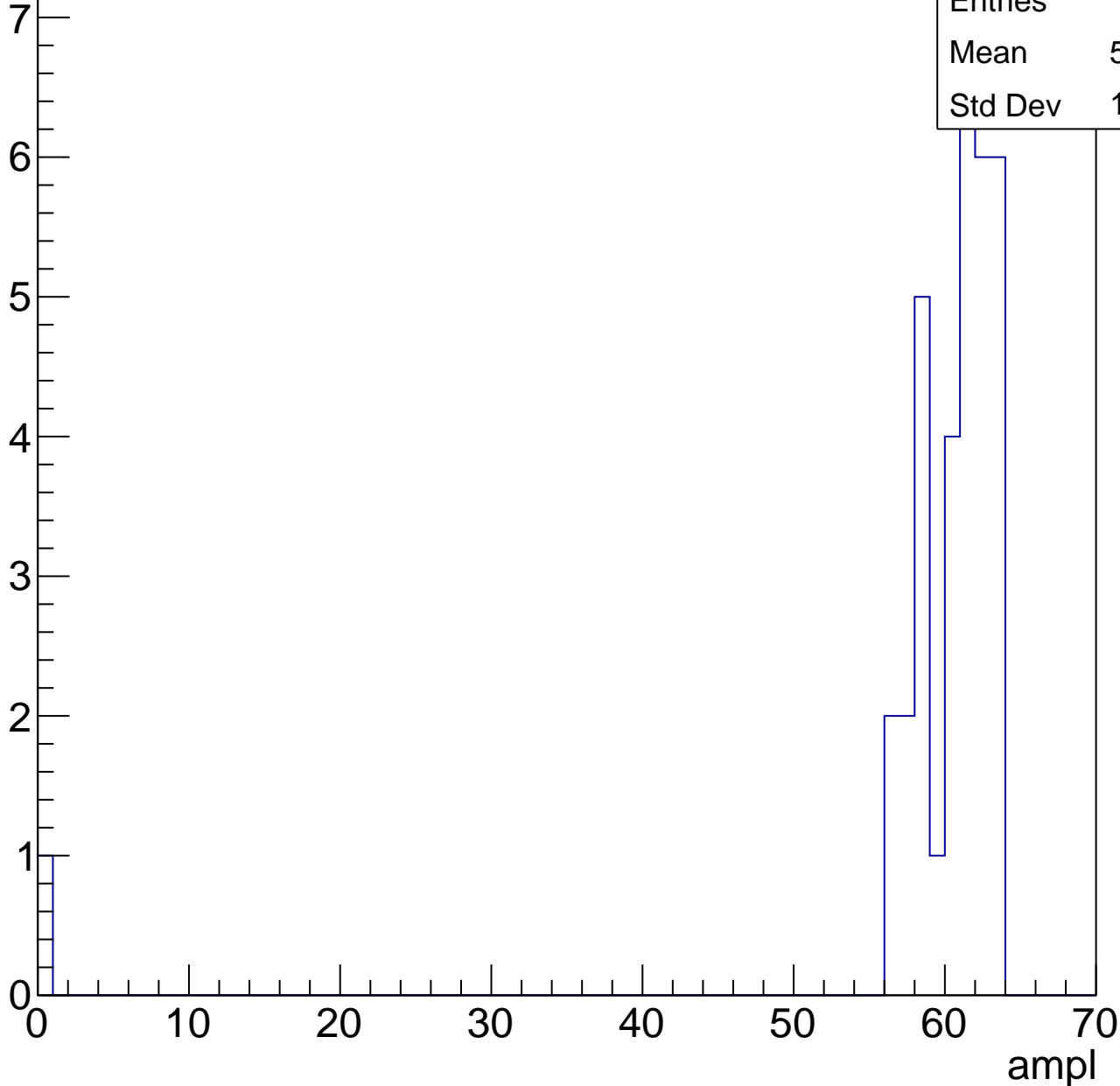


B1L101S, U26-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

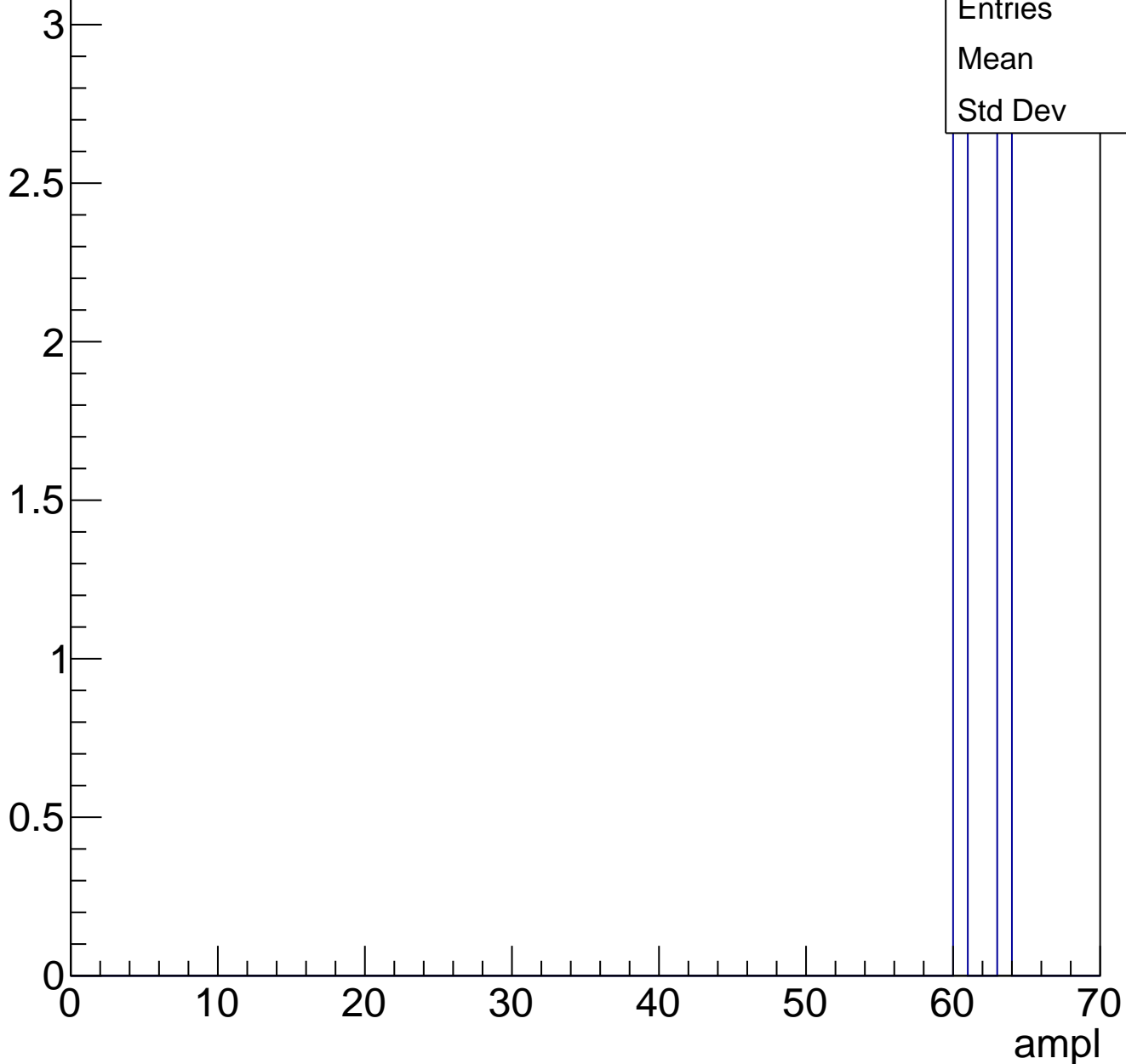
Entries	34
Mean	58.59
Std Dev	10.42



B1L101S, U26-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch87, adc0

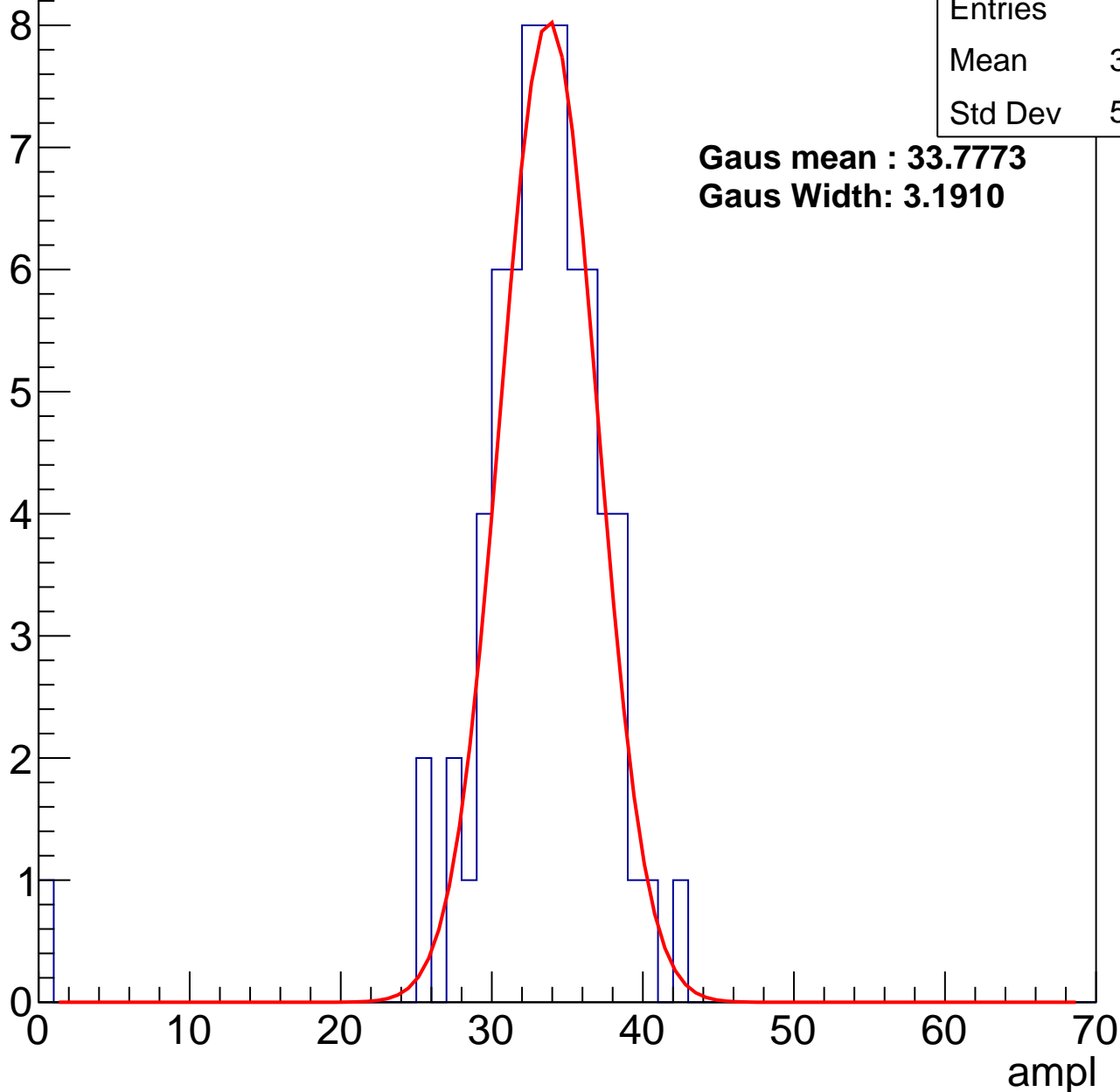
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	32.65
Std Dev	5.208

Gaus mean : 33.7773

Gaus Width: 3.1910



B1L101S, U26-ch87, adc1

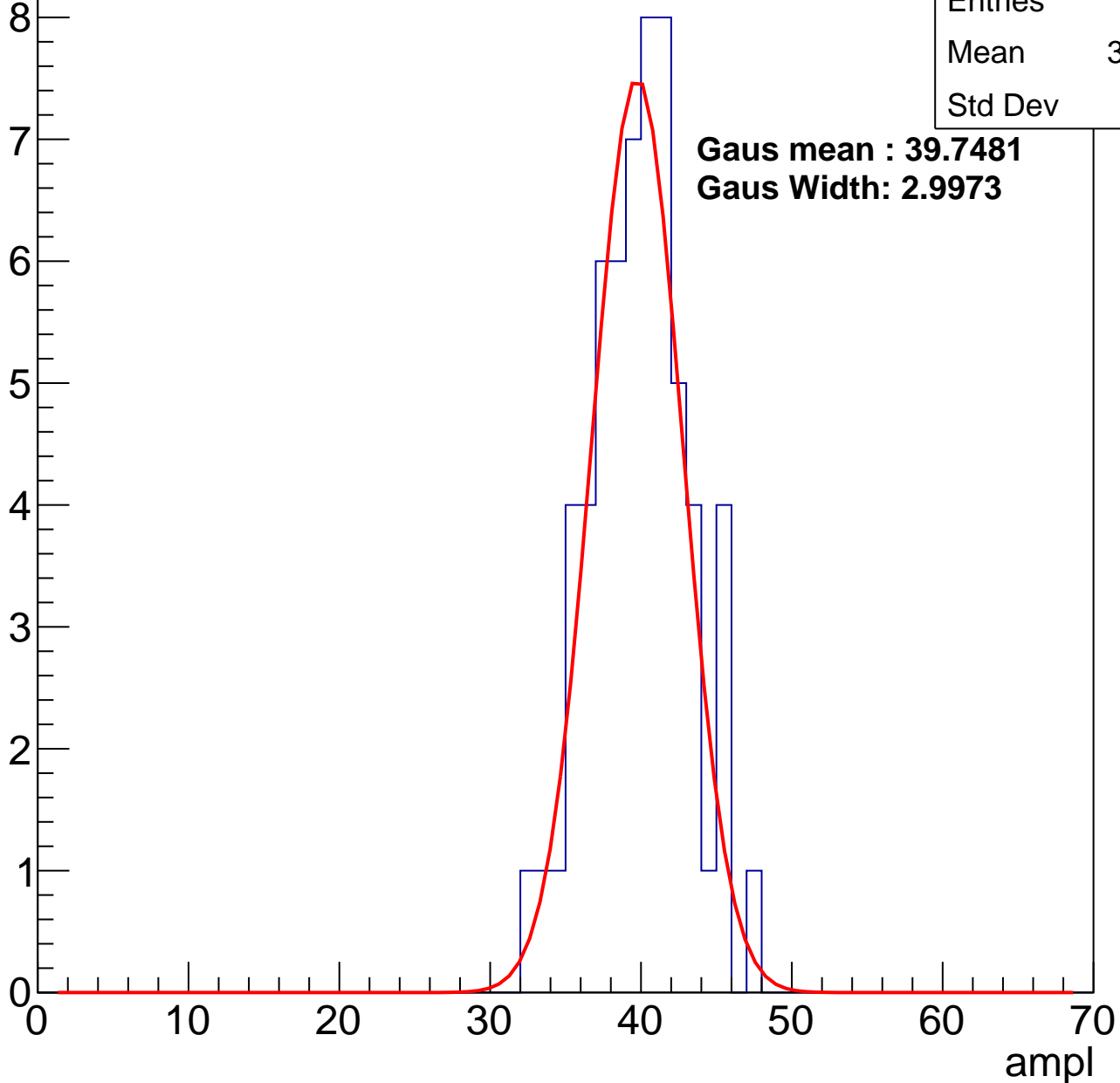
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	39.46
Std Dev	3.16

Gaus mean : 39.7481

Gaus Width: 2.9973



B1L101S, U26-ch87, adc2

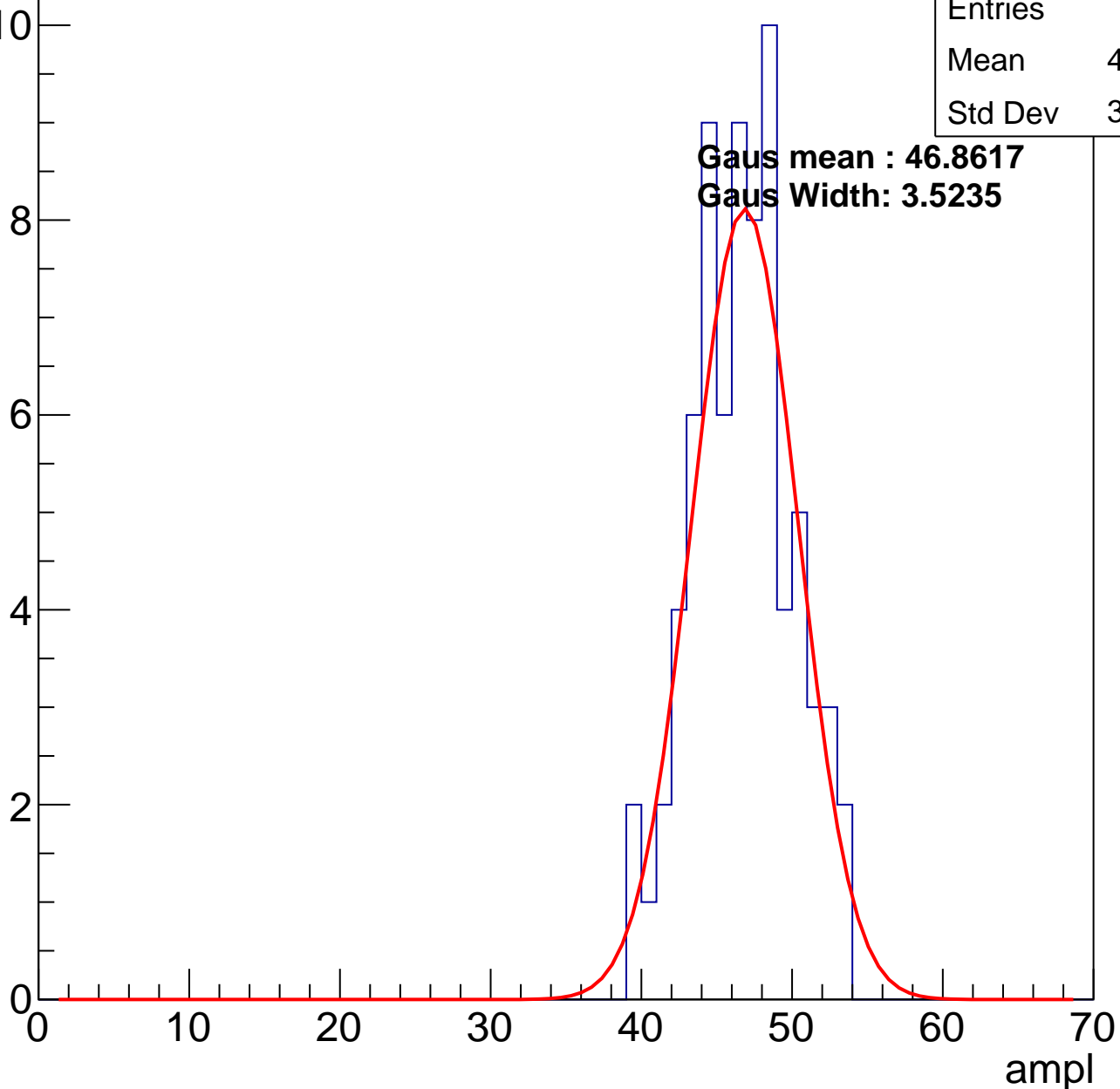
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	46.26
Std Dev	3.255

Gaus mean : 46.8617

Gaus Width: 3.5235

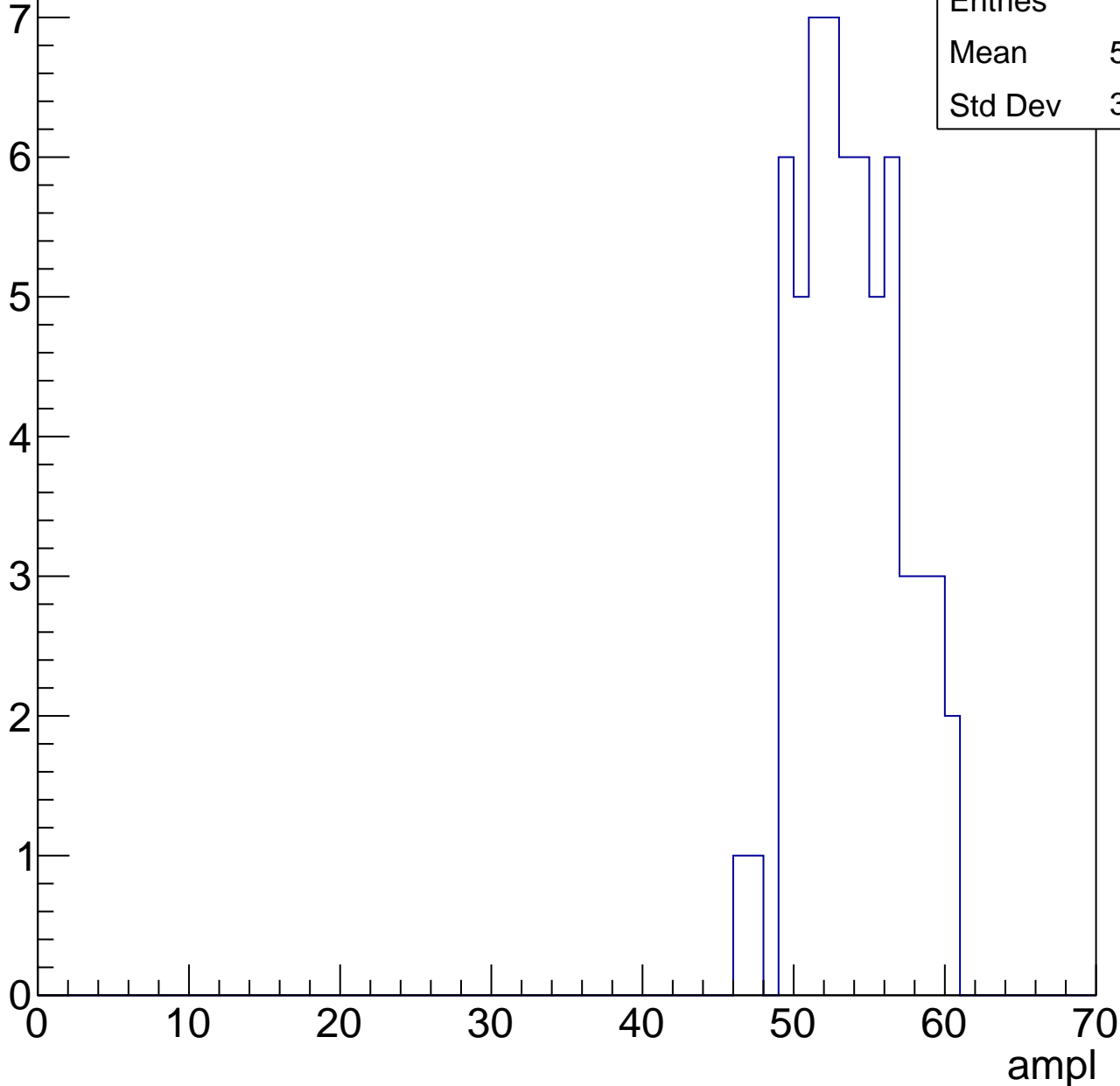


B1L101S, U26-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	53.33
Std Dev	3.298



B1L101S, U26-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 58.4

Std Dev 2.837

ampl

0

10

20

30

40

50

60

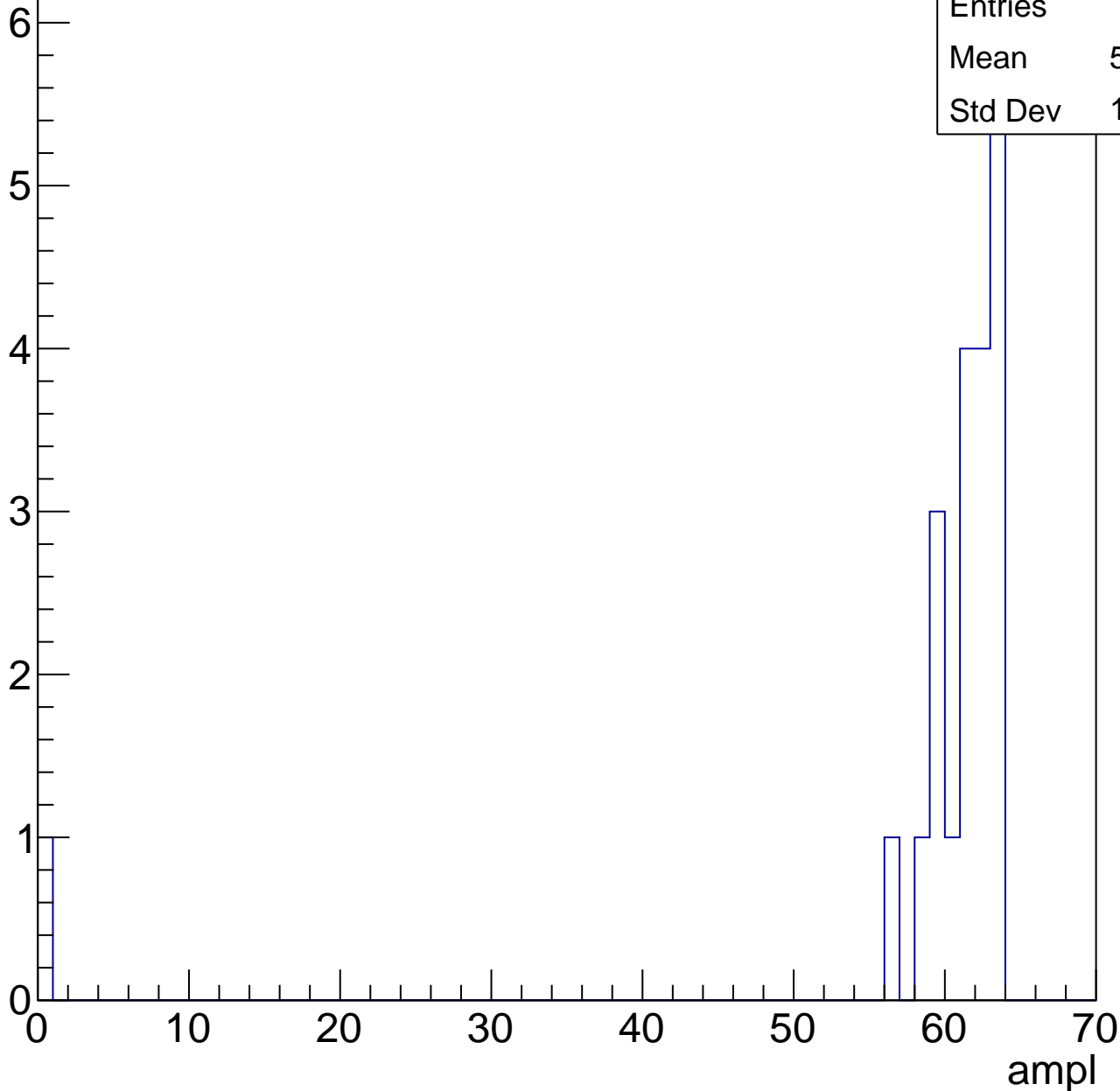
70

B1L101S, U26-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.14
Std Dev	13.14



B1L101S, U26-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch88, adc0

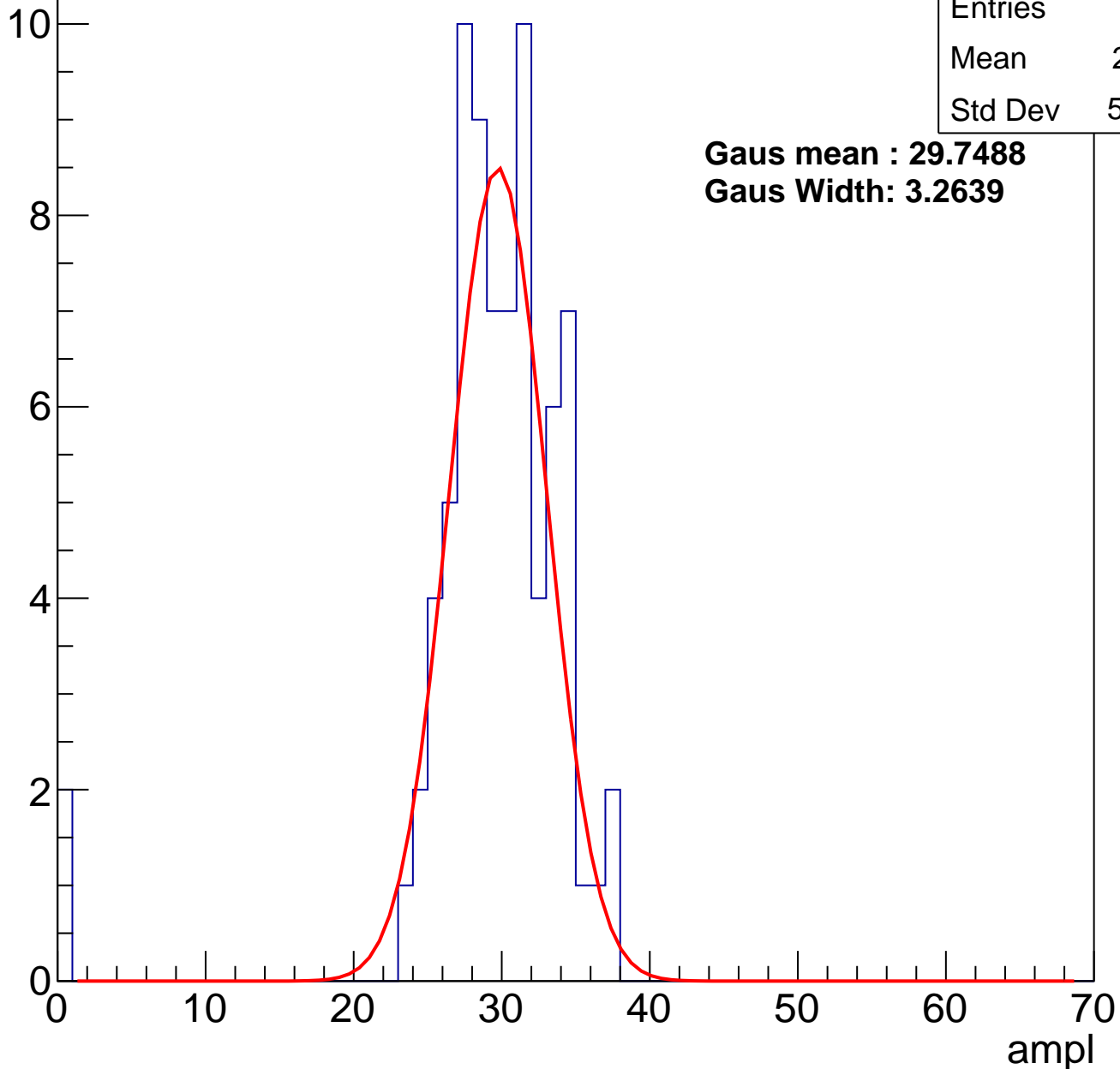
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	28.91
Std Dev	5.653

Gaus mean : 29.7488

Gaus Width: 3.2639

Entry



B1L101S, U26-ch88, adc1

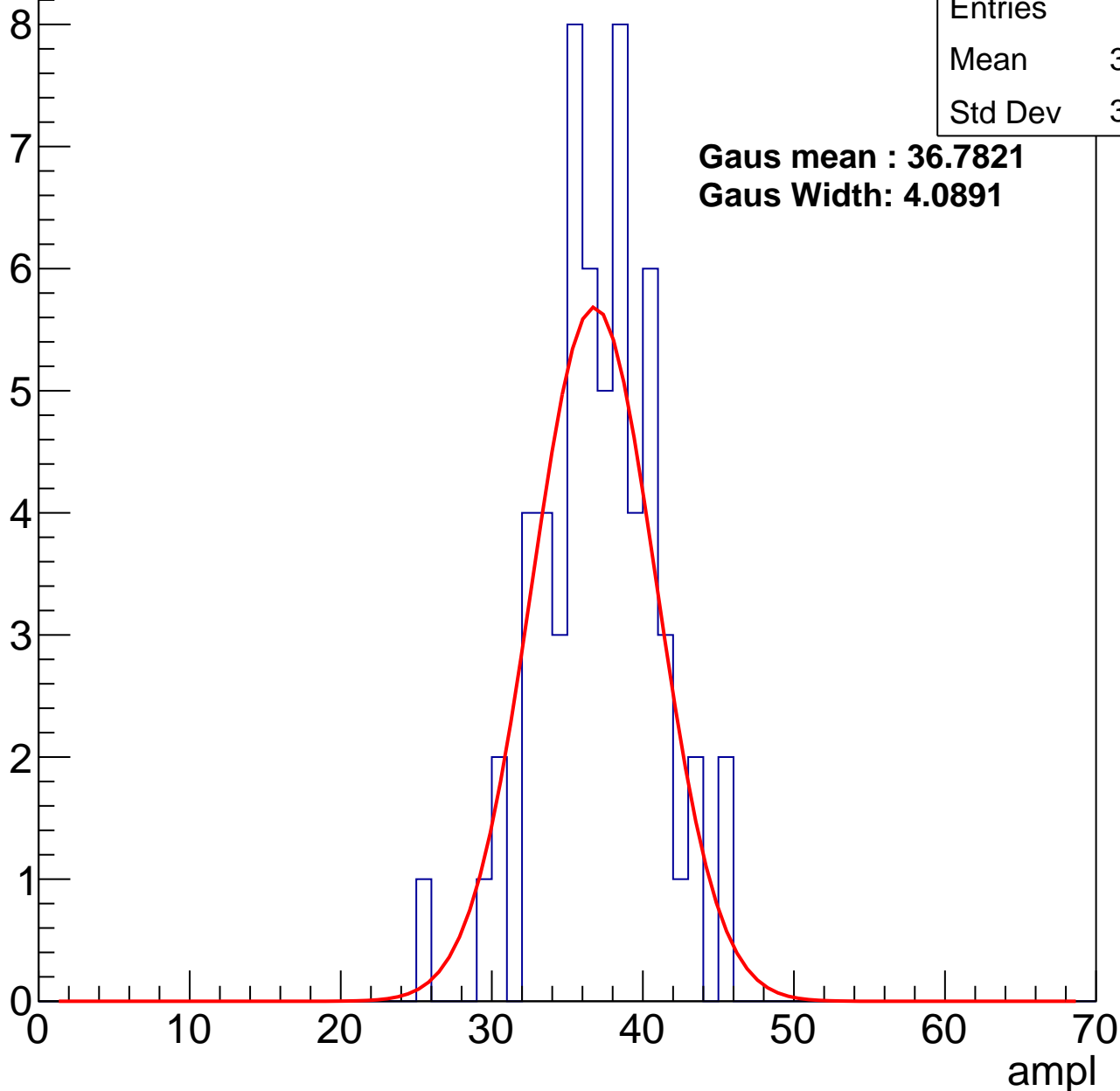
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.63
Std Dev	3.825

Gaus mean : 36.7821

Gaus Width: 4.0891



B1L101S, U26-ch88, adc2

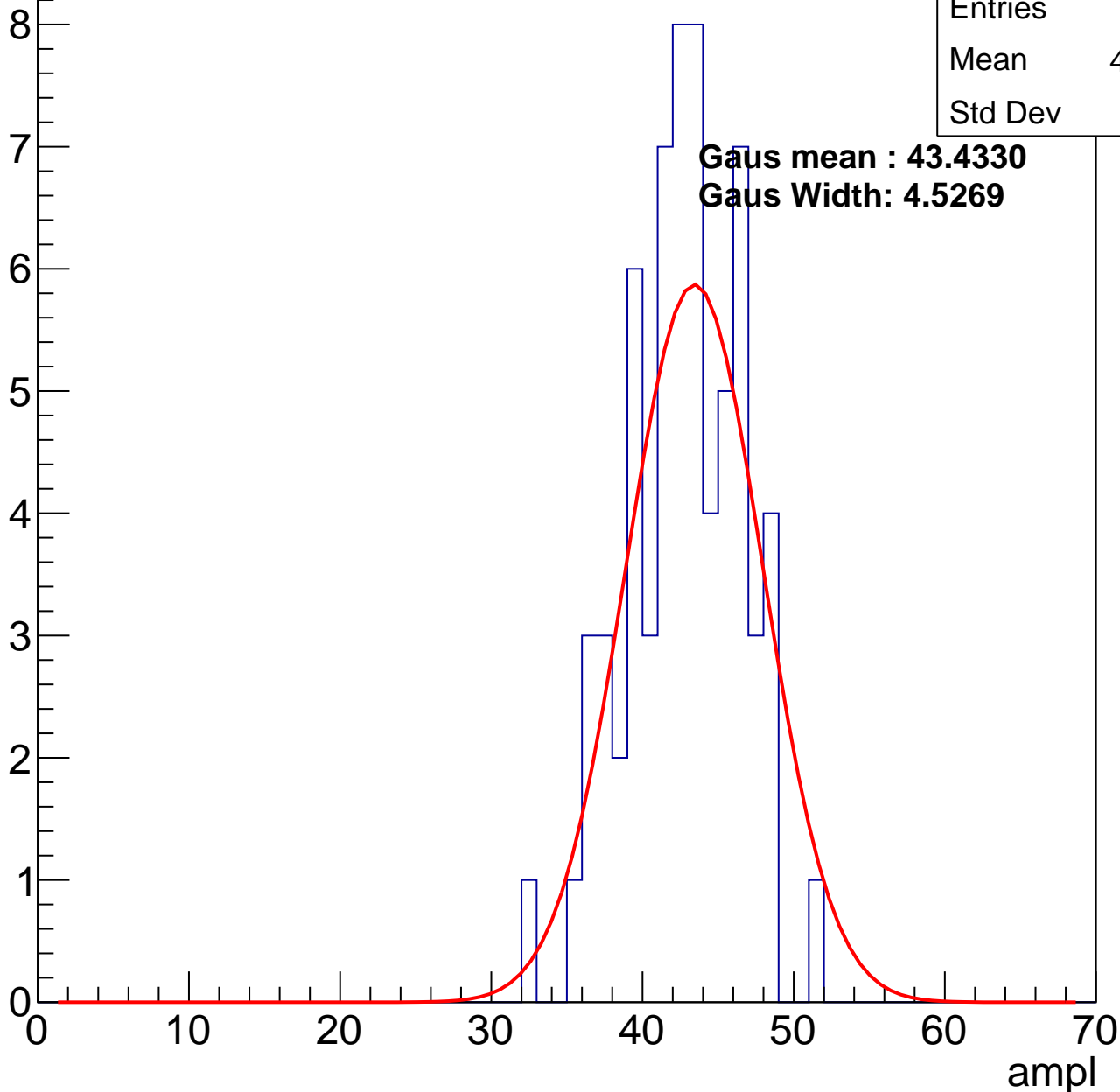
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.27
Std Dev	3.72

Gaus mean : 43.4330

Gaus Width: 4.5269

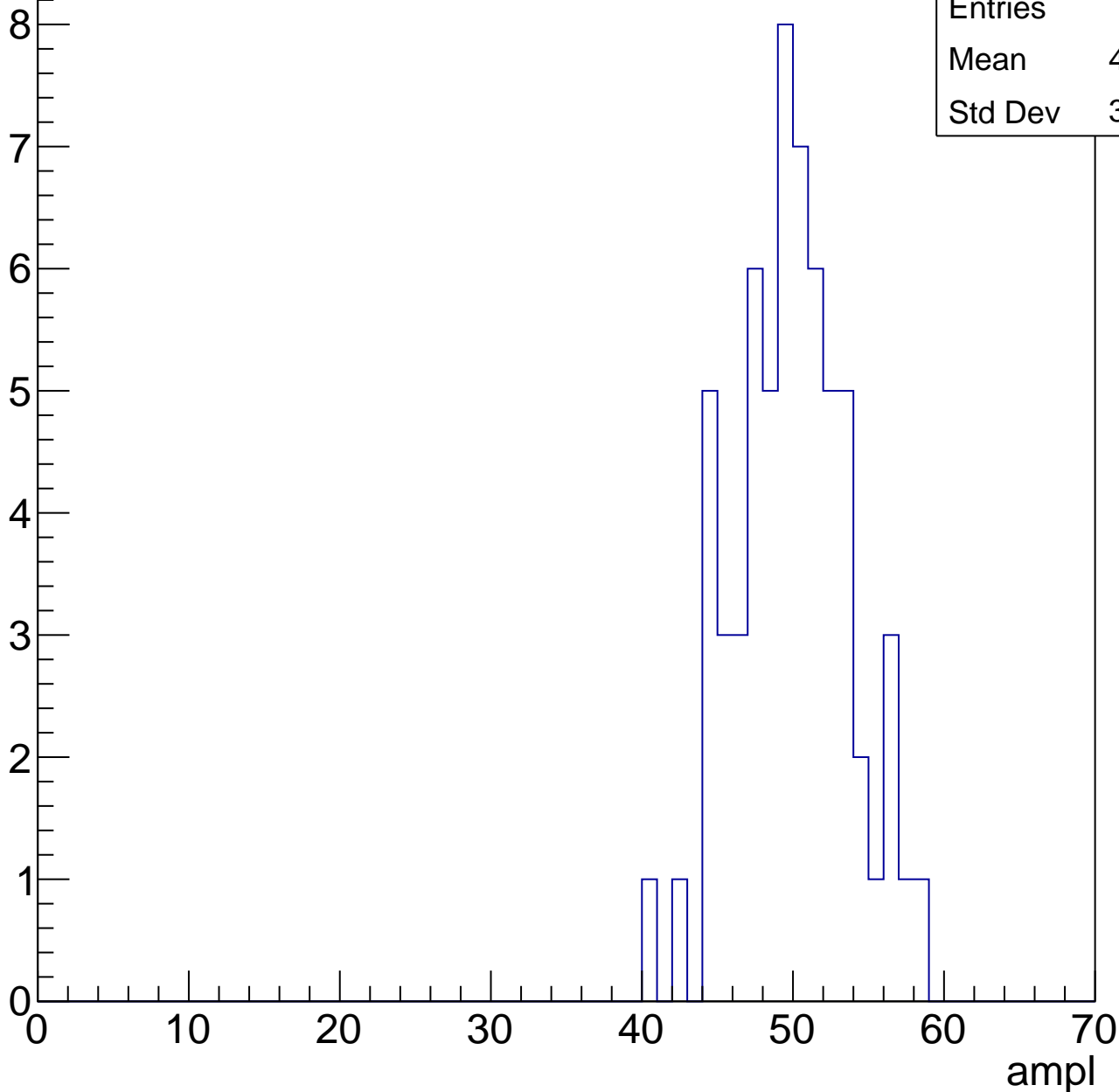


B1L101S, U26-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.46
Std Dev	3.737

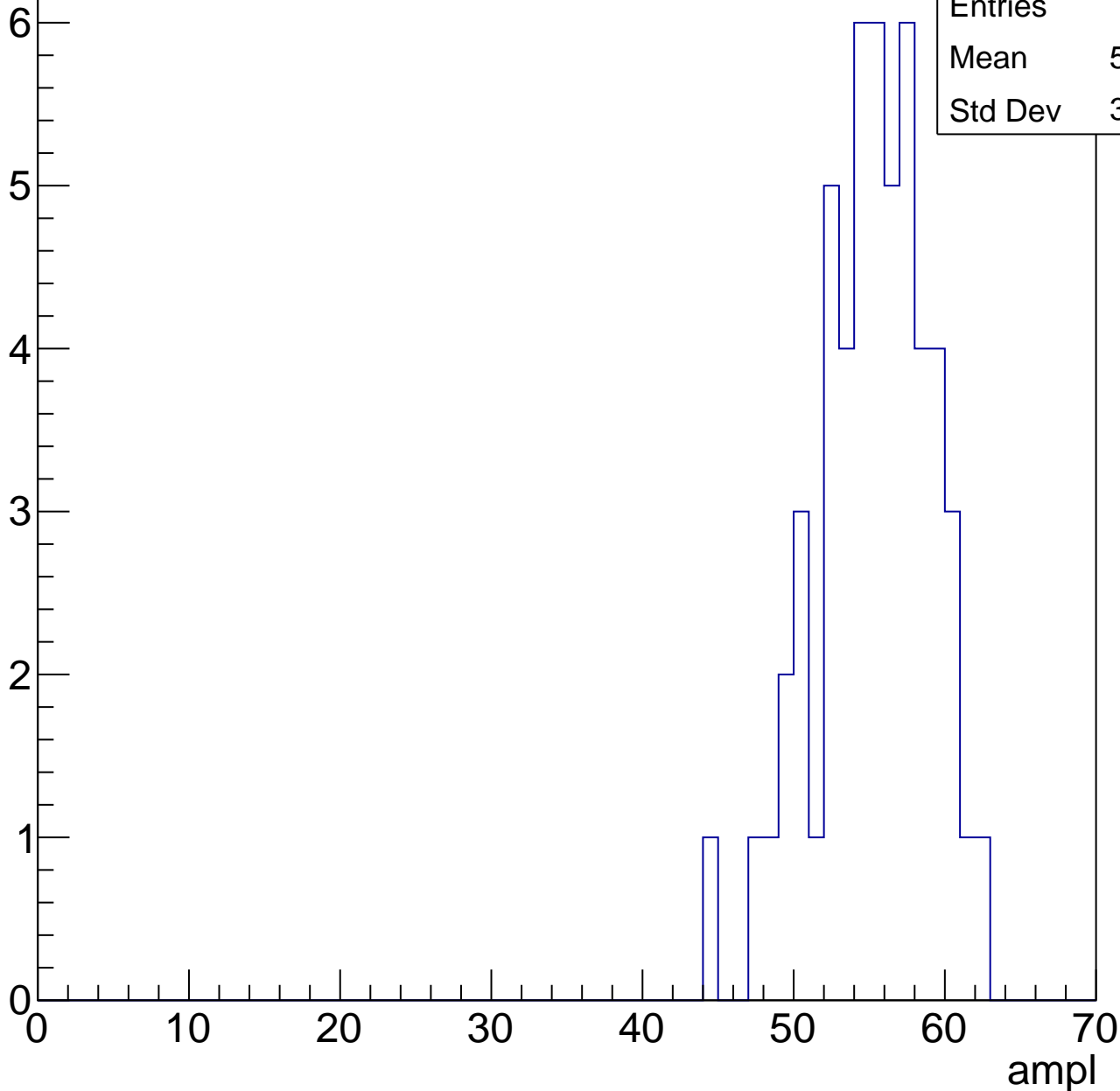


B1L101S, U26-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.76
Std Dev	3.726



B1L101S, U26-ch88, adc5

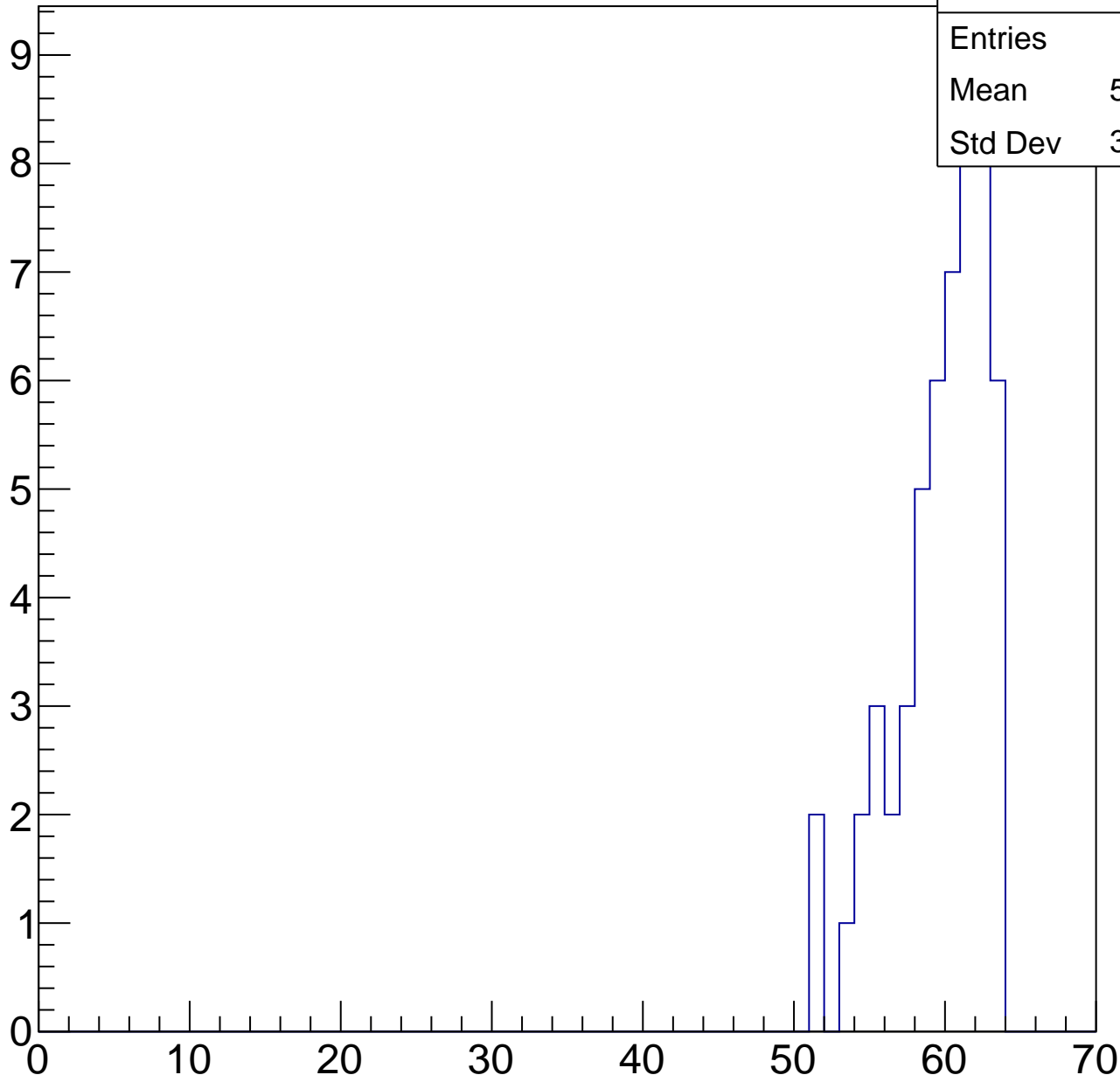
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	59.24
Std Dev	3.073

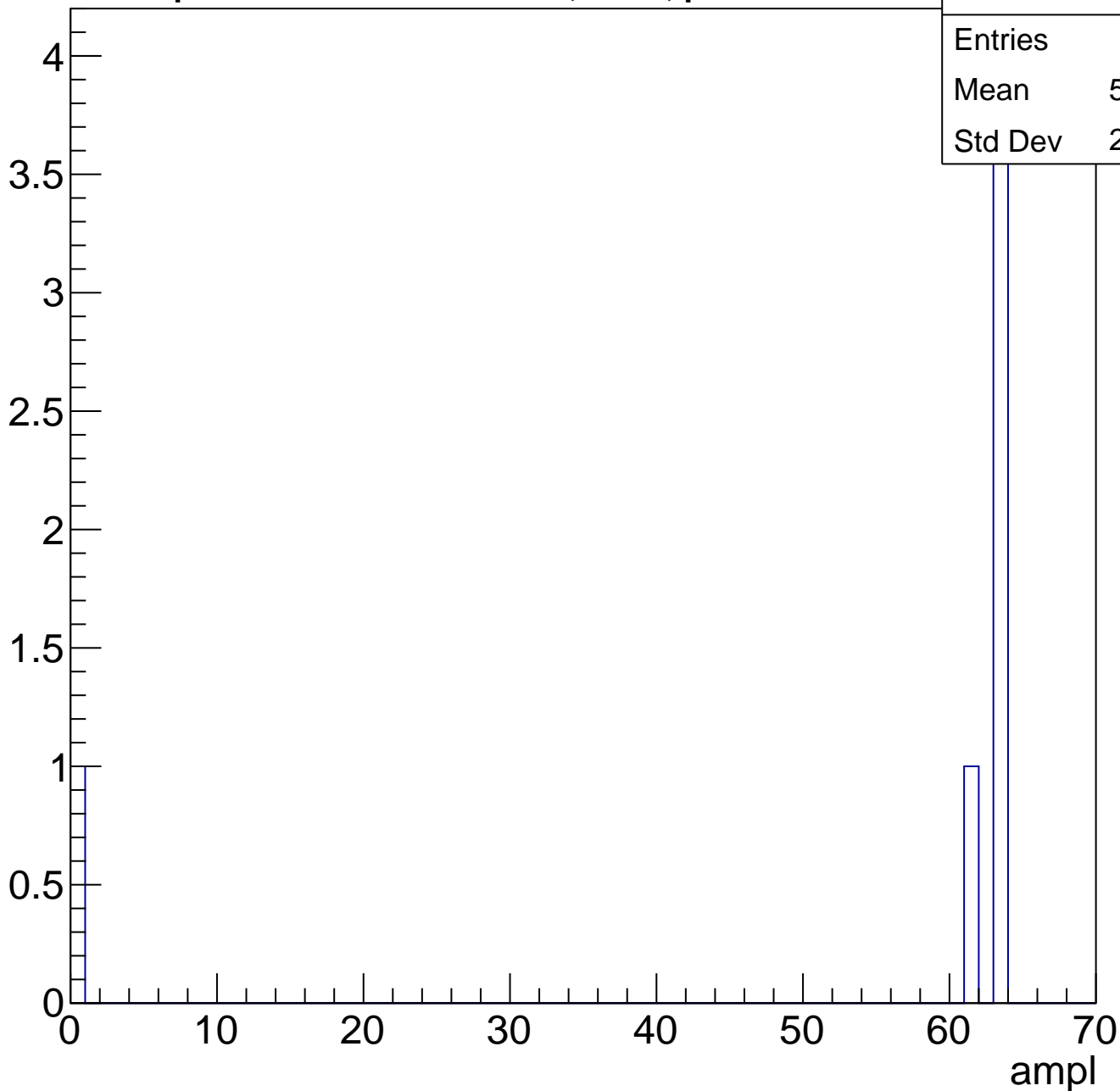
ampl



B1L101S, U26-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch89, adc0

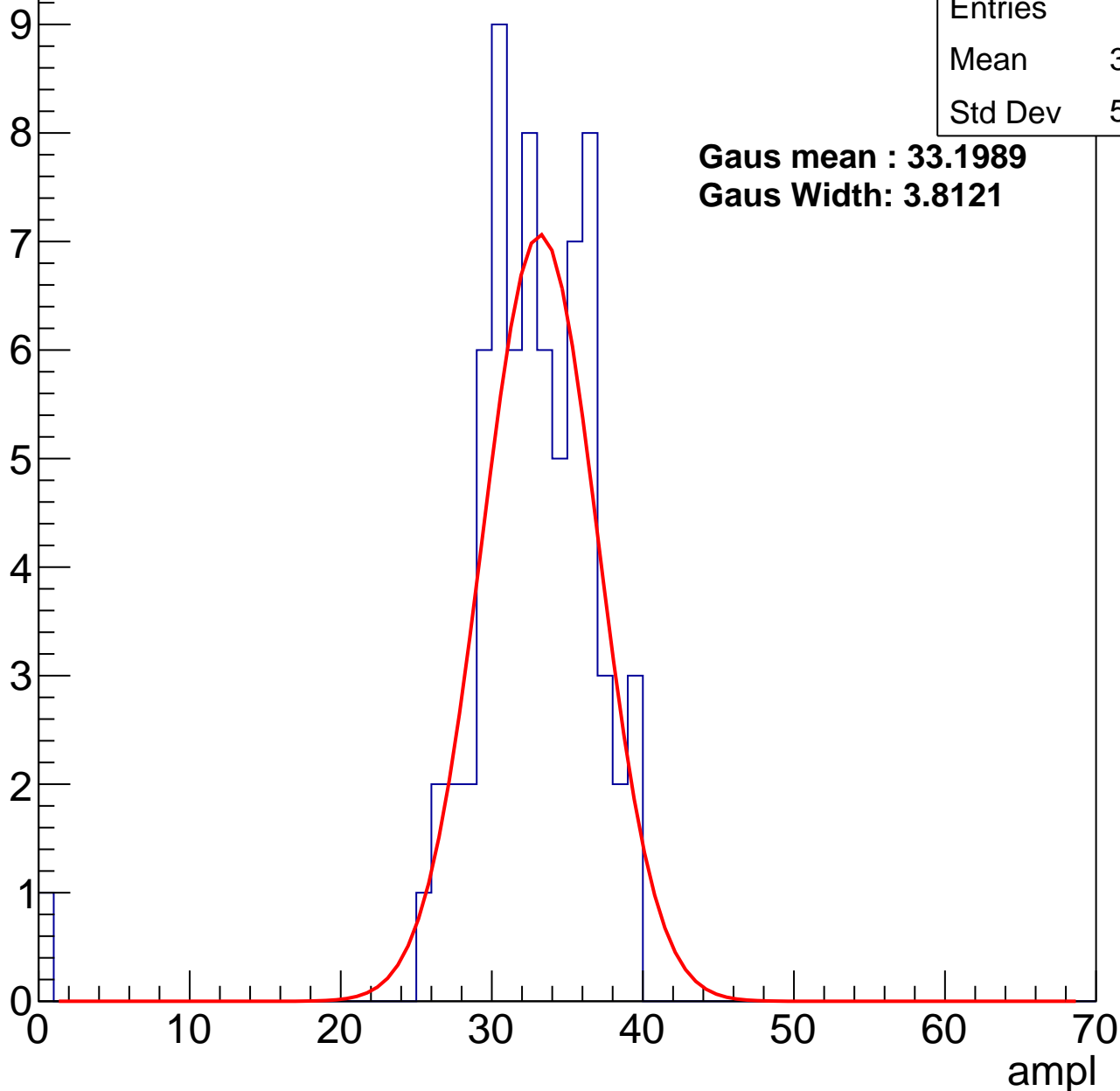
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	32.08
Std Dev	5.082

Gaus mean : 33.1989

Gaus Width: 3.8121



B1L101S, U26-ch89, adc1

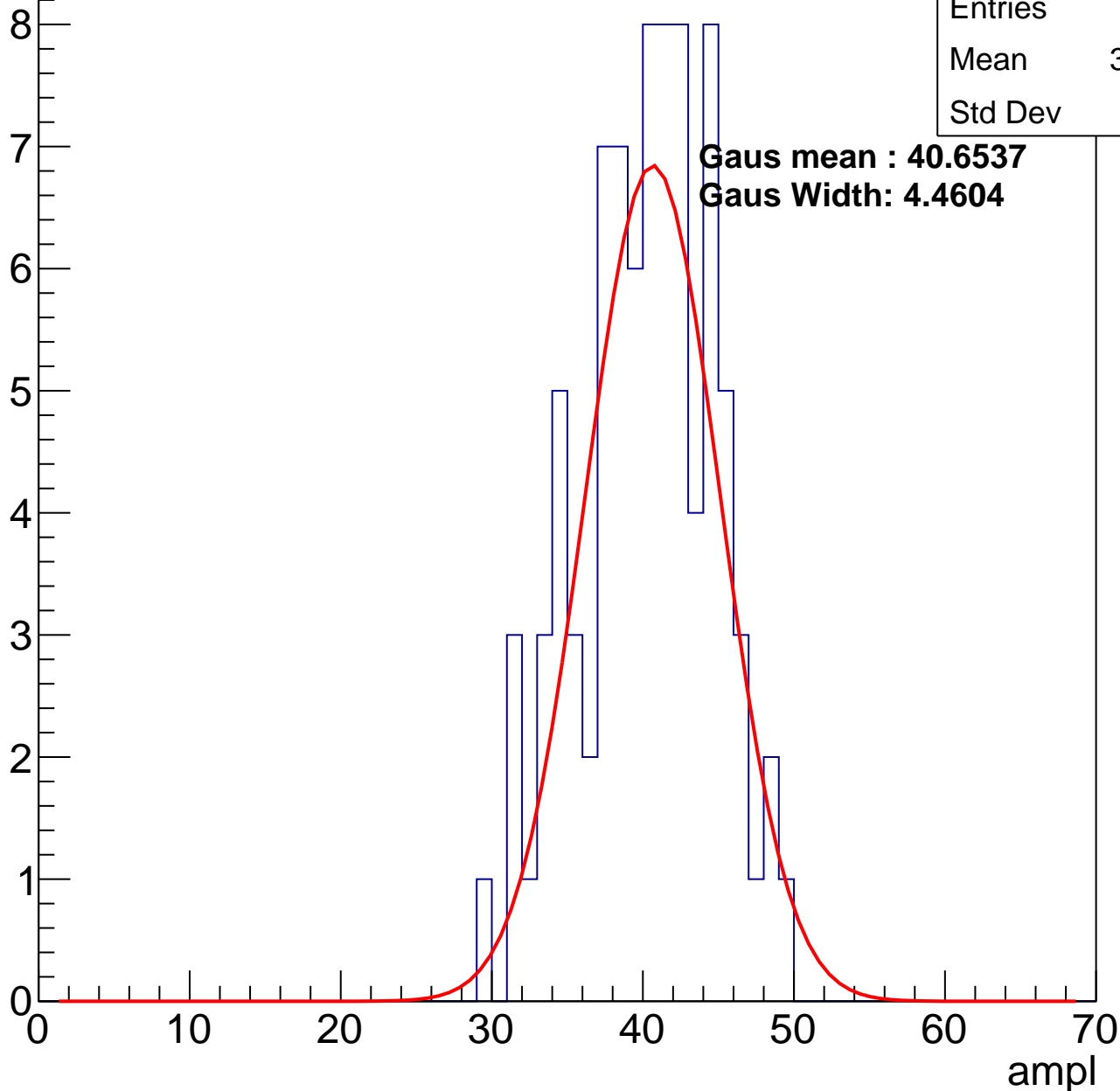
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	39.79
Std Dev	4.38

Gaus mean : 40.6537

Gaus Width: 4.4604



B1L101S, U26-ch89, adc2

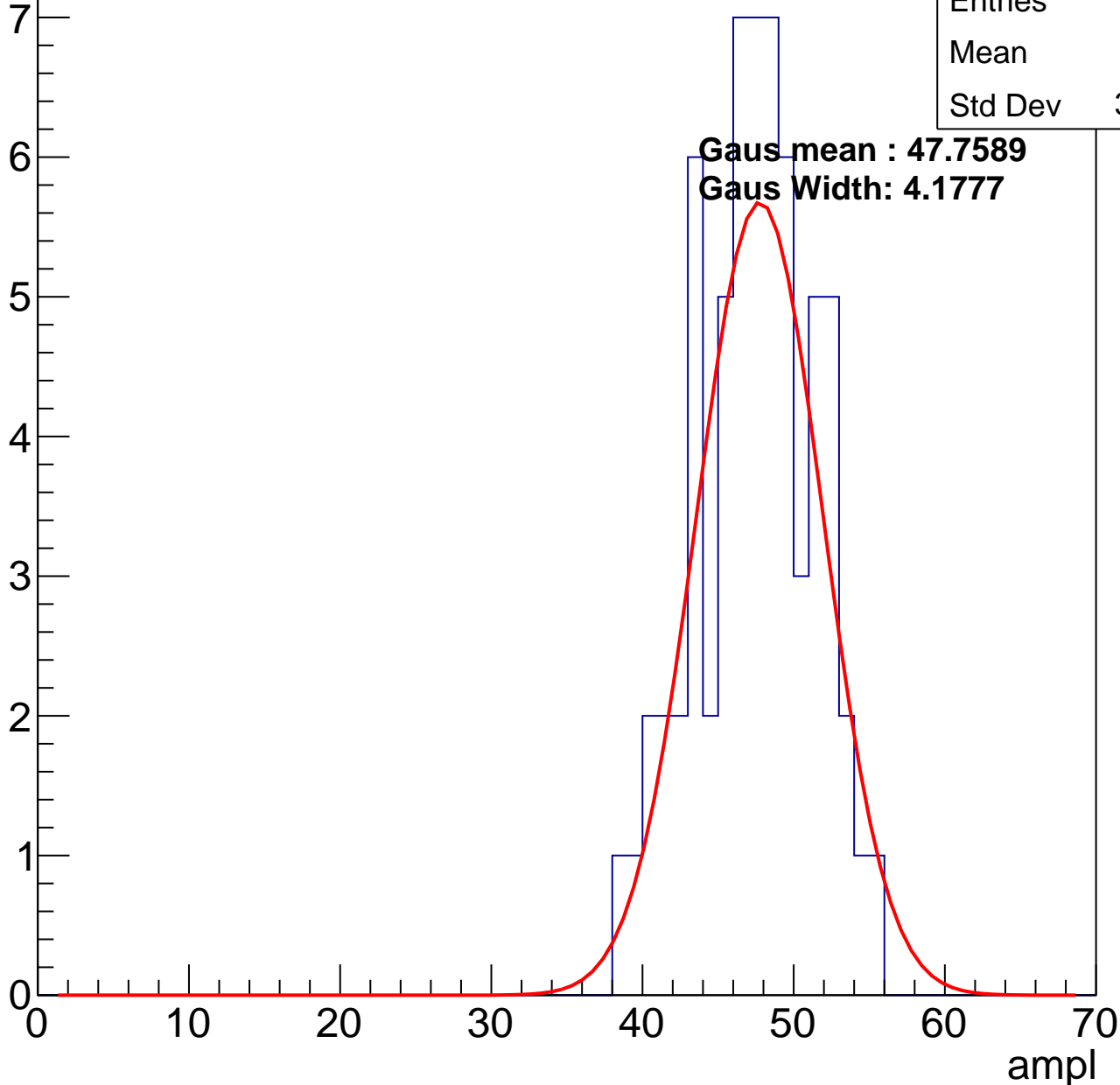
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	47
Std Dev	3.831

Gaus mean : 47.7589

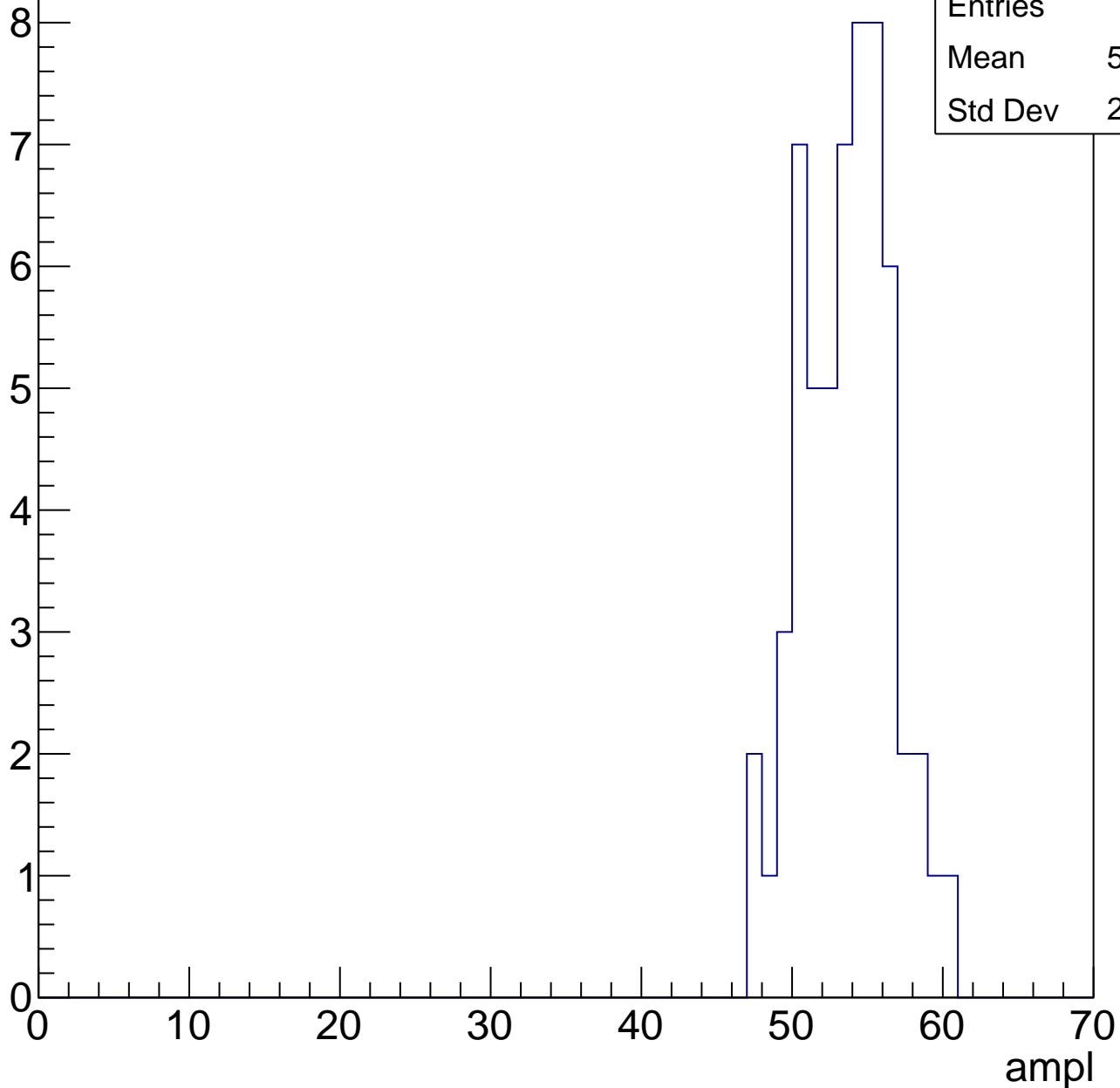
Gaus Width: 4.1777



B1L101S, U26-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



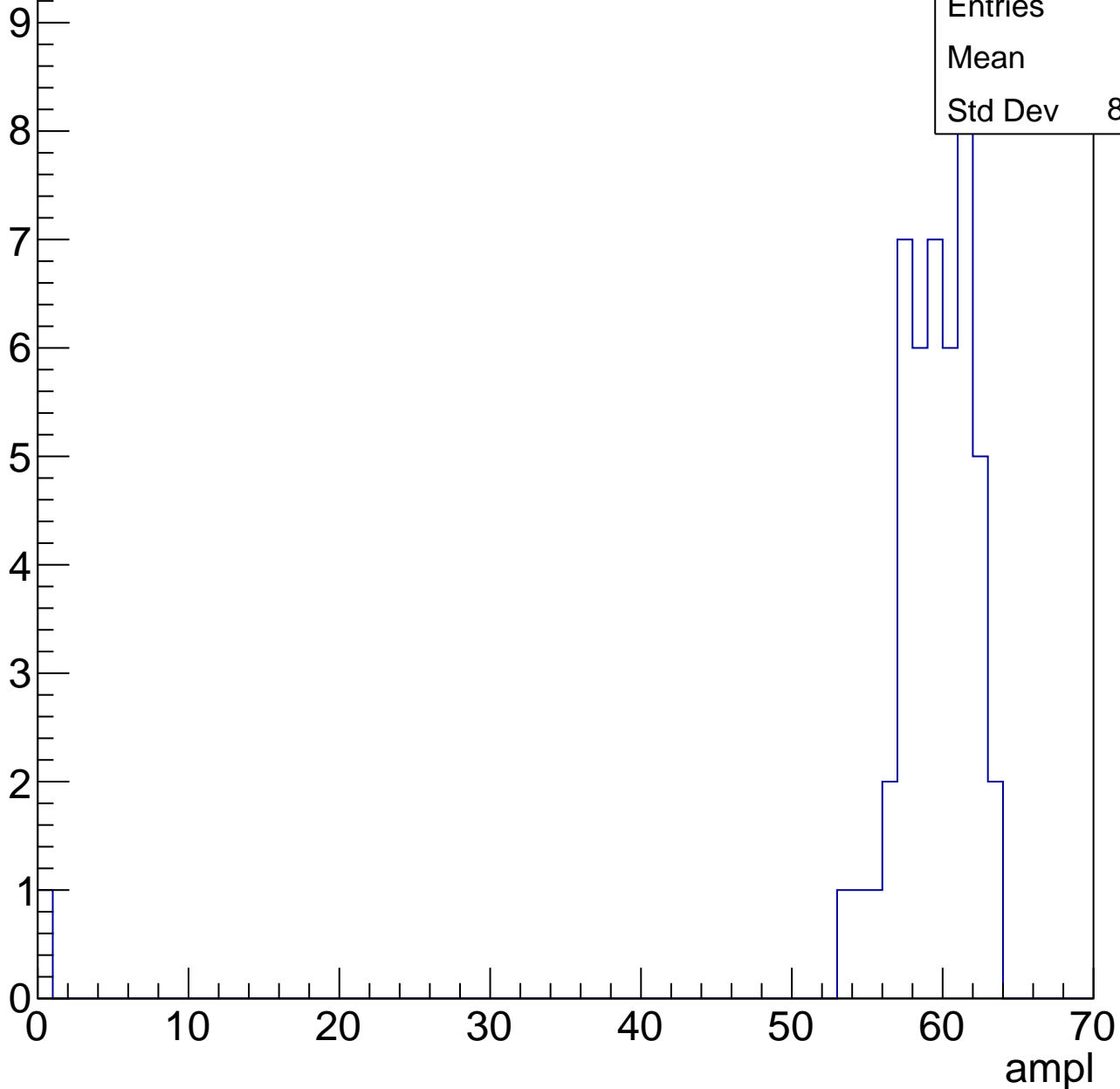
Entries	58
Mean	53.14
Std Dev	2.915

B1L101S, U26-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.9
Std Dev	8.745



B1L101S, U26-ch89, adc5

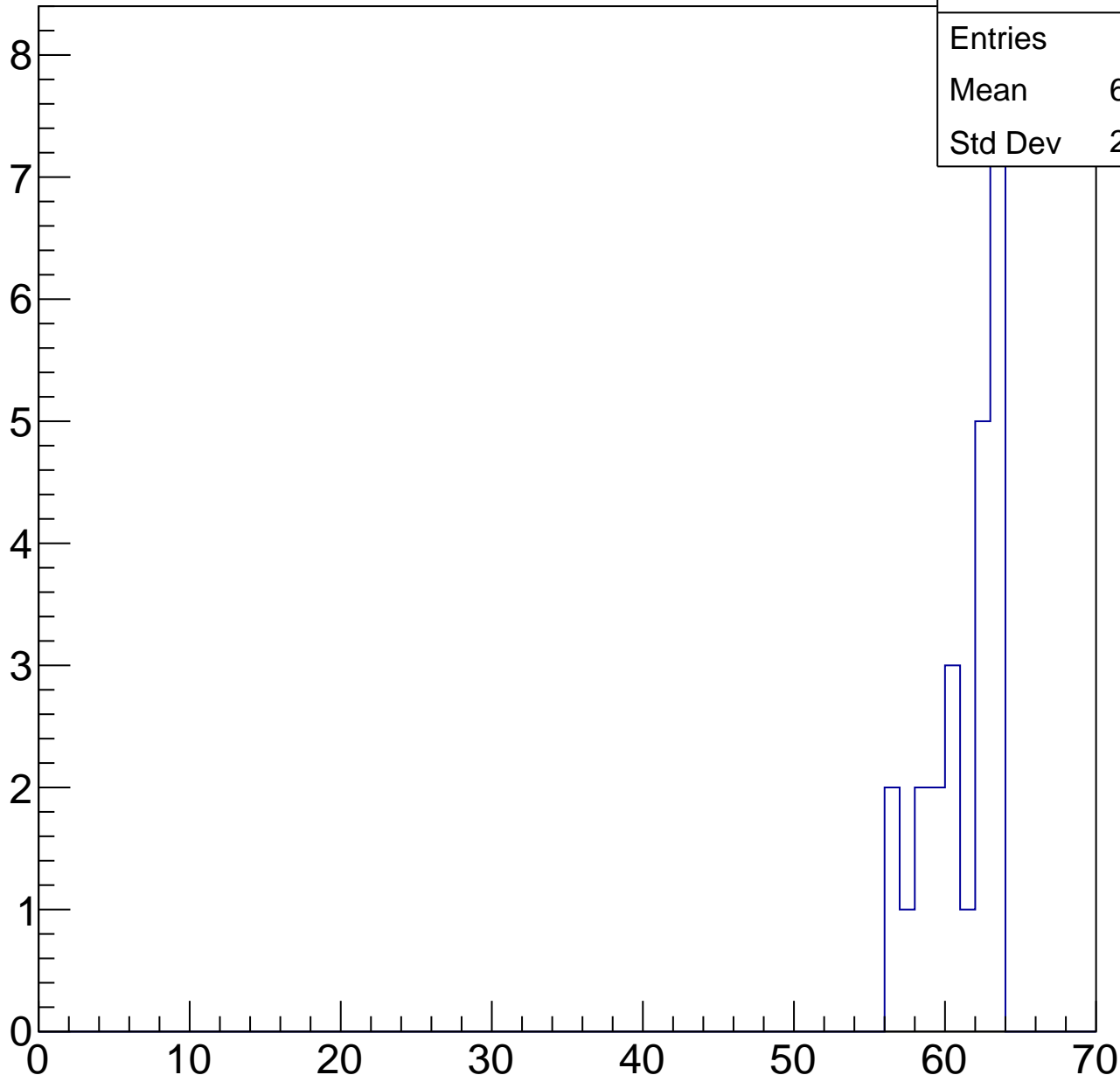
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	24
Mean	60.75
Std Dev	2.332

ampl



B1L101S, U26-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U26-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch90, adc0

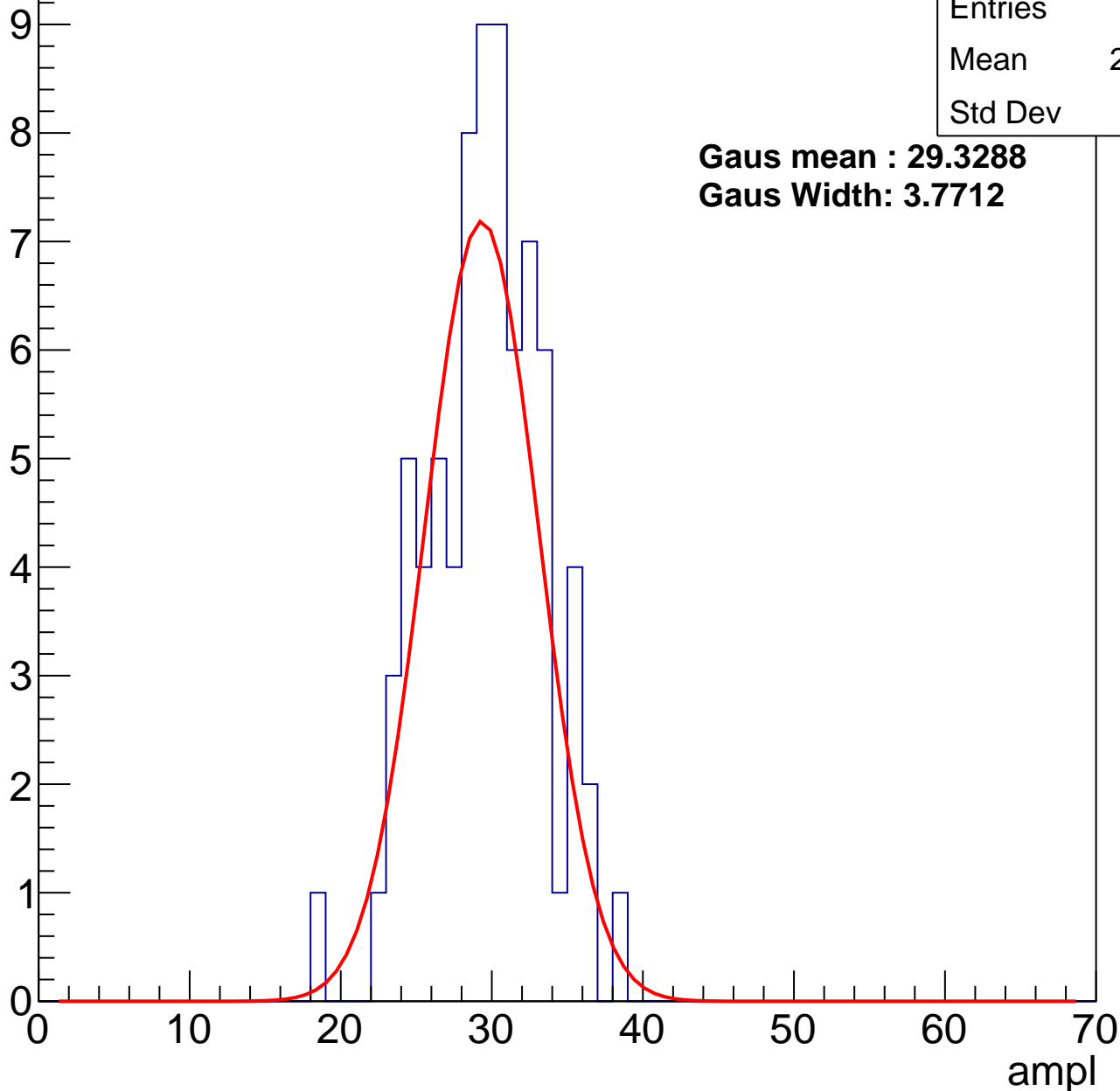
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.13
Std Dev	3.76

Gaus mean : 29.3288

Gaus Width: 3.7712



B1L101S, U26-ch90, adc1

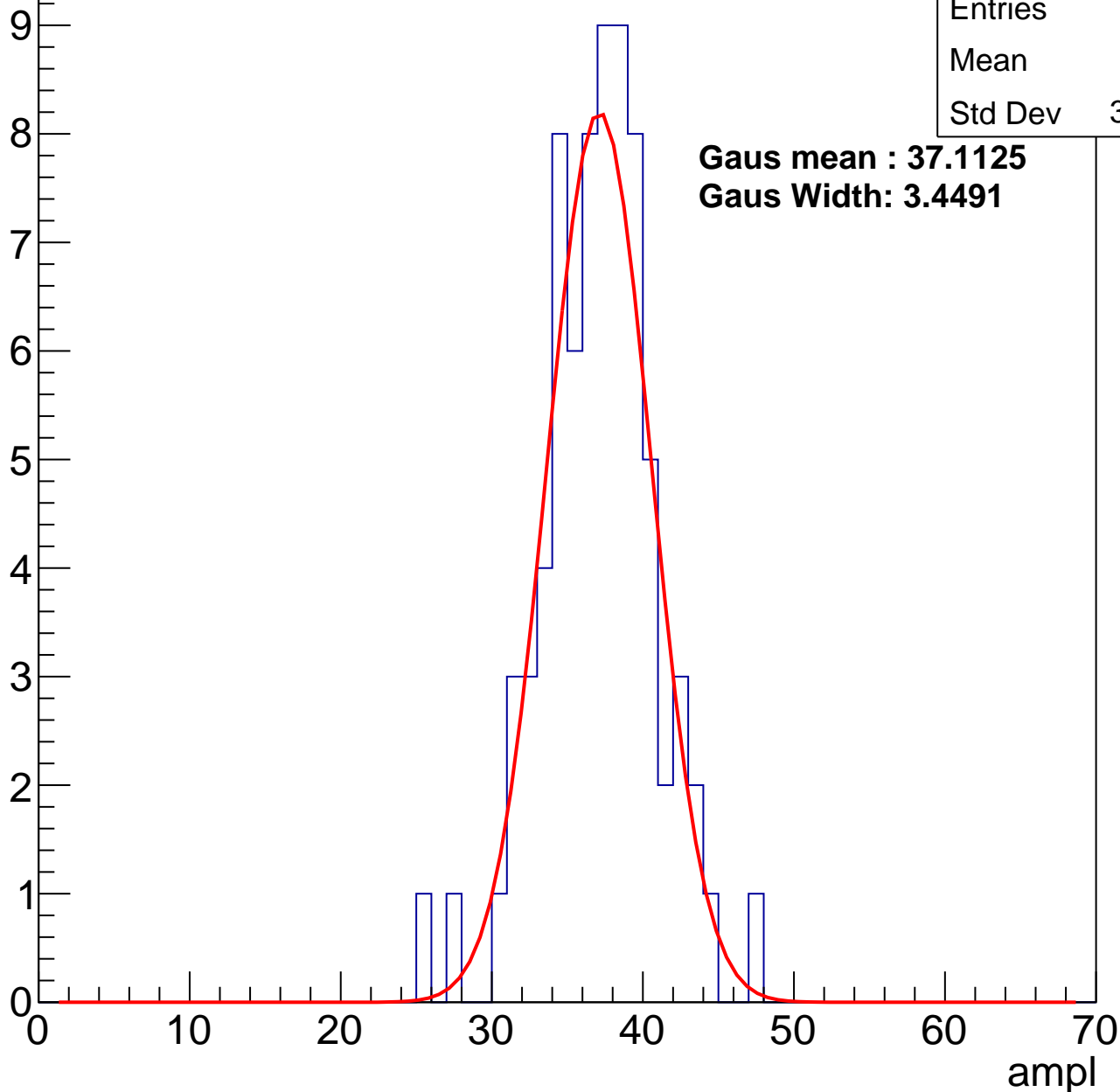
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.6
Std Dev	3.735

Gaus mean : 37.125

Gaus Width: 3.4491



B1L101S, U26-ch90, adc2

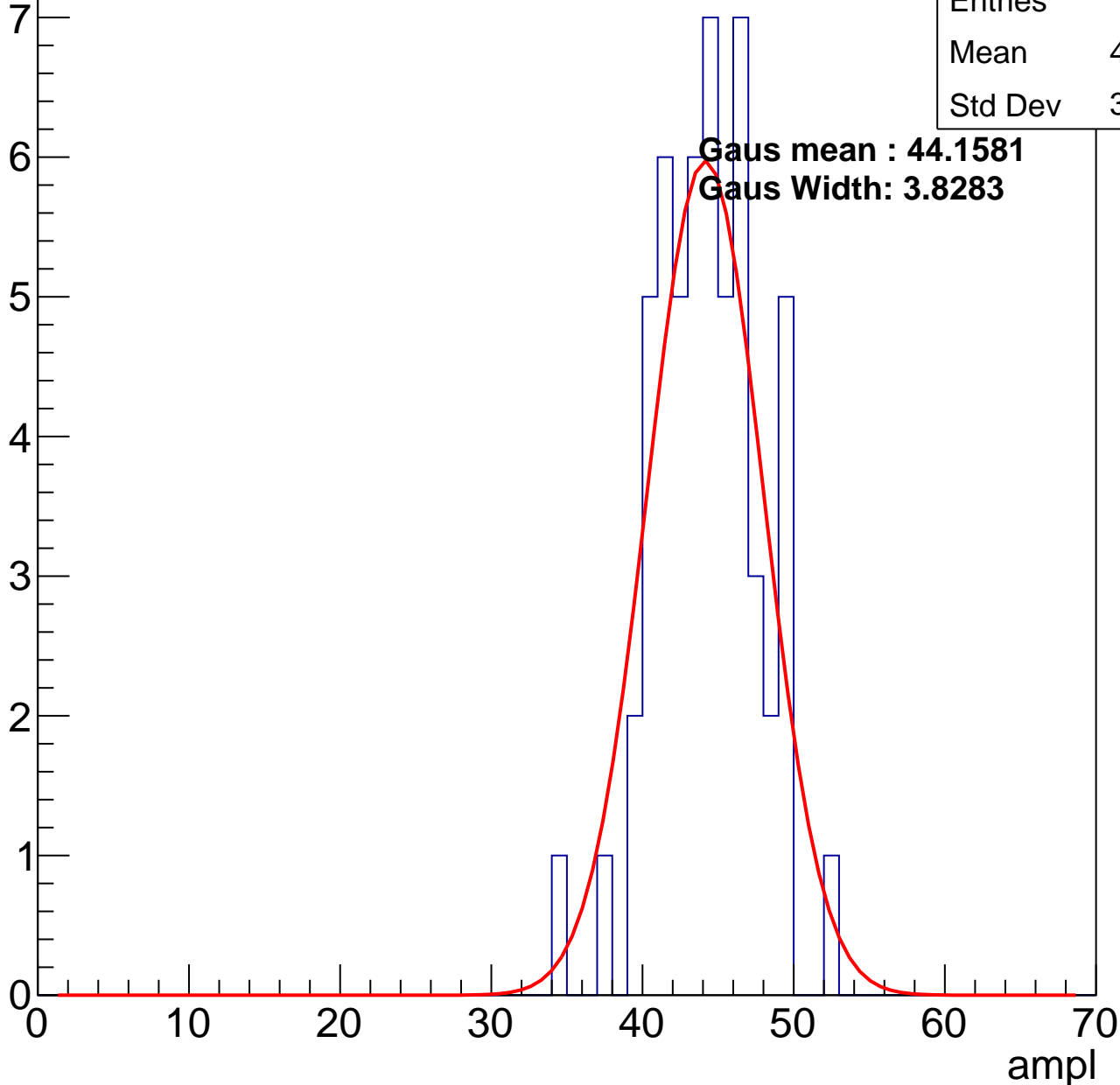
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.79
Std Dev	3.374

Gaus mean : 44.1581

Gaus Width: 3.8283

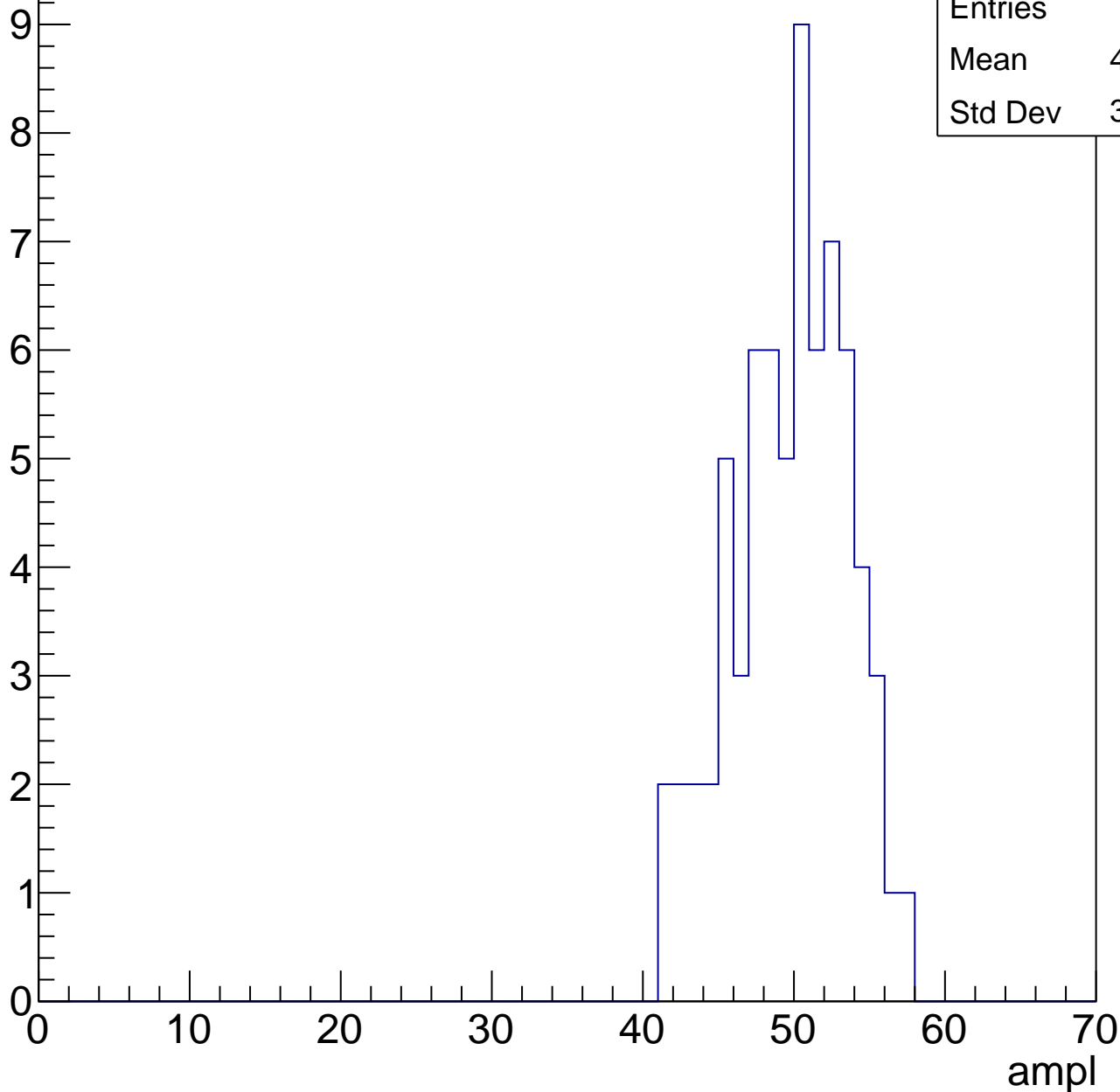


B1L101S, U26-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.29
Std Dev	3.765

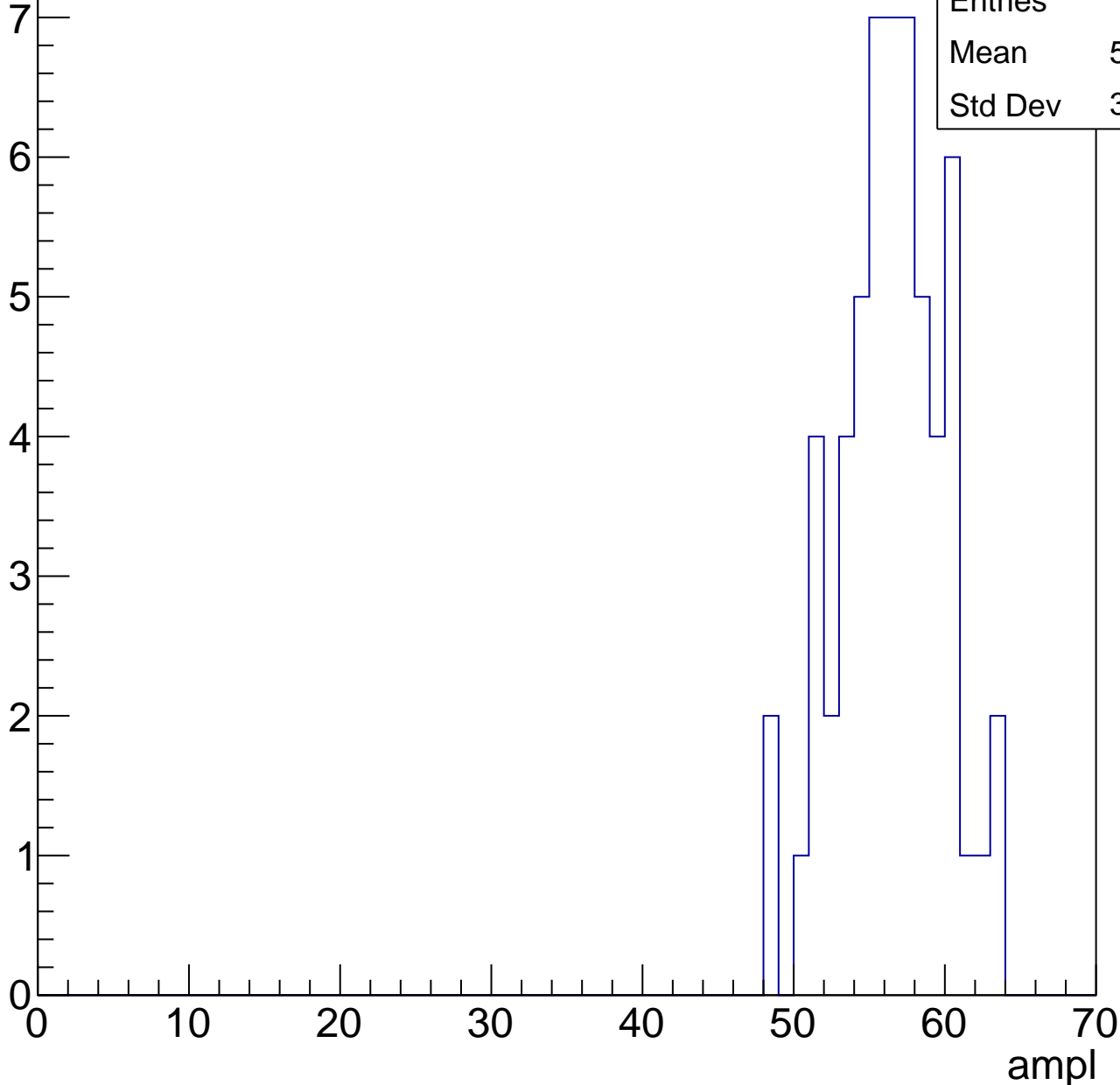


B1L101S, U26-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.98
Std Dev	3.416

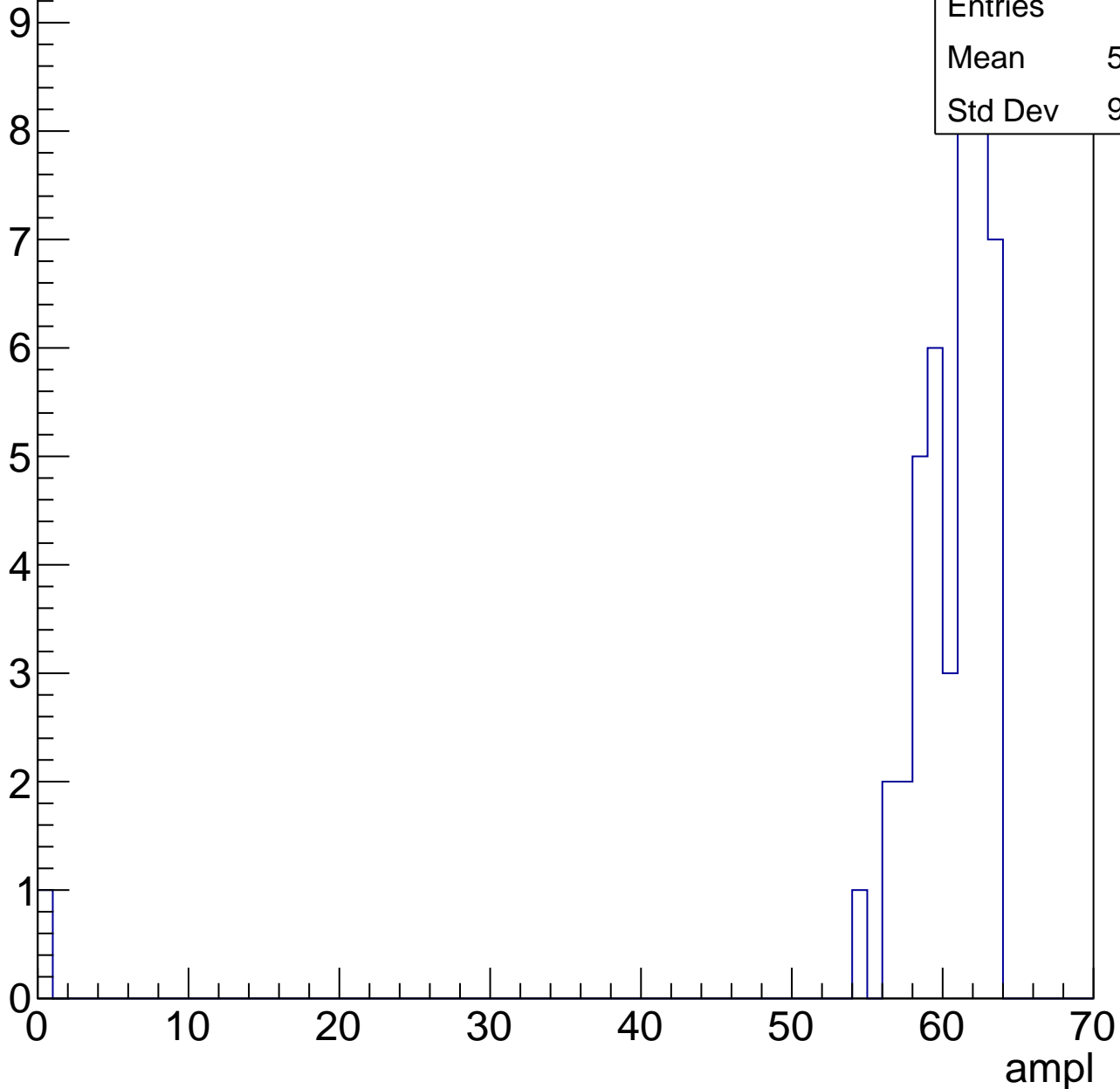


B1L101S, U26-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.86
Std Dev	9.243



B1L101S, U26-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

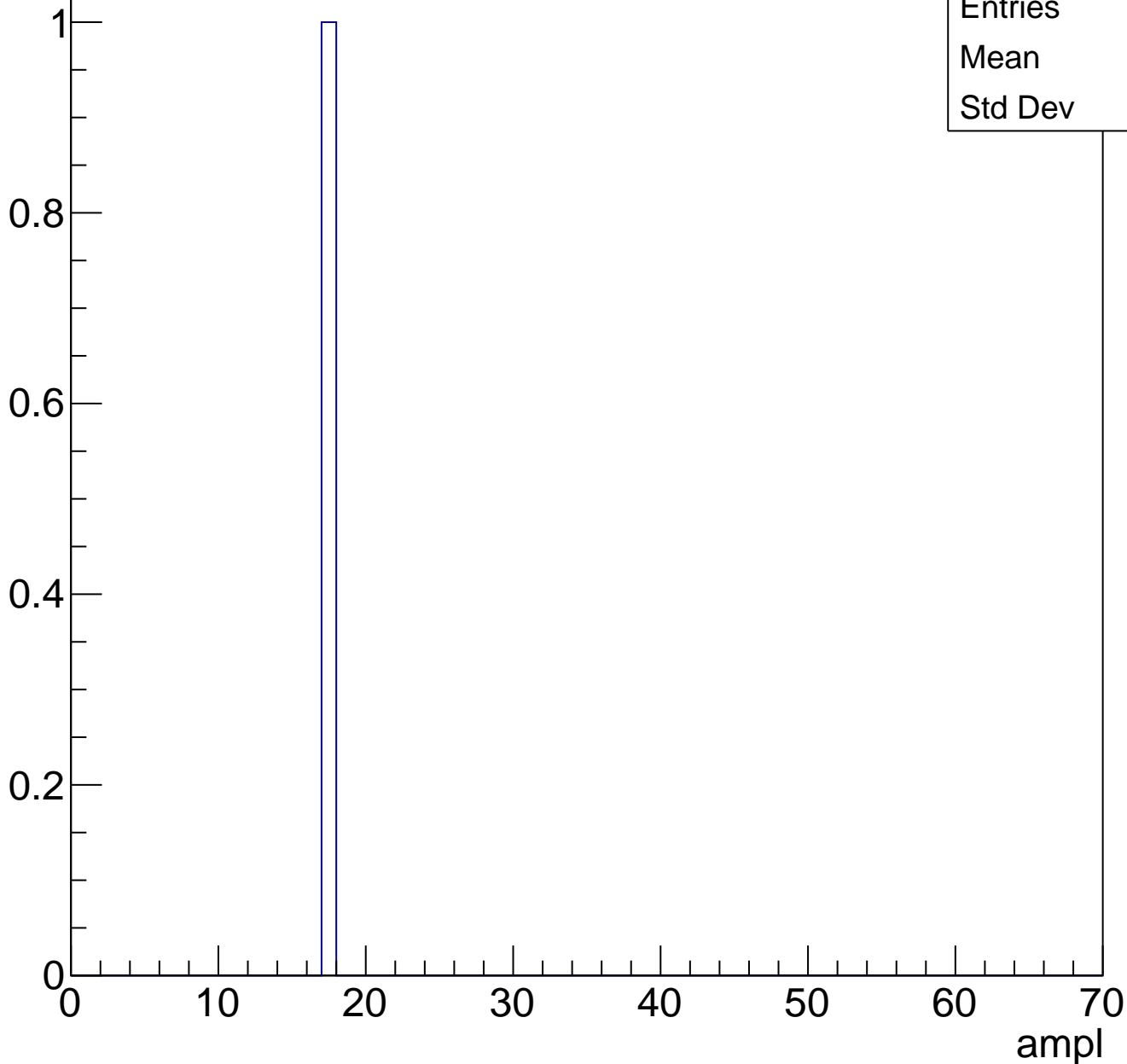
Entry



B1L101S, U26-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch91, adc0

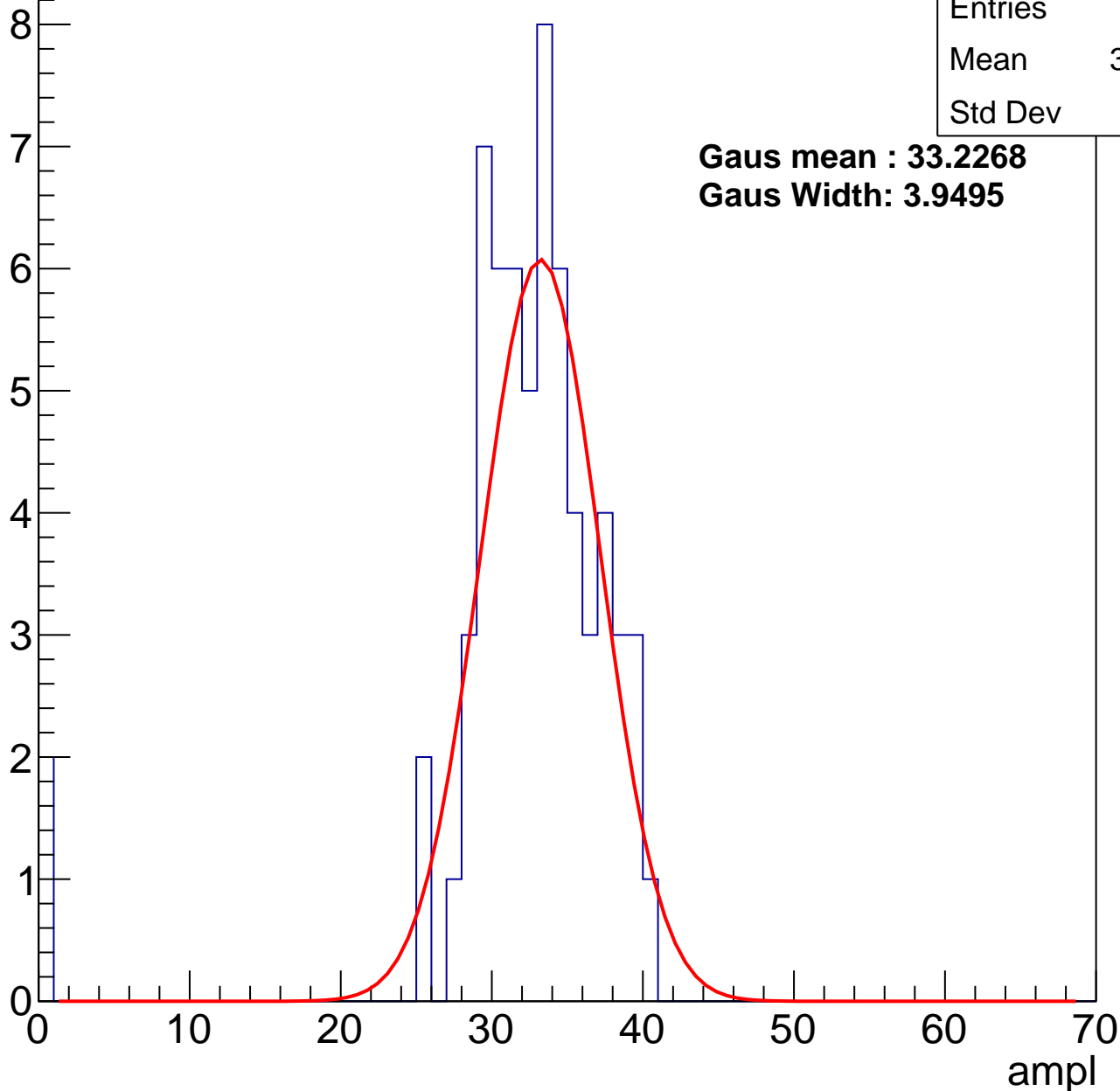
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	31.64
Std Dev	6.66

Gaus mean : 33.2268

Gaus Width: 3.9495



B1L101S, U26-ch91, adc1

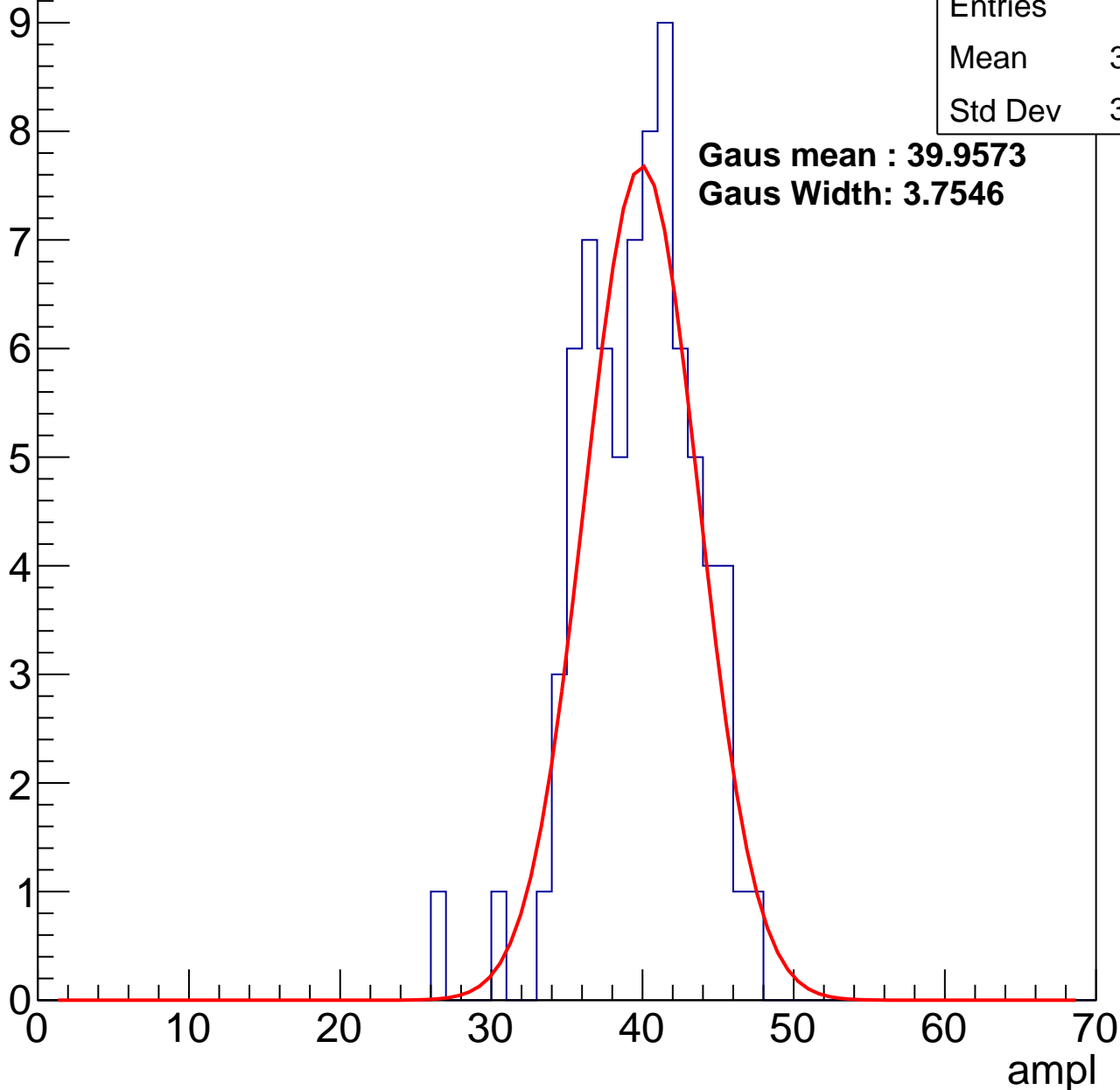
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	39.24
Std Dev	3.794

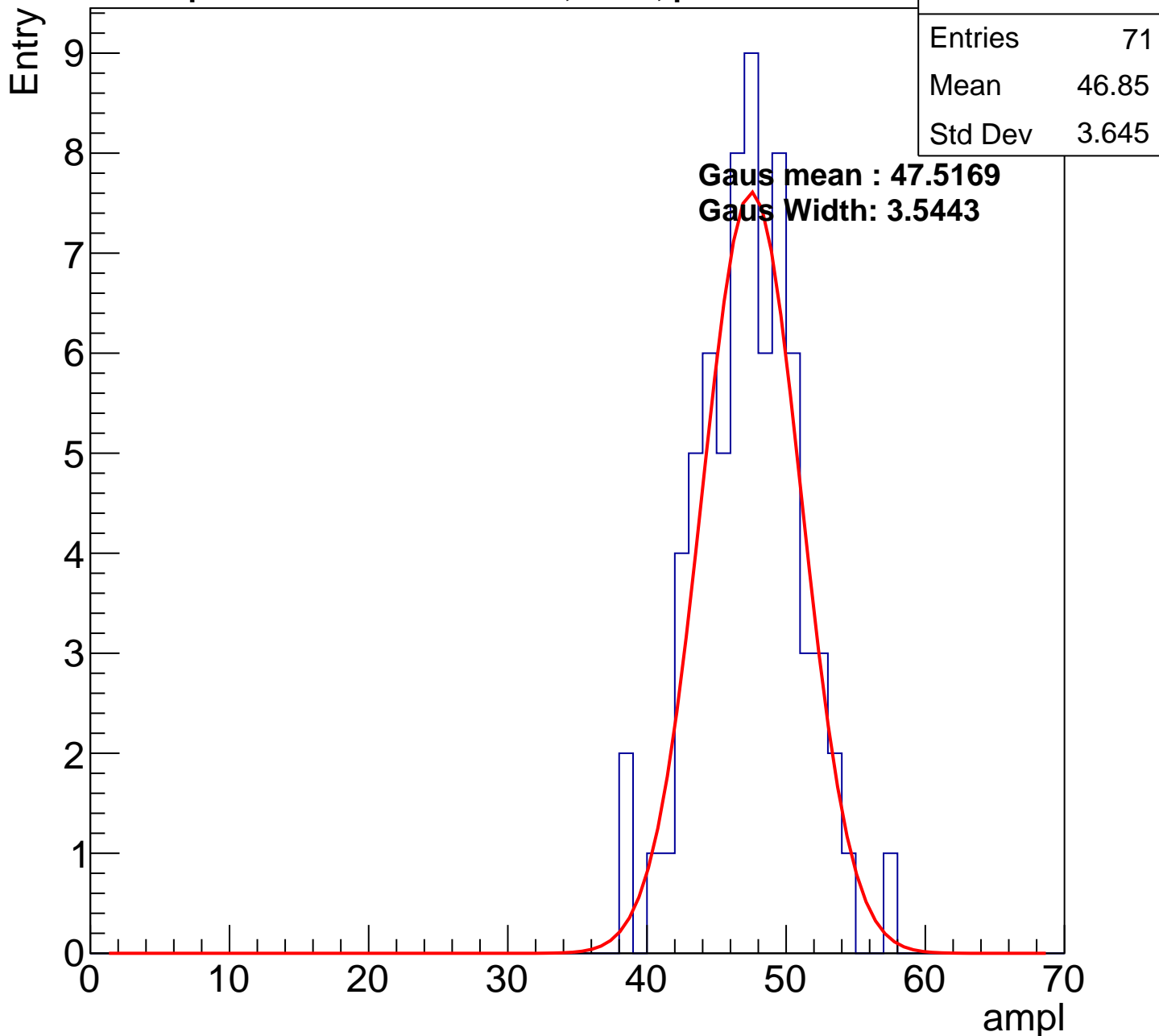
Gaus mean : 39.9573

Gaus Width: 3.7546



B1L101S, U26-ch91, adc2

calib_packv5_042523_0143.root, FC#0, port D2

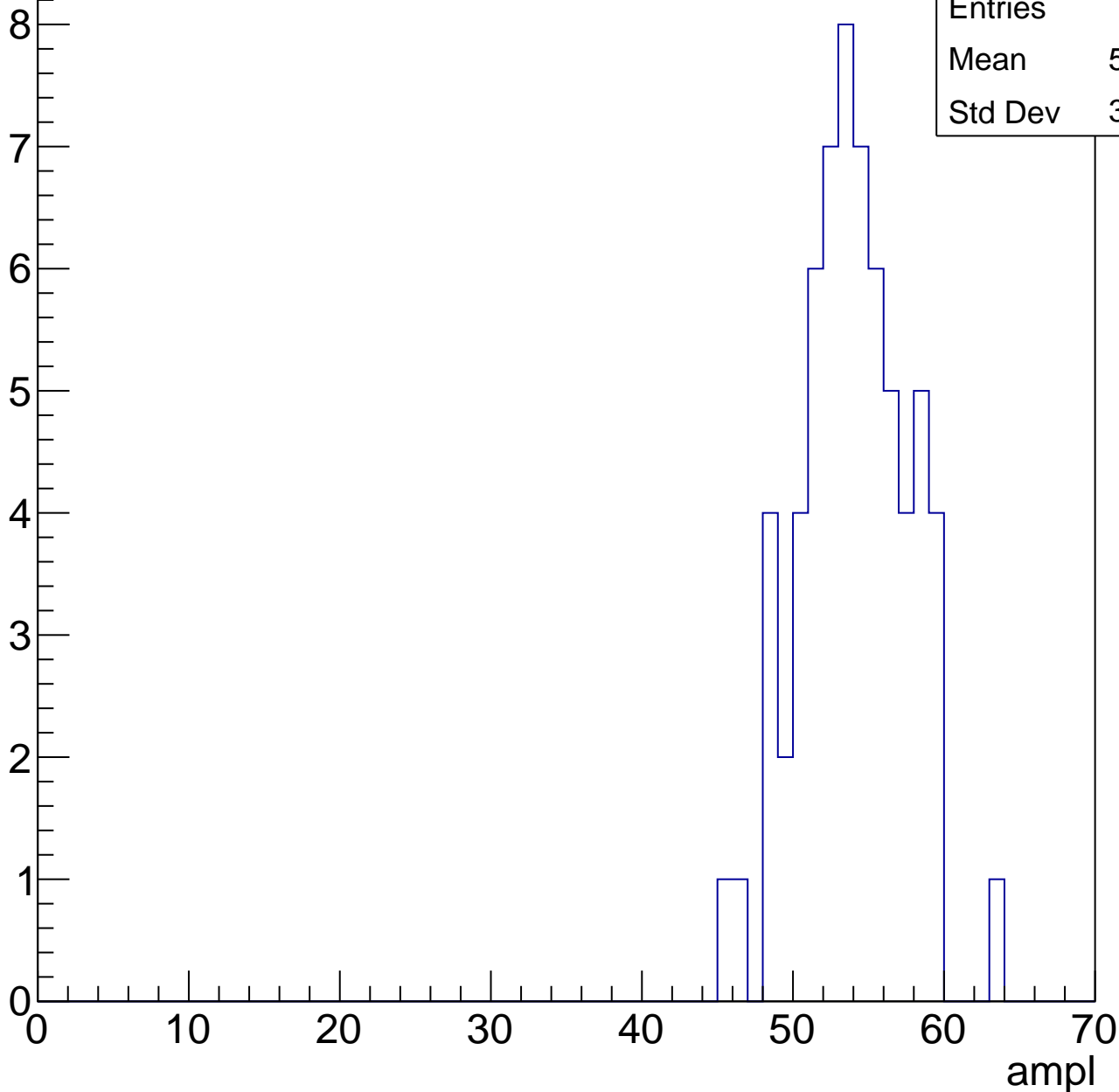


B1L101S, U26-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	53.54
Std Dev	3.513

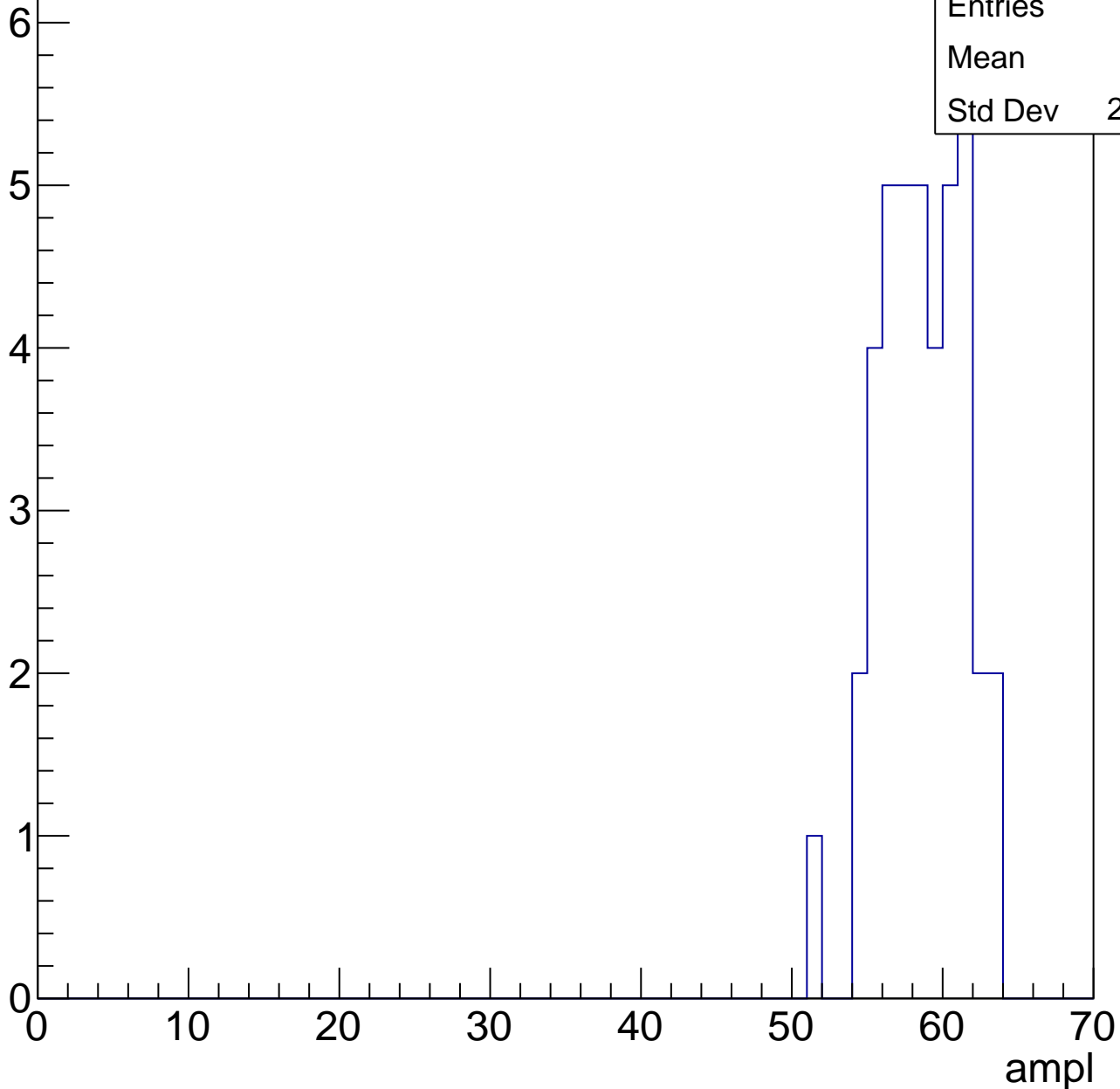


B1L101S, U26-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.2
Std Dev	2.707

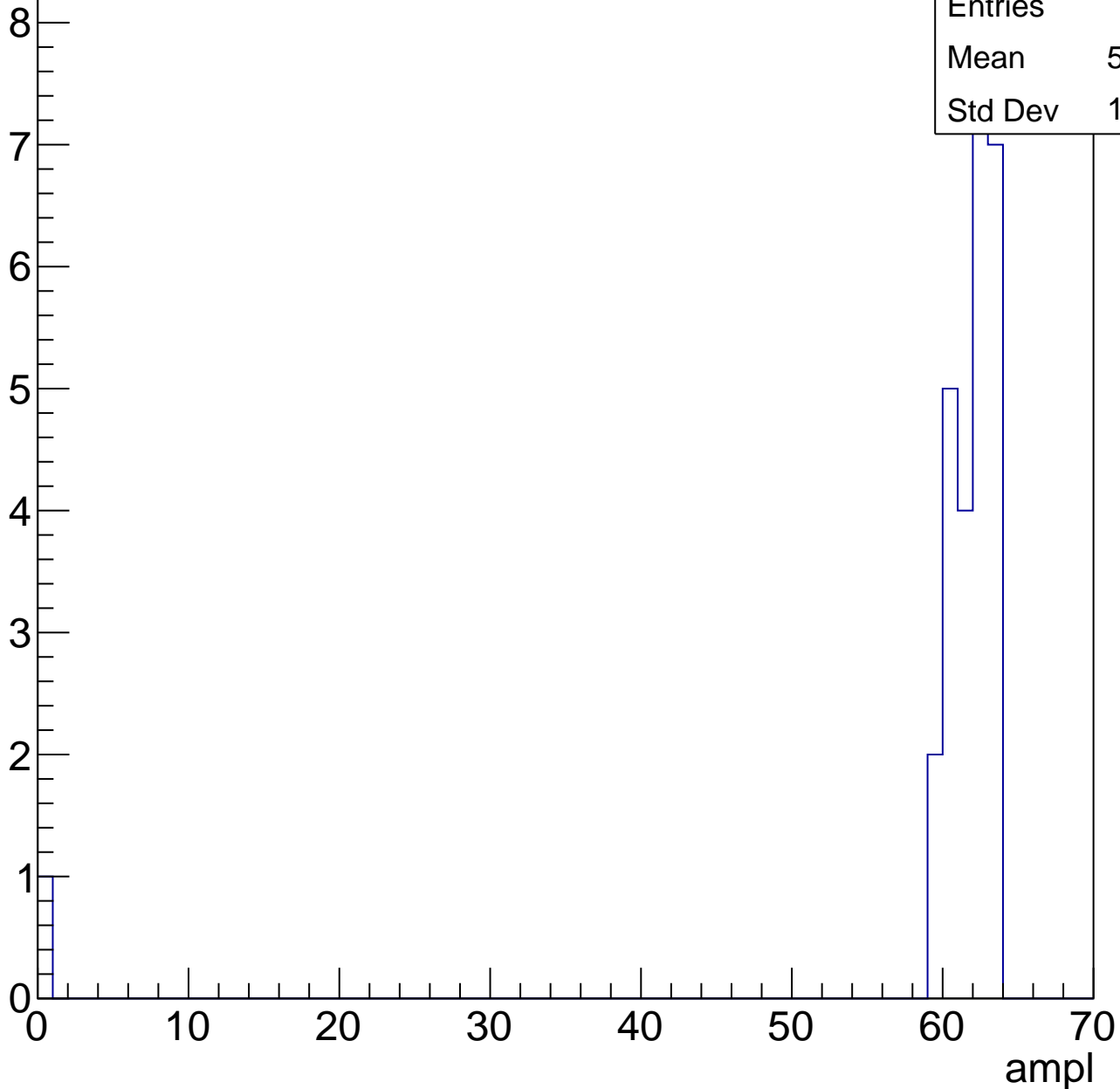


B1L101S, U26-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	59.22
Std Dev	11.68



B1L101S, U26-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch92, adc0

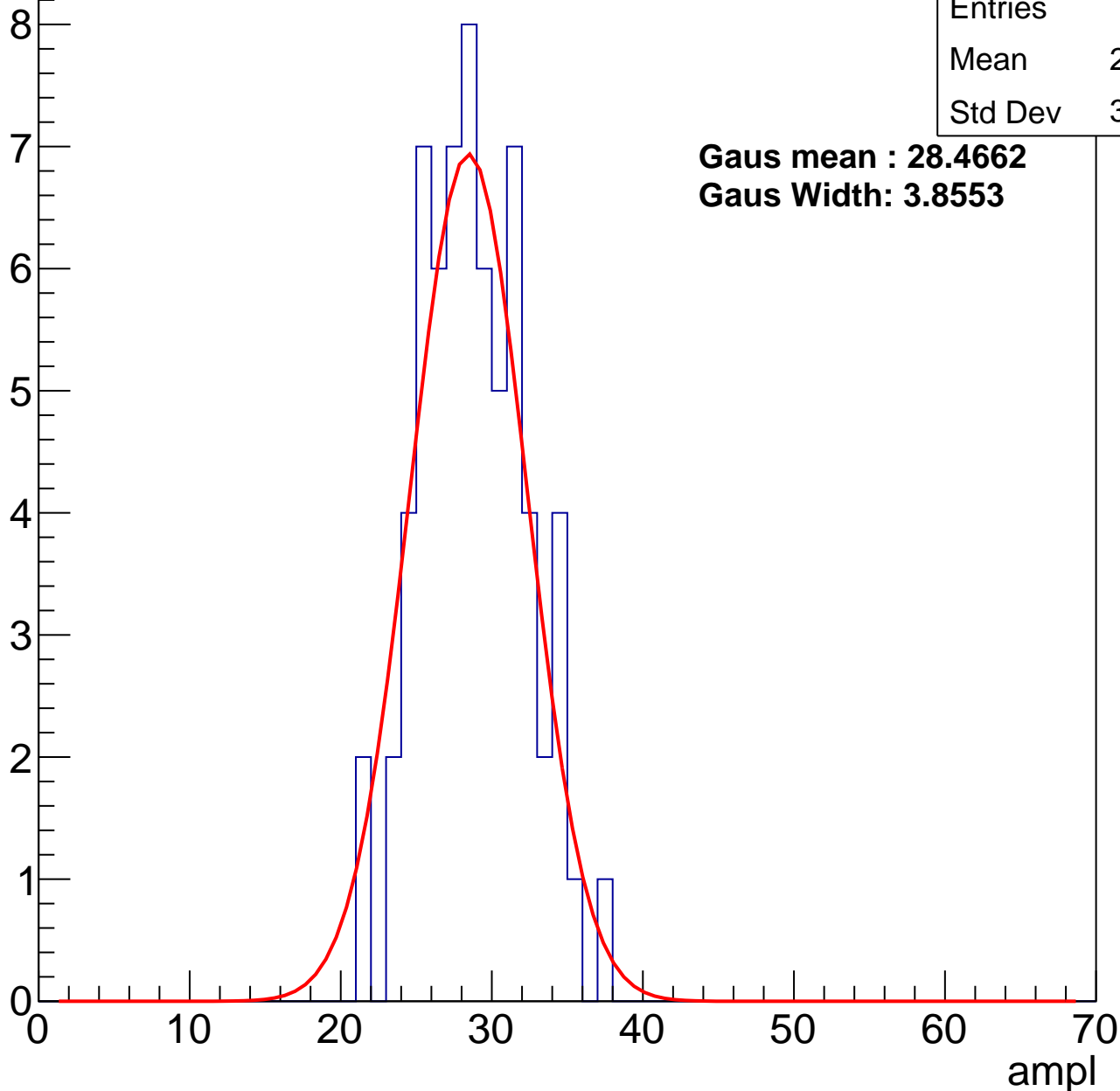
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.35
Std Dev	3.427

Gaus mean : 28.4662

Gaus Width: 3.8553



B1L101S, U26-ch92, adc1

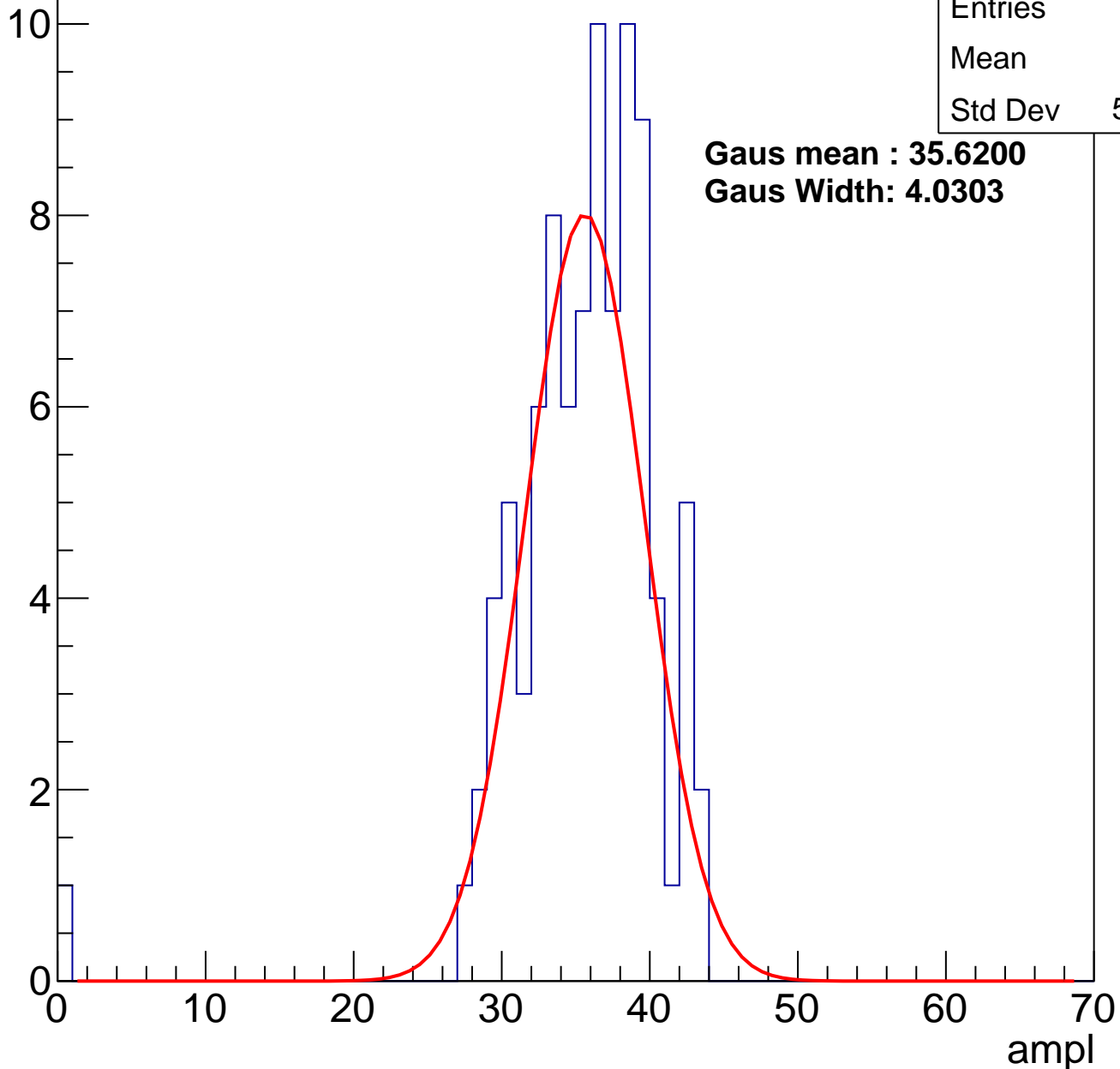
calib_packv5_042523_0143.root, FC#0, port D2

Entries	91
Mean	35.1
Std Dev	5.321

Gaus mean : 35.6200

Gaus Width: 4.0303

Entry



B1L101S, U26-ch92, adc2

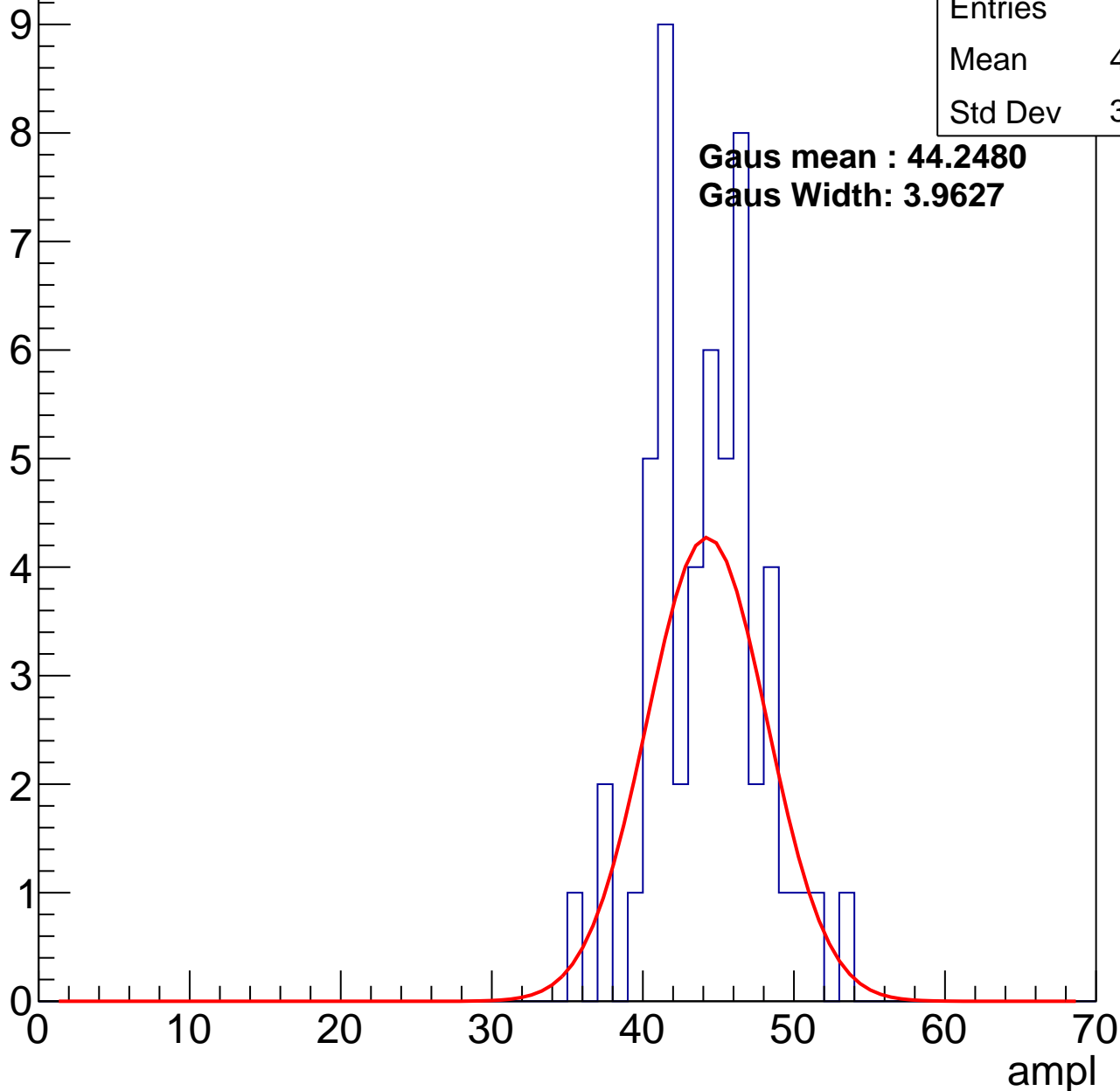
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	43.75
Std Dev	3.592

Gaus mean : 44.2480

Gaus Width: 3.9627

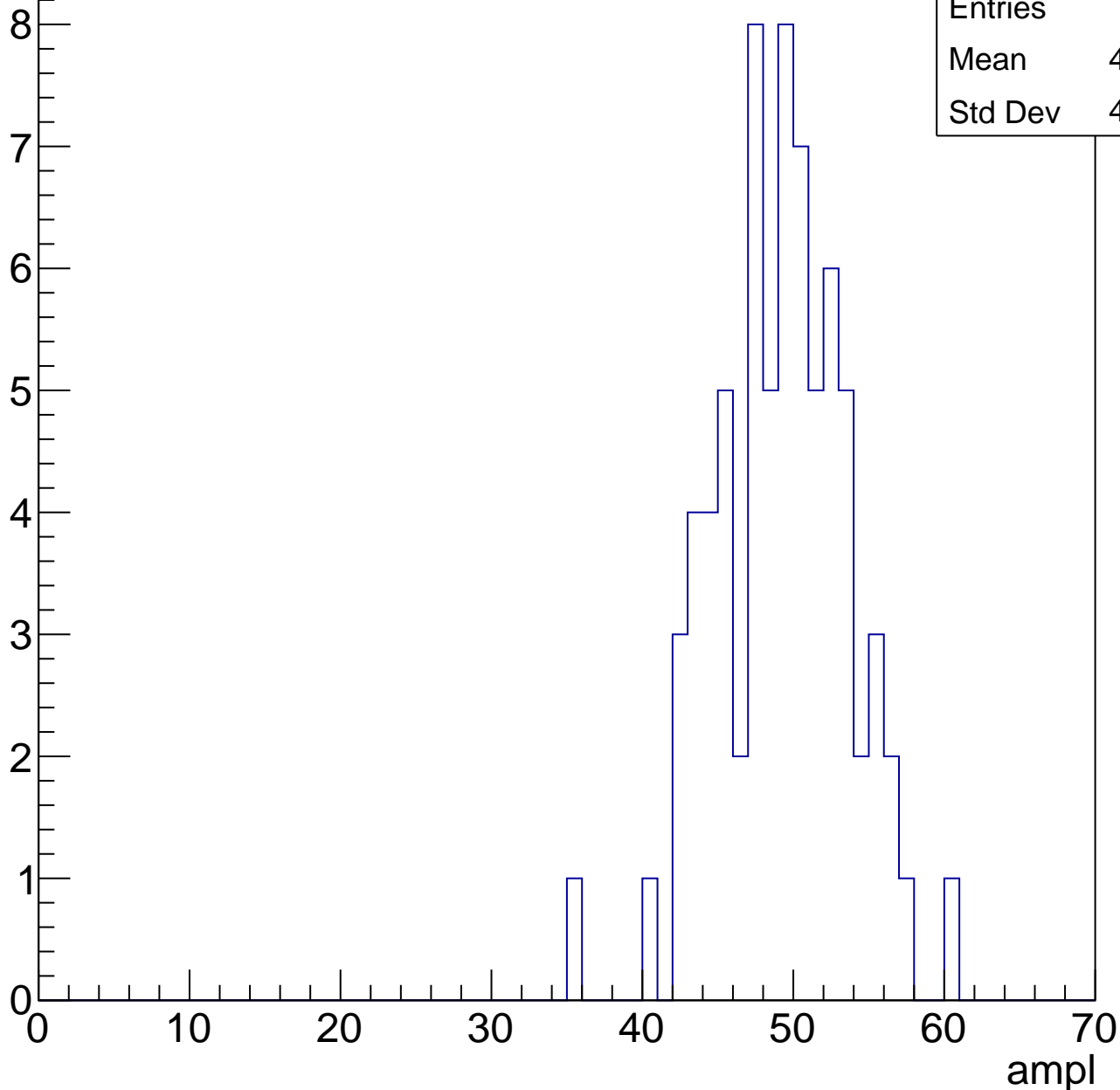


B1L101S, U26-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

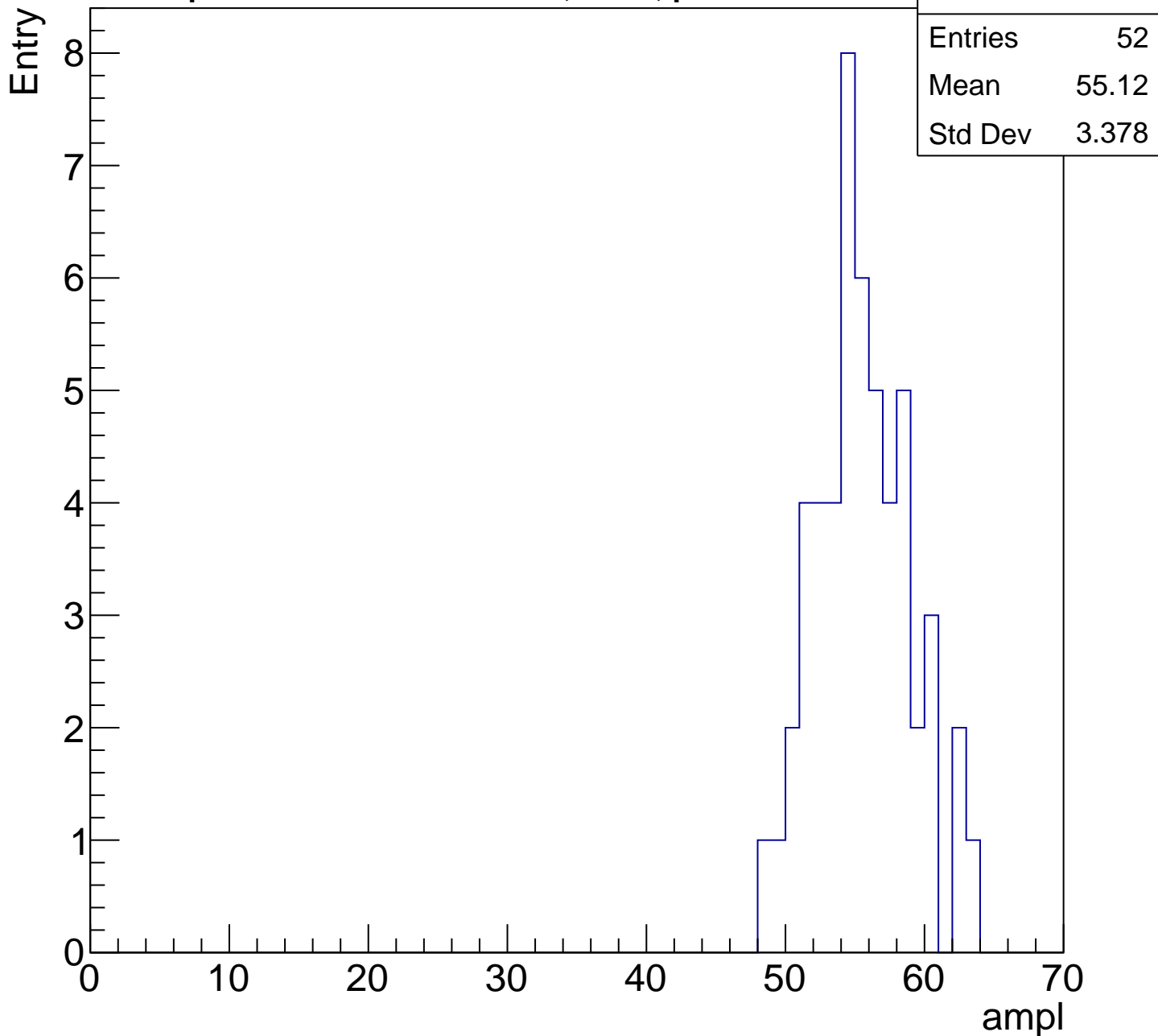
Entry

Entries	73
Mean	48.74
Std Dev	4.379



B1L101S, U26-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

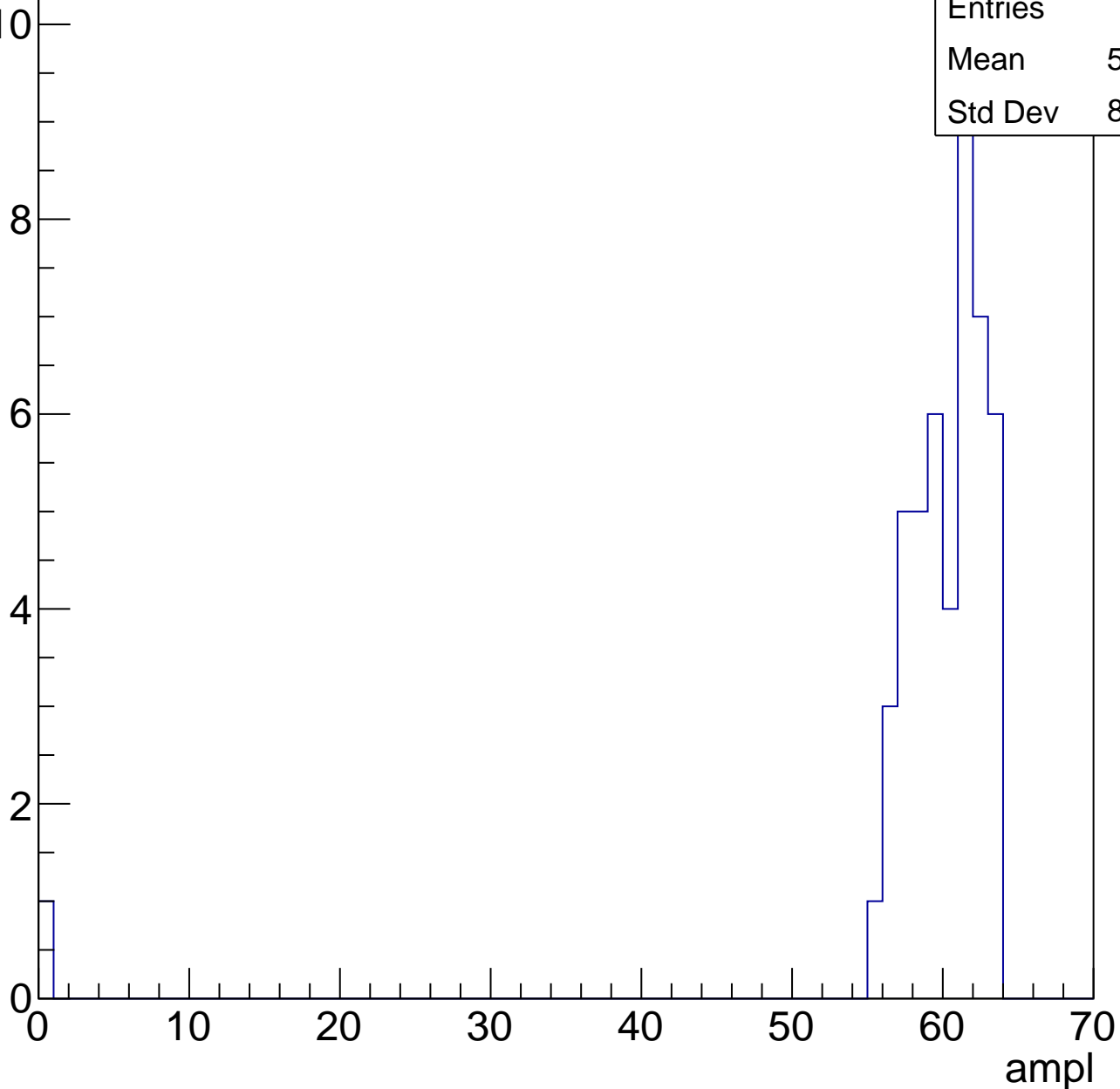


B1L101S, U26-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.62
Std Dev	8.833



B1L101S, U26-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

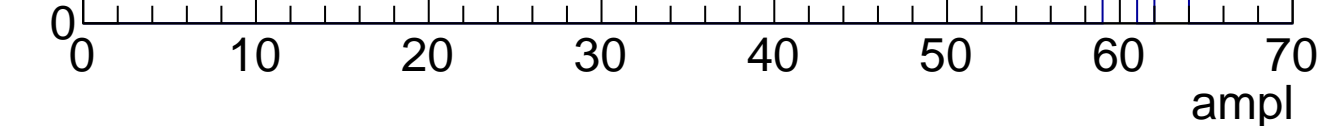
8

Mean

61.12

Std Dev

1.691



B1L101S, U26-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch93, adc0

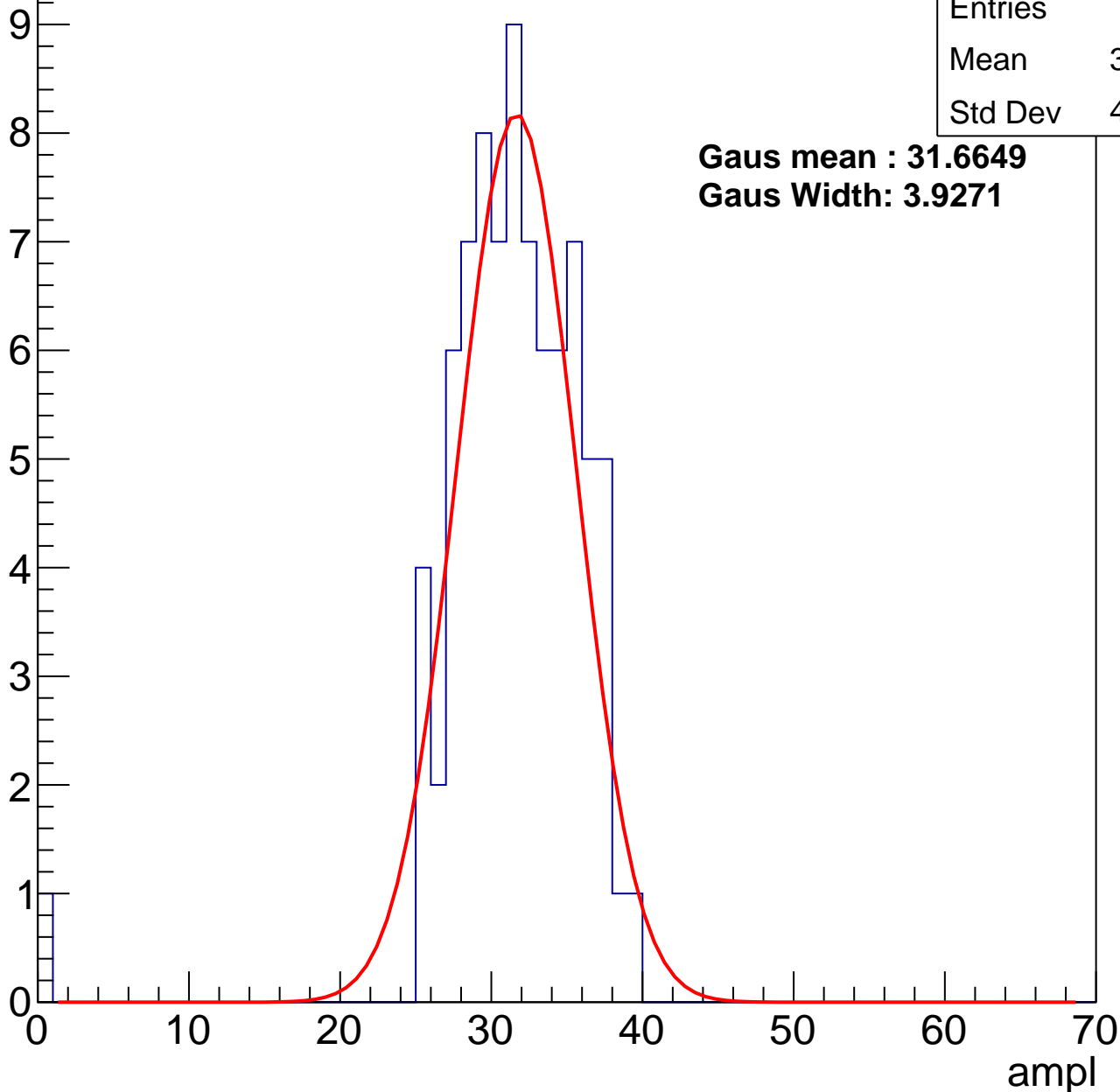
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	31.02
Std Dev	4.899

Gaus mean : 31.6649

Gaus Width: 3.9271



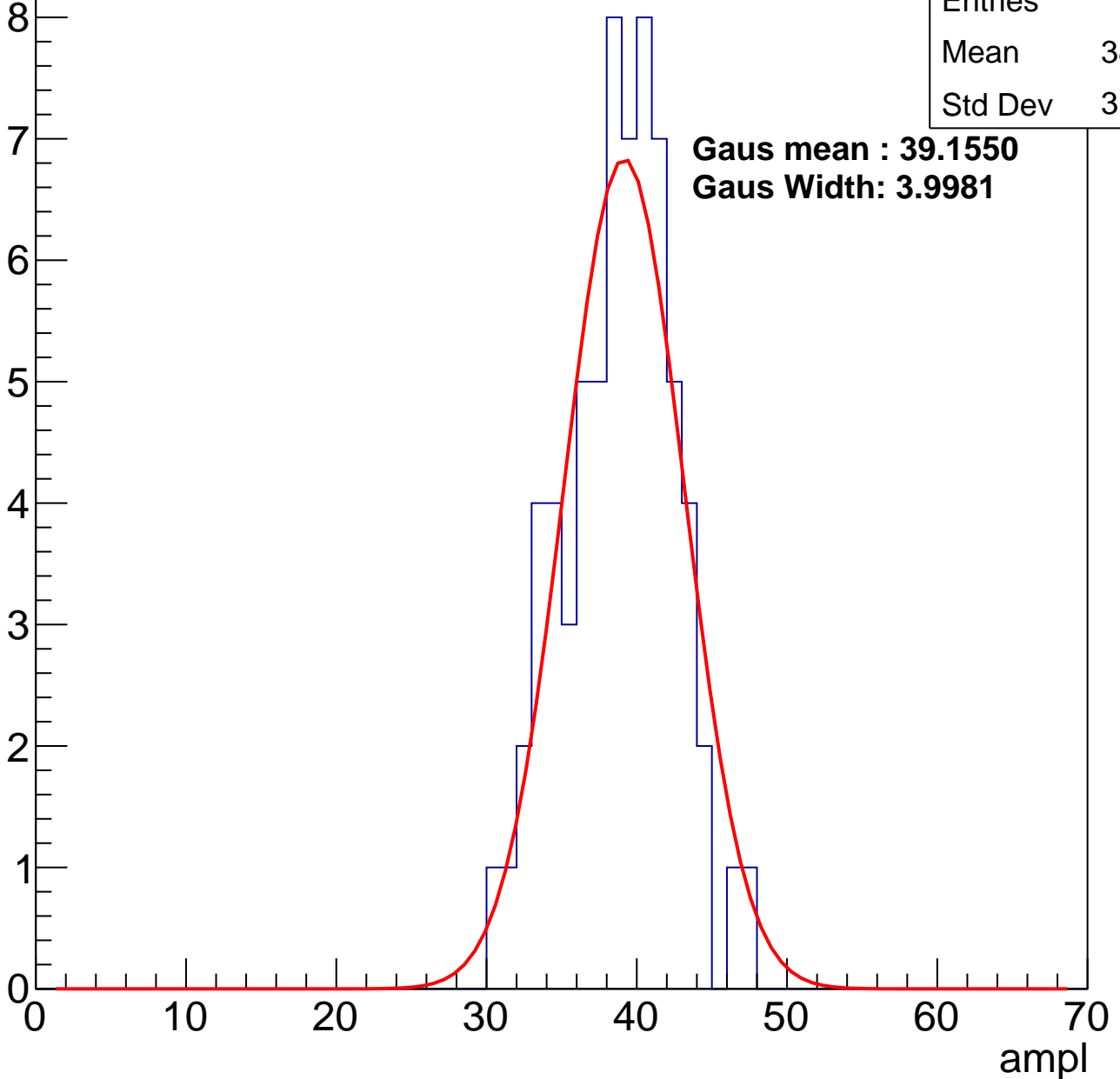
B1L101S, U26-ch93, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

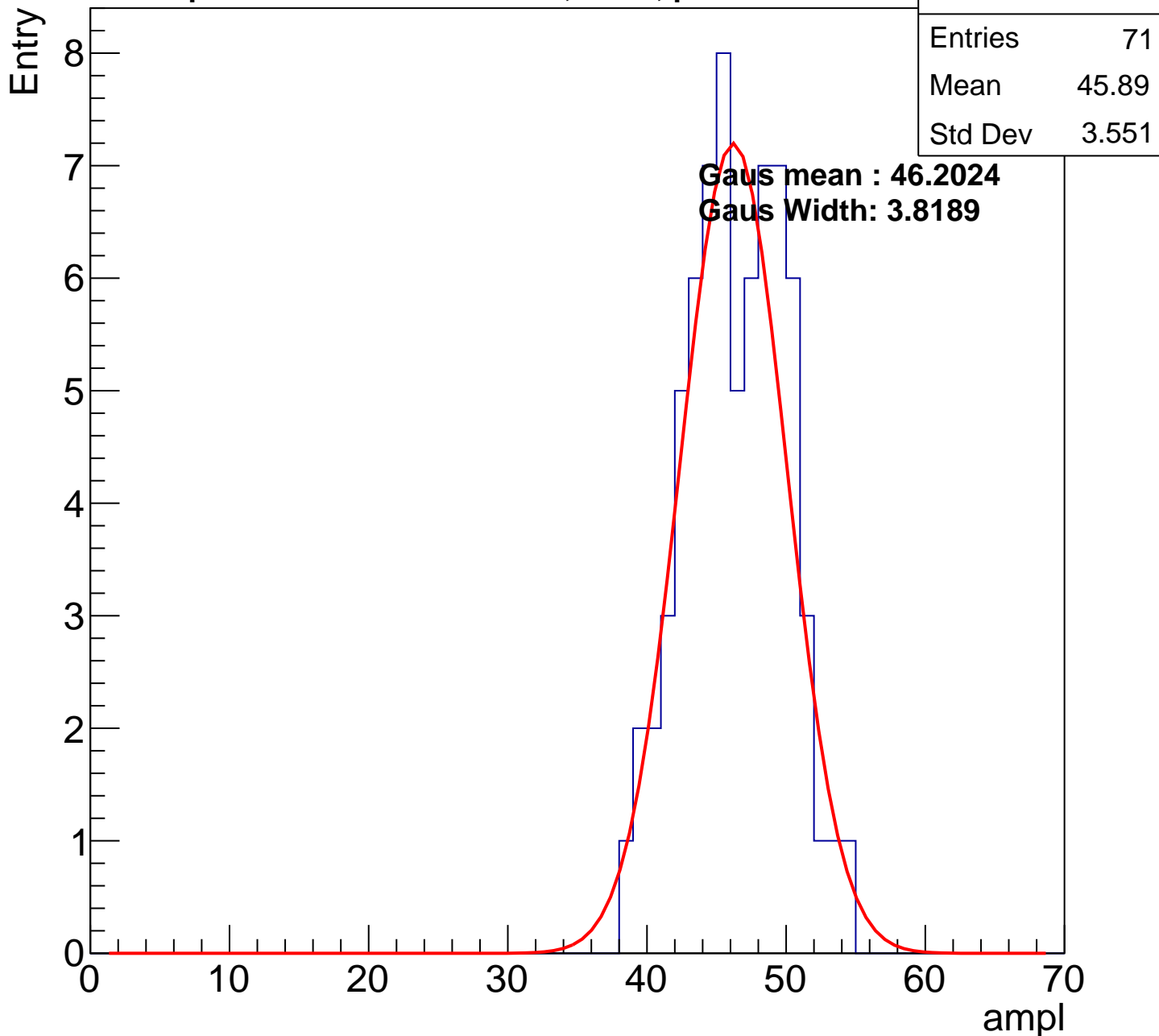
Entries	68
Mean	38.38
Std Dev	3.618

Gaus mean : 39.1550
Gaus Width: 3.9981



B1L101S, U26-ch93, adc2

calib_packv5_042523_0143.root, FC#0, port D2

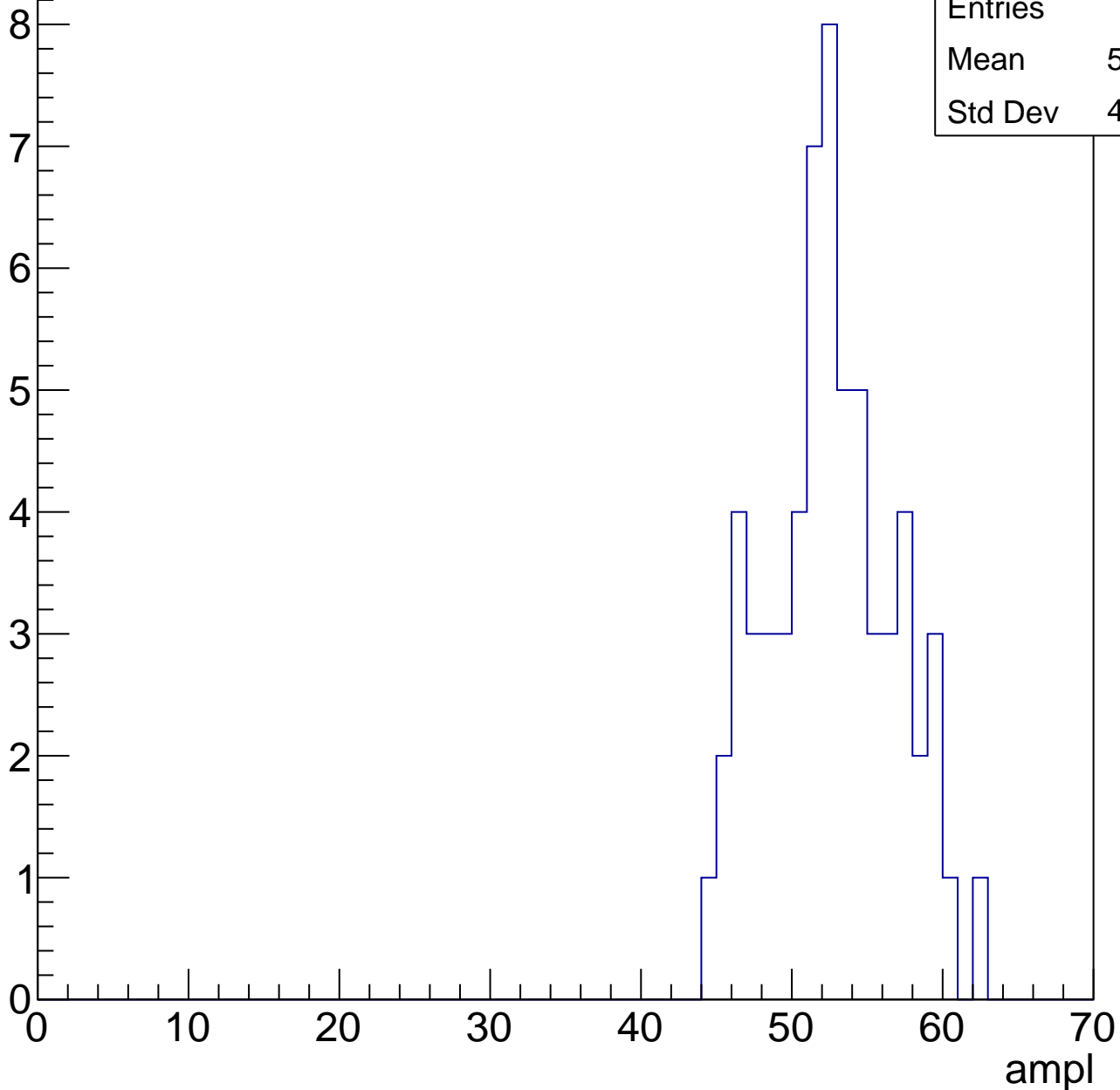


B1L101S, U26-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	52.16
Std Dev	4.136



B1L101S, U26-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

59

Mean

57.88

Std Dev

2.941

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

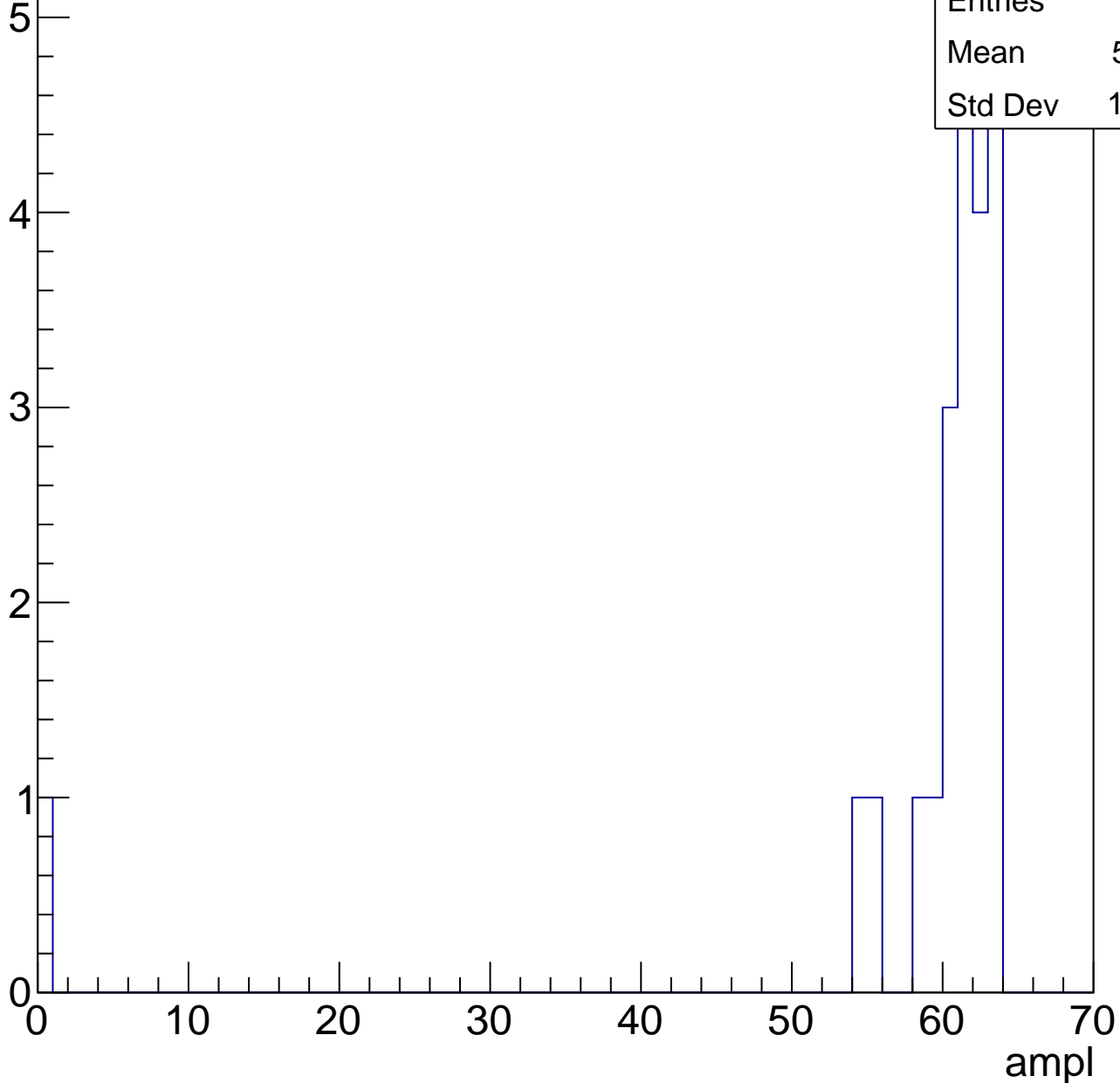
ampl

B1L101S, U26-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	57.91
Std Dev	12.86



B1L101S, U26-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch94, adc0

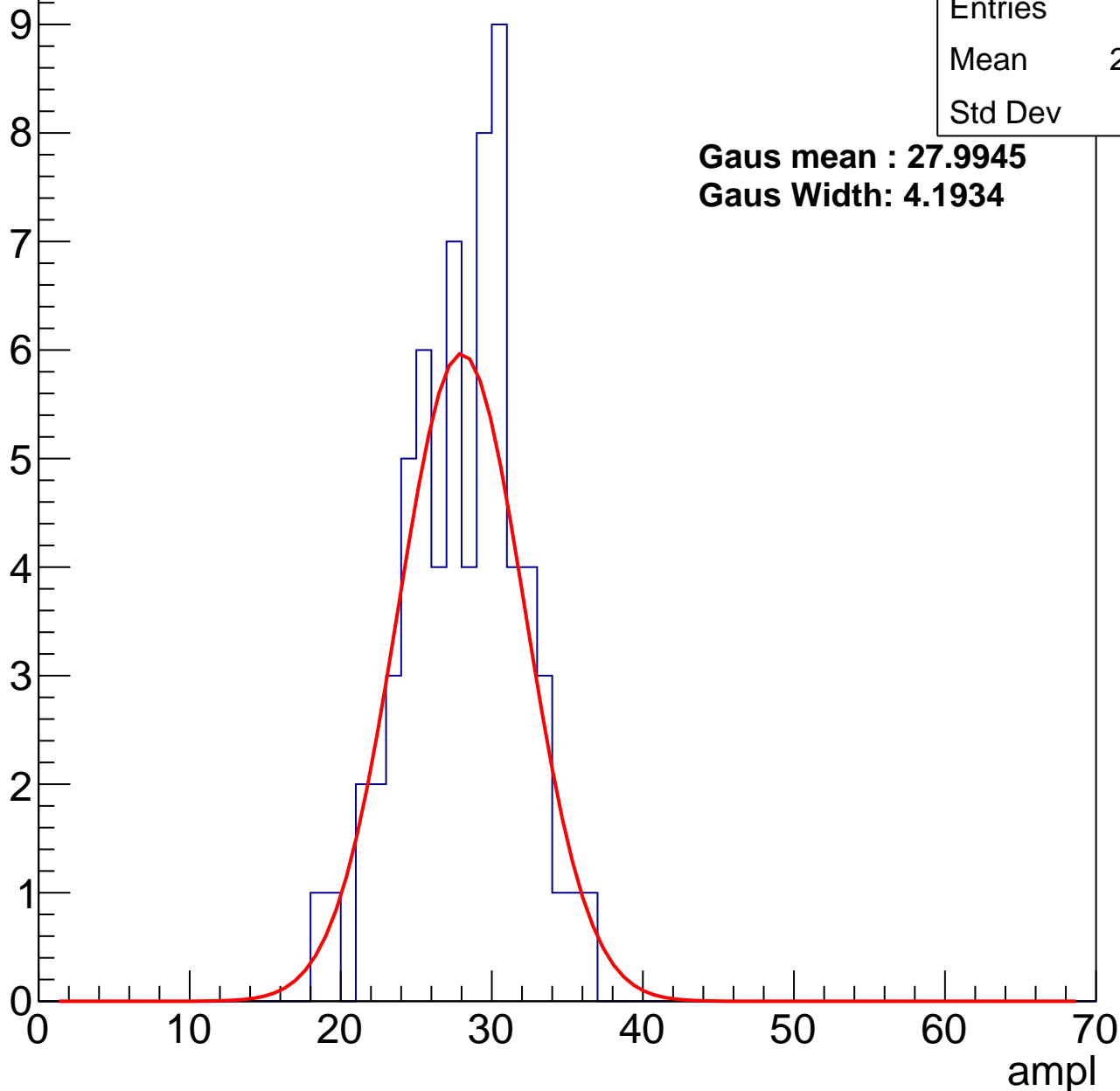
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.65
Std Dev	3.78

Gaus mean : 27.9945

Gaus Width: 4.1934



B1L101S, U26-ch94, adc1

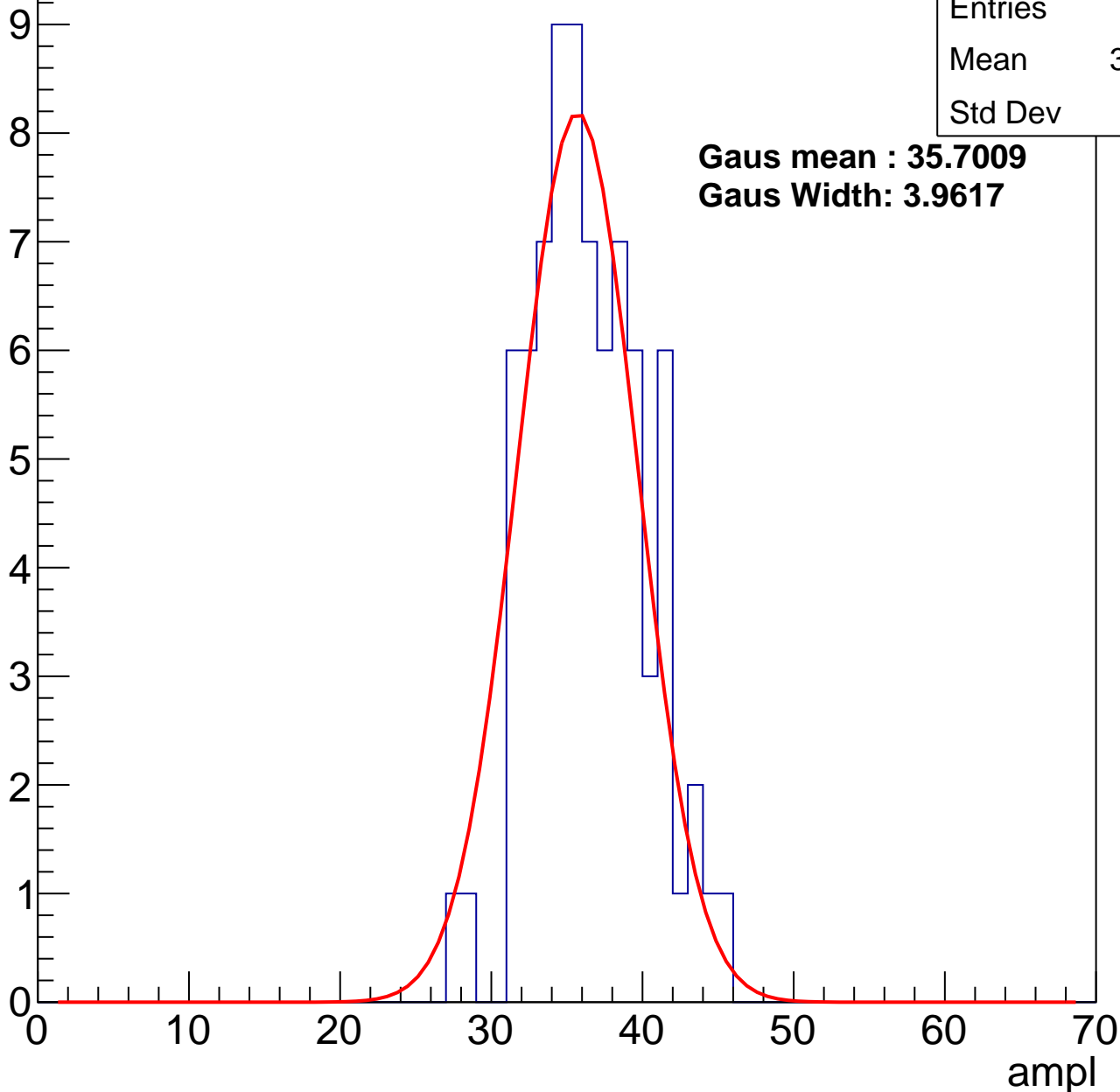
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	35.97
Std Dev	3.67

Gaus mean : 35.7009

Gaus Width: 3.9617



B1L101S, U26-ch94, adc2

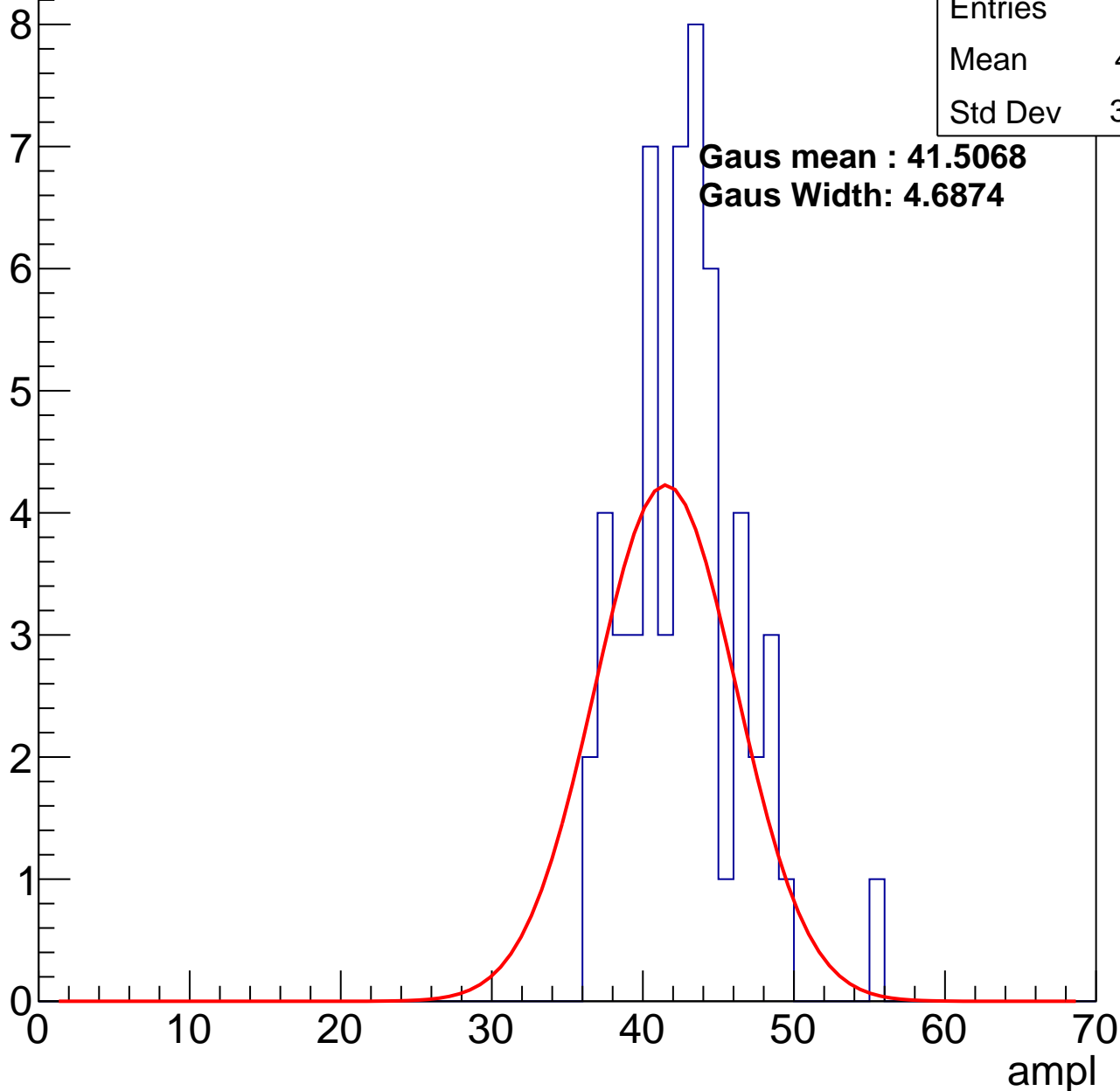
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.31
Std Dev	3.702

Gaus mean : 41.5068

Gaus Width: 4.6874

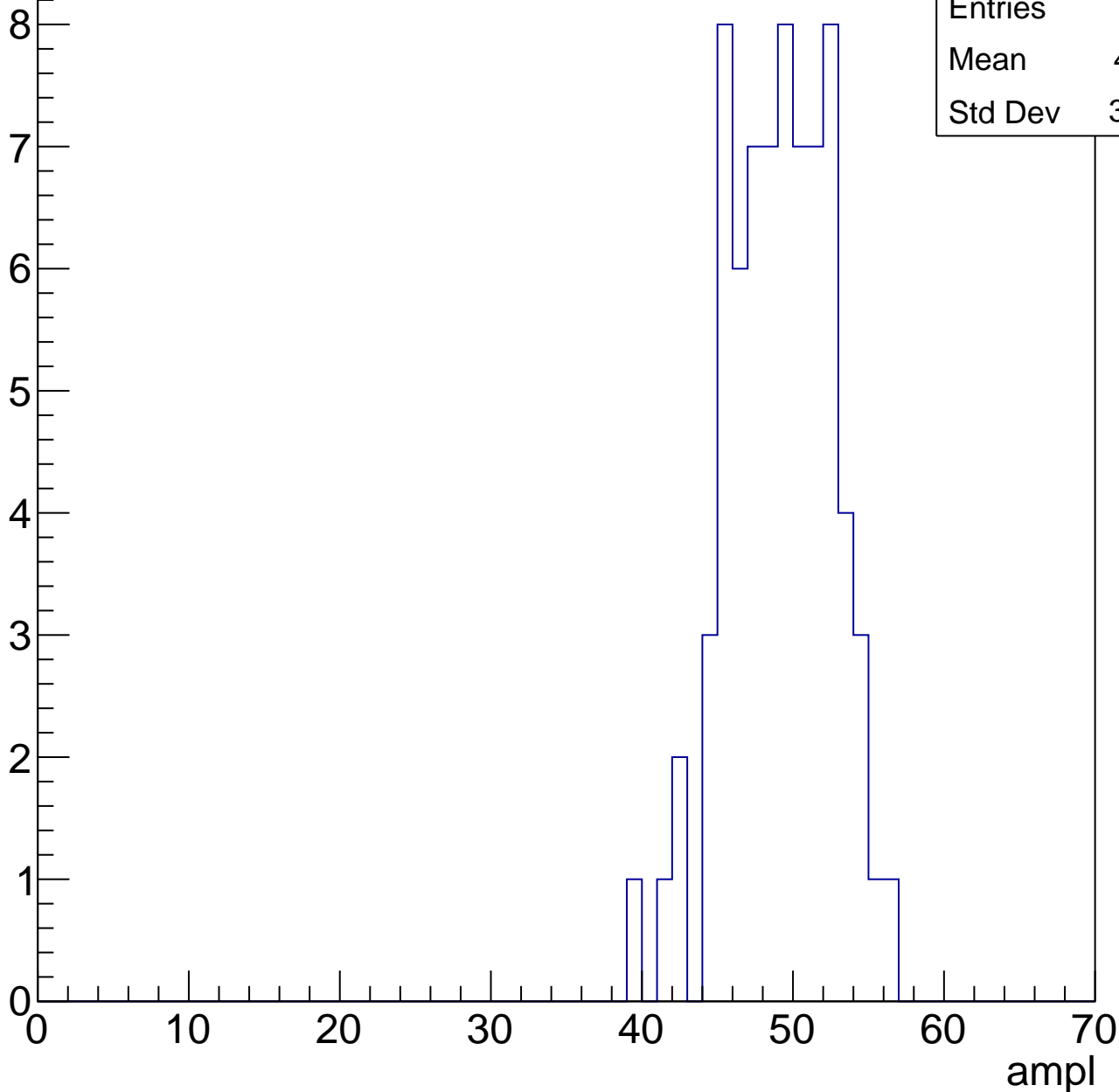


B1L101S, U26-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	48.61
Std Dev	3.436

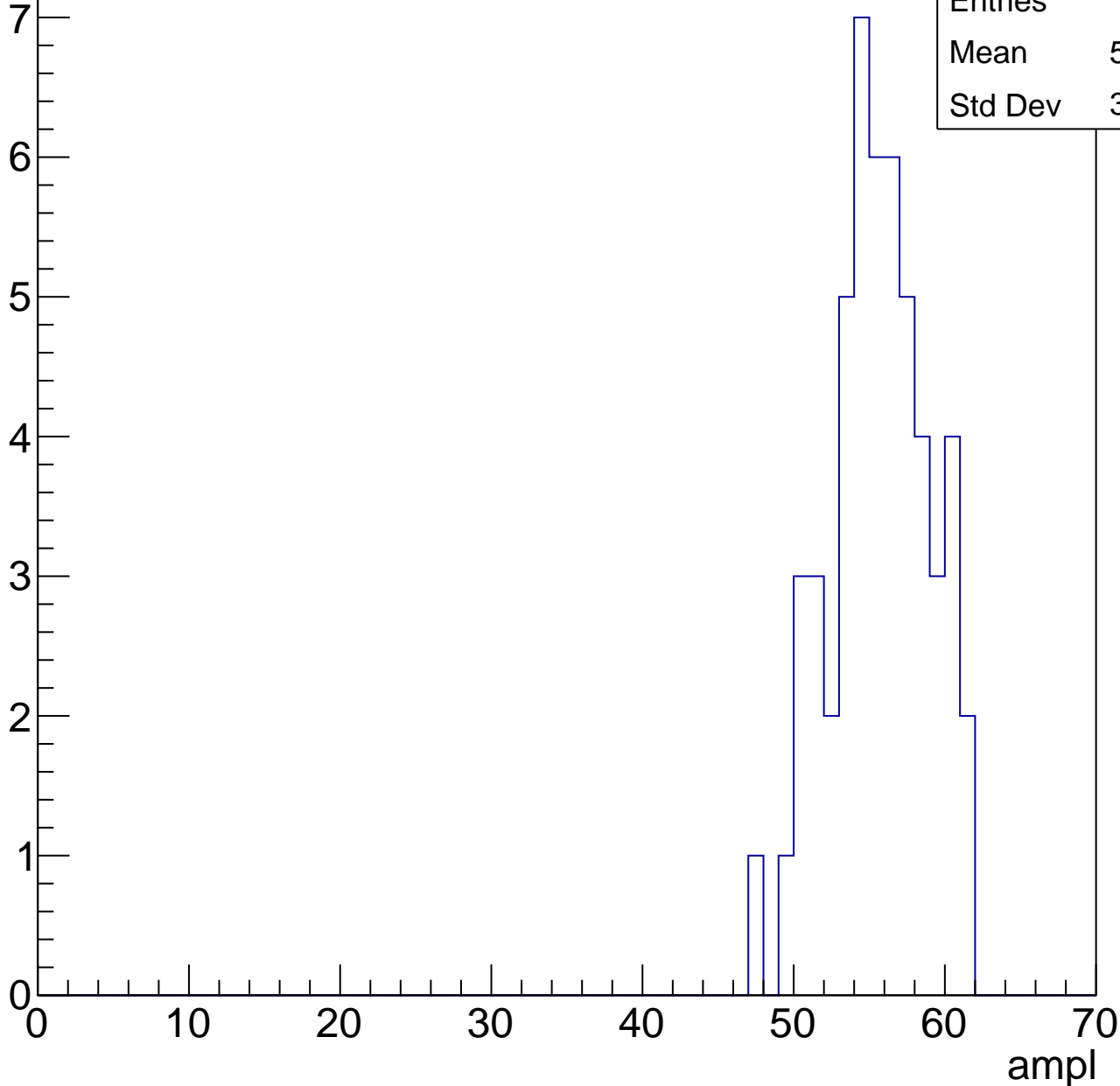


B1L101S, U26-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

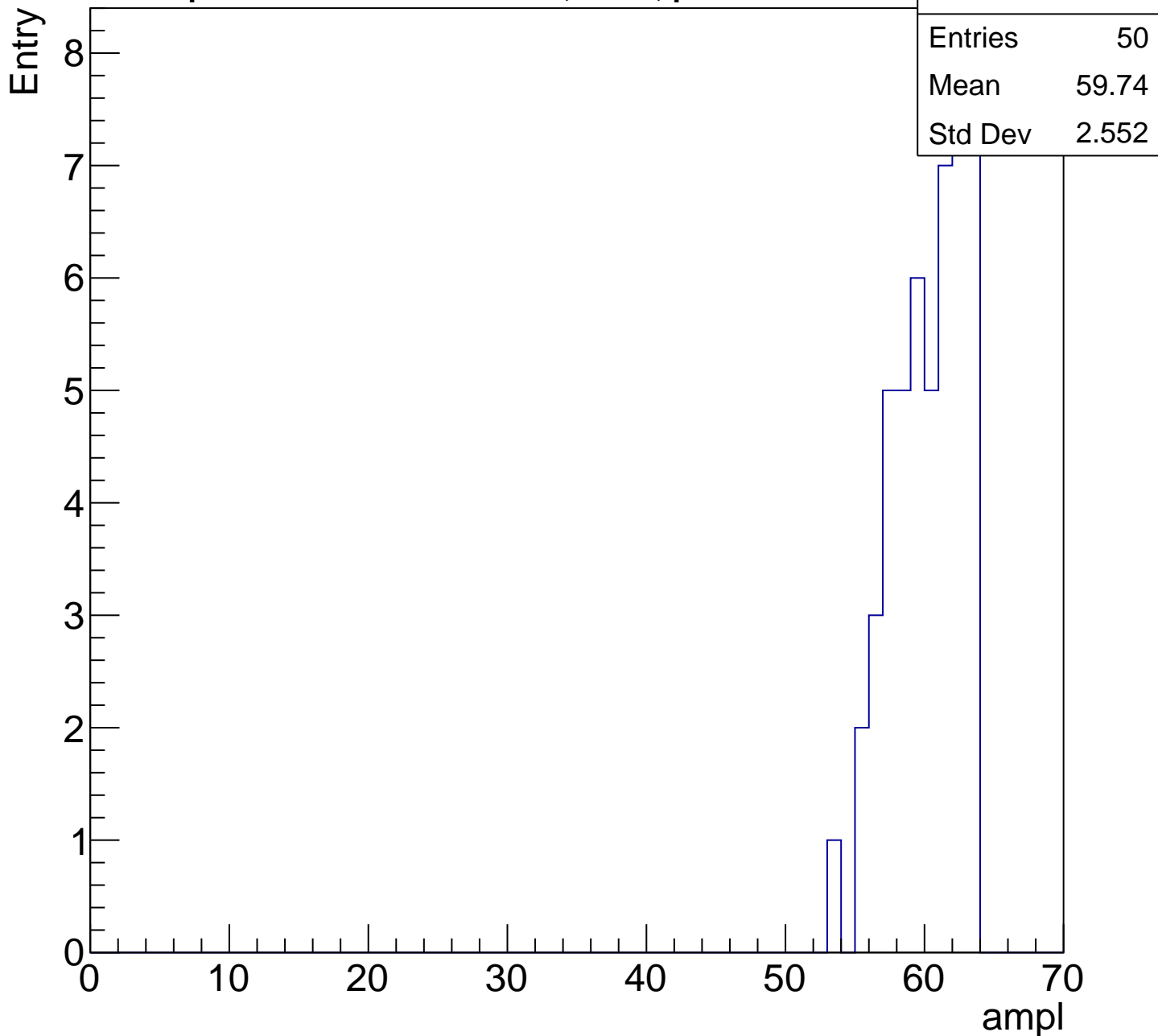
Entry

Entries	52
Mean	55.15
Std Dev	3.249



B1L101S, U26-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U26-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

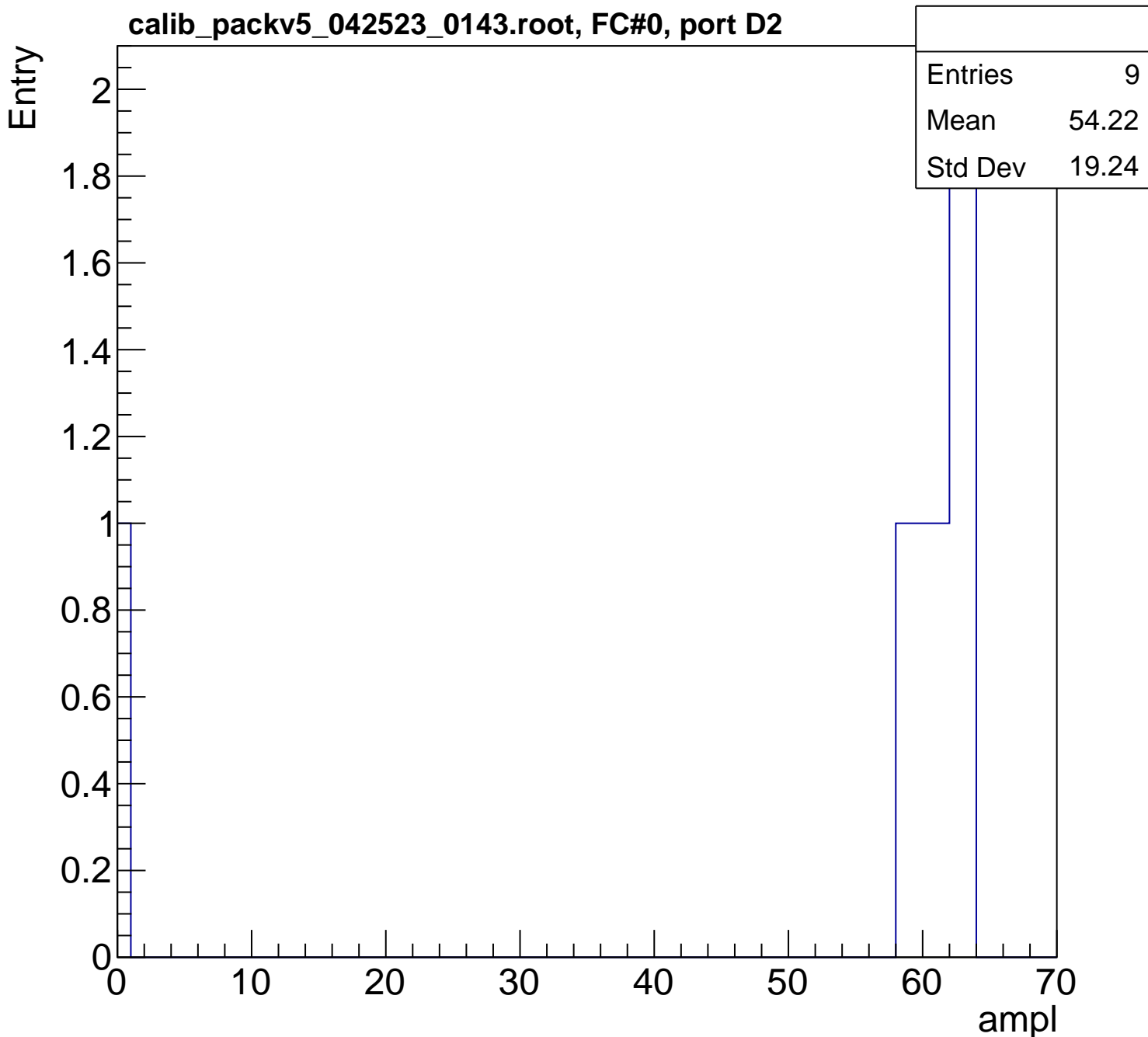
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.22
Std Dev	19.24

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch95, adc0

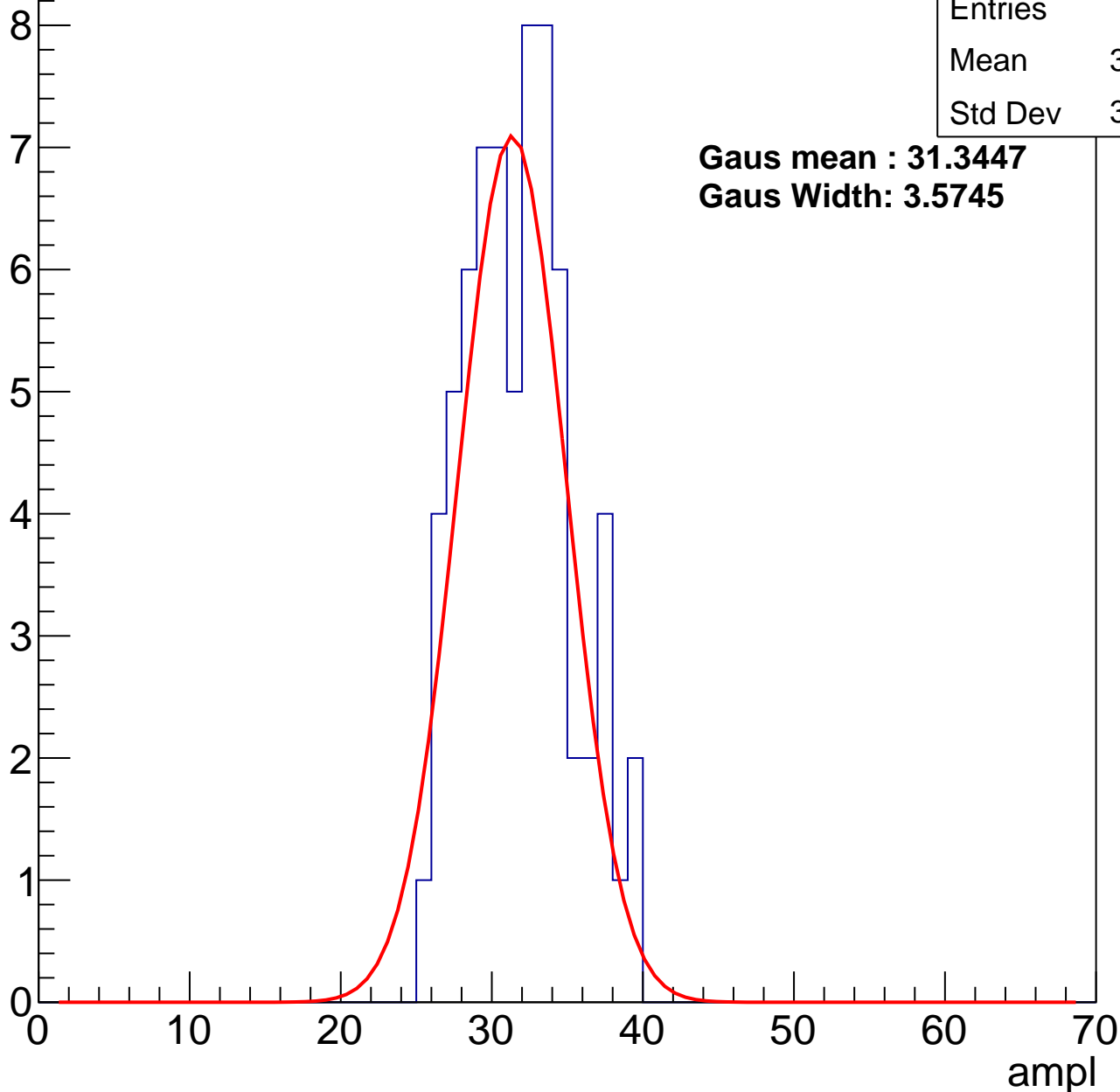
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	31.32
Std Dev	3.419

Gaus mean : 31.3447

Gaus Width: 3.5745



B1L101S, U26-ch95, adc1

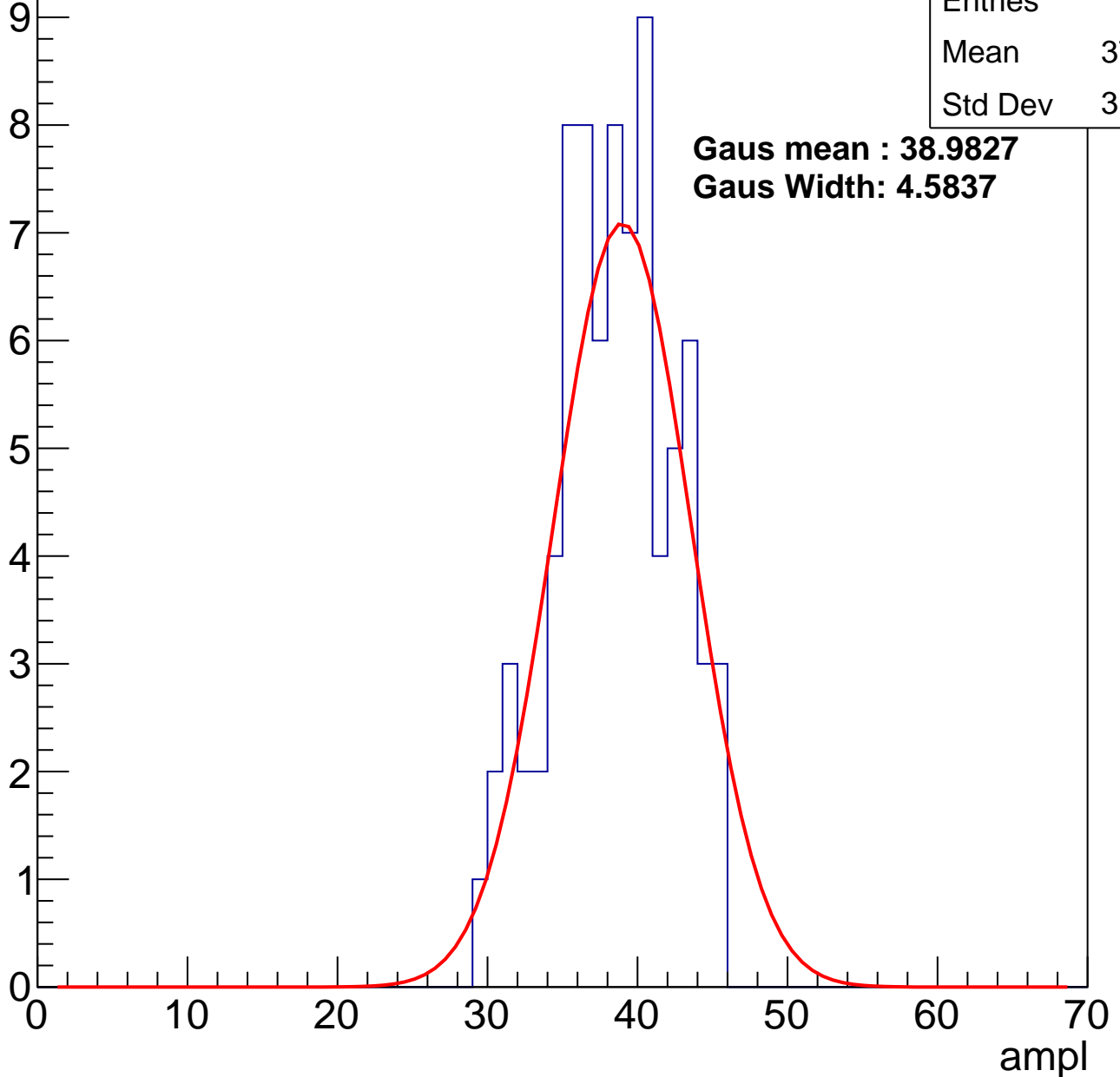
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.95
Std Dev	3.874

Gaus mean : 38.9827

Gaus Width: 4.5837



B1L101S, U26-ch95, adc2

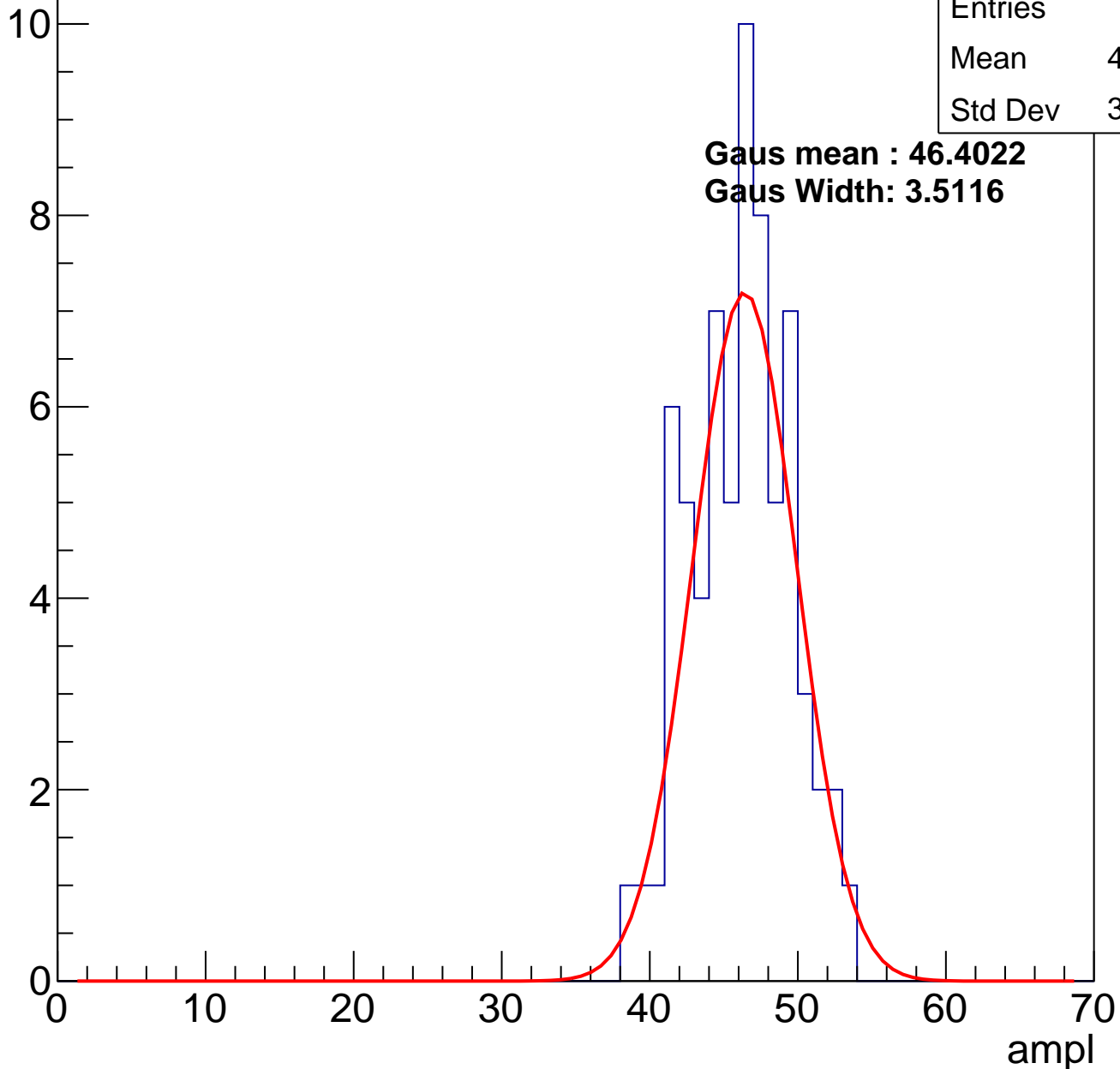
calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	45.68
Std Dev	3.323

Gaus mean : 46.4022

Gaus Width: 3.5116

Entry

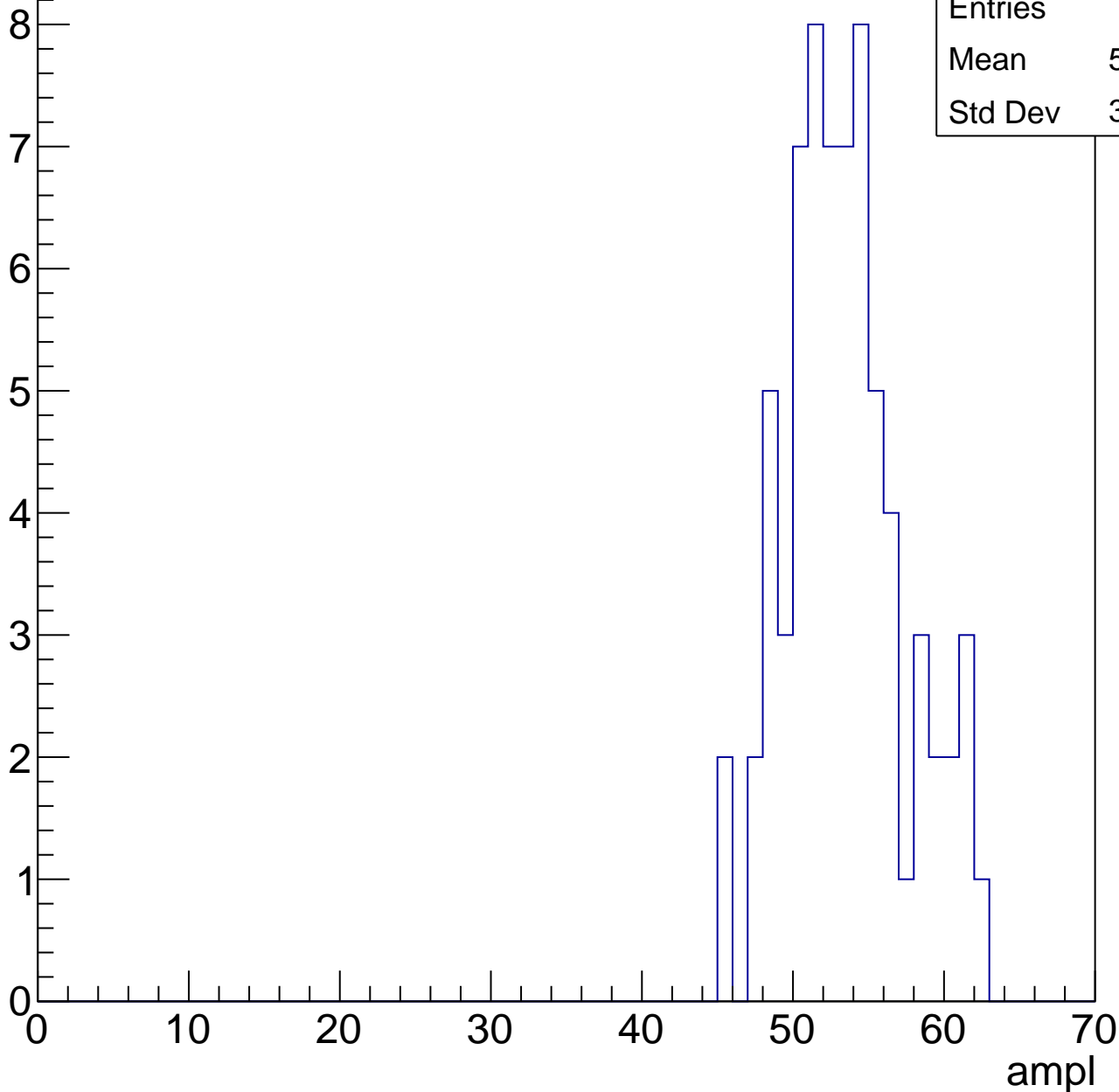


B1L101S, U26-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

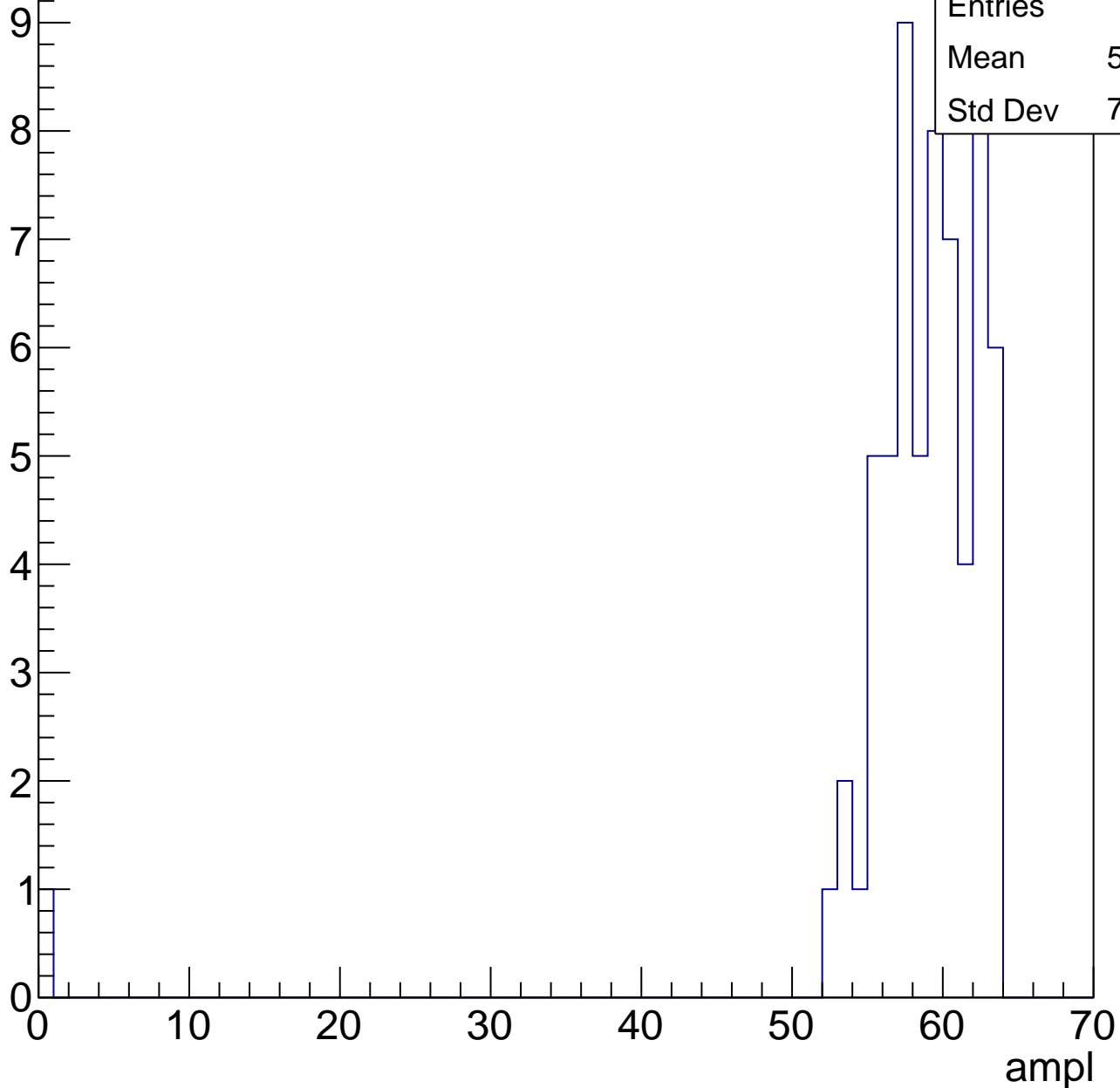
Entries	70
Mean	52.99
Std Dev	3.915



B1L101S, U26-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

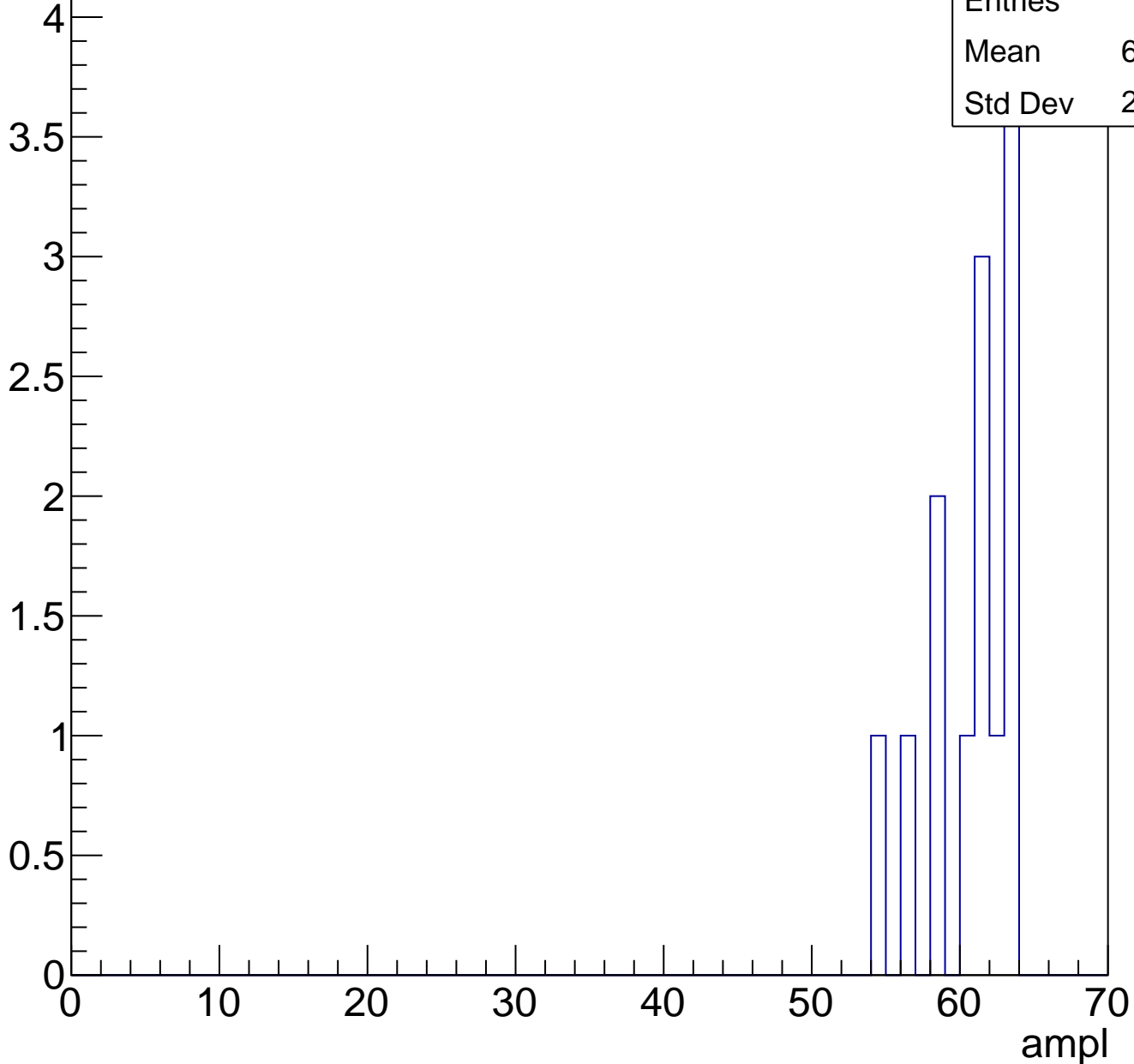
Entry



B1L101S, U26-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch96, adc0

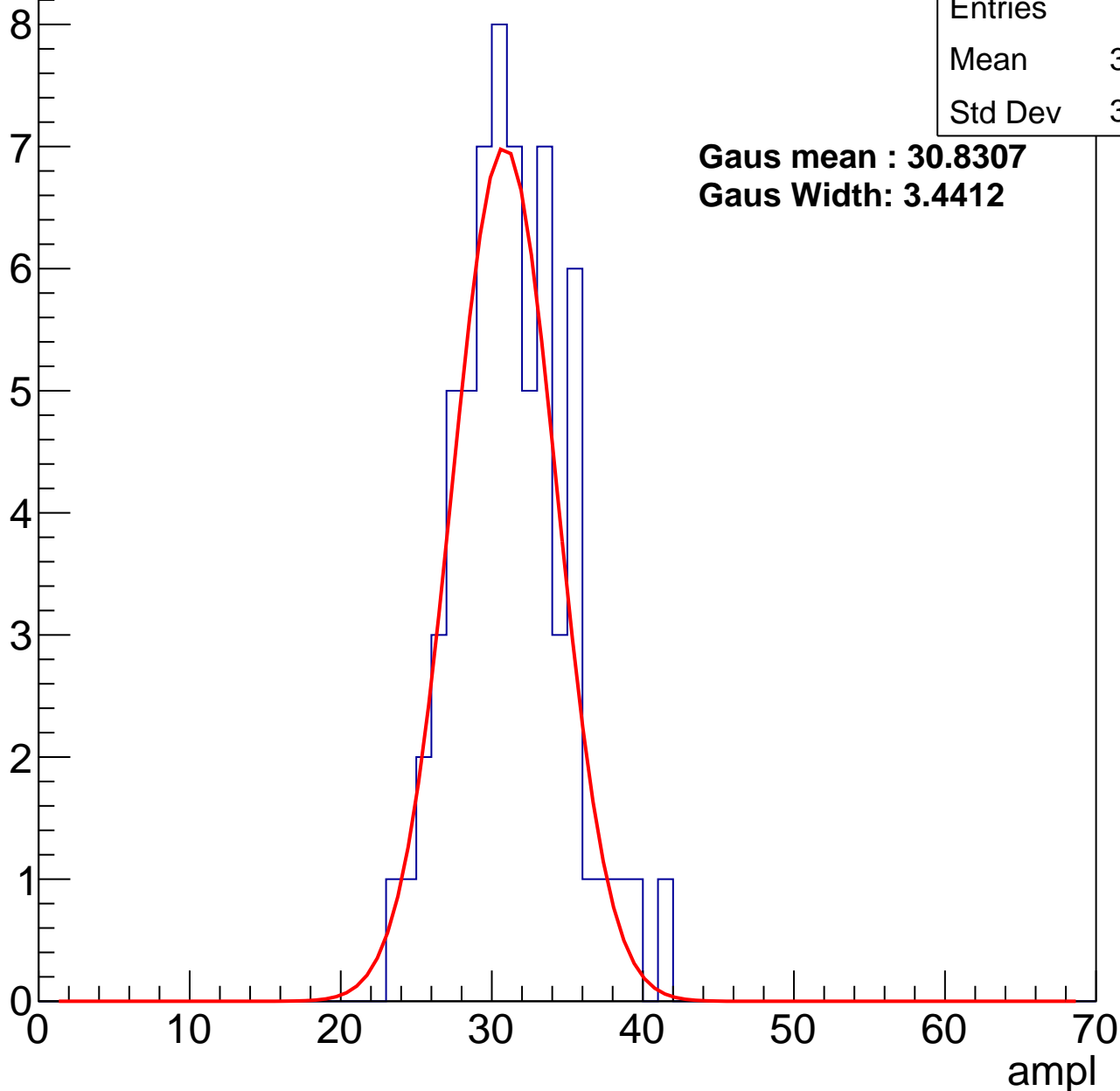
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.83
Std Dev	3.614

Gaus mean : 30.8307

Gaus Width: 3.4412



B1L101S, U26-ch96, adc1

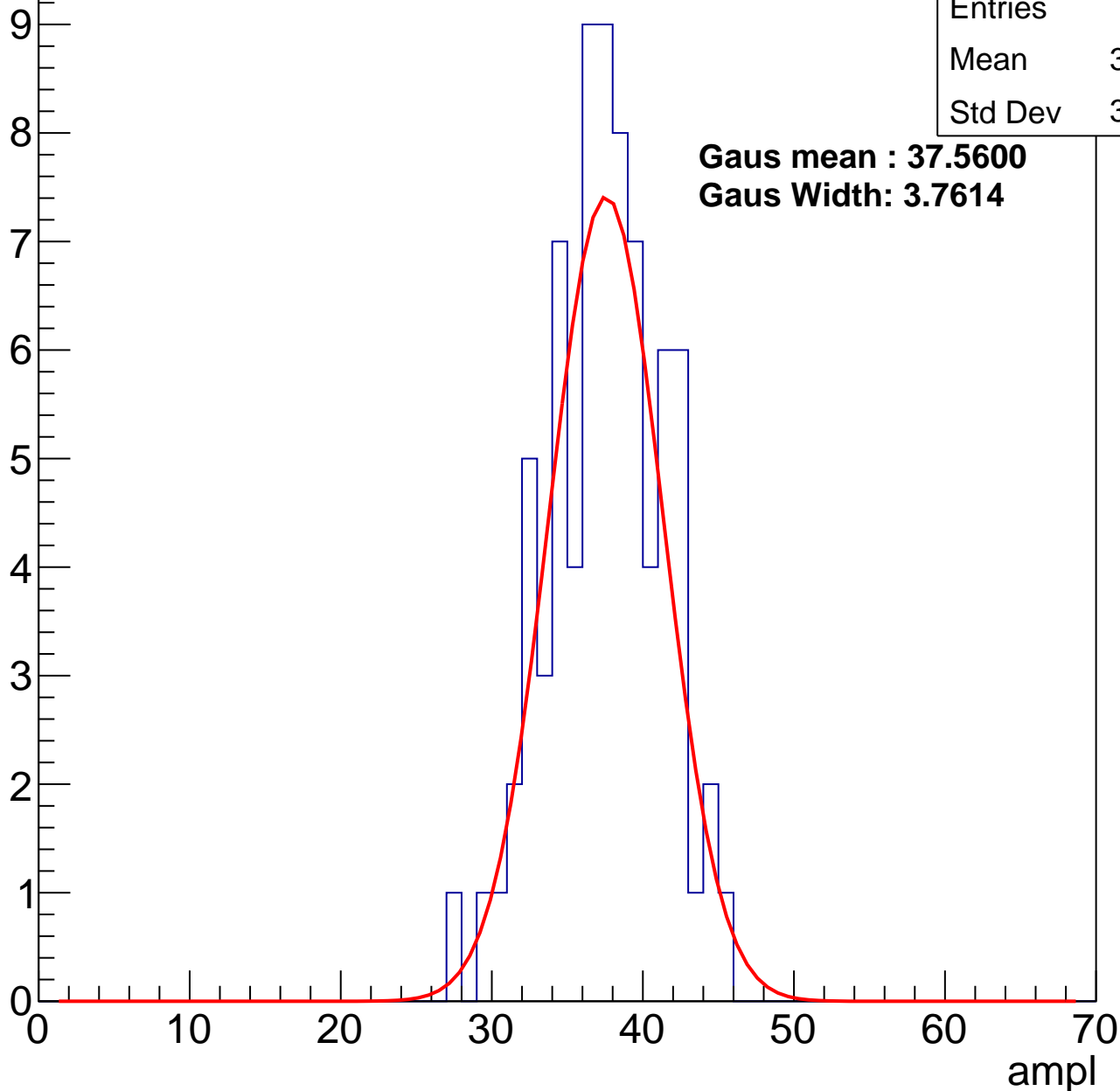
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.05
Std Dev	3.713

Gaus mean : 37.5600

Gaus Width: 3.7614



B1L101S, U26-ch96, adc2

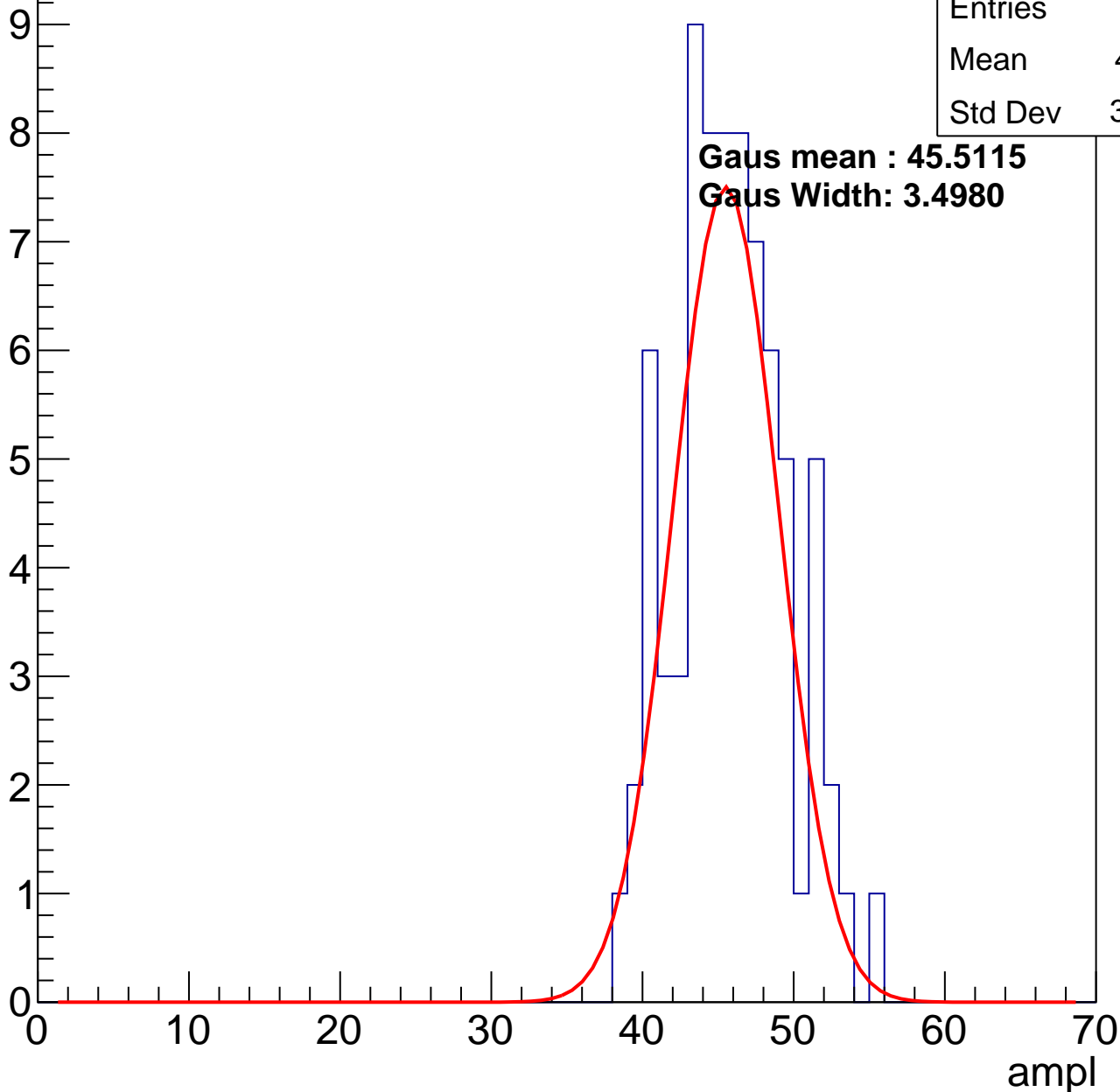
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	45.41
Std Dev	3.653

Gaus mean : 45.5115

Gaus Width: 3.4980

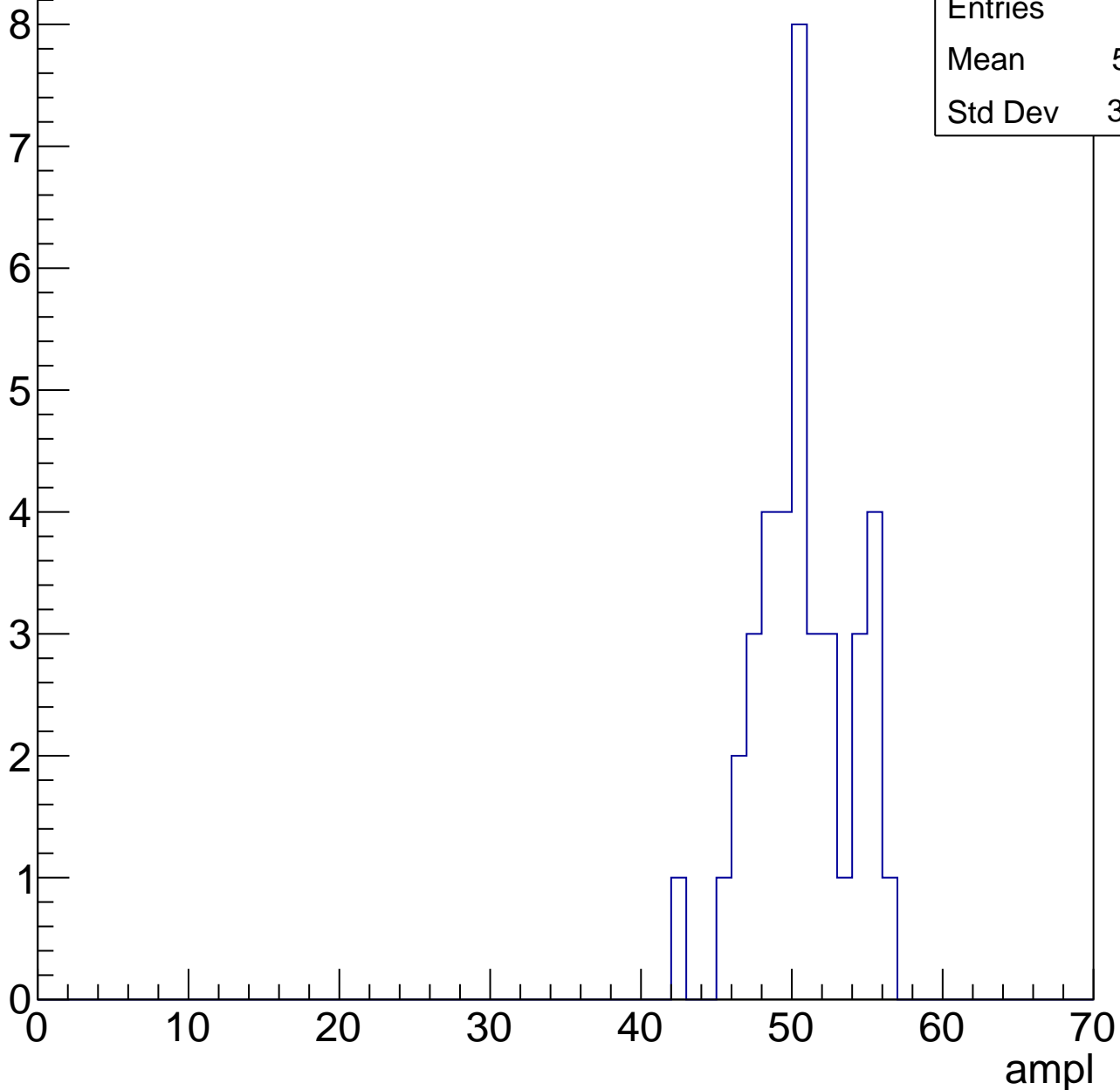


B1L101S, U26-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	50.21
Std Dev	3.139

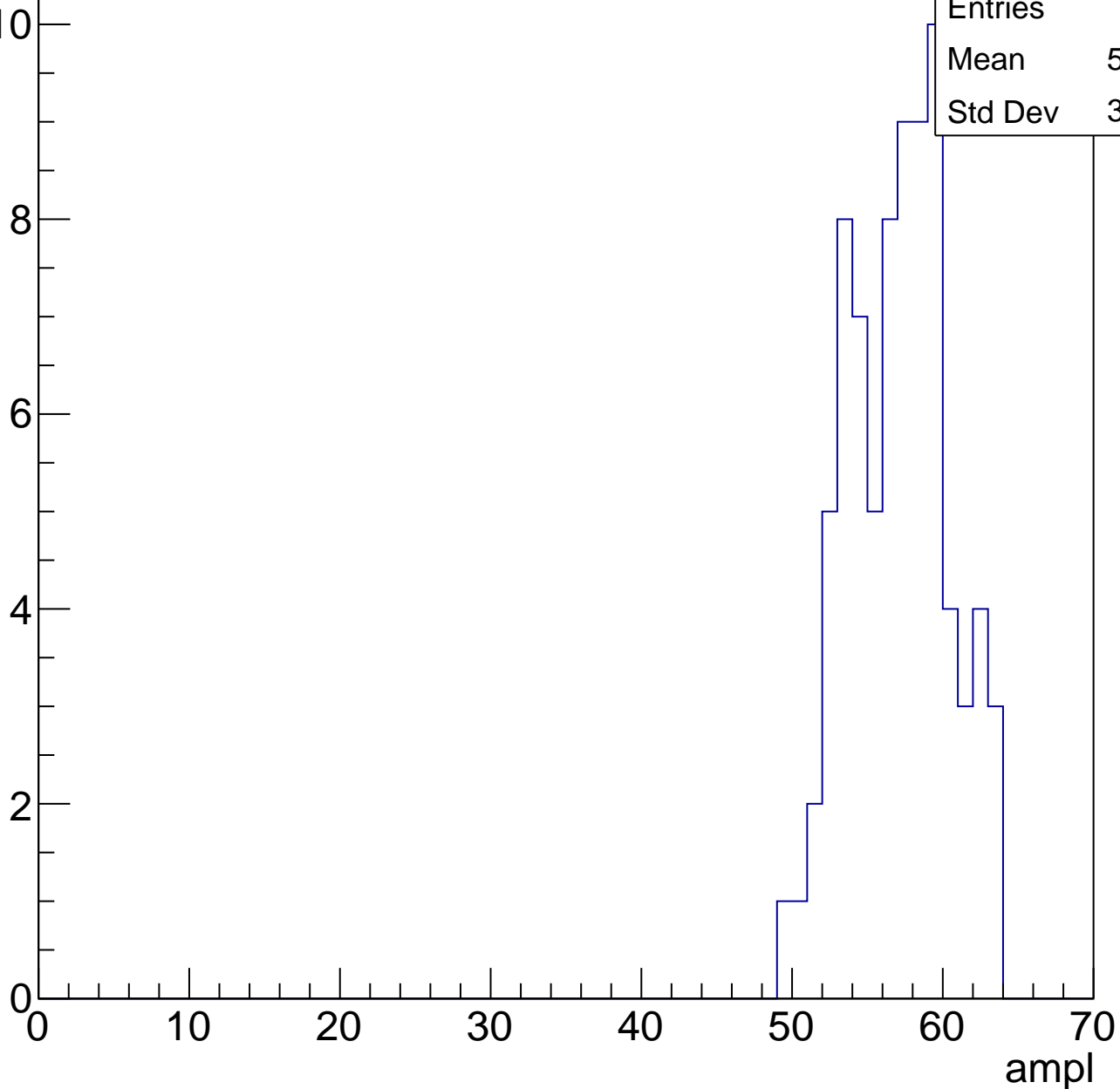


B1L101S, U26-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	56.59
Std Dev	3.294

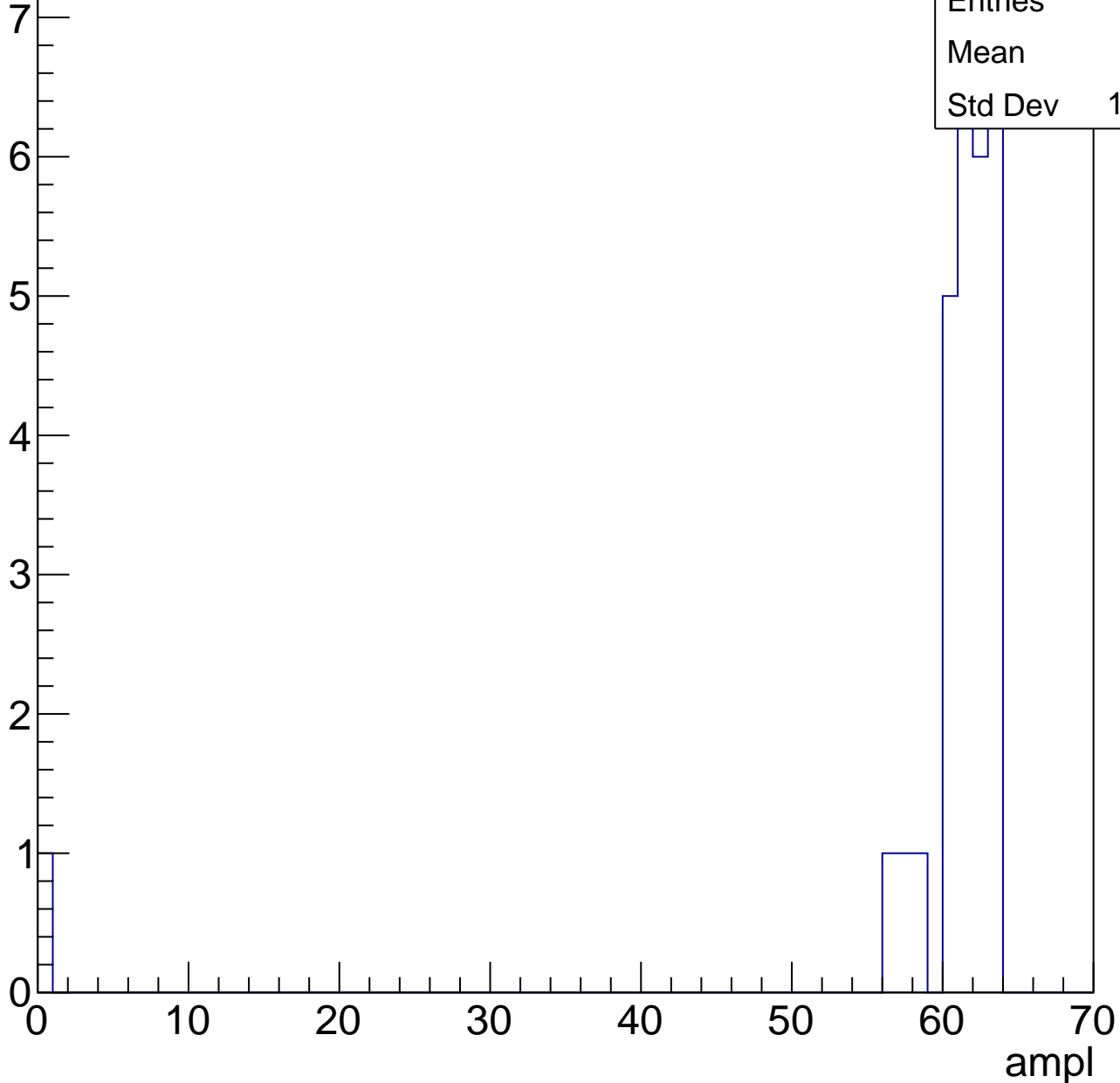


B1L101S, U26-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	59
Std Dev	11.29



B1L101S, U26-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	60
Std Dev	0

B1L101S, U26-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch97, adc0

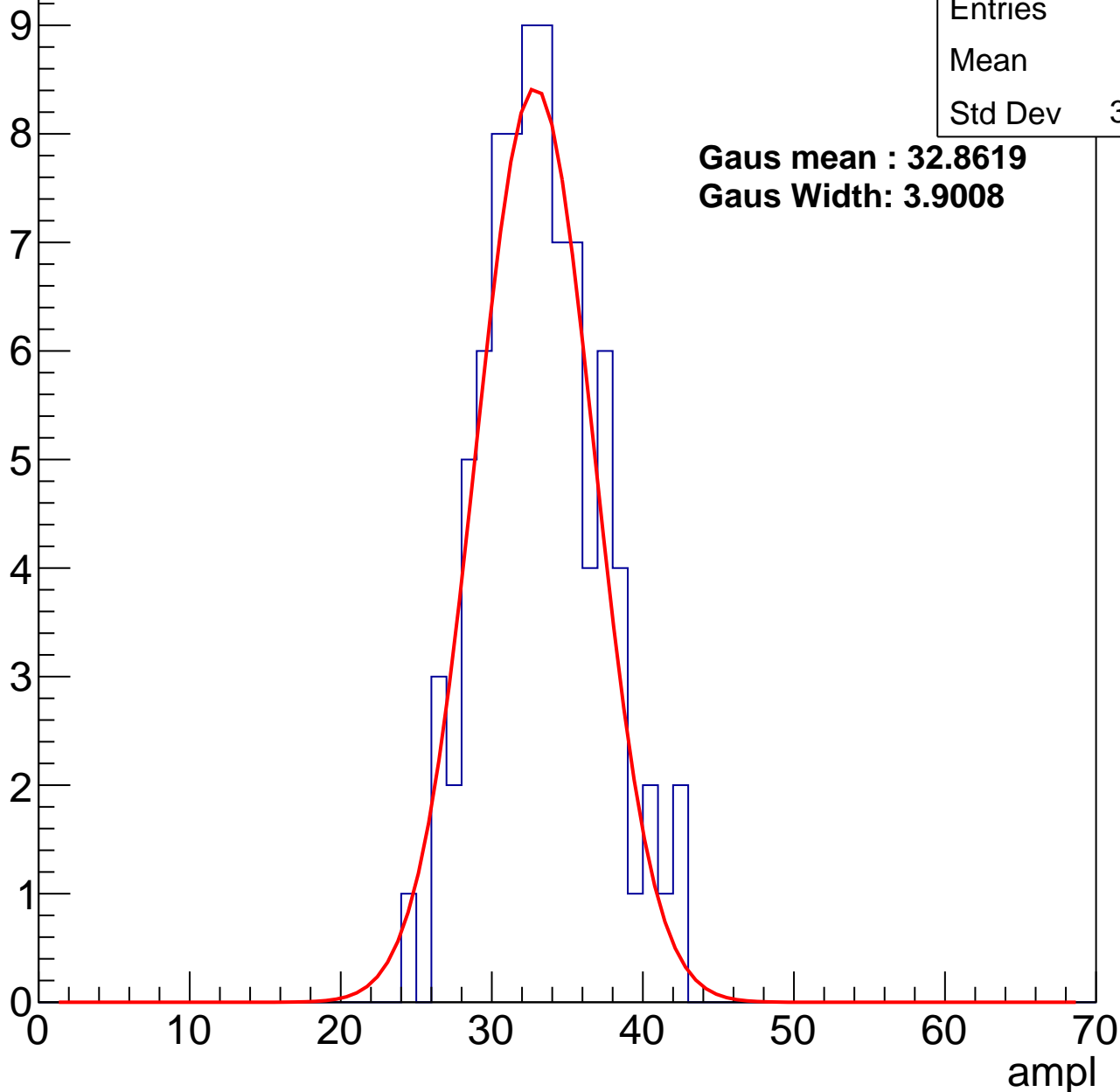
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	32.8
Std Dev	3.846

Gaus mean : 32.8619

Gaus Width: 3.9008



B1L101S, U26-ch97, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	40.24
Std Dev	3.429

Gaus mean : 40.5245

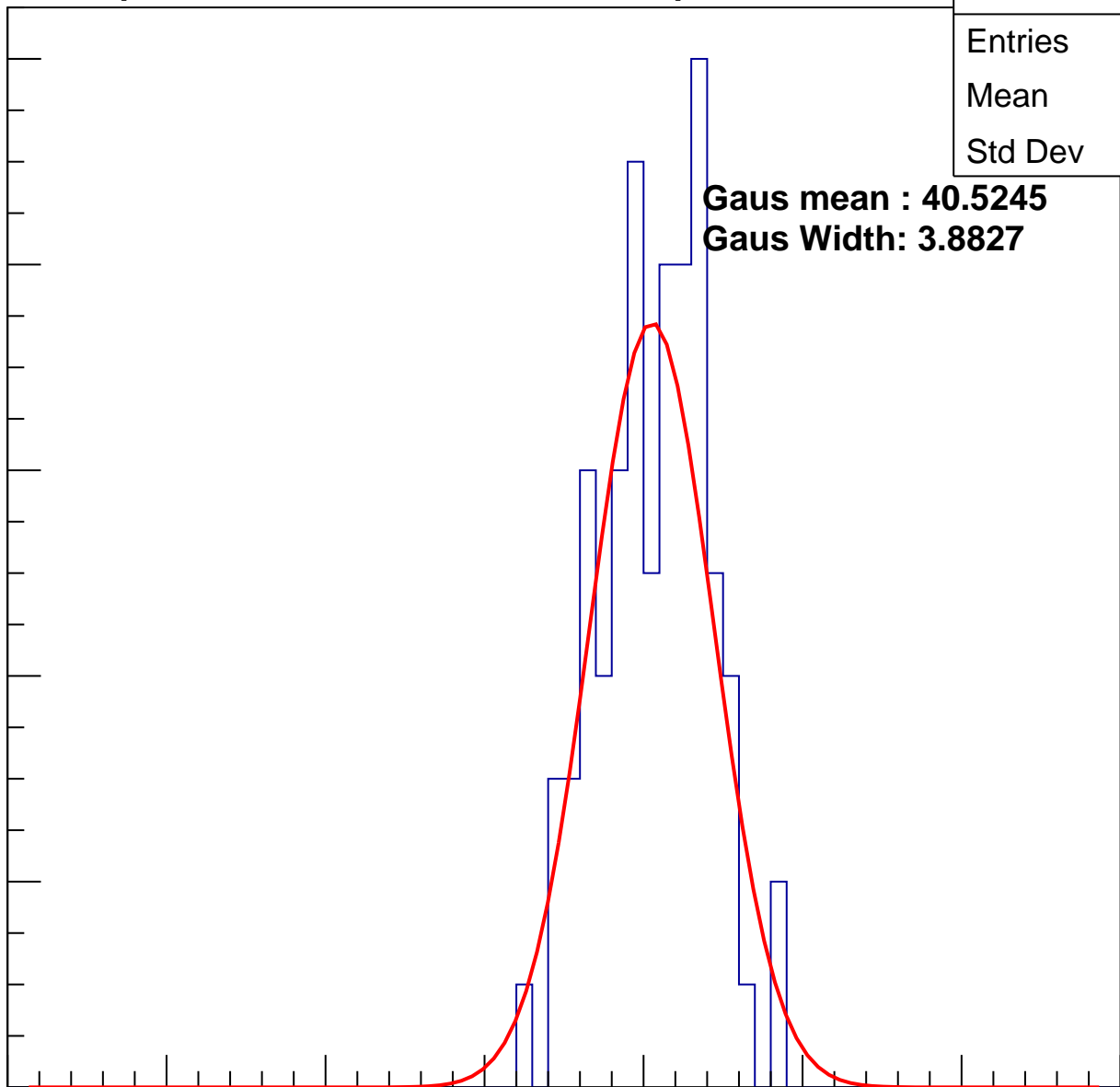
Gaus Width: 3.8827

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch97, adc2

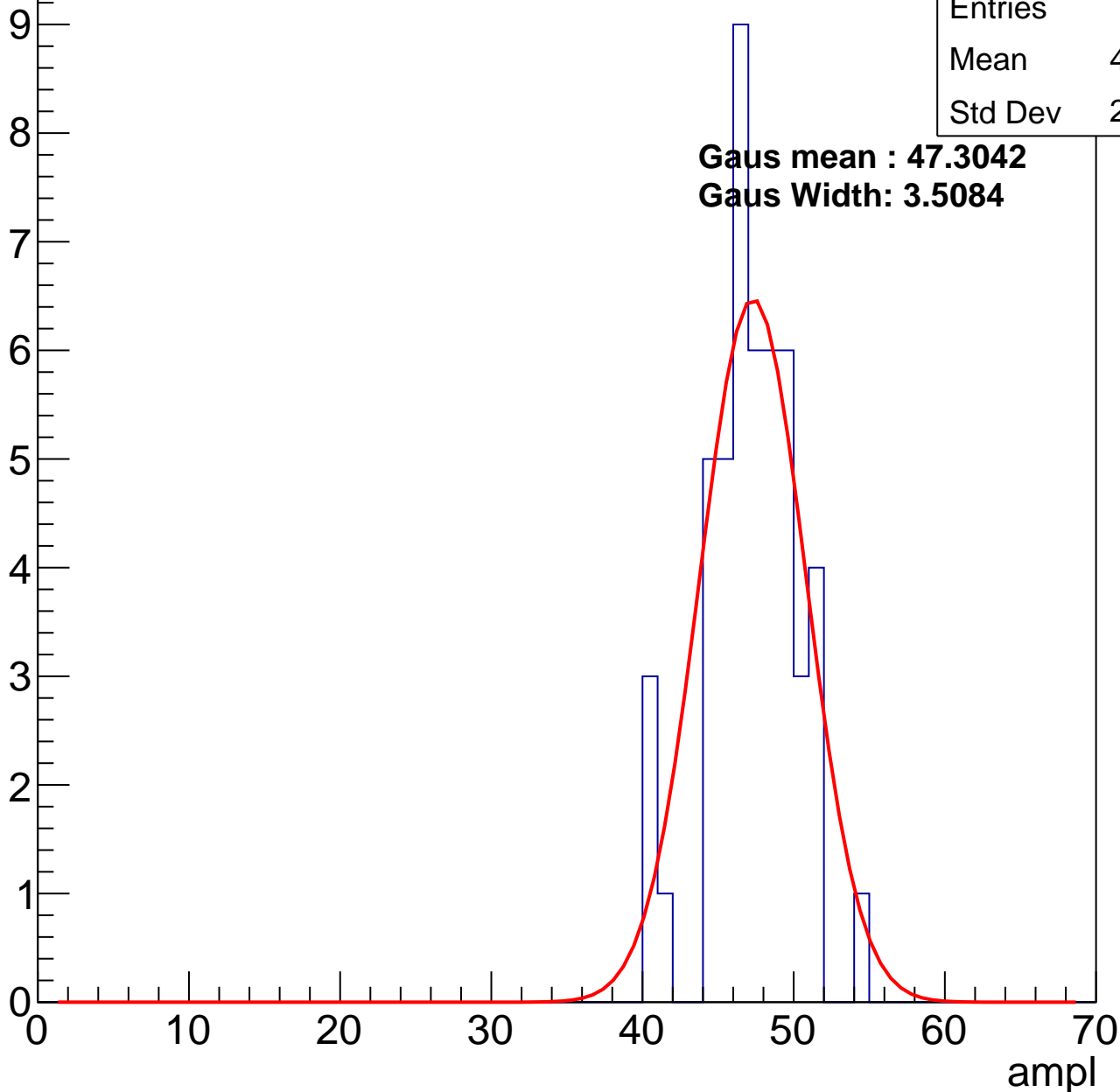
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	46.78
Std Dev	2.943

Gaus mean : 47.3042

Gaus Width: 3.5084

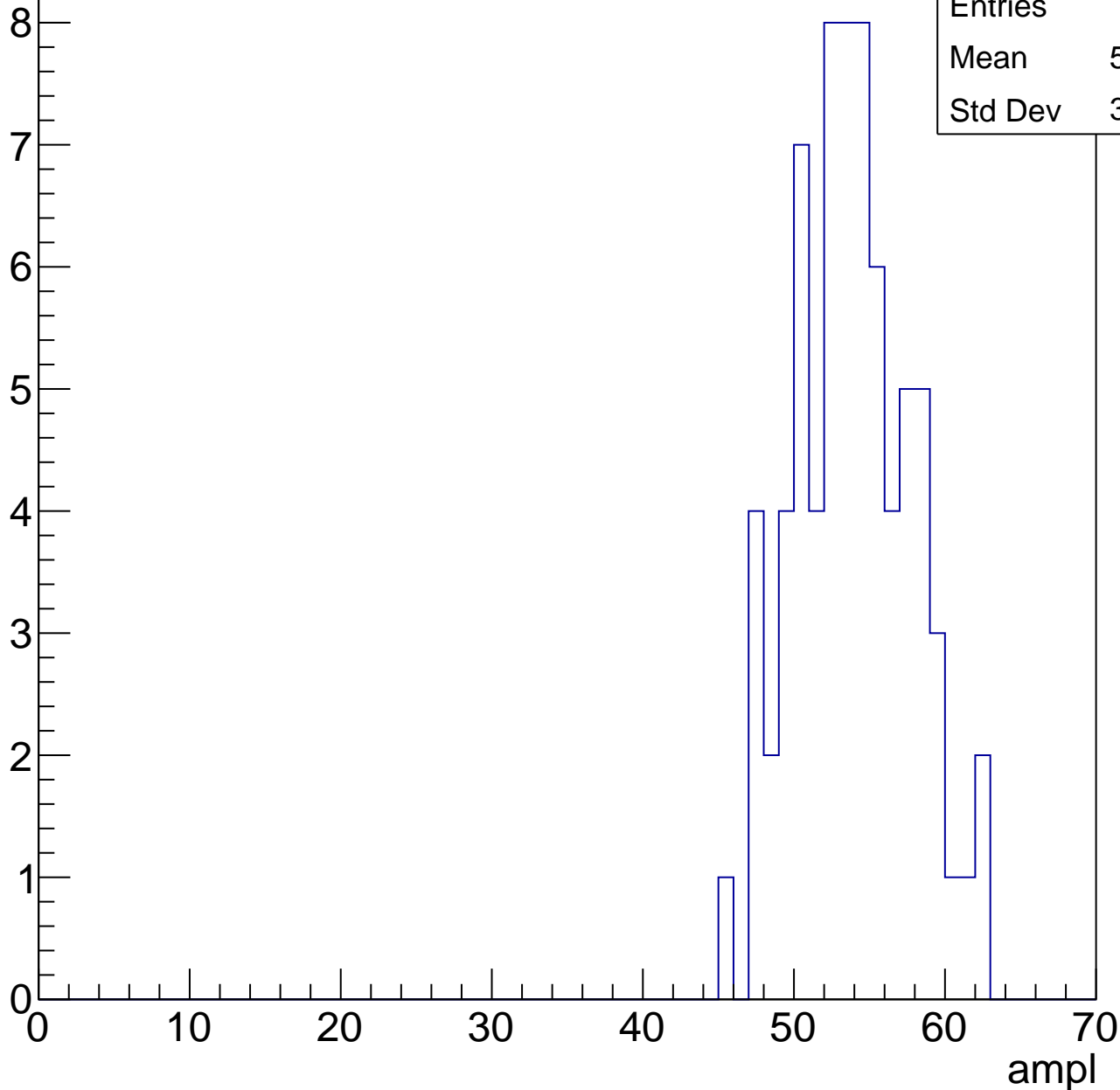


B1L101S, U26-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

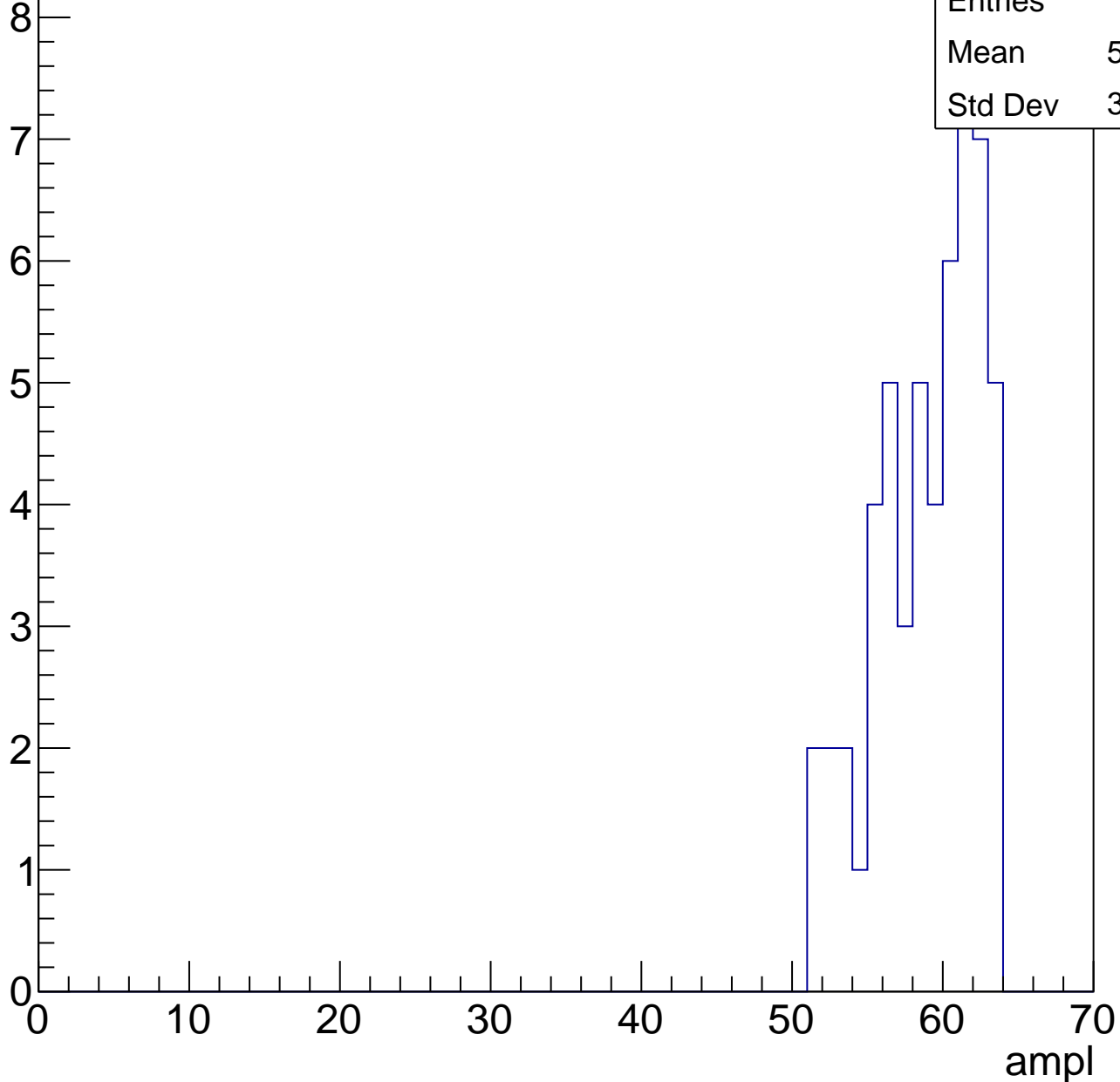
Entries	73
Mean	53.45
Std Dev	3.793



B1L101S, U26-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

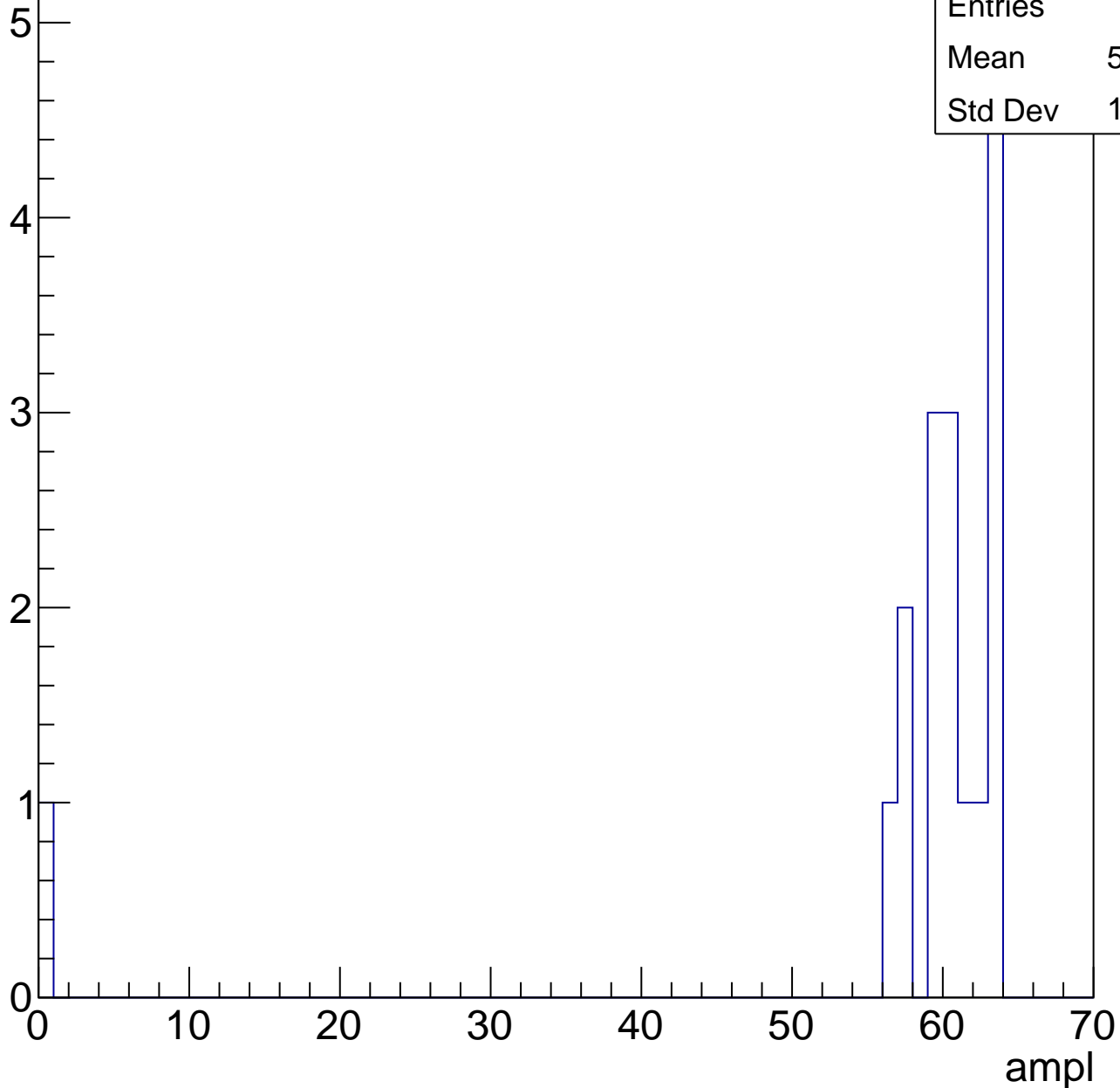


B1L101S, U26-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	56.76
Std Dev	14.37



B1L101S, U26-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch98, adc0

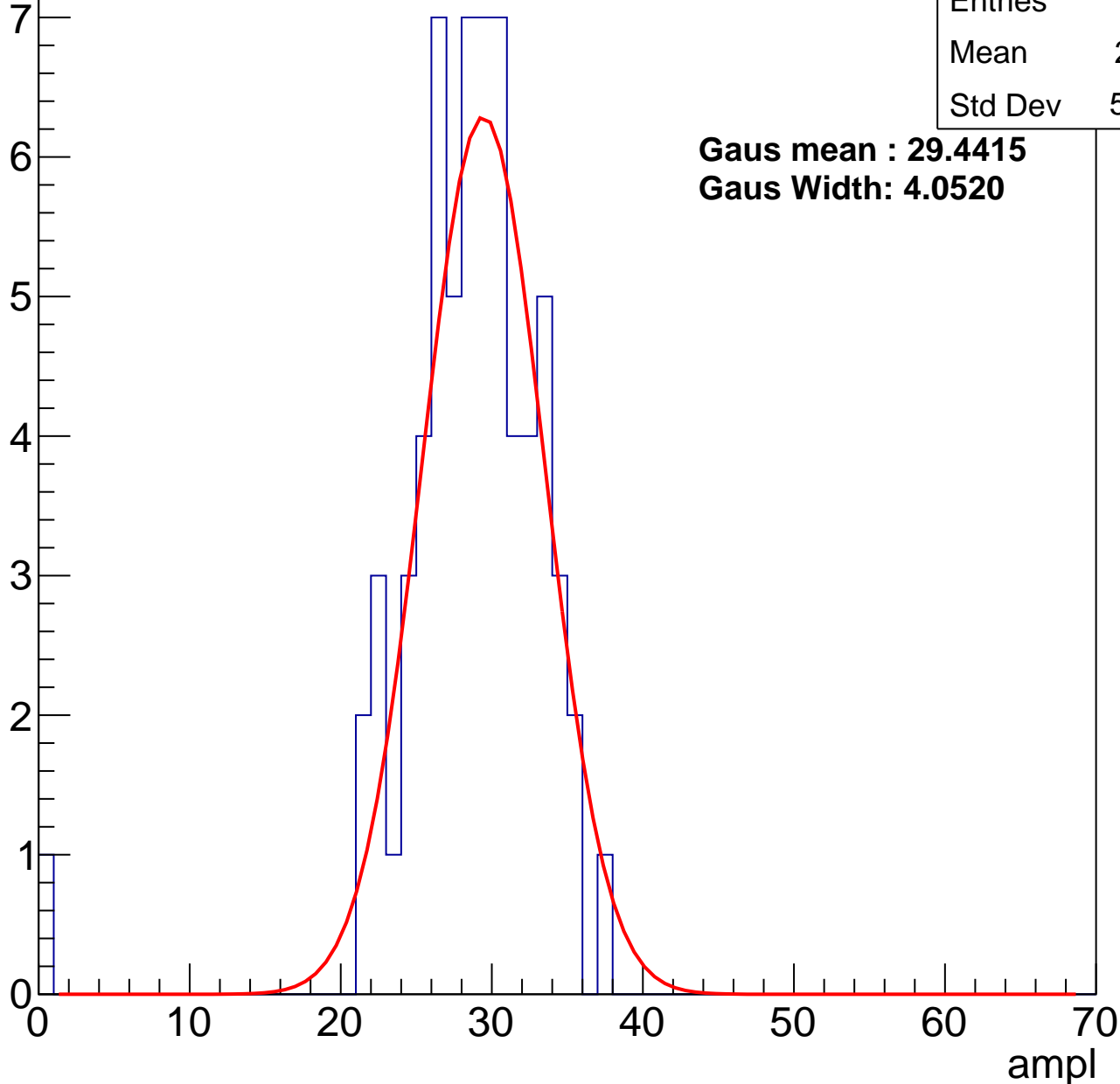
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.11
Std Dev	5.043

Gaus mean : 29.4415

Gaus Width: 4.0520



B1L101S, U26-ch98, adc1

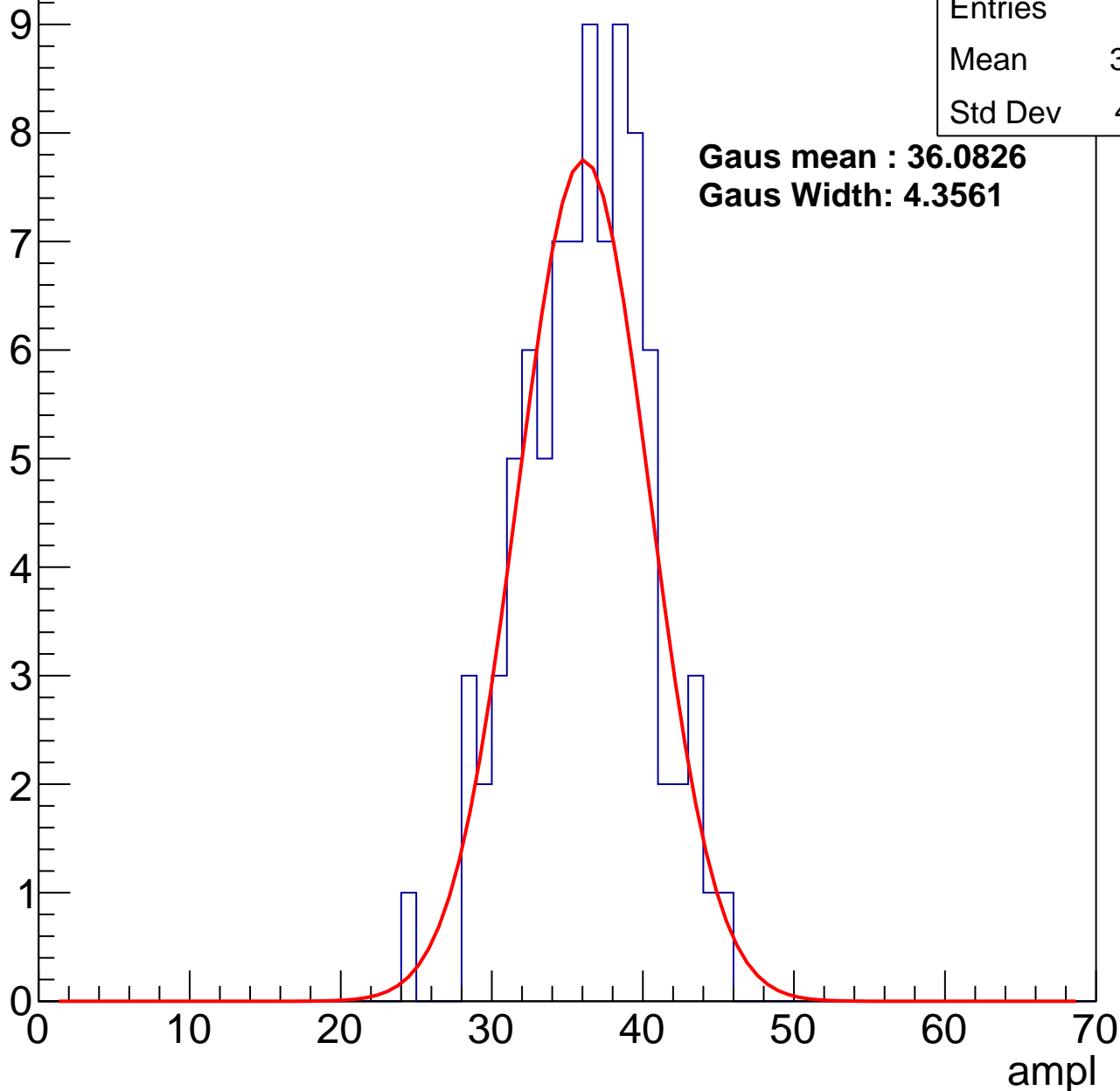
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	35.77
Std Dev	4.101

Gaus mean : 36.0826

Gaus Width: 4.3561



B1L101S, U26-ch98, adc2

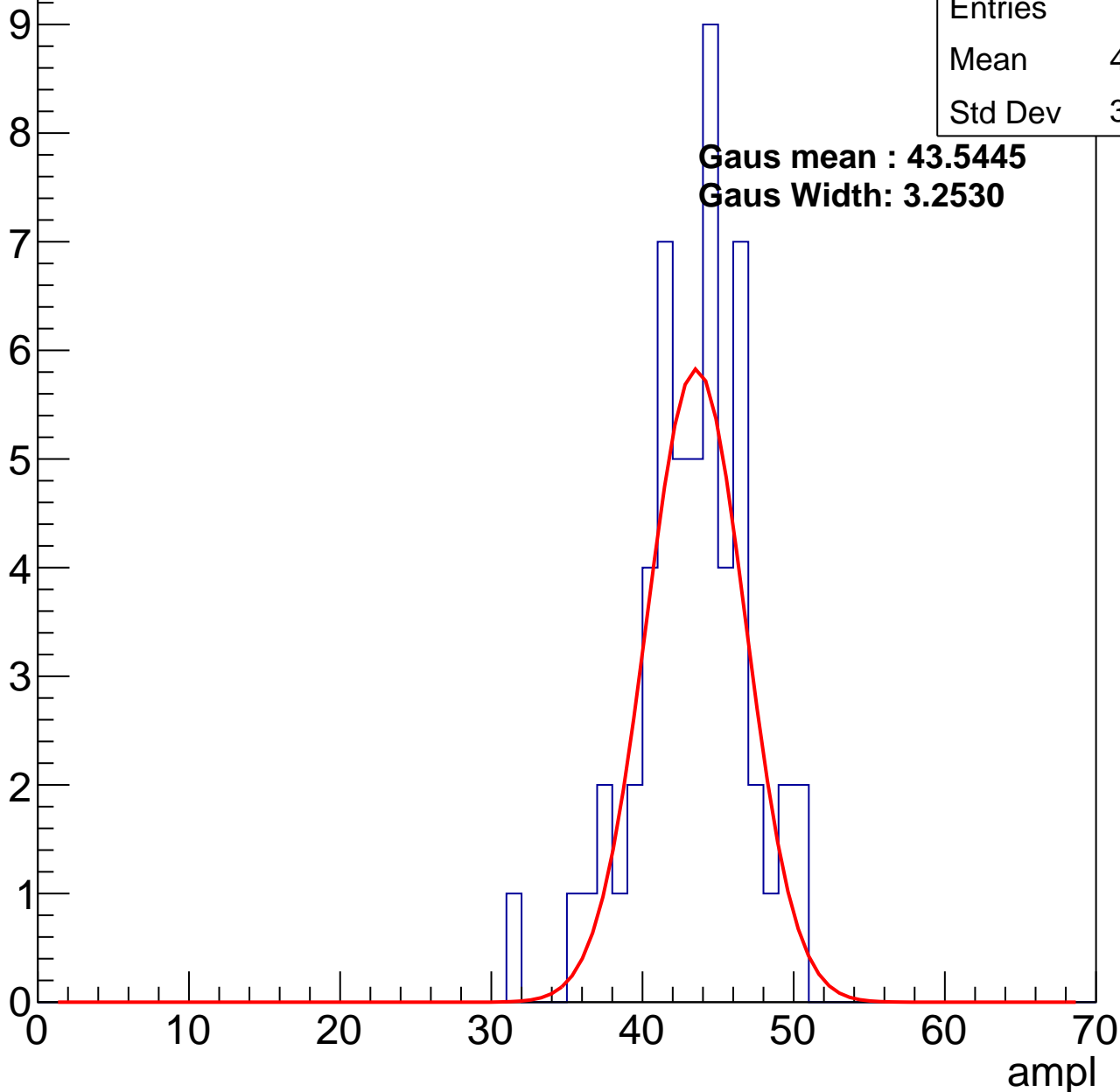
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.89
Std Dev	3.707

Gaus mean : 43.5445

Gaus Width: 3.2530

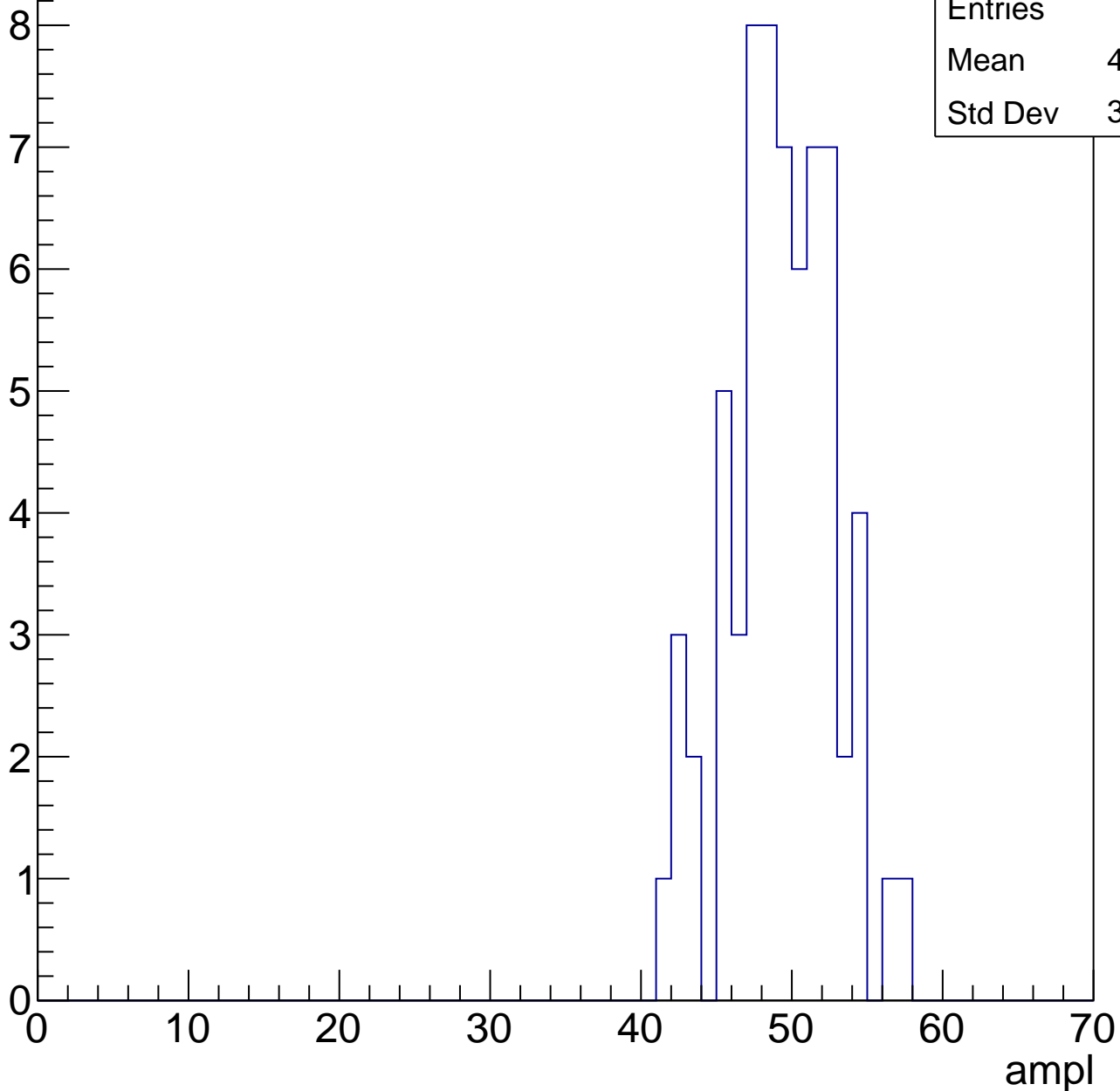


B1L101S, U26-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

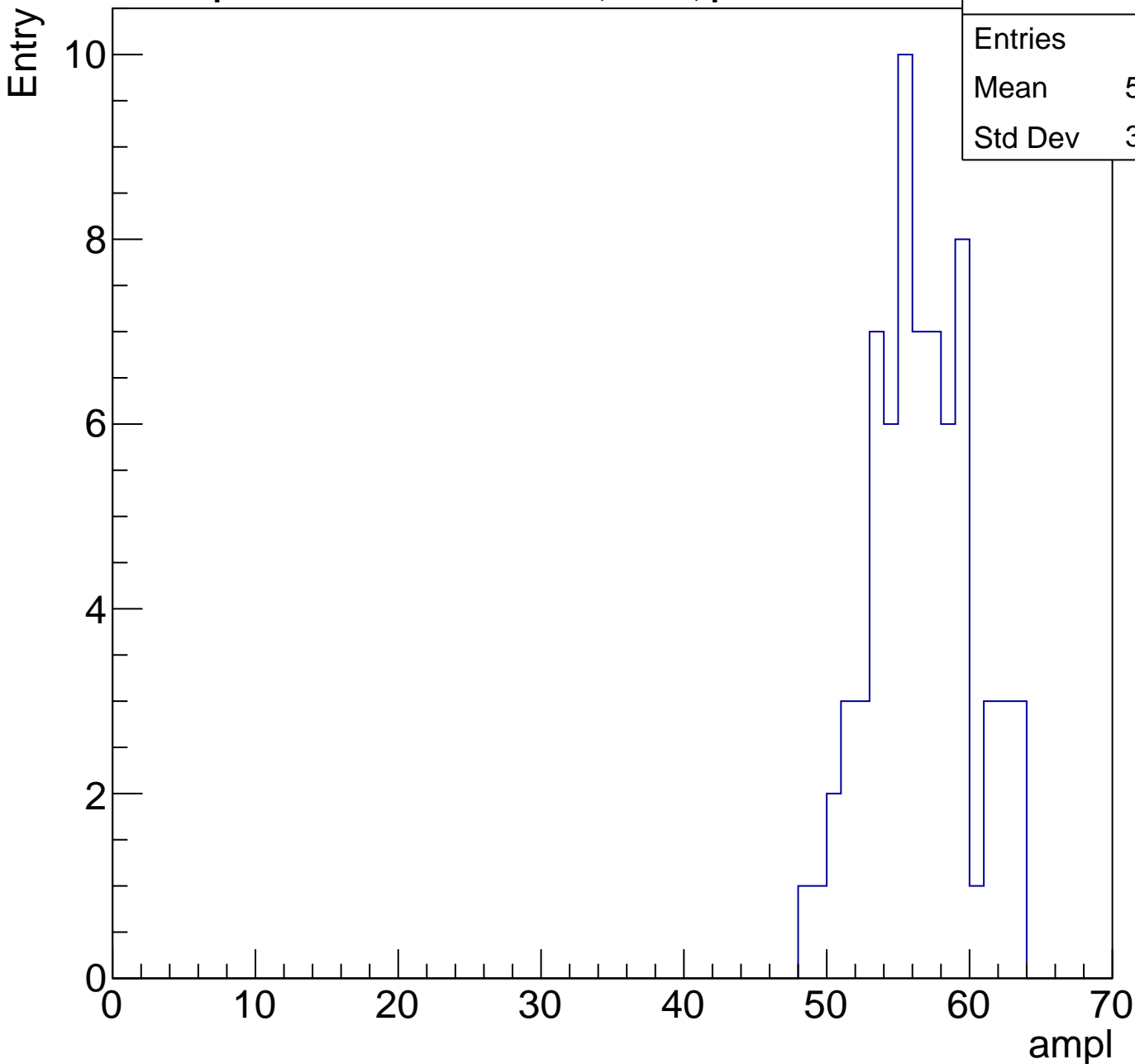
Entries	65
Mean	48.85
Std Dev	3.443



B1L101S, U26-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	56.06
Std Dev	3.468

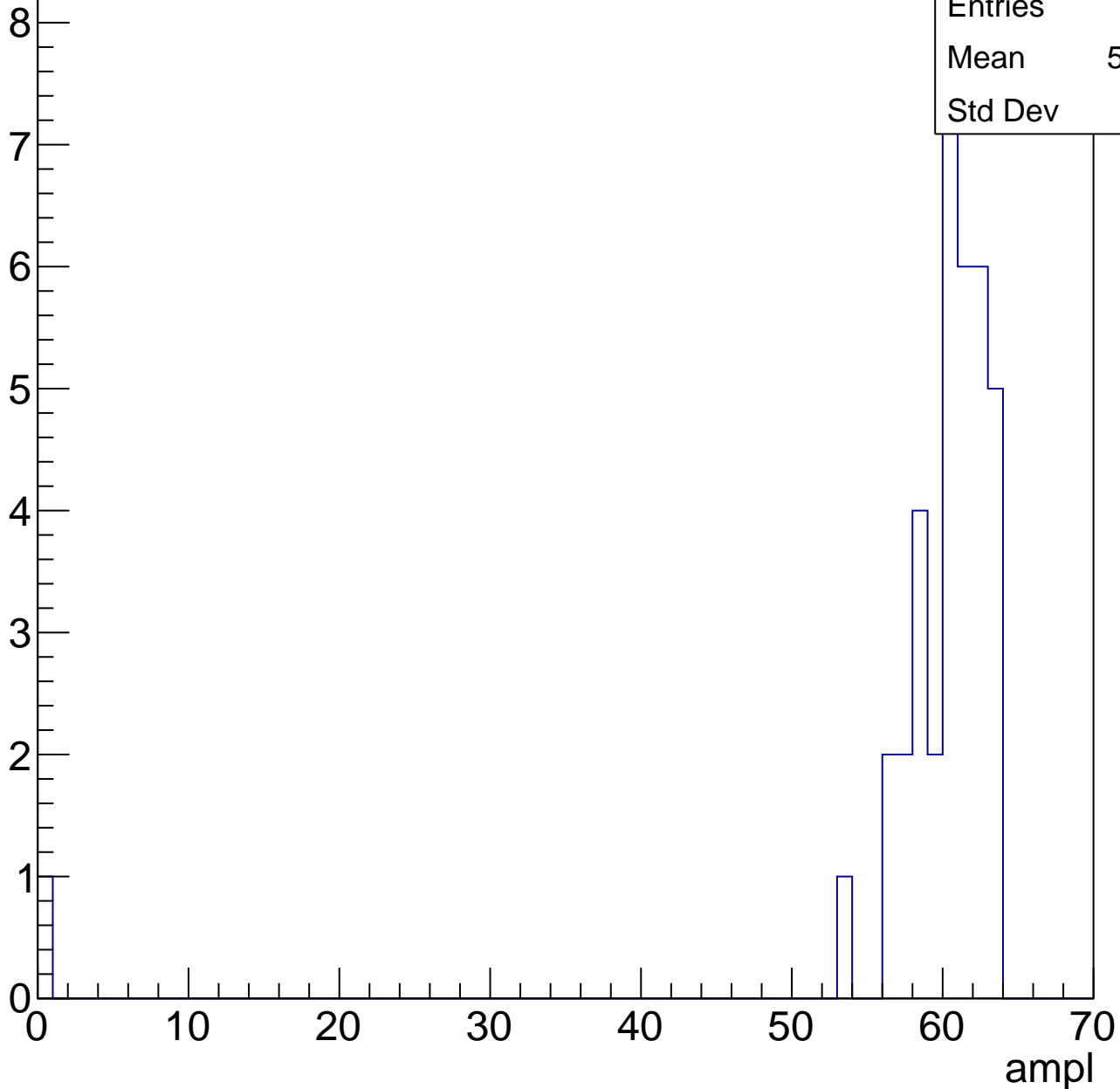


B1L101S, U26-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.43
Std Dev	10



B1L101S, U26-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

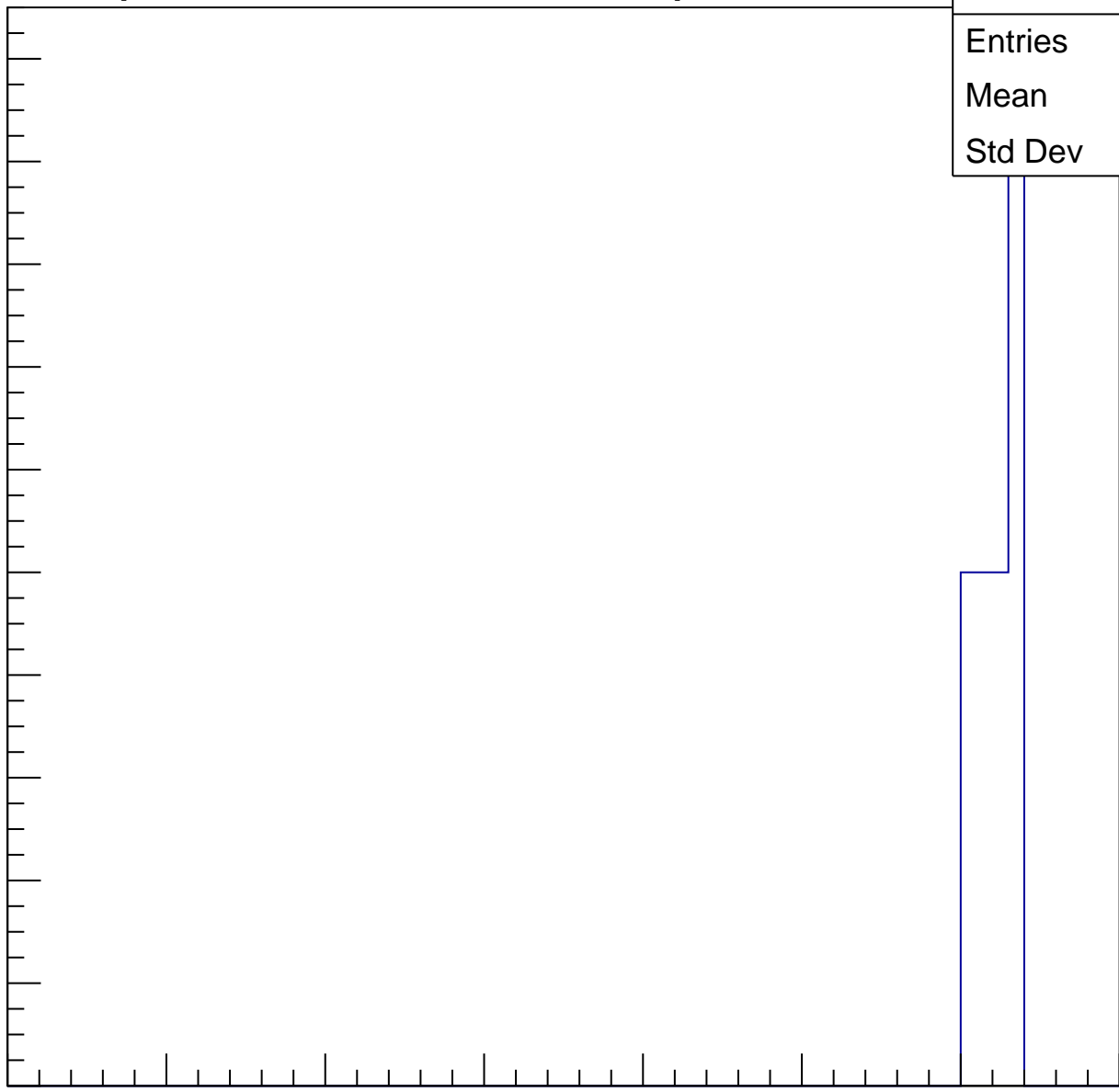
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U26-ch99, adc0

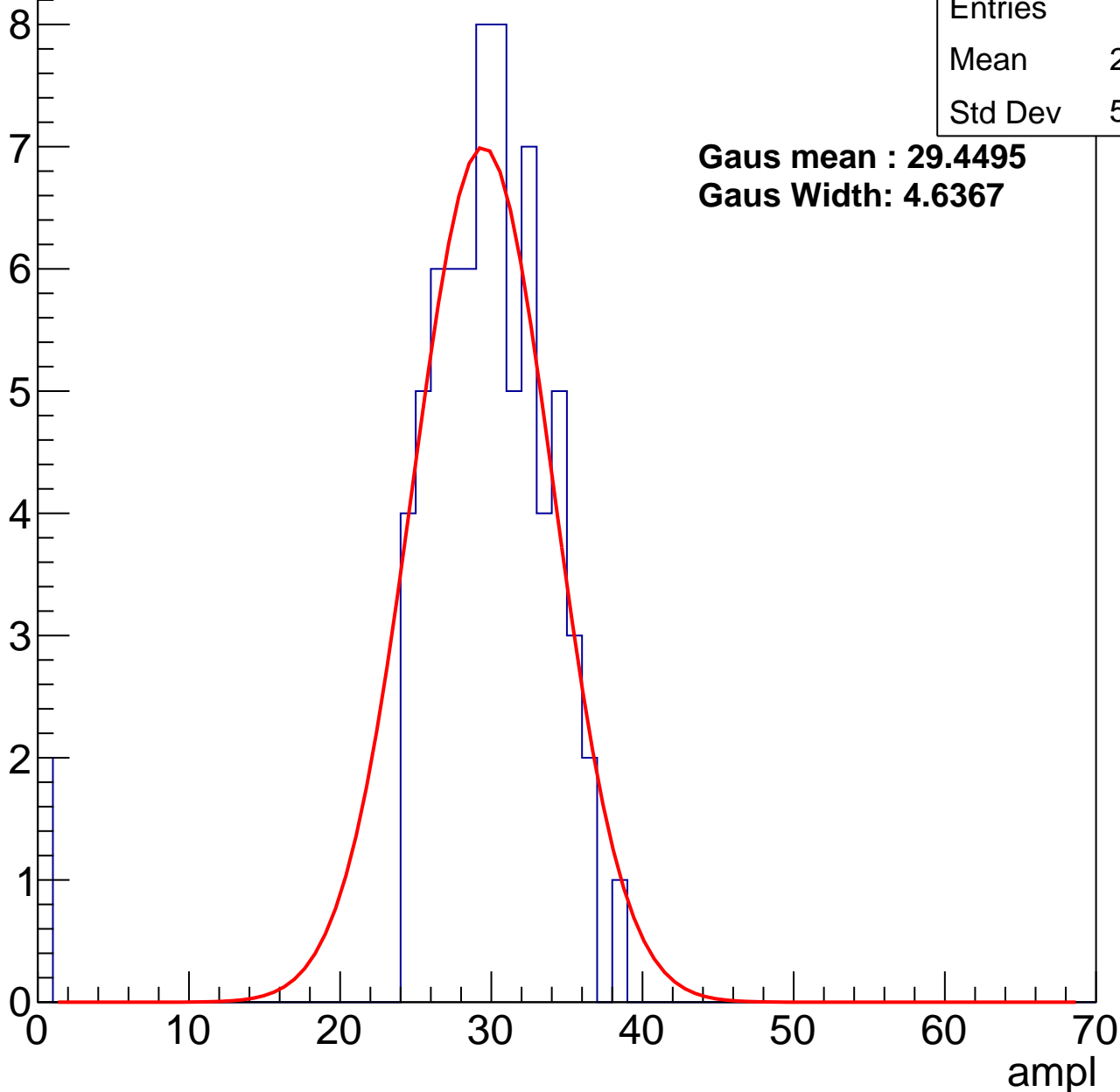
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.82
Std Dev	5.905

Gaus mean : 29.4495

Gaus Width: 4.6367



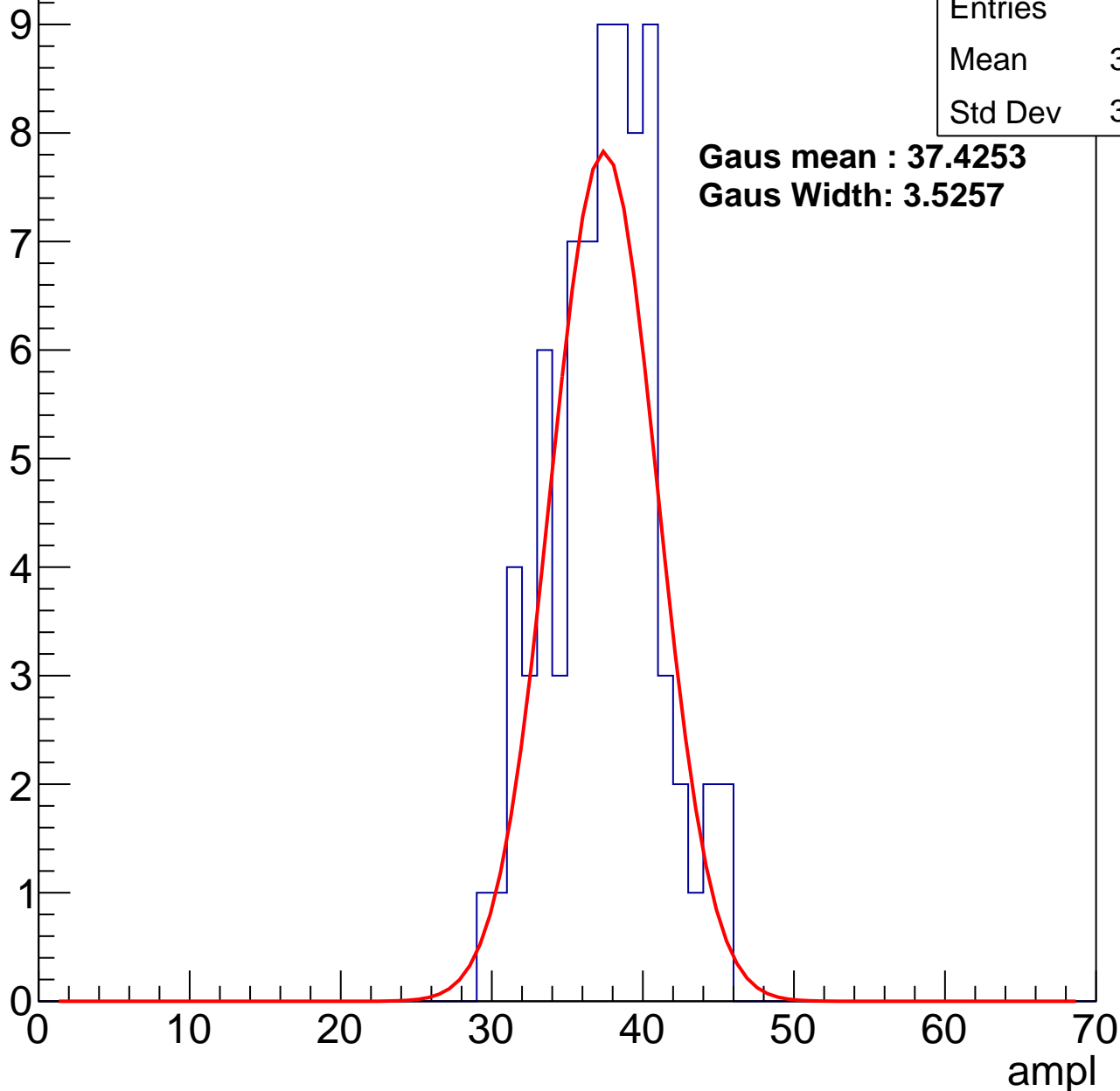
B1L101S, U26-ch99, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.03
Std Dev	3.553

Gaus mean : 37.4253
Gaus Width: 3.5257



B1L101S, U26-ch99, adc2

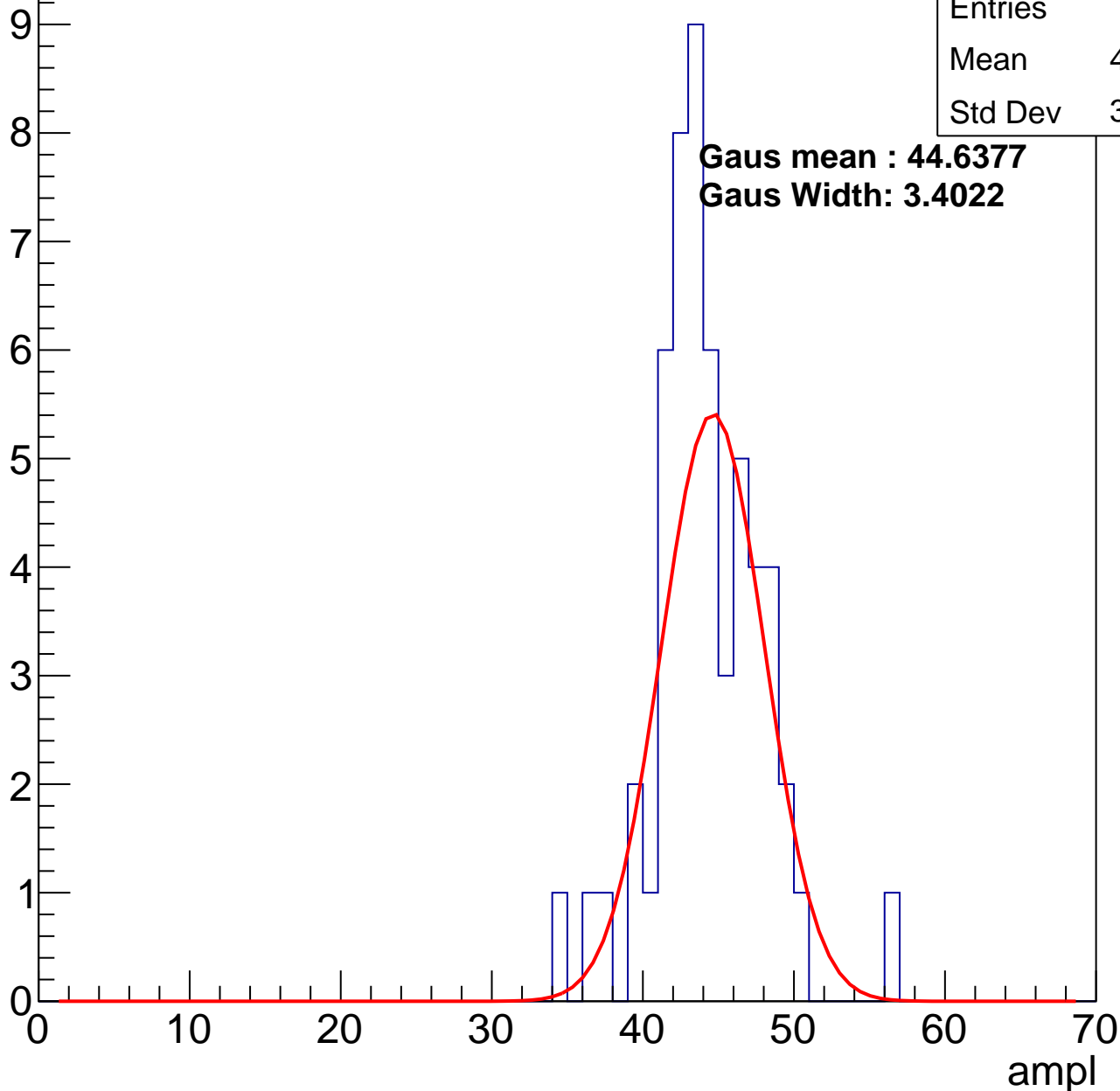
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.76
Std Dev	3.628

Gaus mean : 44.6377

Gaus Width: 3.4022

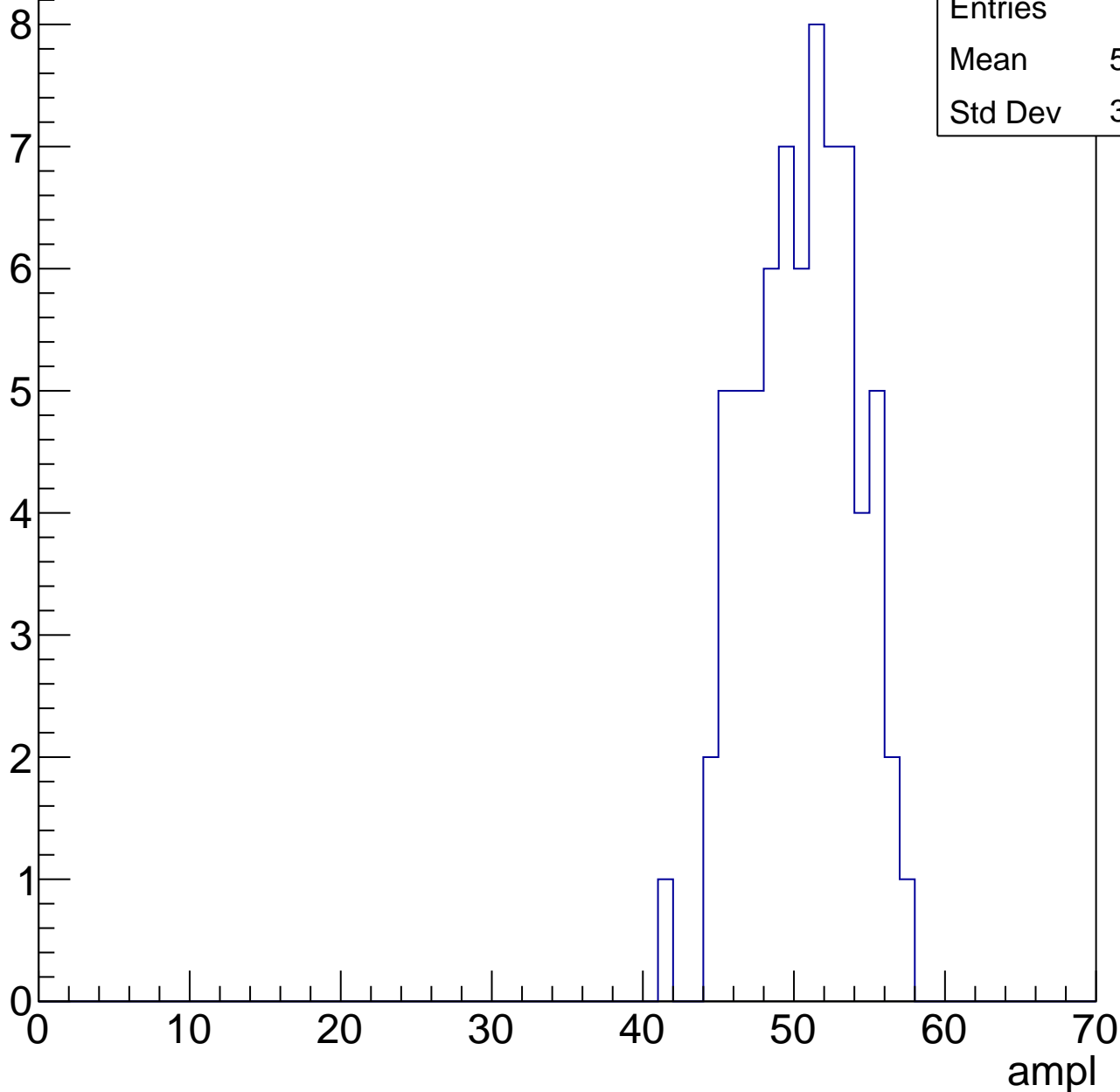


B1L101S, U26-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.04
Std Dev	3.445

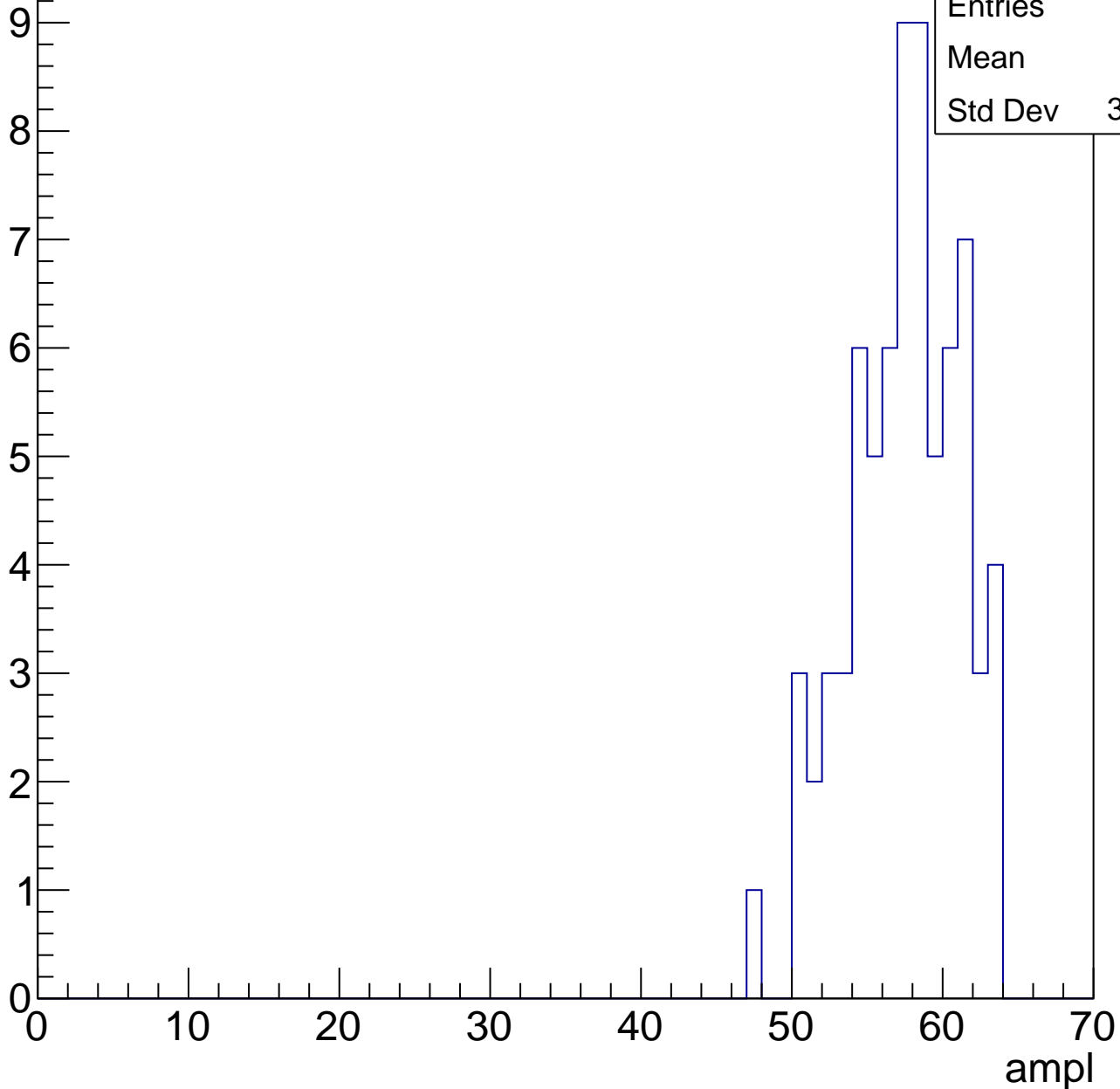


B1L101S, U26-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	57
Std Dev	3.606

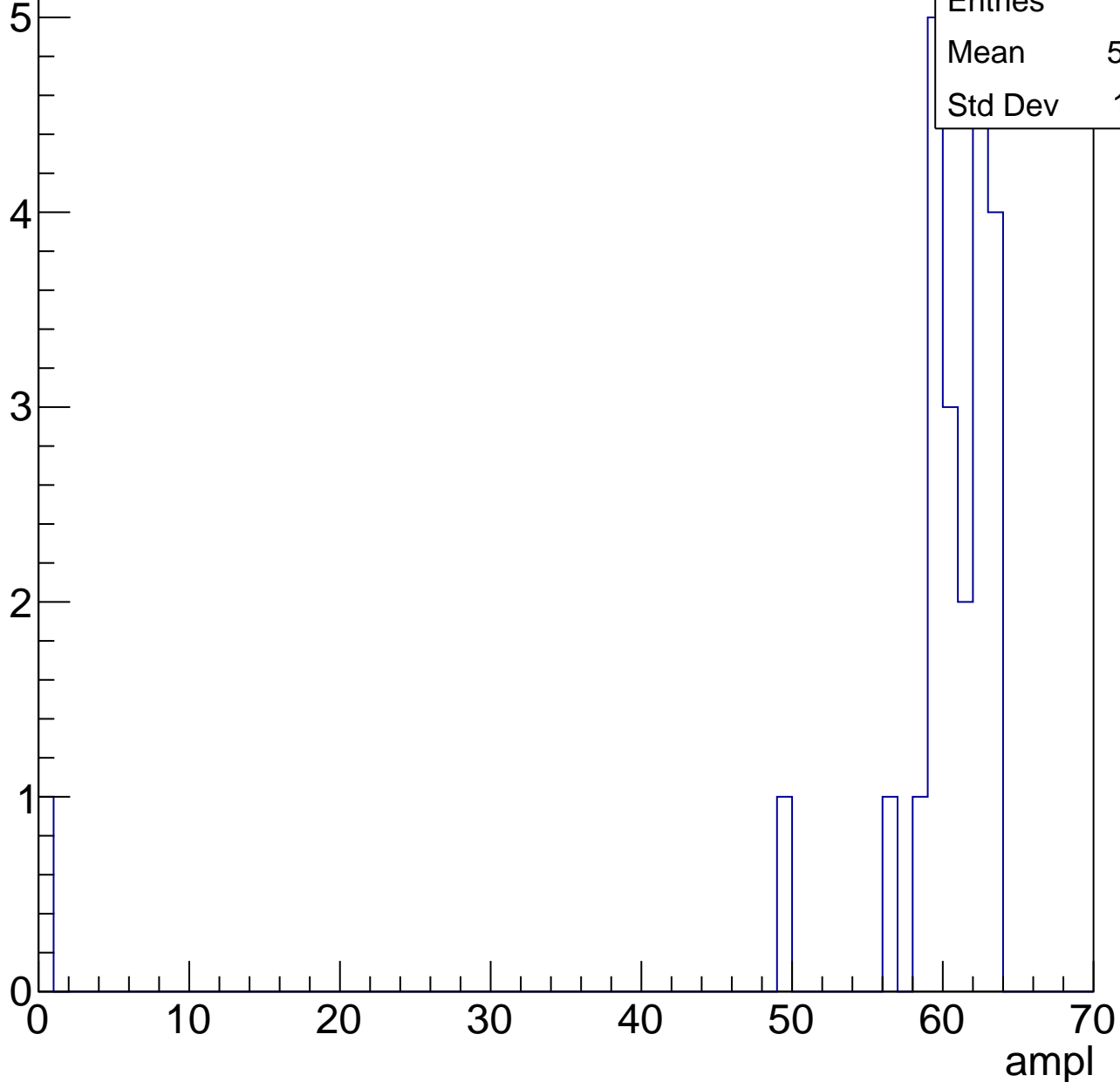


B1L101S, U26-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	57.48
Std Dev	12.61



B1L101S, U26-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

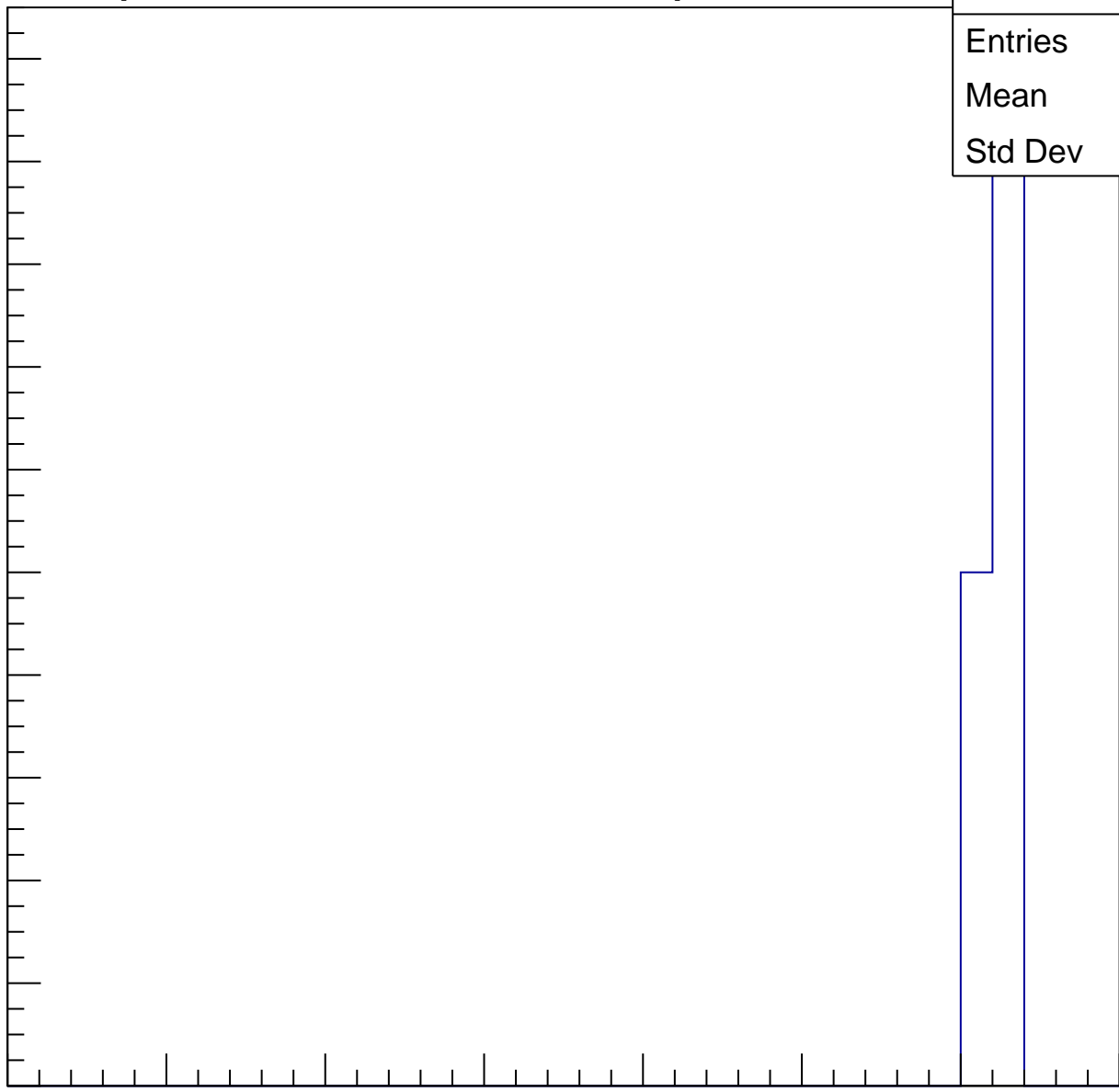
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch100, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	29.76
Std Dev	4.855

Gaus mean : 30.6649

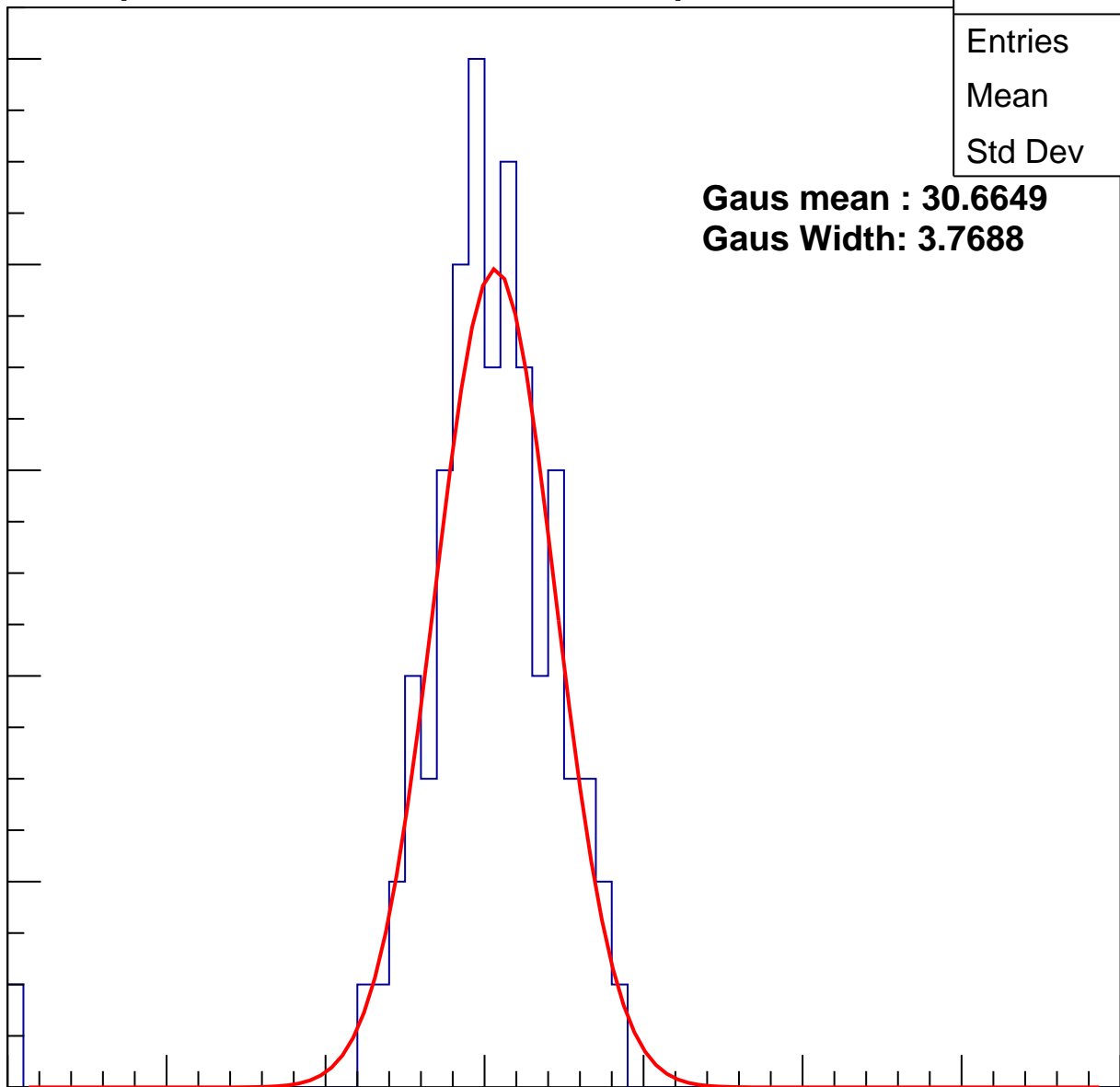
Gaus Width: 3.7688

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch100, adc1

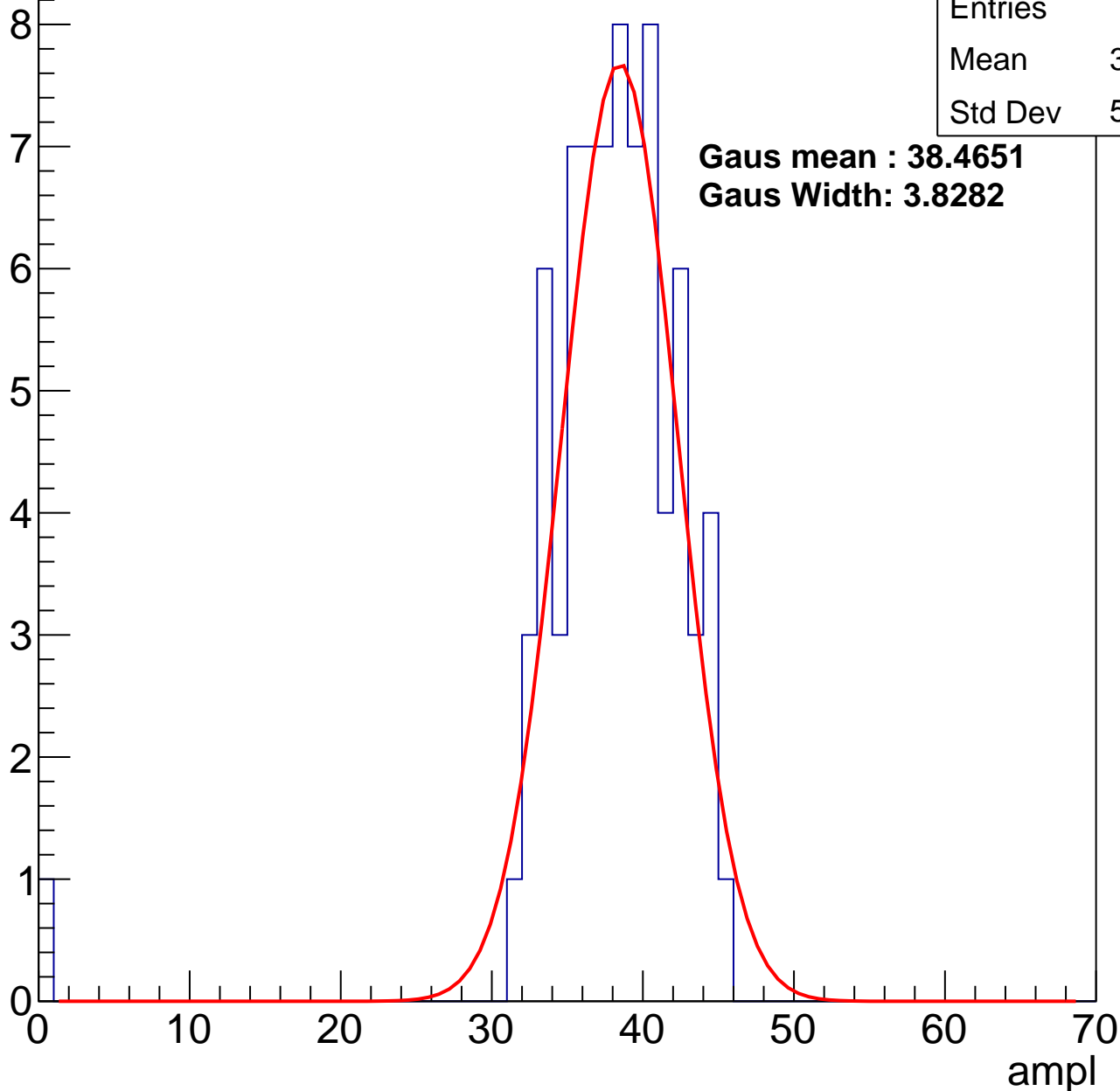
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.45
Std Dev	5.516

Gaus mean : 38.4651

Gaus Width: 3.8282



B1L101S, U26-ch100, adc2

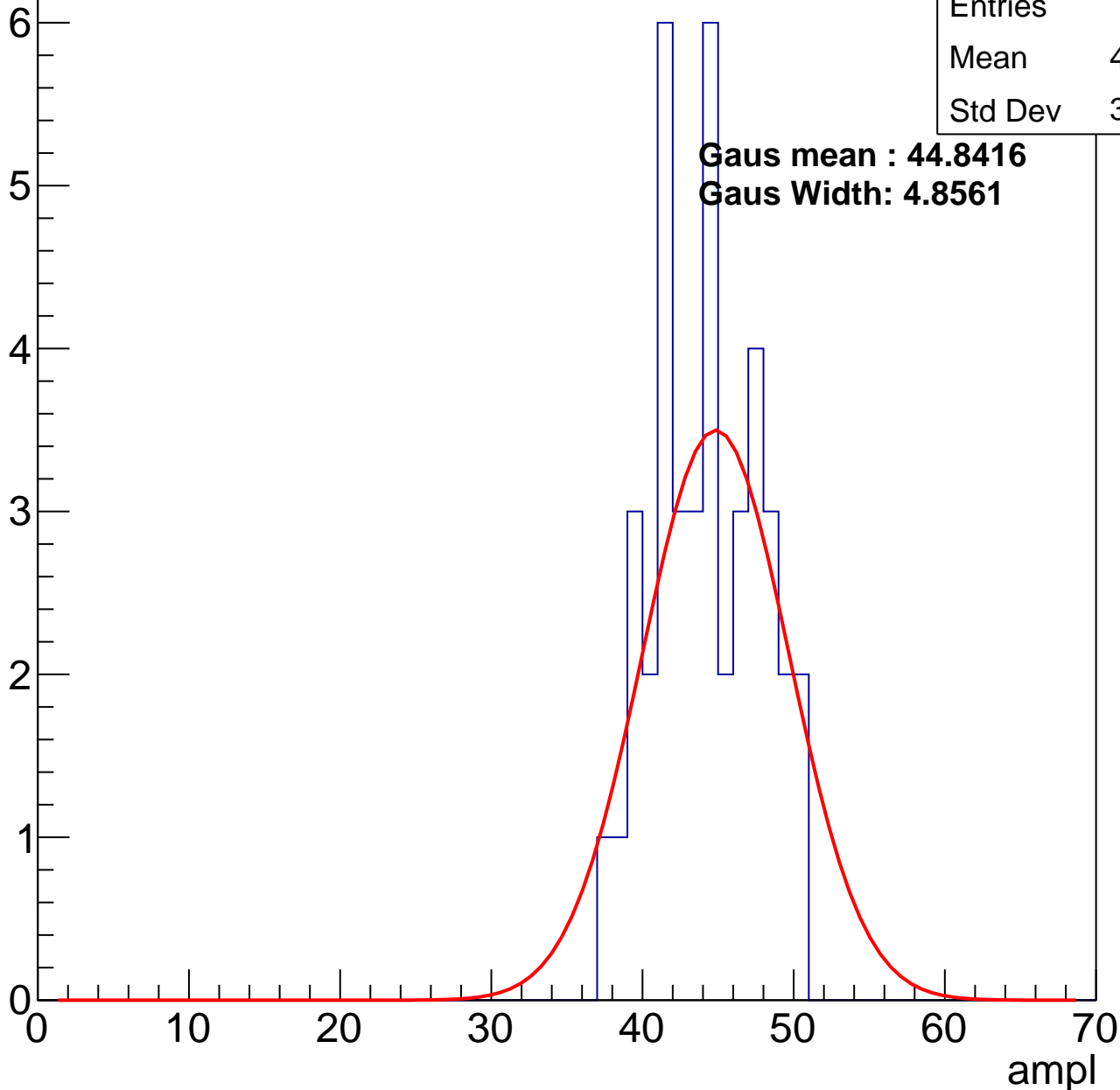
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	43.78
Std Dev	3.418

Gaus mean : 44.8416

Gaus Width: 4.8561

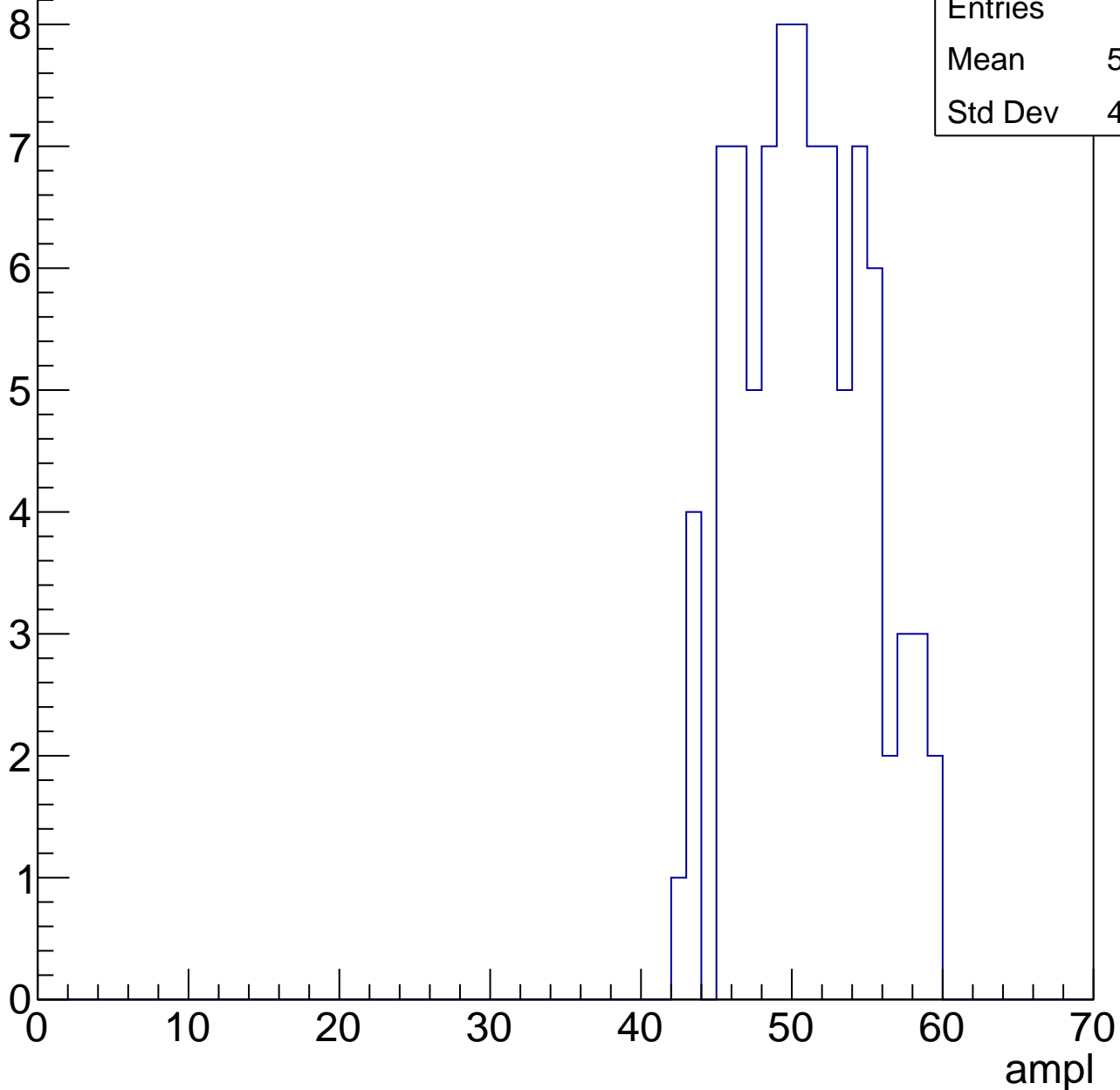


B1L101S, U26-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	50.37
Std Dev	4.147

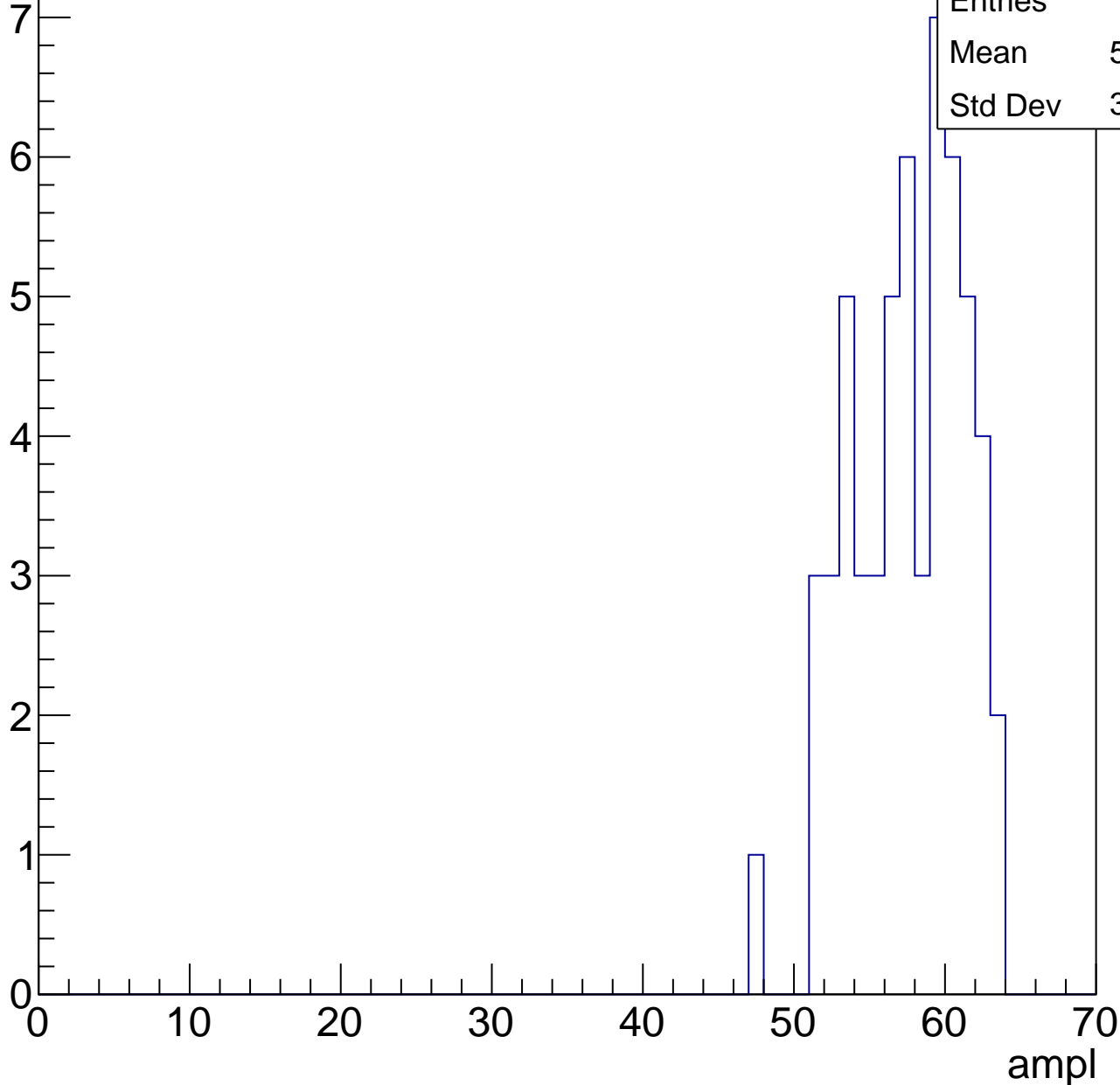


B1L101S, U26-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.07
Std Dev	3.644

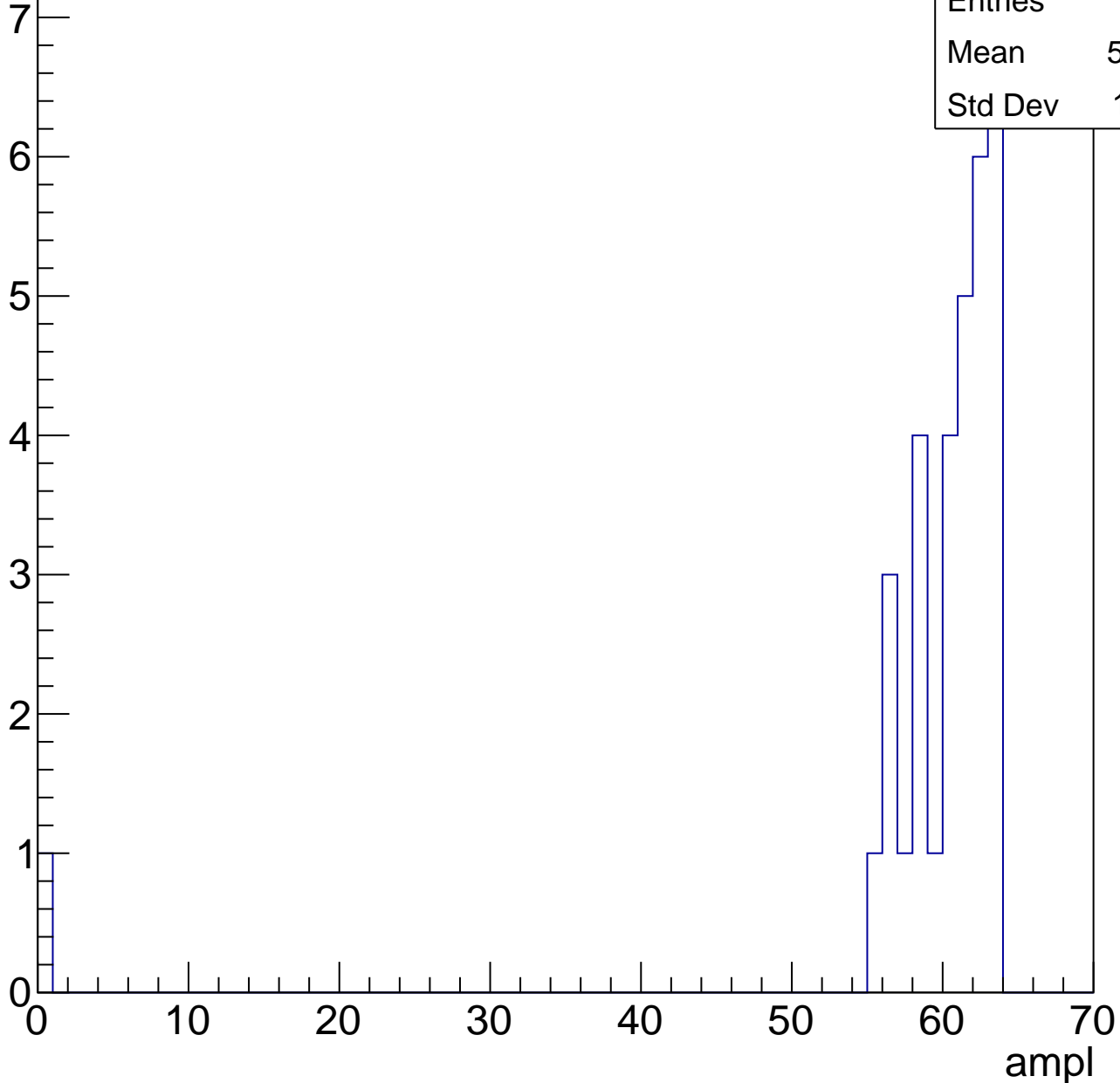


B1L101S, U26-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.45
Std Dev	10.61



B1L101S, U26-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch101, adc0

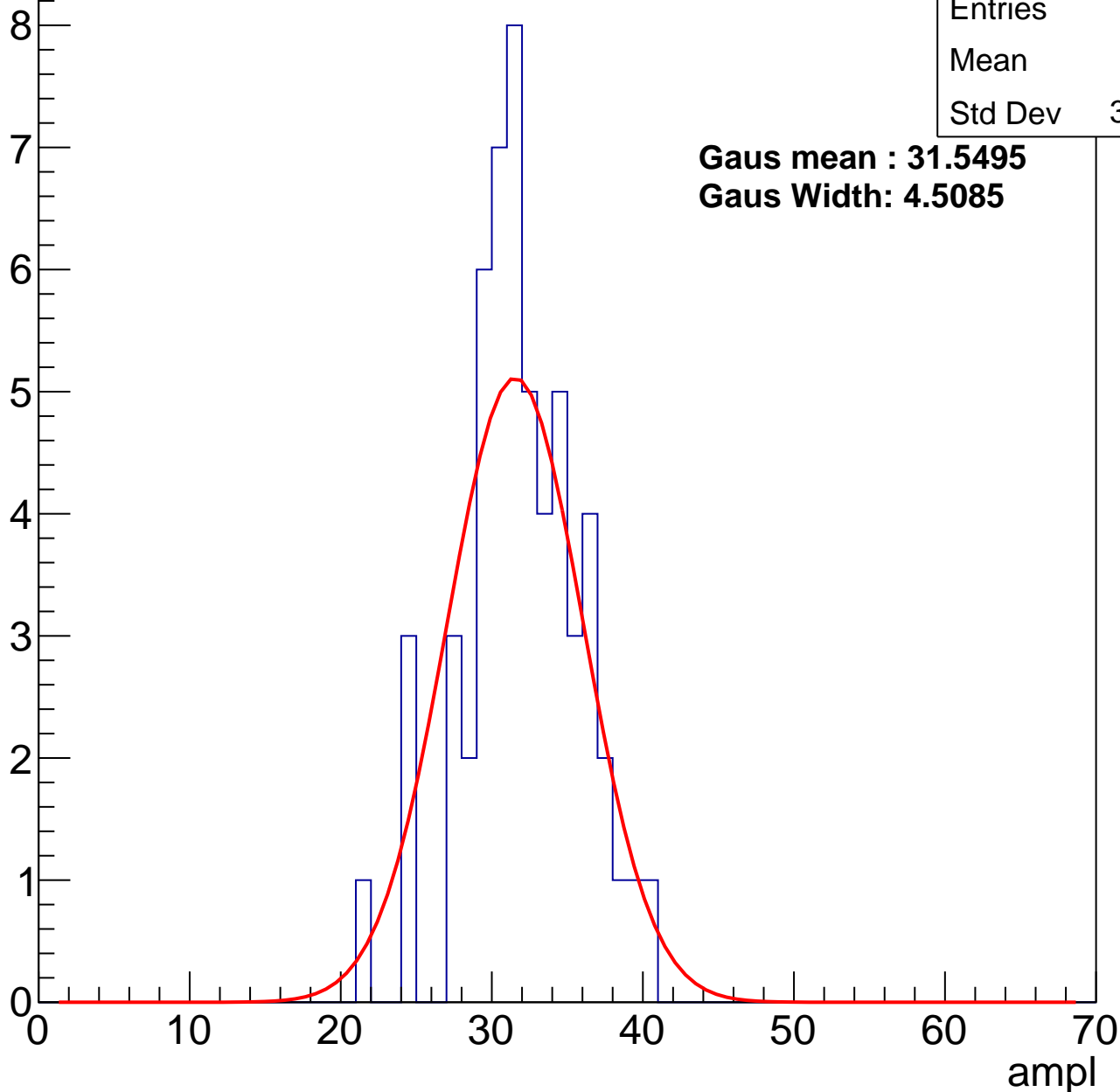
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	31.5
Std Dev	3.803

Gaus mean : 31.5495

Gaus Width: 4.5085



B1L101S, U26-ch101, adc1

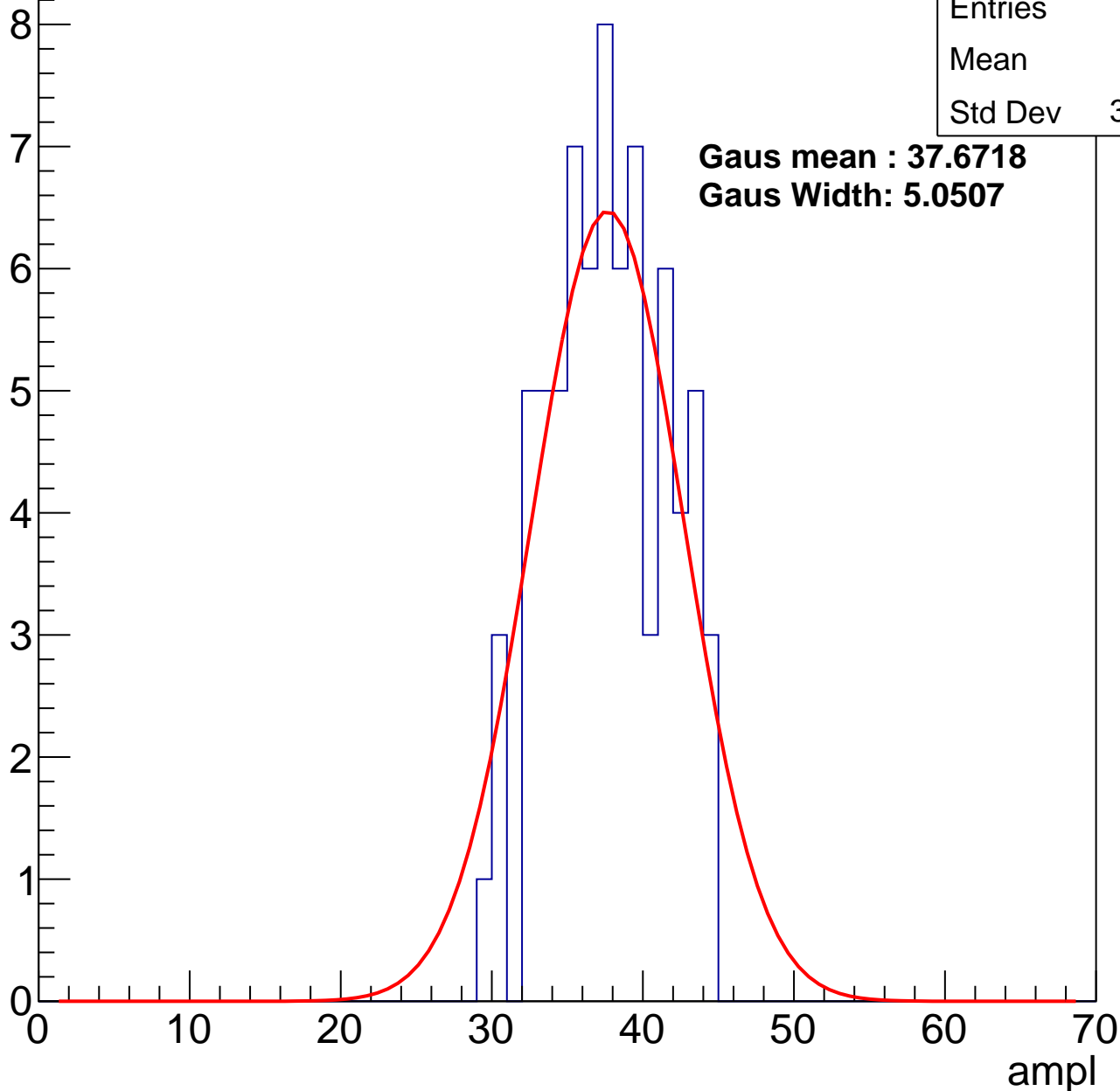
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.2
Std Dev	3.813

Gaus mean : 37.6718

Gaus Width: 5.0507



B1L101S, U26-ch101, adc2

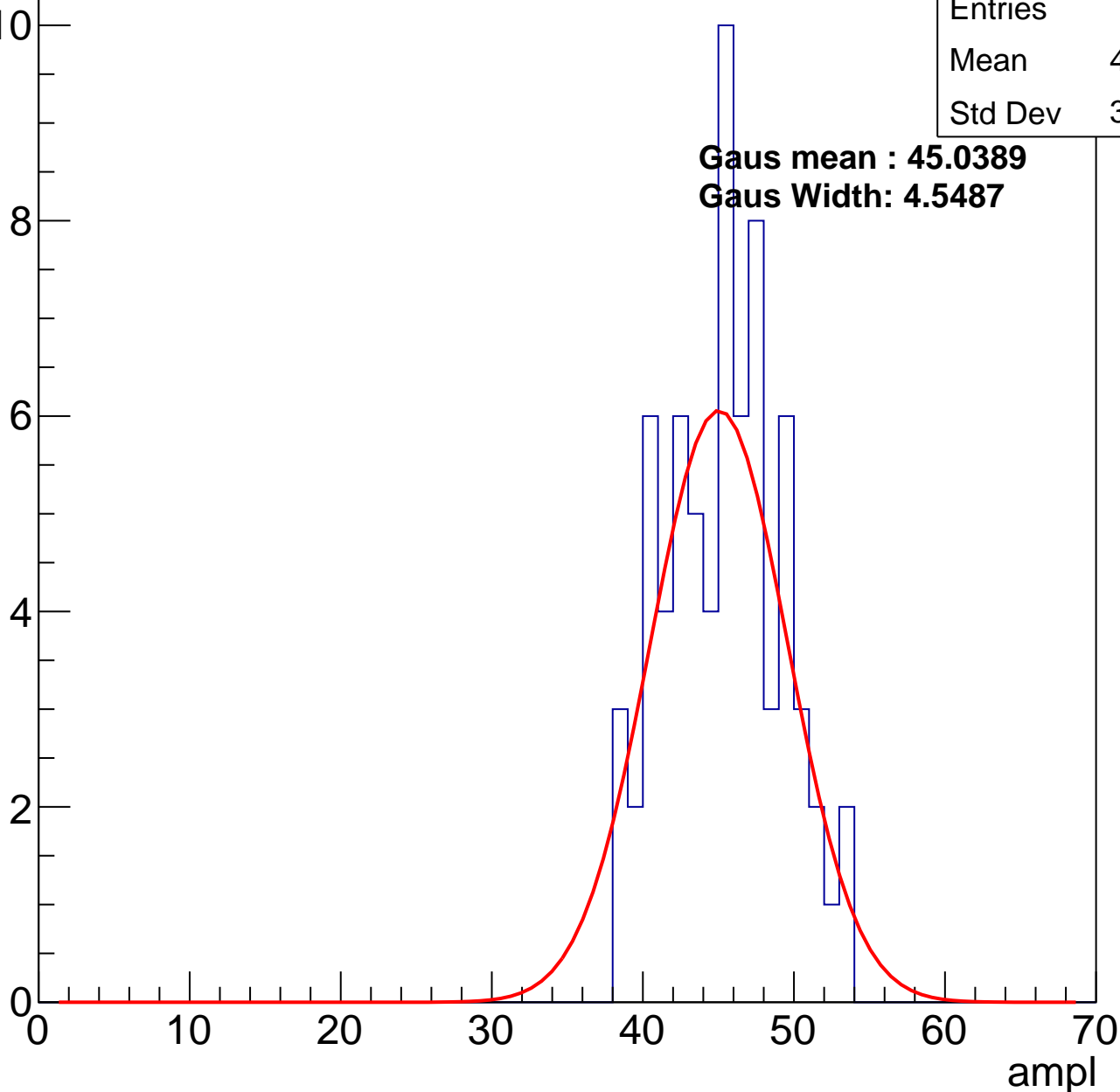
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.92
Std Dev	3.744

Gaus mean : 45.0389

Gaus Width: 4.5487

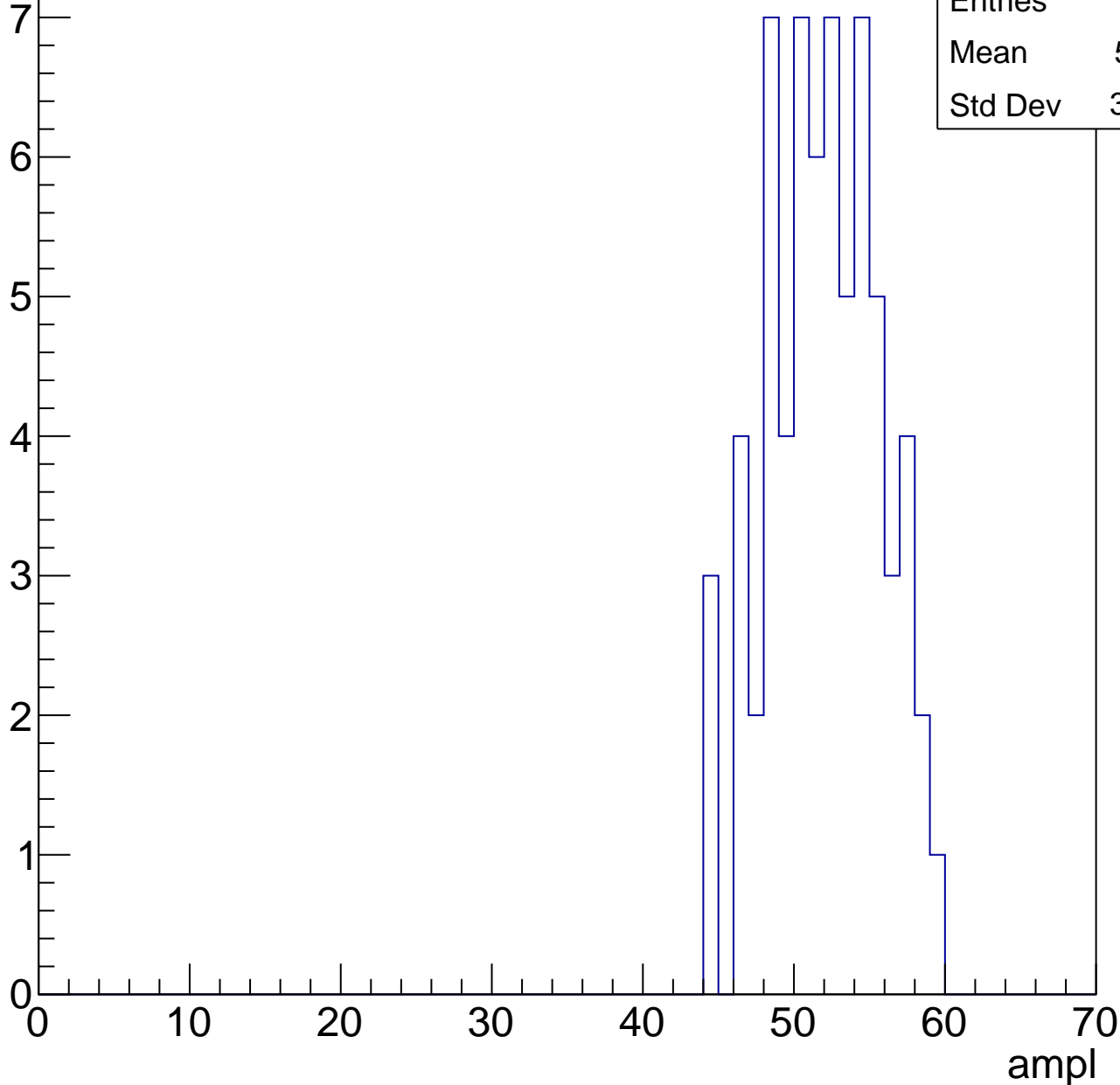


B1L101S, U26-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.51
Std Dev	3.663

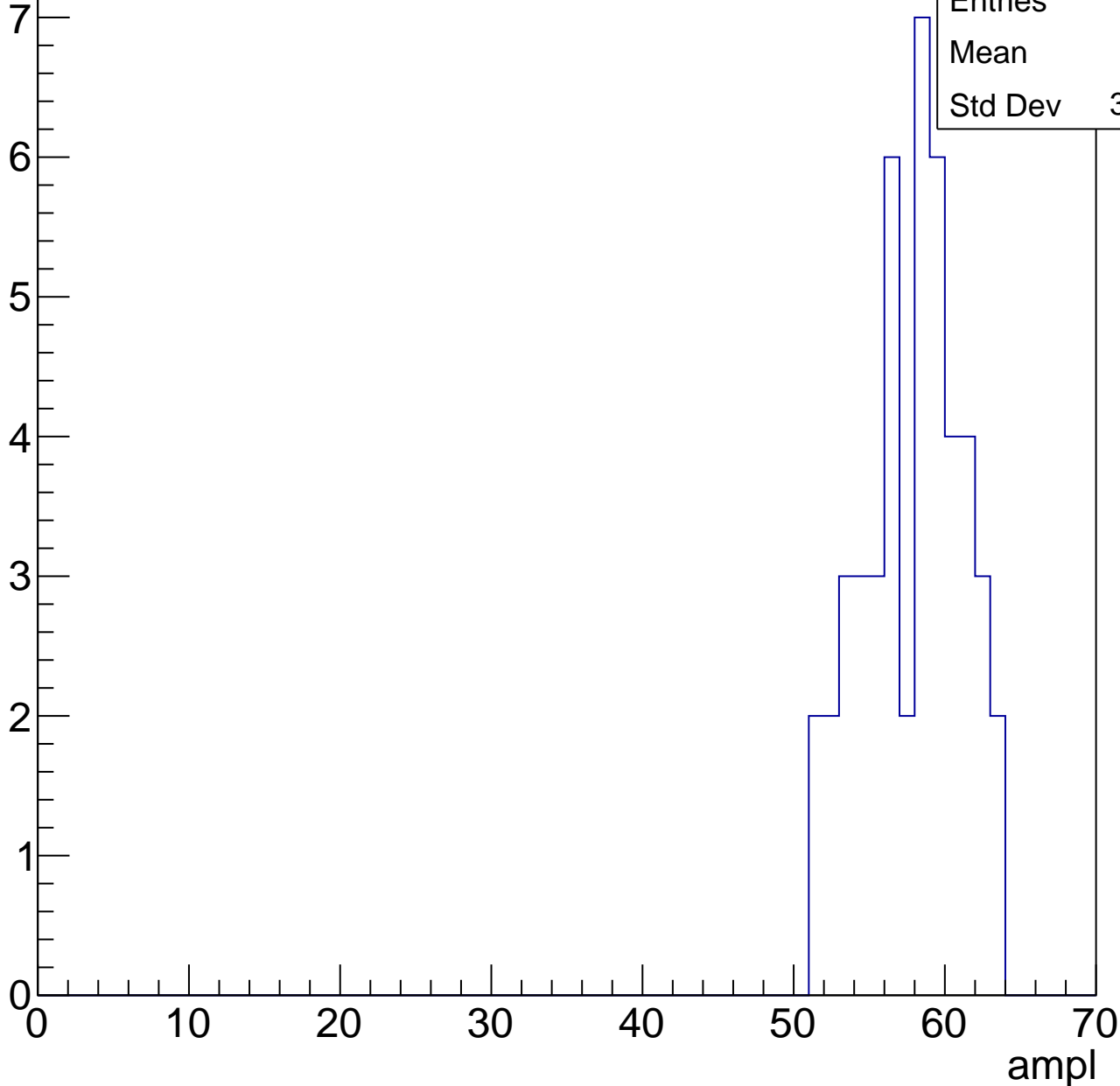


B1L101S, U26-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	57.4
Std Dev	3.213

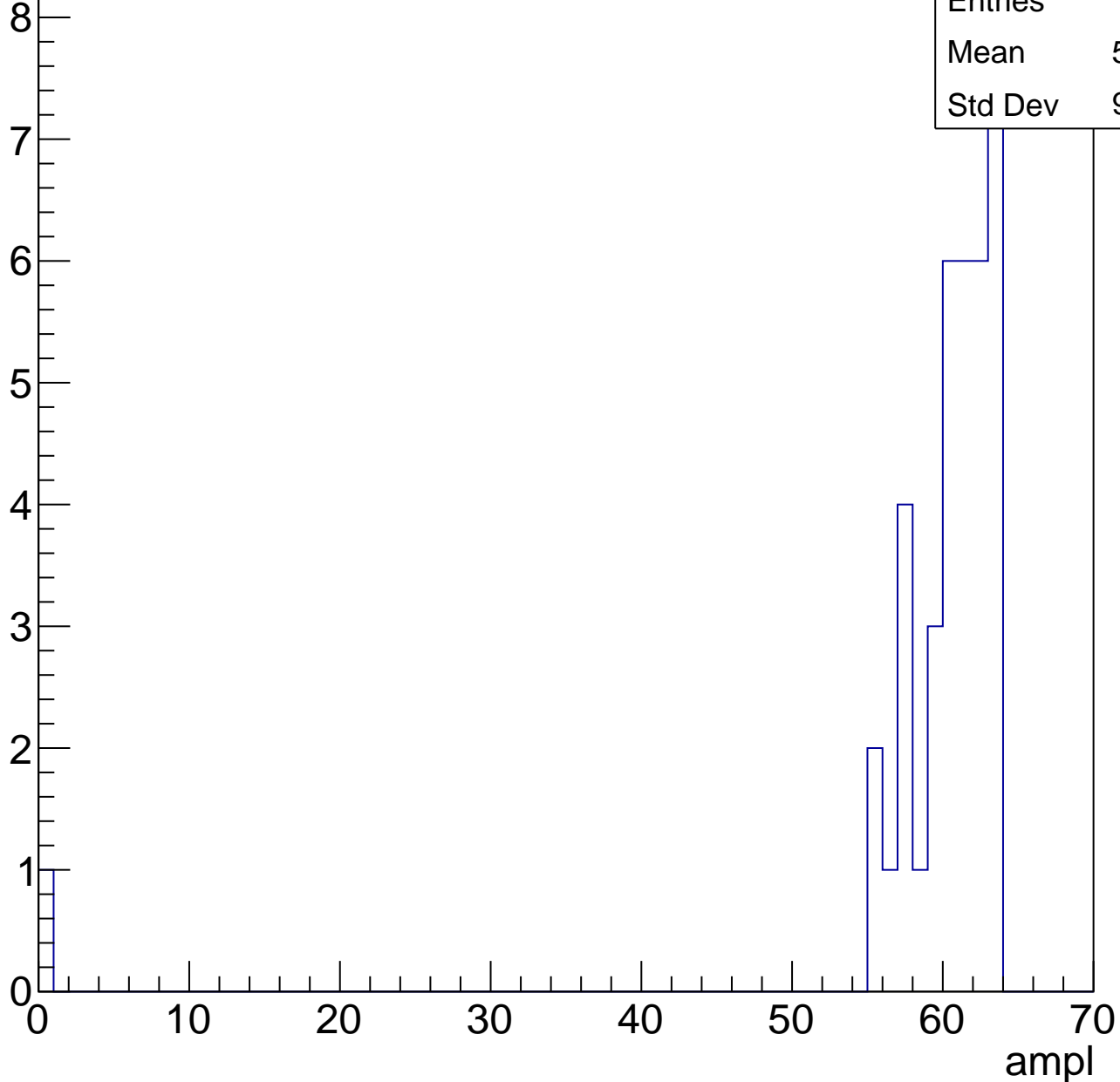


B1L101S, U26-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.71
Std Dev	9.931



B1L101S, U26-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch102, adc0

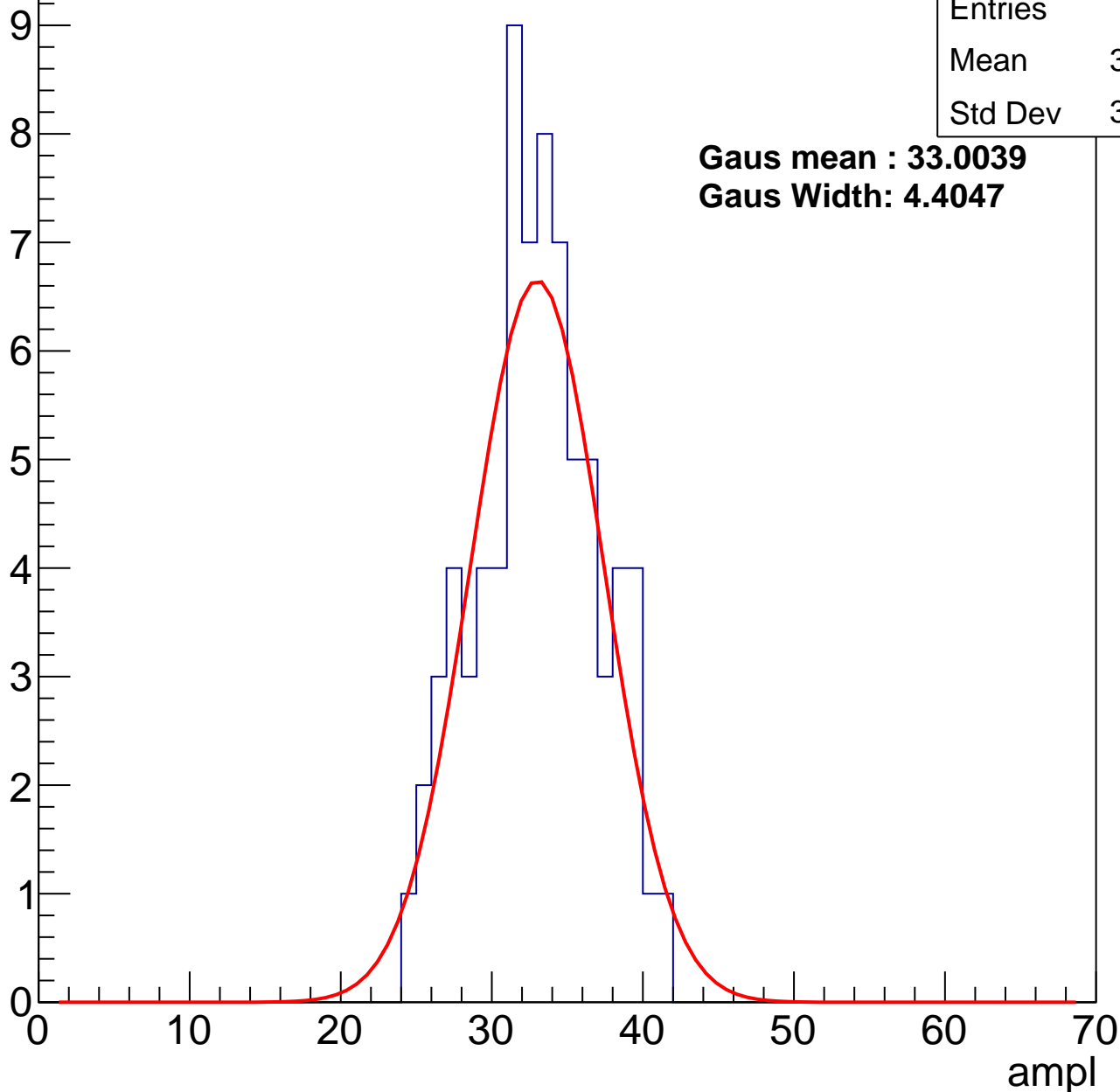
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	32.53
Std Dev	3.964

Gaus mean : 33.0039

Gaus Width: 4.4047



B1L101S, U26-ch102, adc1

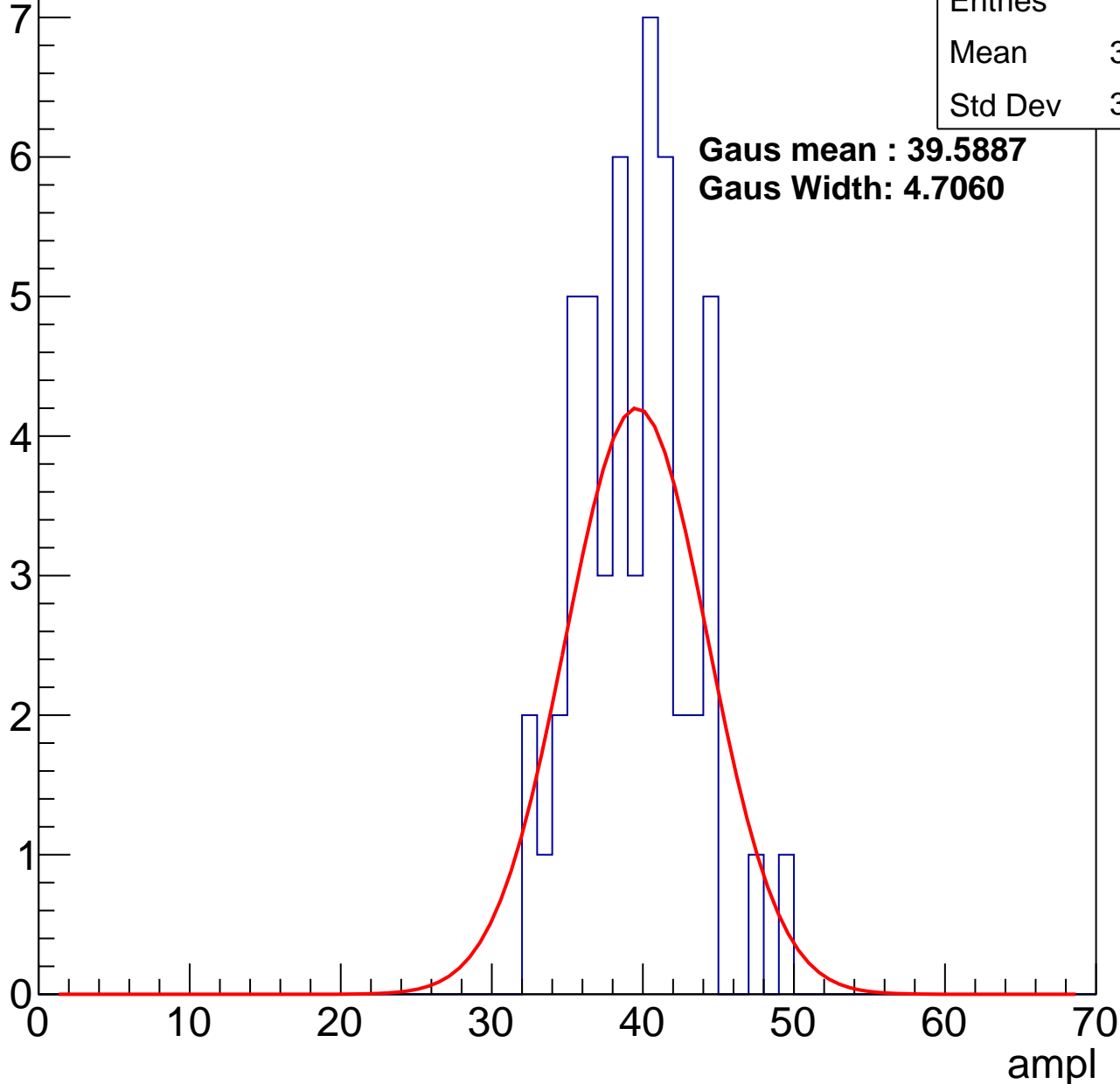
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	38.98
Std Dev	3.697

Gaus mean : 39.5887

Gaus Width: 4.7060



B1L101S, U26-ch102, adc2

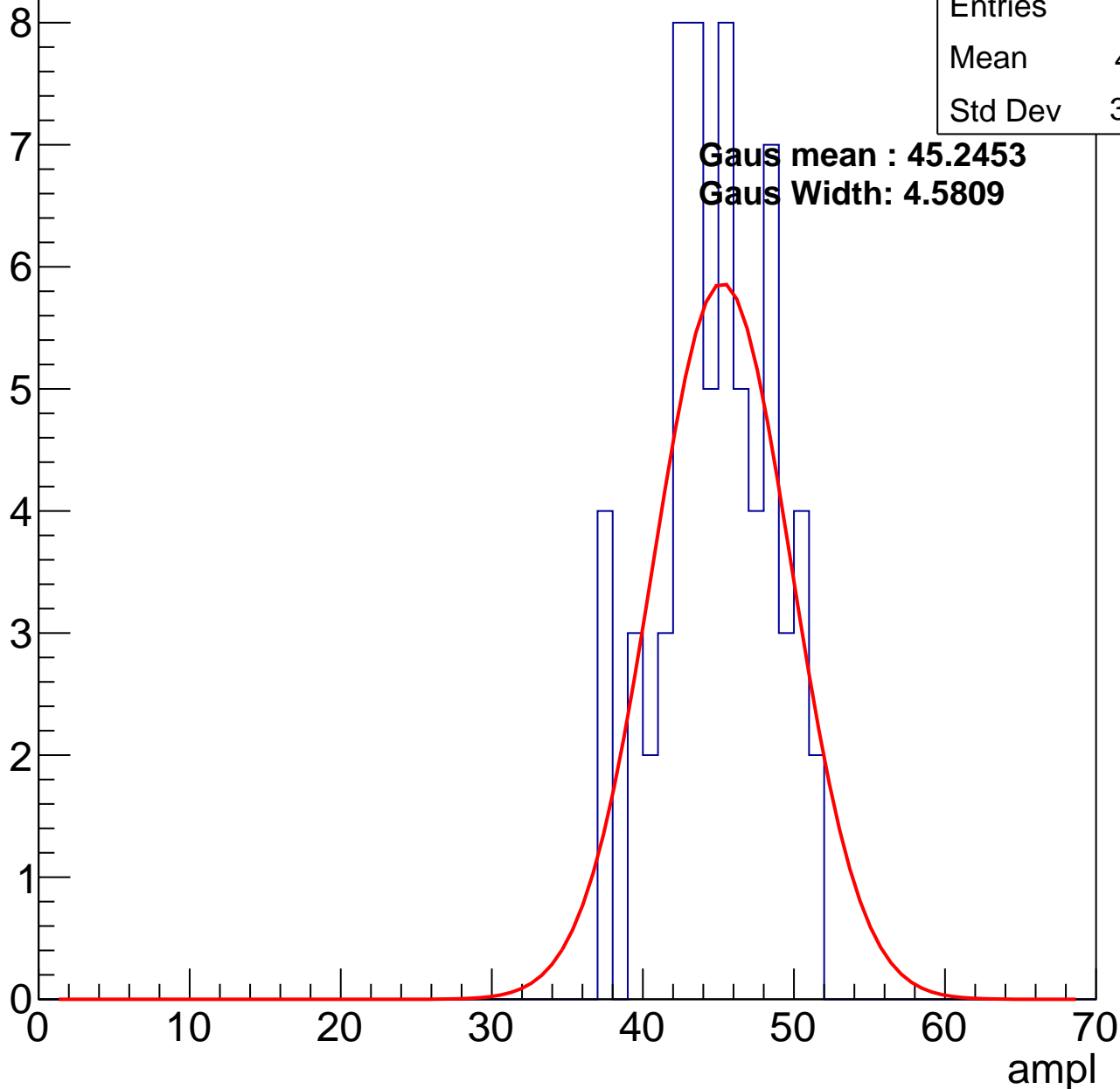
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.41
Std Dev	3.593

Gaus mean : 45.2453

Gaus Width: 4.5809

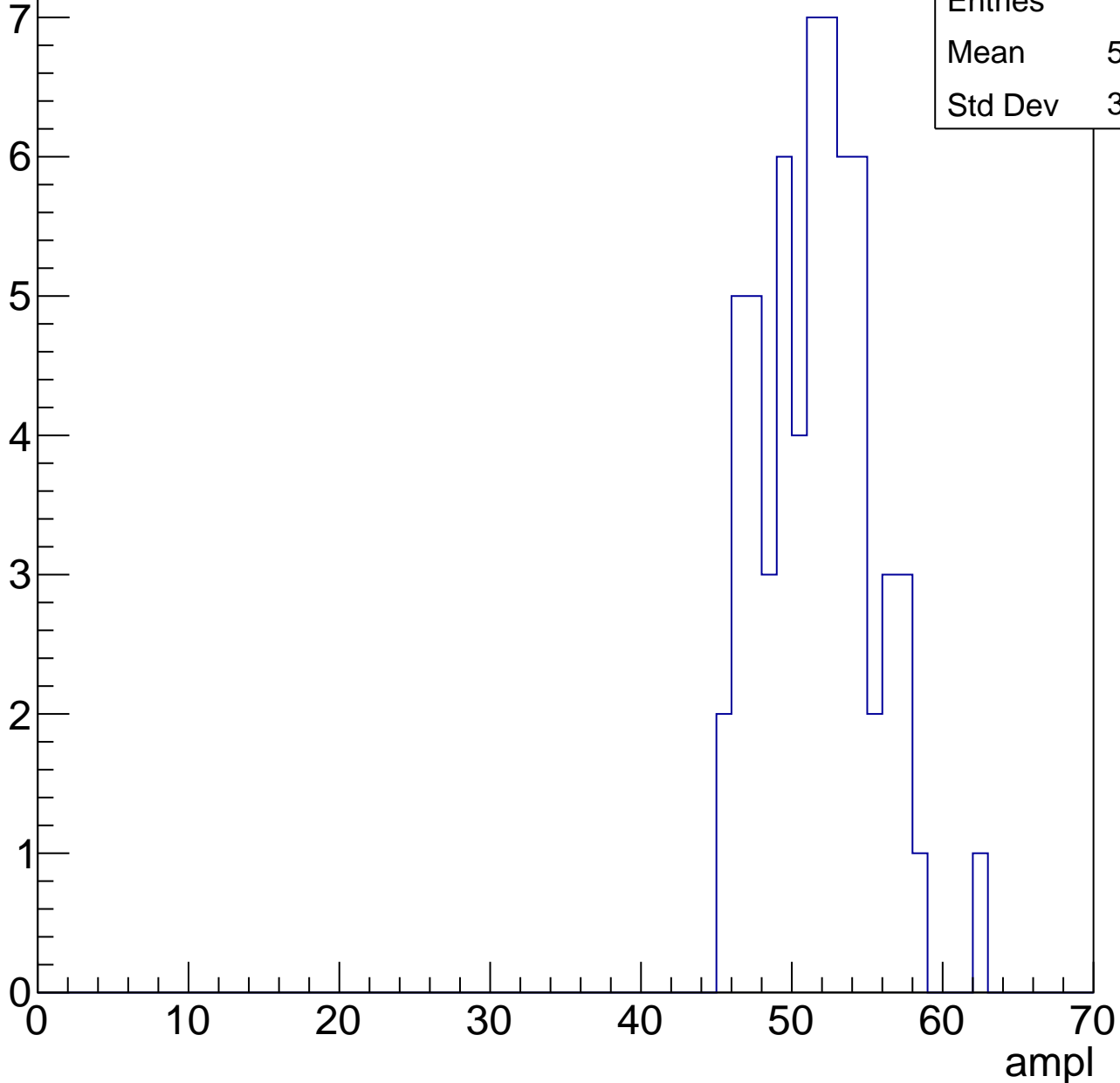


B1L101S, U26-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

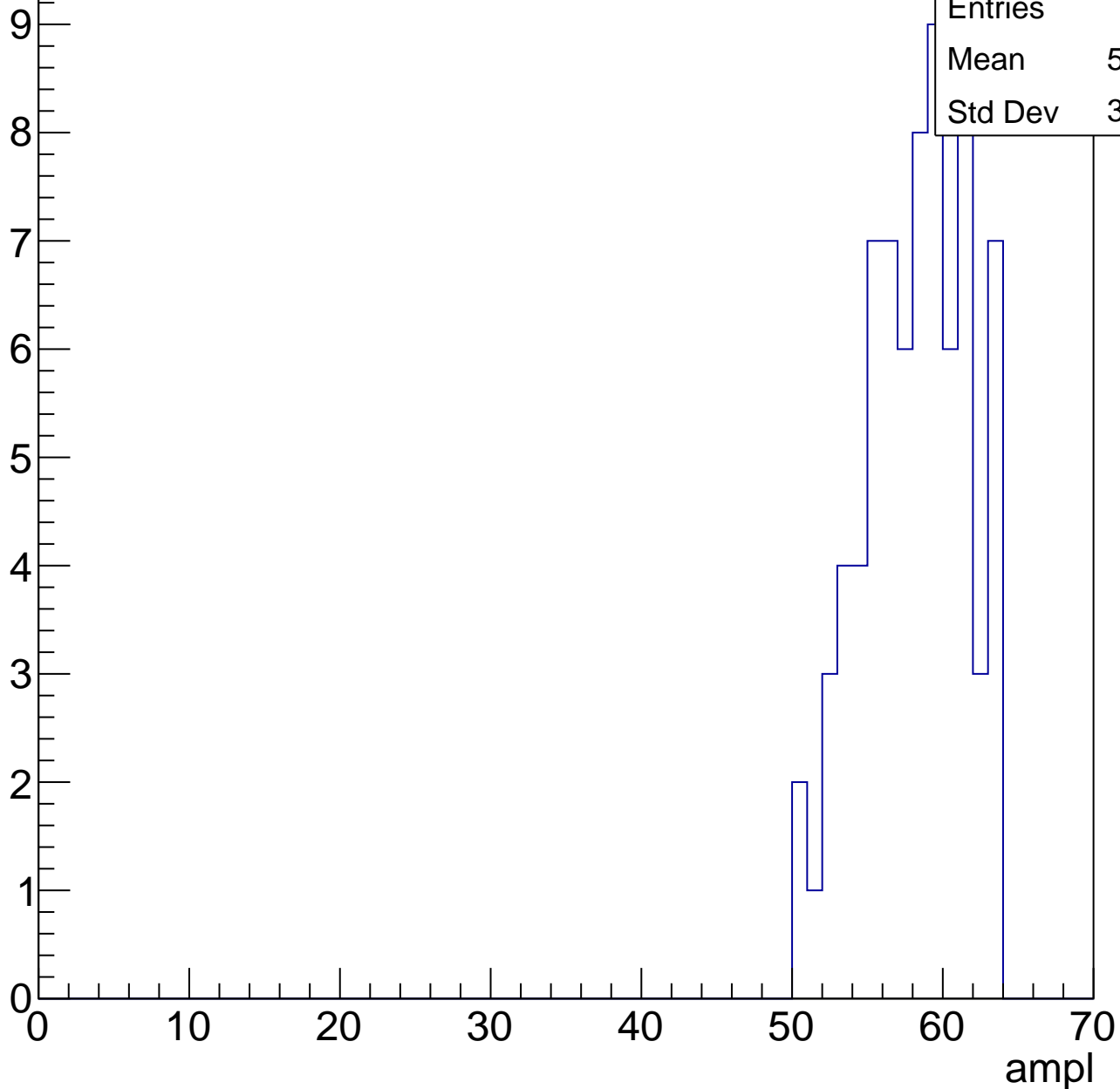
Entries	61
Mean	51.23
Std Dev	3.619



B1L101S, U26-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



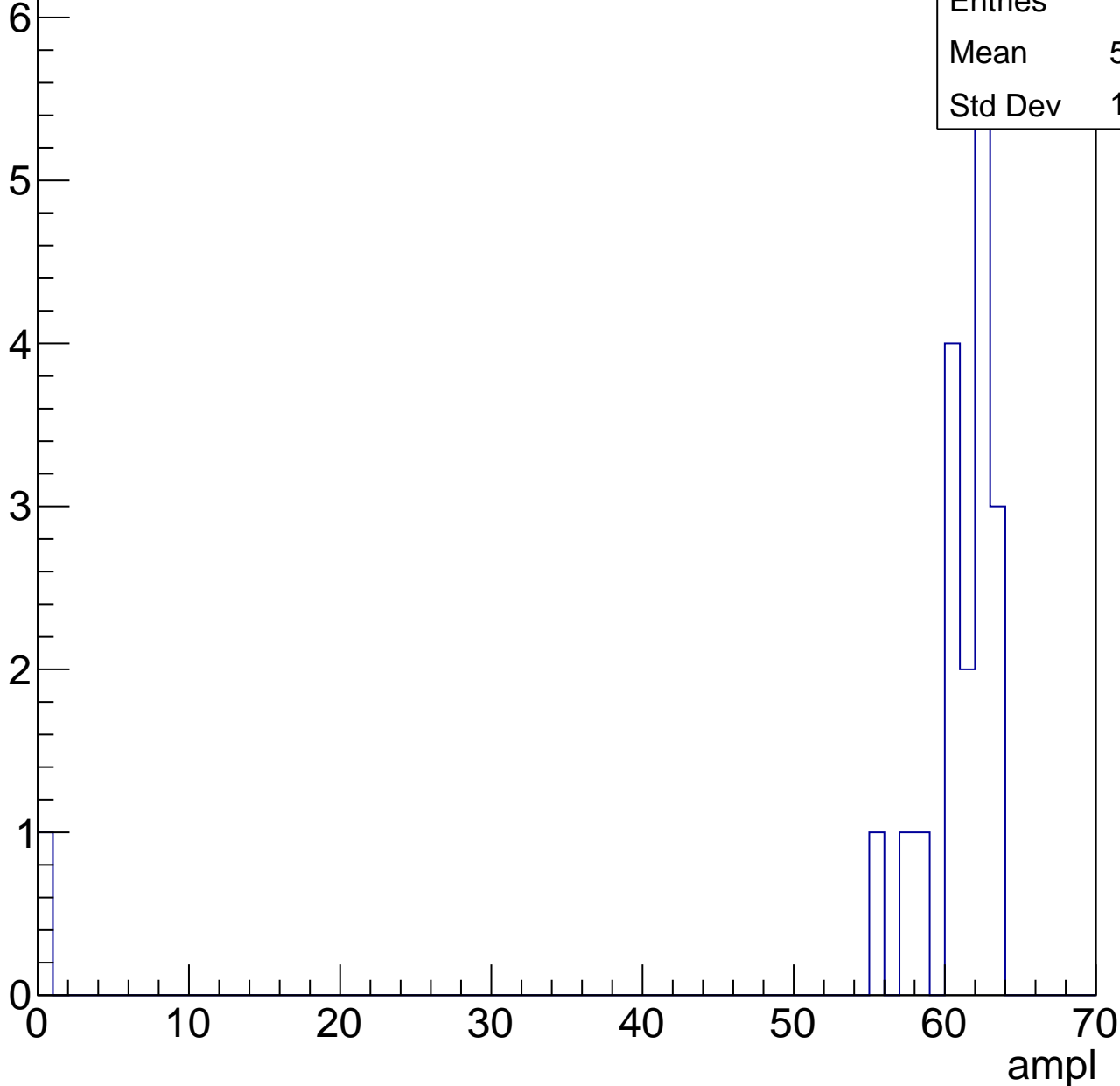
Entries	75
Mean	57.65
Std Dev	3.396

B1L101S, U26-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

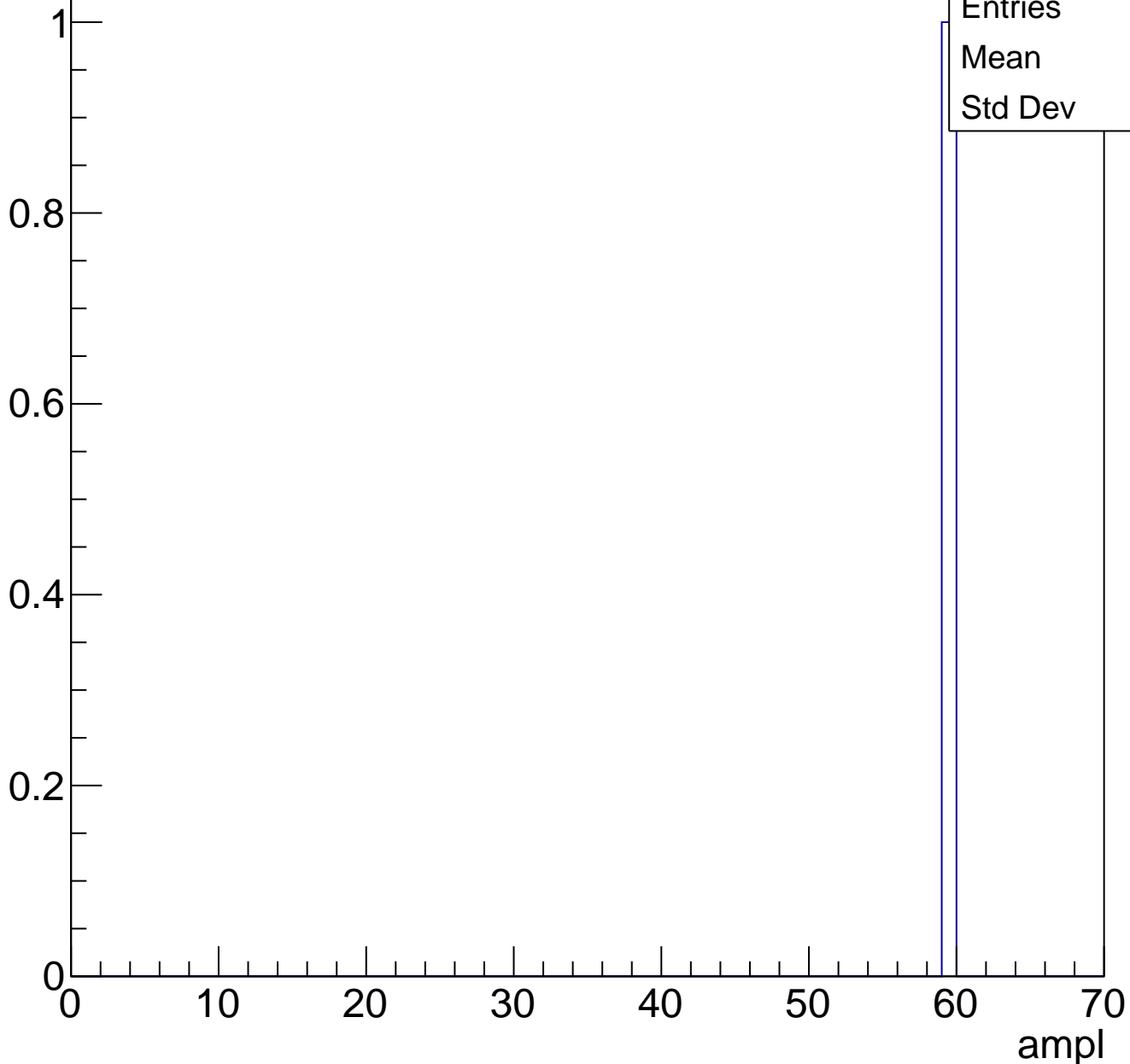
Entries	19
Mean	57.53
Std Dev	13.72



B1L101S, U26-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch103, adc0

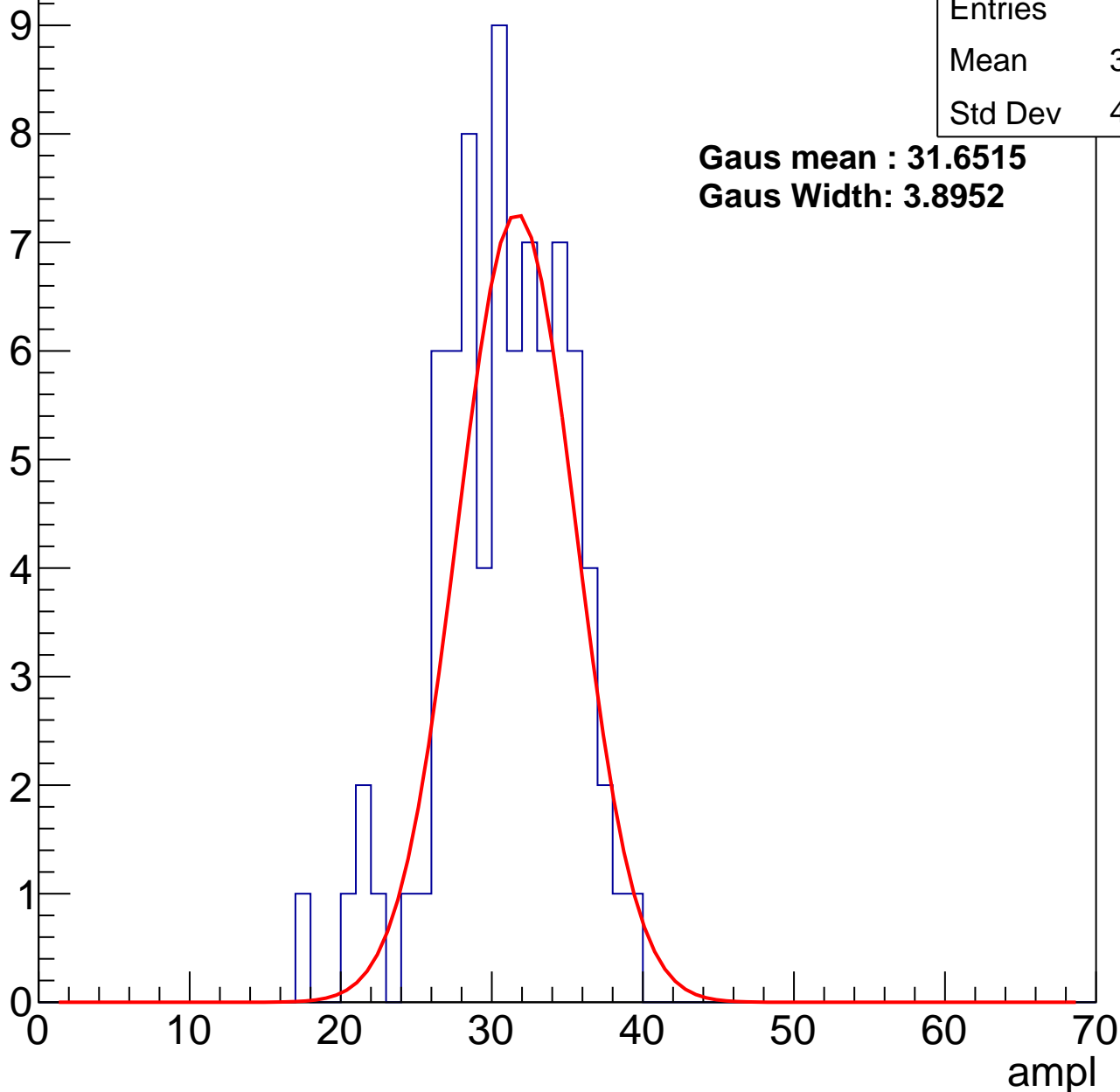
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.36
Std Dev	4.284

Gaus mean : 31.6515

Gaus Width: 3.8952



B1L101S, U26-ch103, adc1

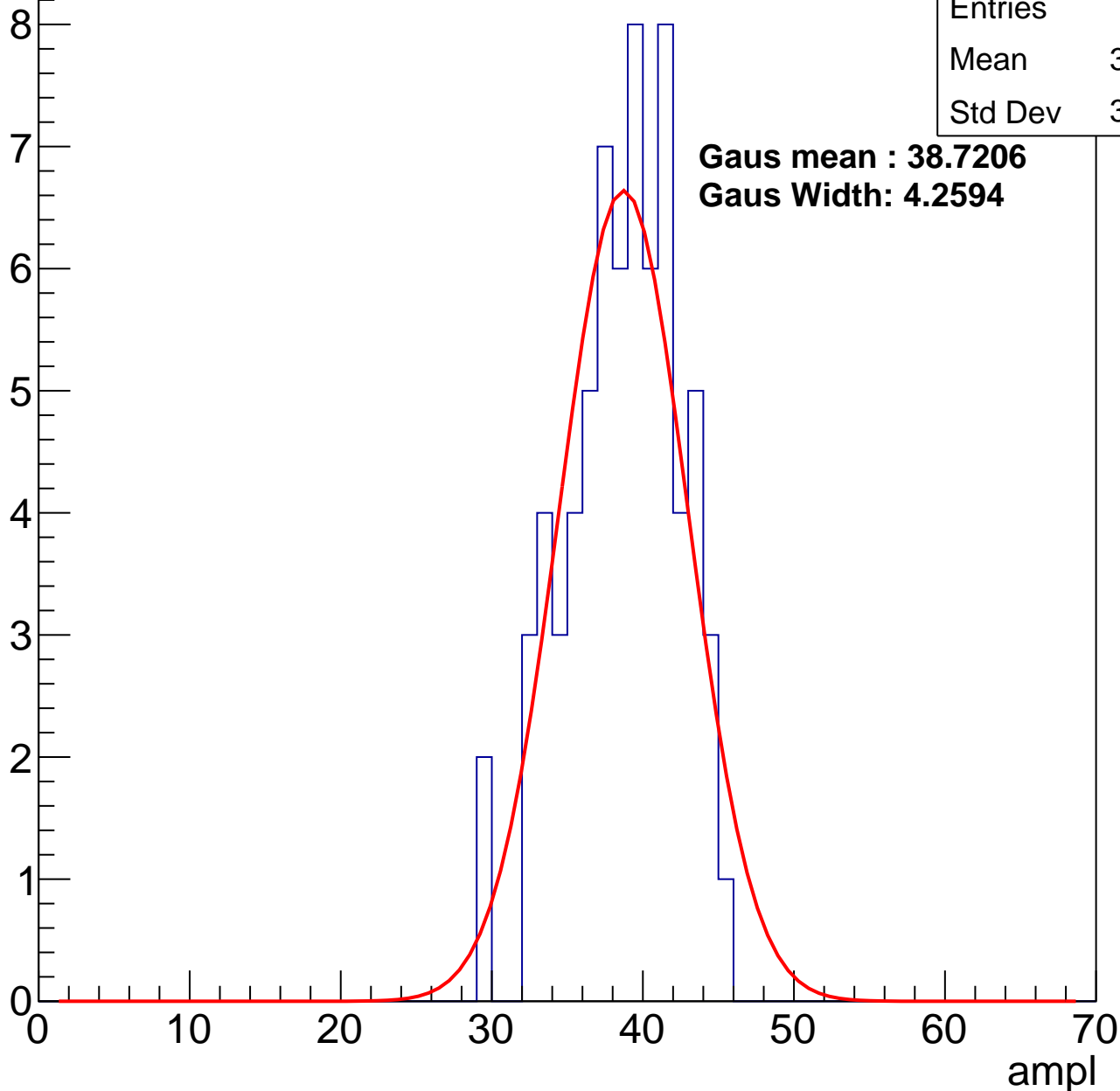
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.19
Std Dev	3.668

Gaus mean : 38.7206

Gaus Width: 4.2594



B1L101S, U26-ch103, adc2

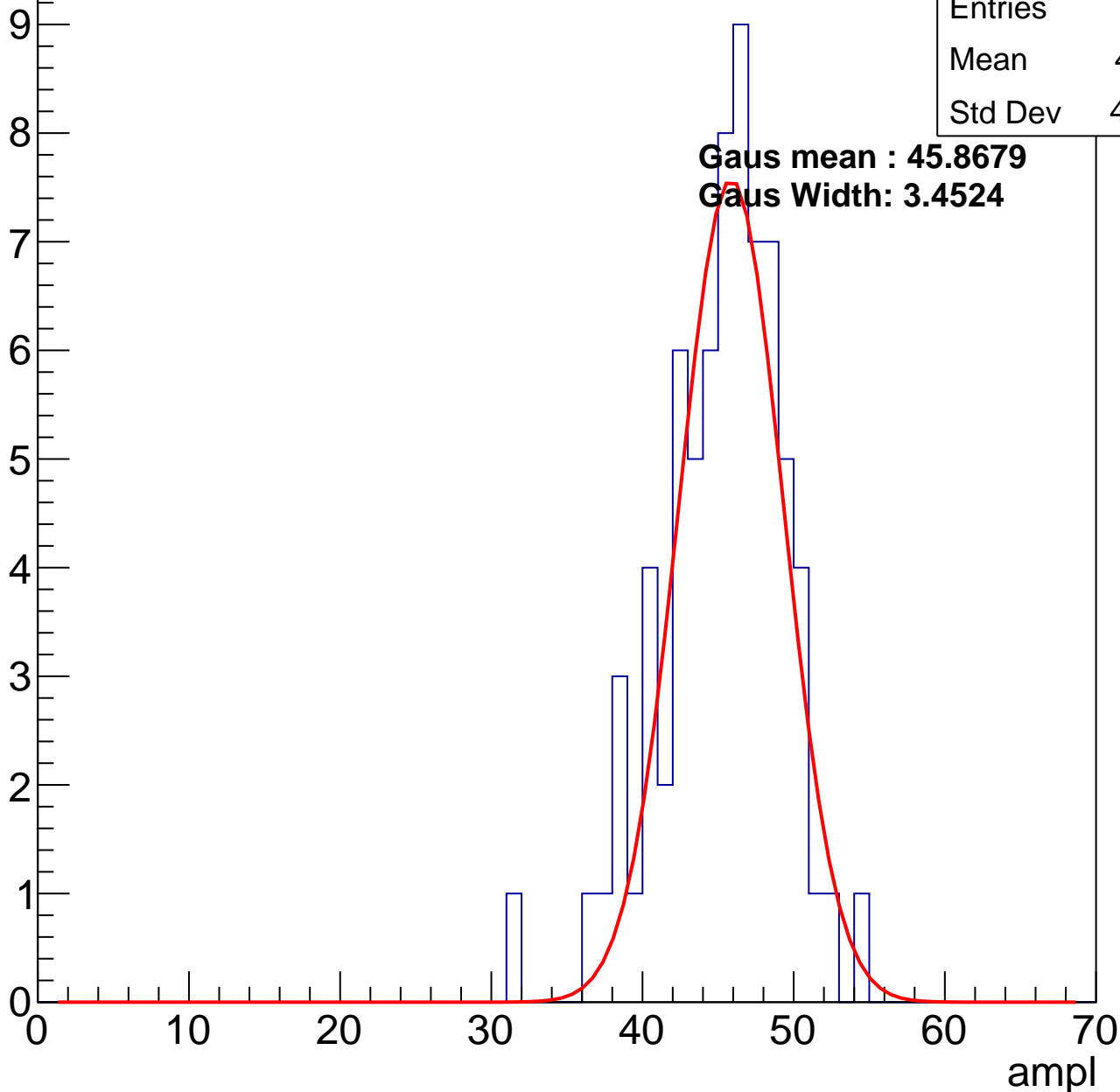
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.81
Std Dev	4.019

Gaus mean : 45.8679

Gaus Width: 3.4524

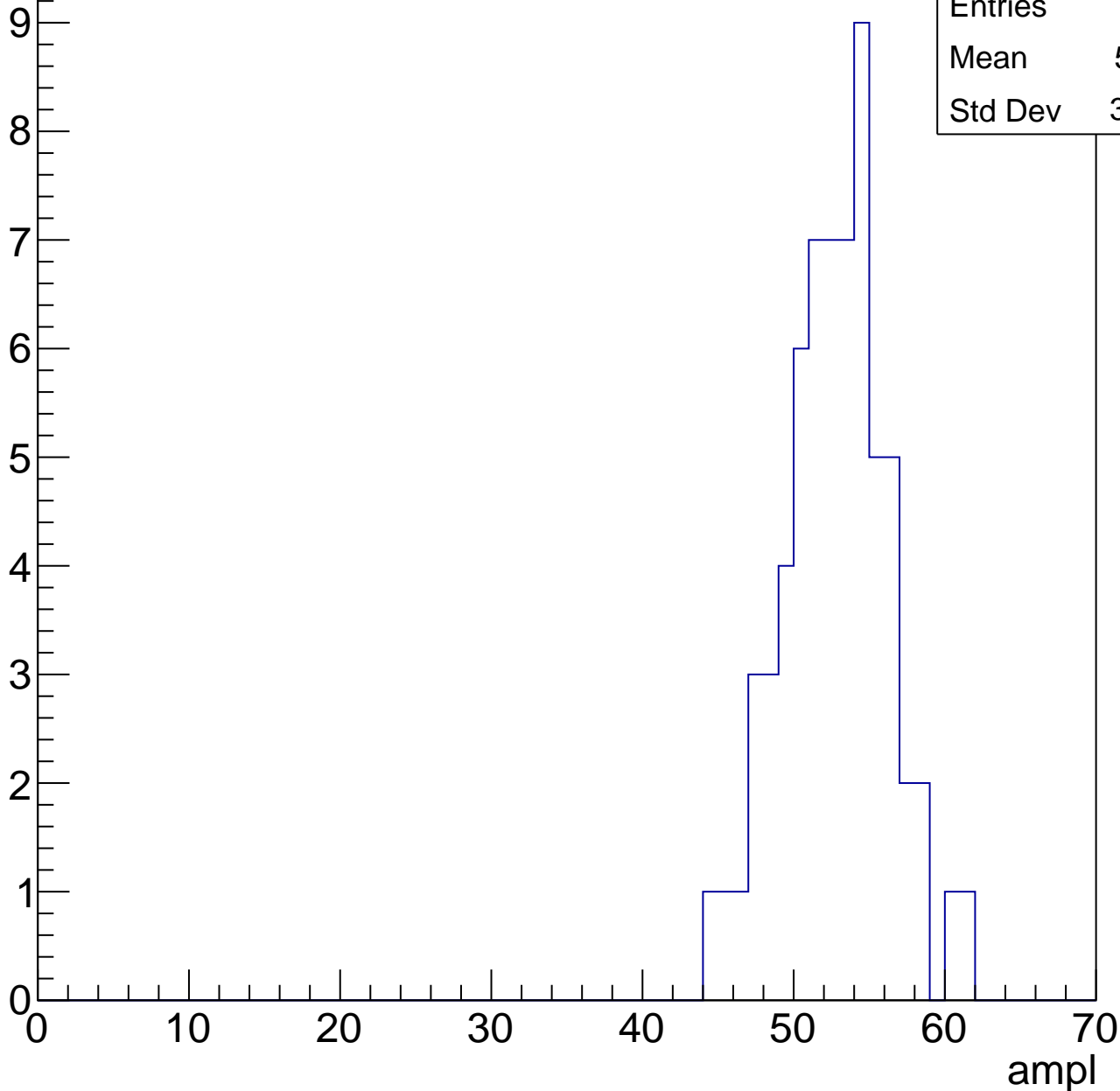


B1L101S, U26-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	52.31
Std Dev	3.437

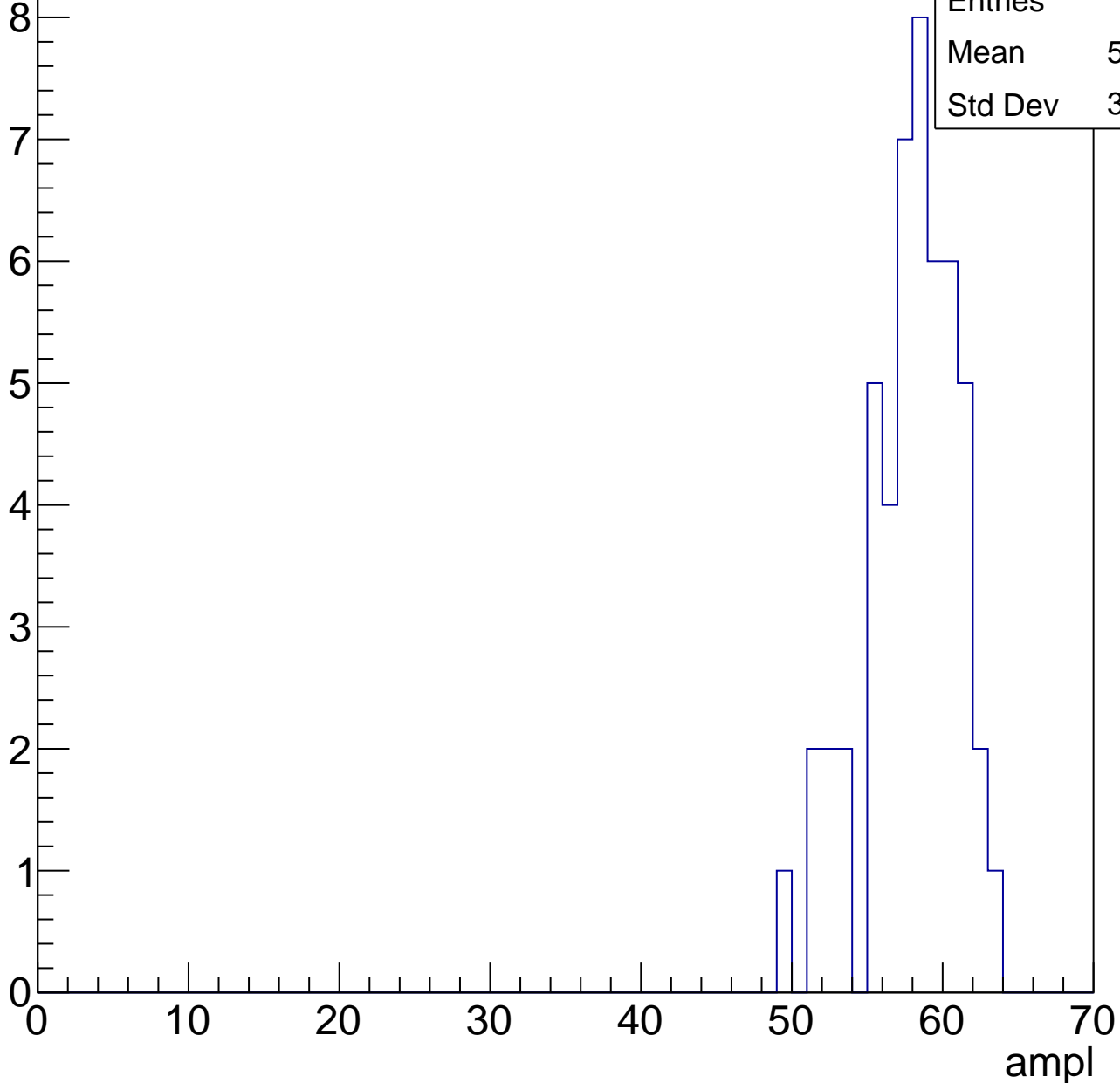


B1L101S, U26-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.43
Std Dev	3.082

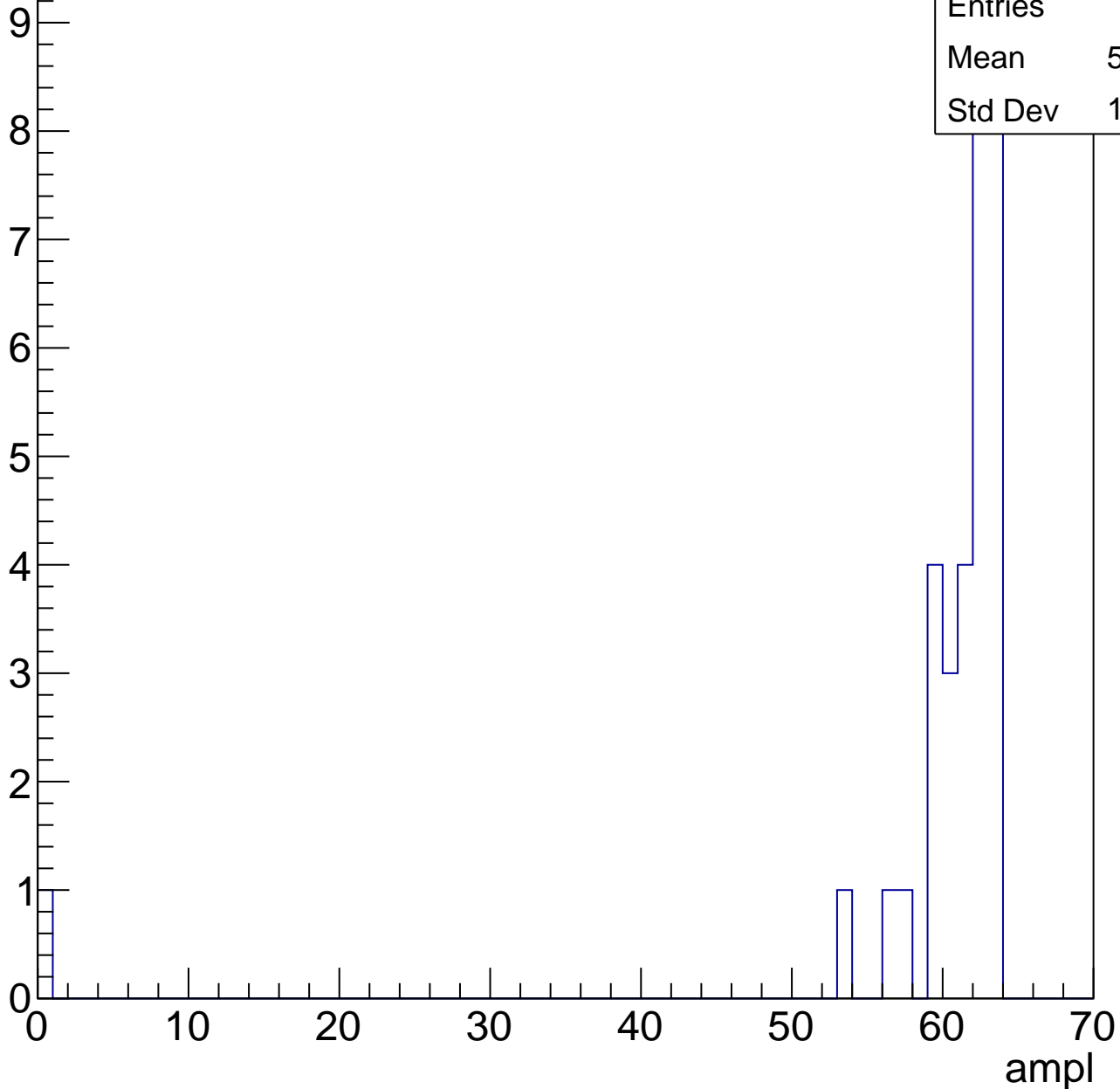


B1L101S, U26-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	59.03
Std Dev	10.85



B1L101S, U26-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U26-ch104, adc0

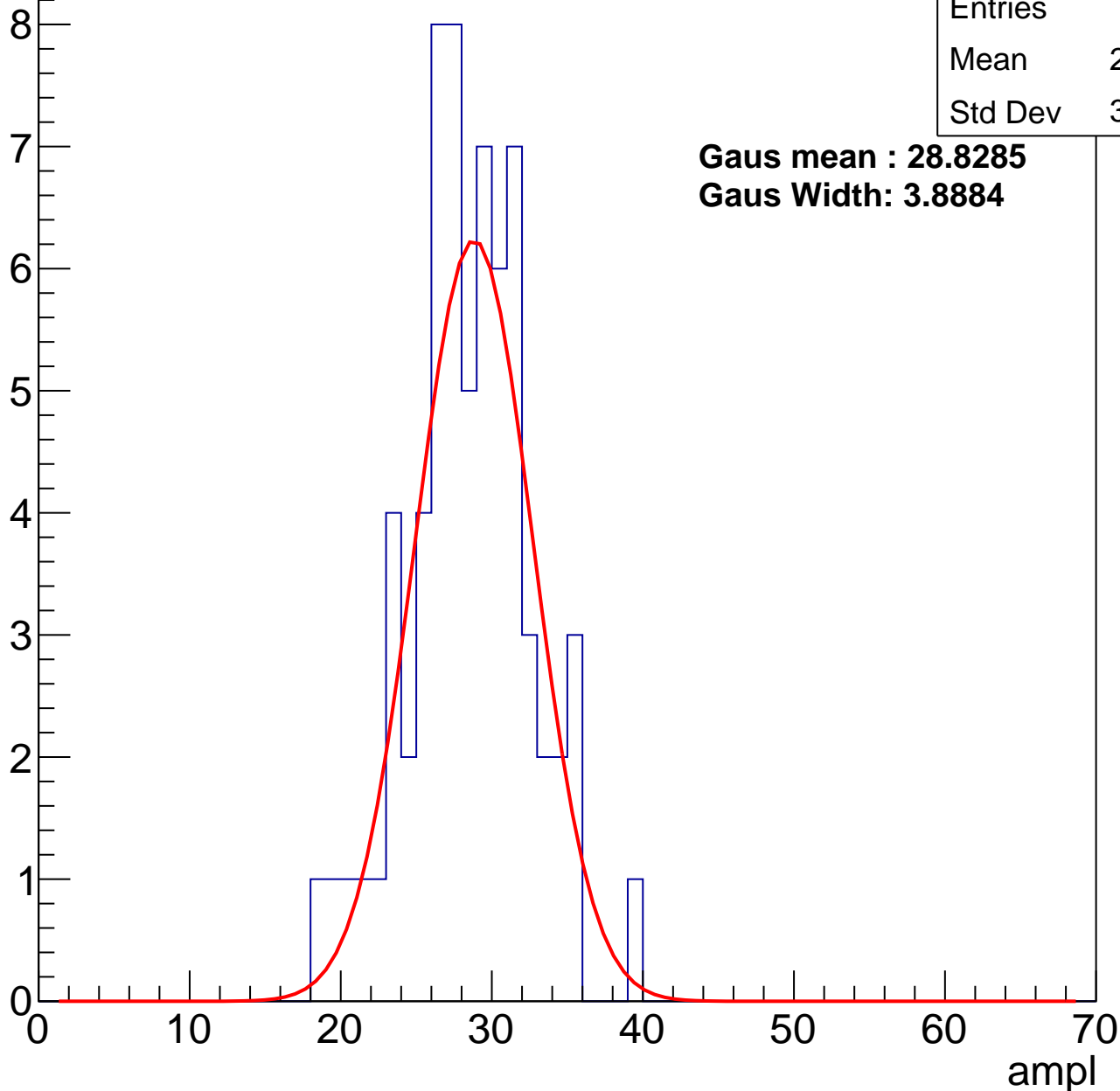
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.03
Std Dev	3.996

Gaus mean : 28.8285

Gaus Width: 3.8884



B1L101S, U26-ch104, adc1

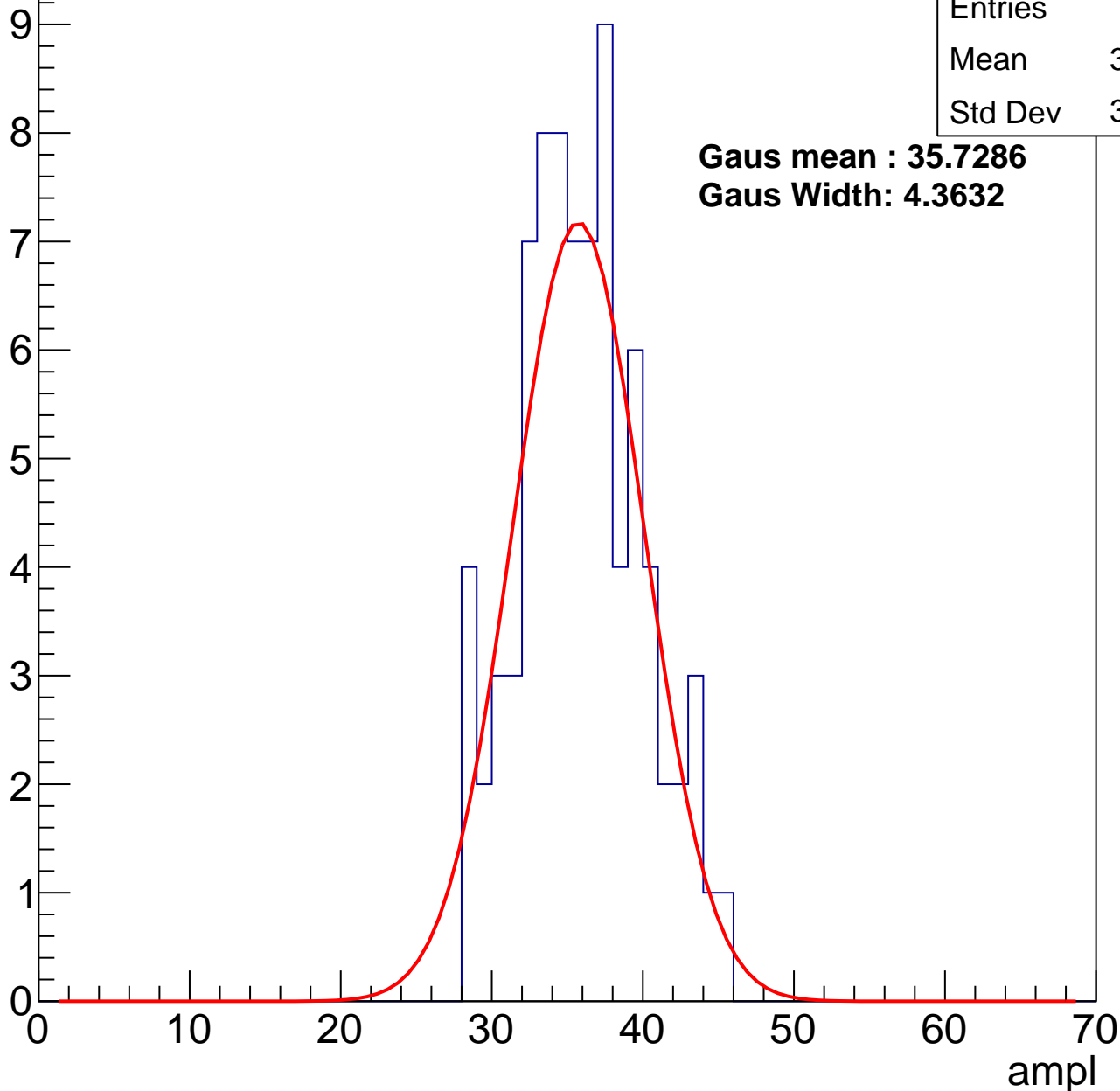
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	35.47
Std Dev	3.997

Gaus mean : 35.7286

Gaus Width: 4.3632



B1L101S, U26-ch104, adc2

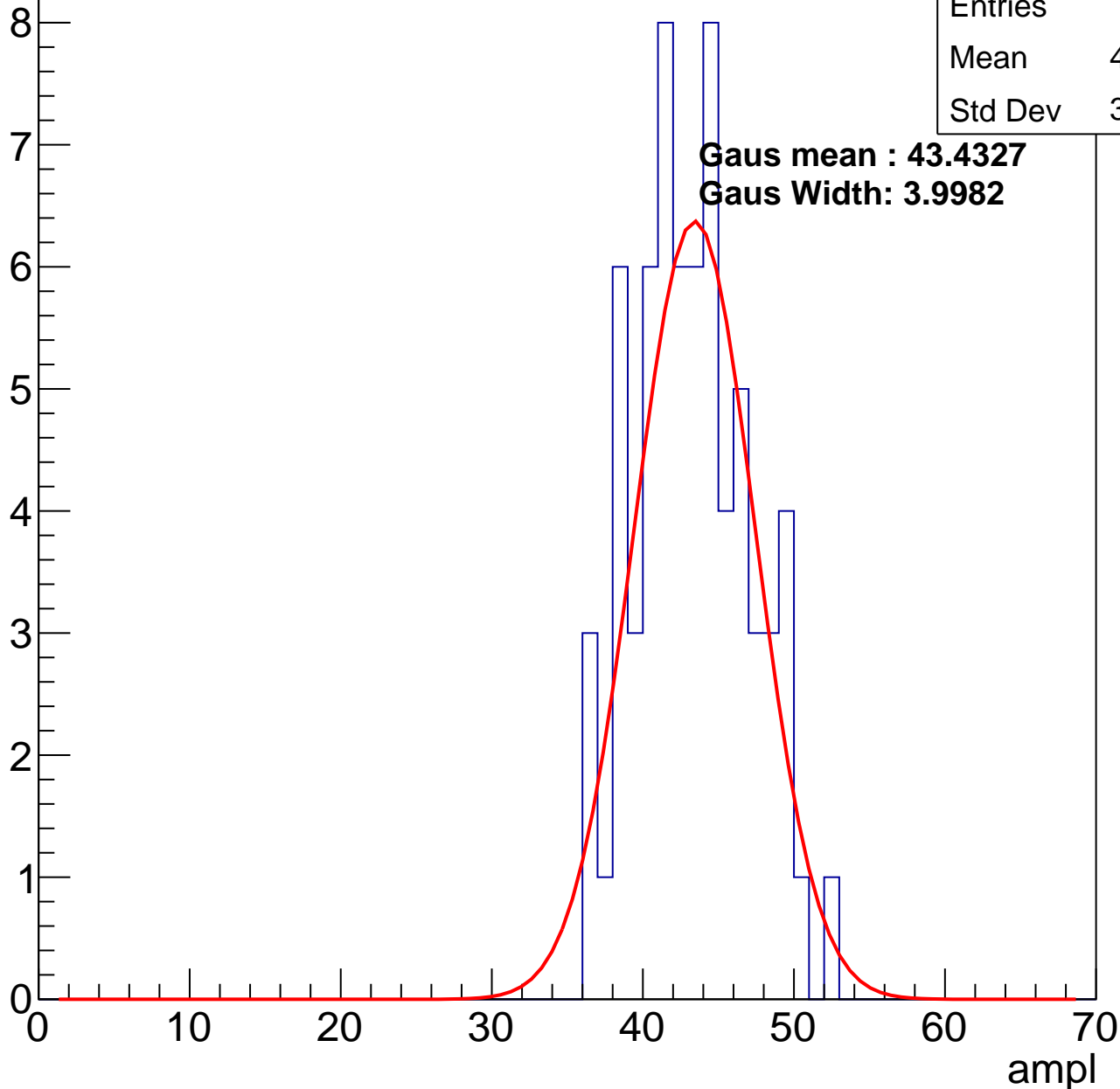
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.84
Std Dev	3.712

Gaus mean : 43.4327

Gaus Width: 3.9982

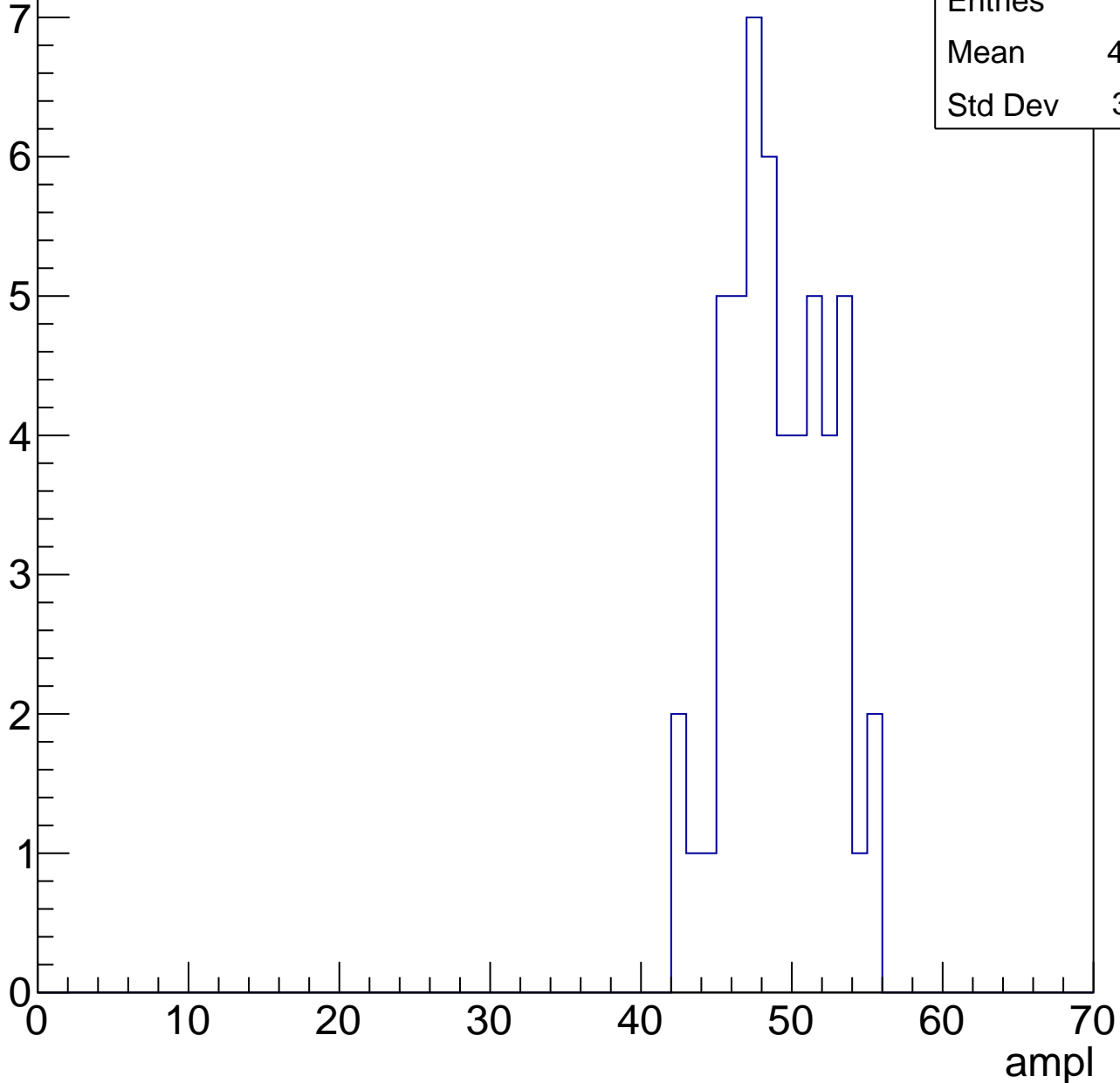


B1L101S, U26-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.67
Std Dev	3.251

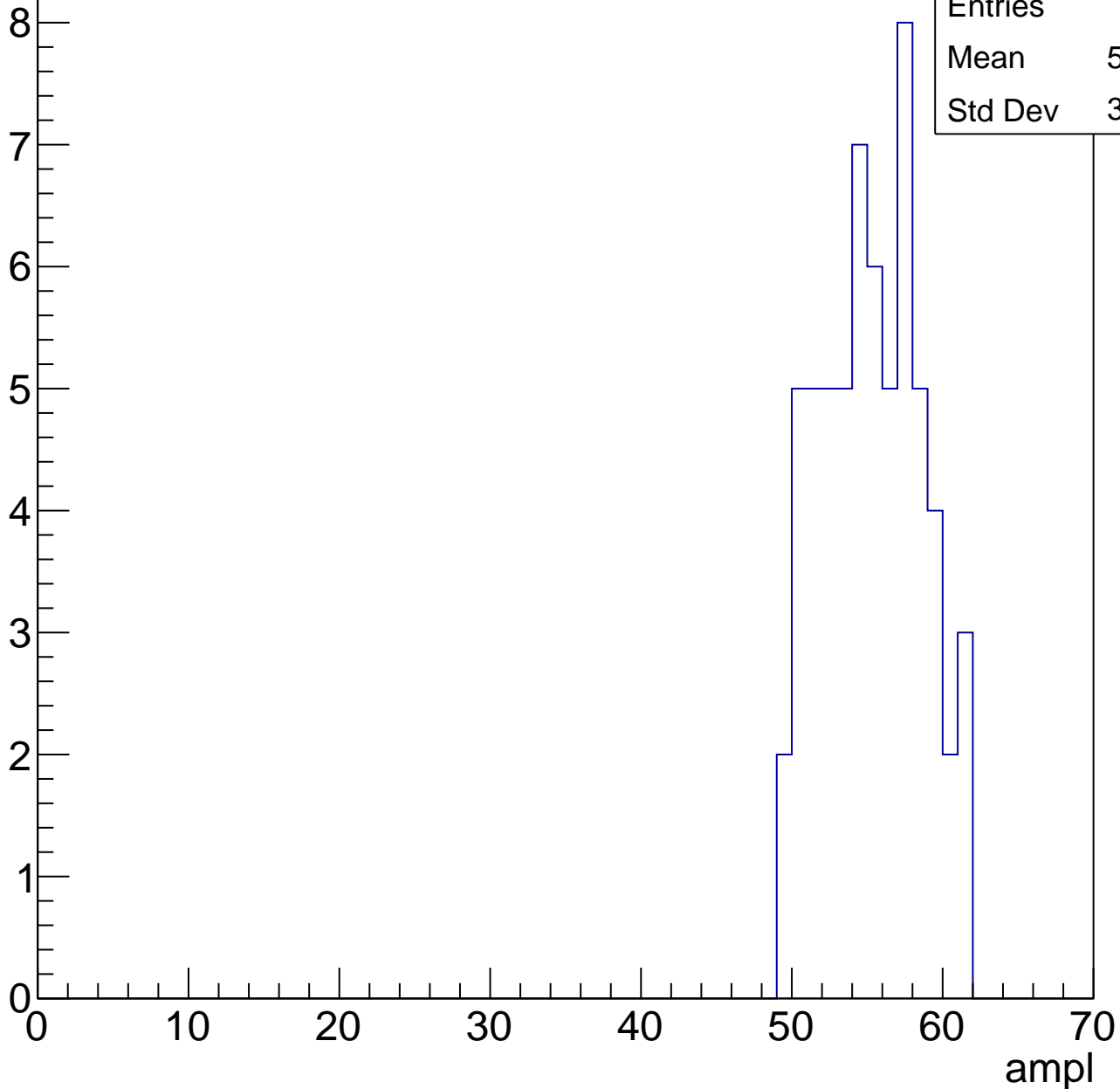


B1L101S, U26-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.85
Std Dev	3.242

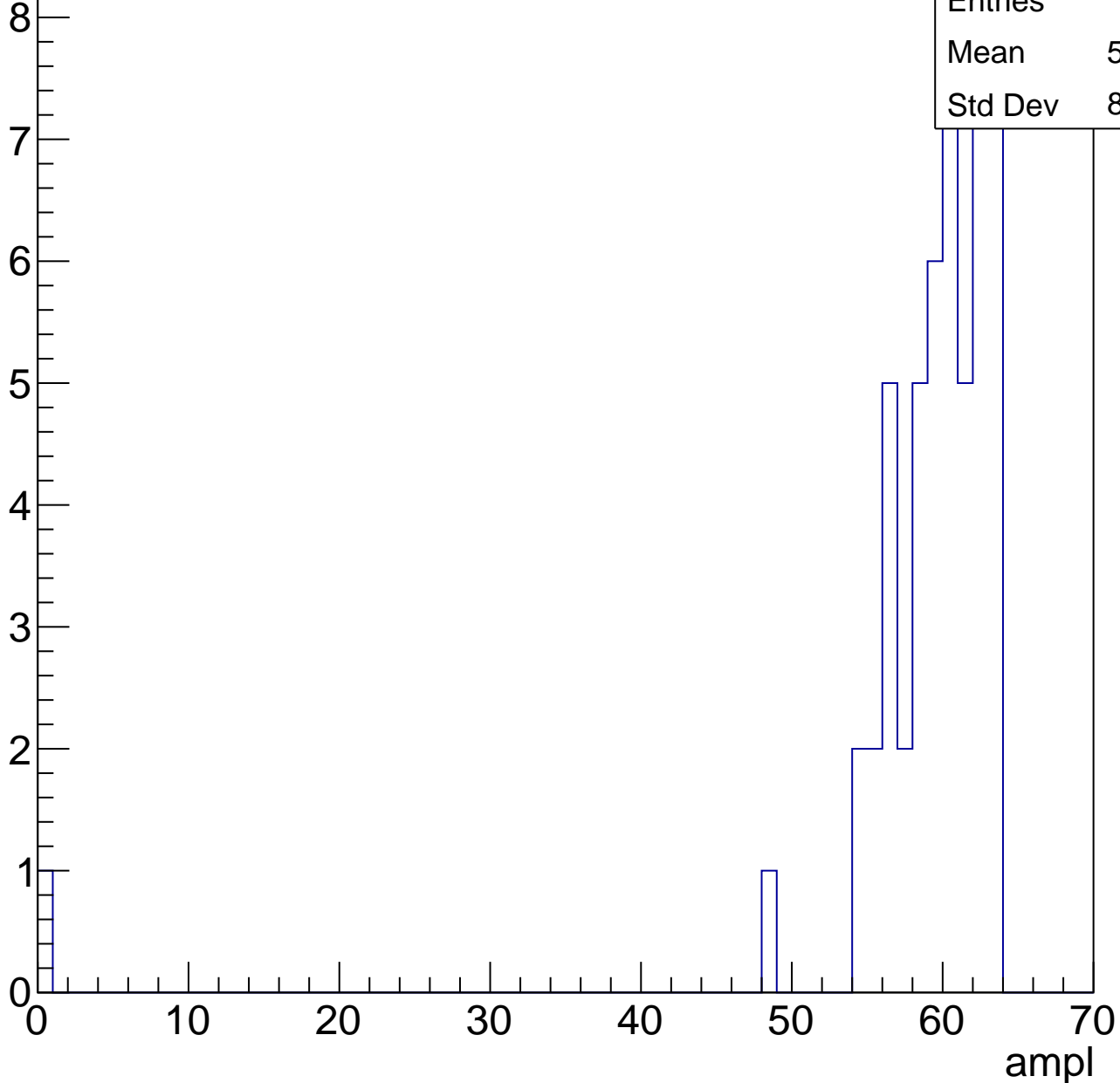


B1L101S, U26-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.28
Std Dev	8.625



B1L101S, U26-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	62
Std Dev	0.8165

ampl

B1L101S, U26-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch105, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	30.69
Std Dev	4.999

Gaus mean : 31.5065

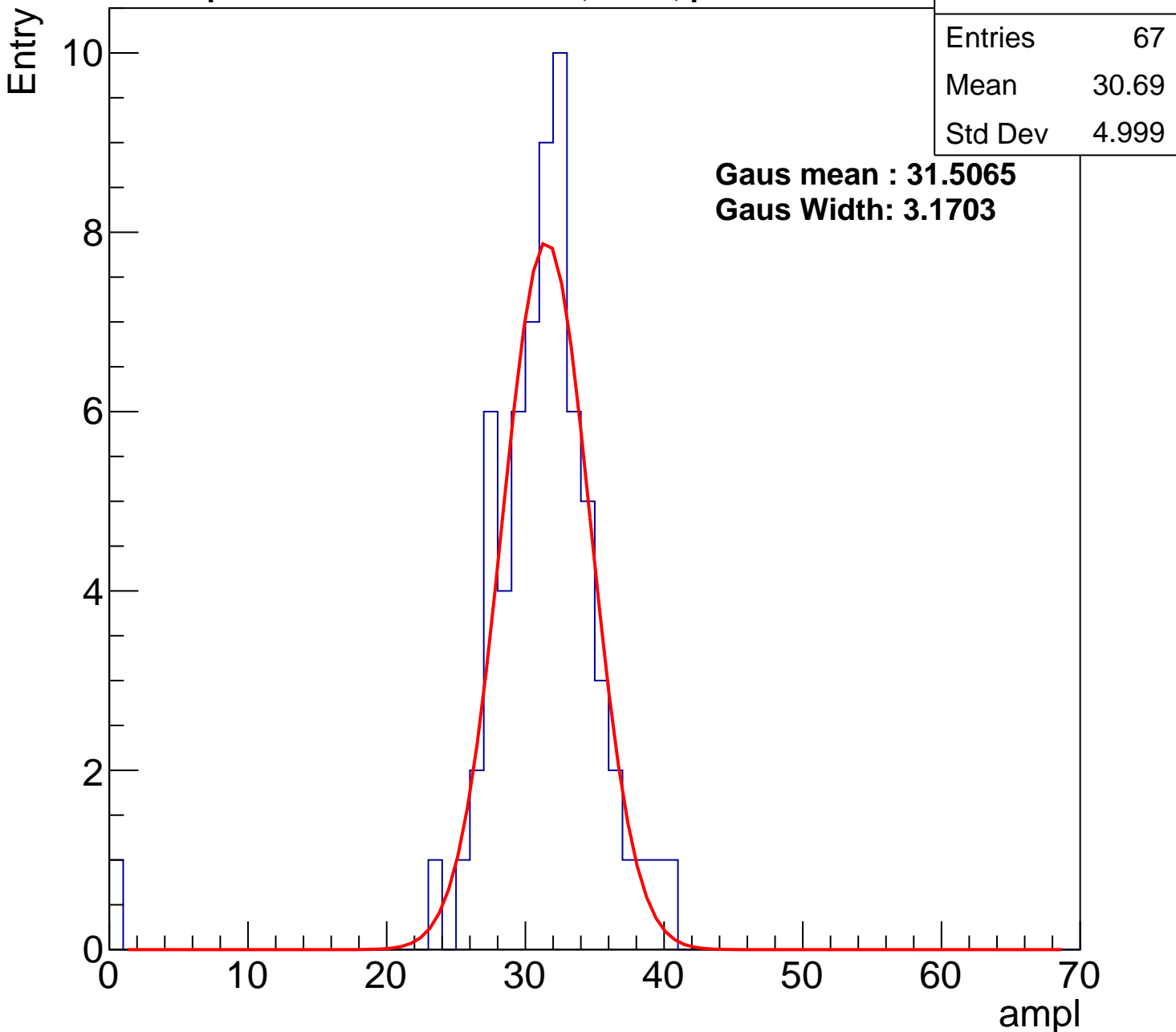
Gaus Width: 3.1703

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch105, adc1

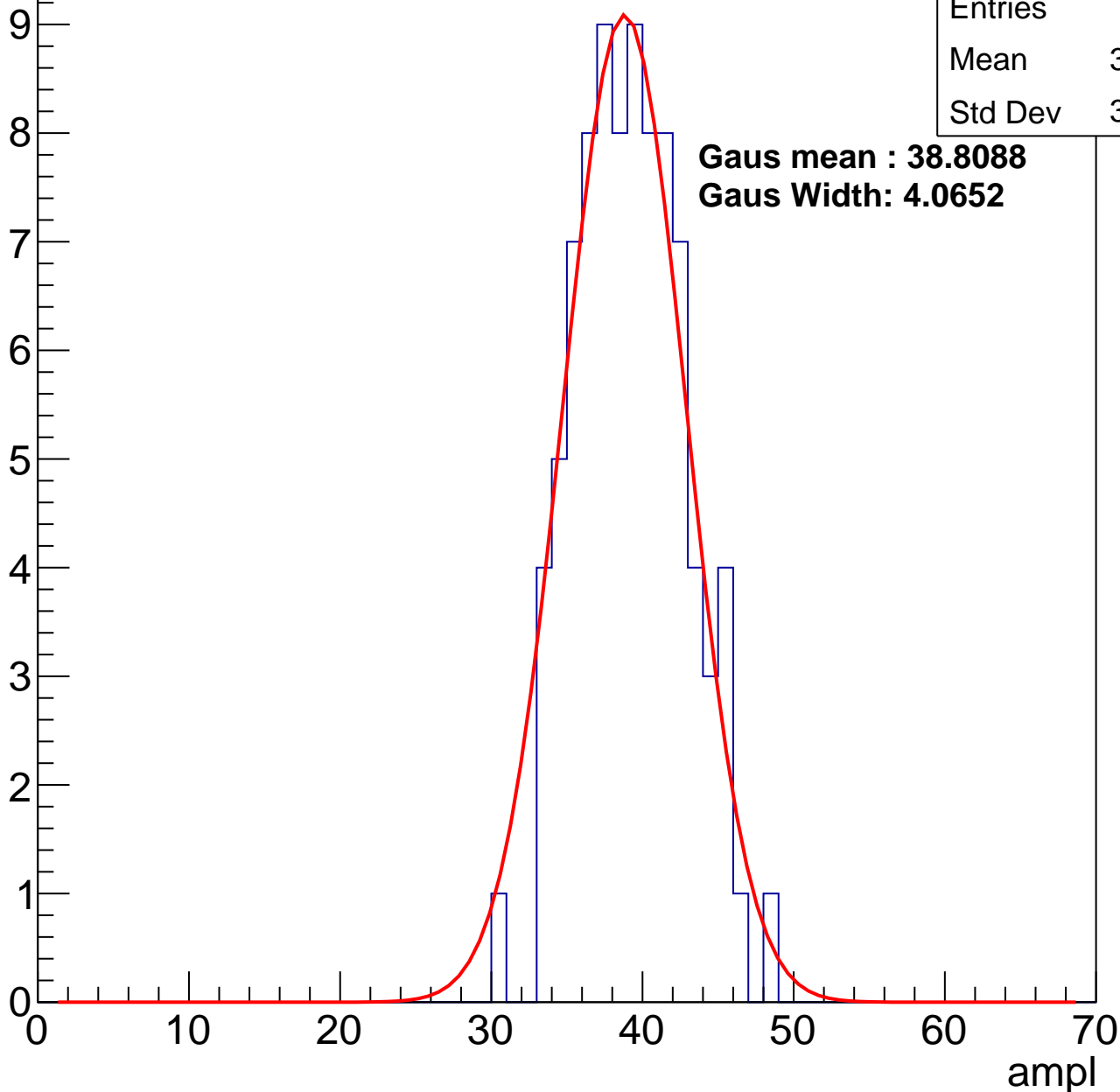
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	38.77
Std Dev	3.539

Gaus mean : 38.8088

Gaus Width: 4.0652



B1L101S, U26-ch105, adc2

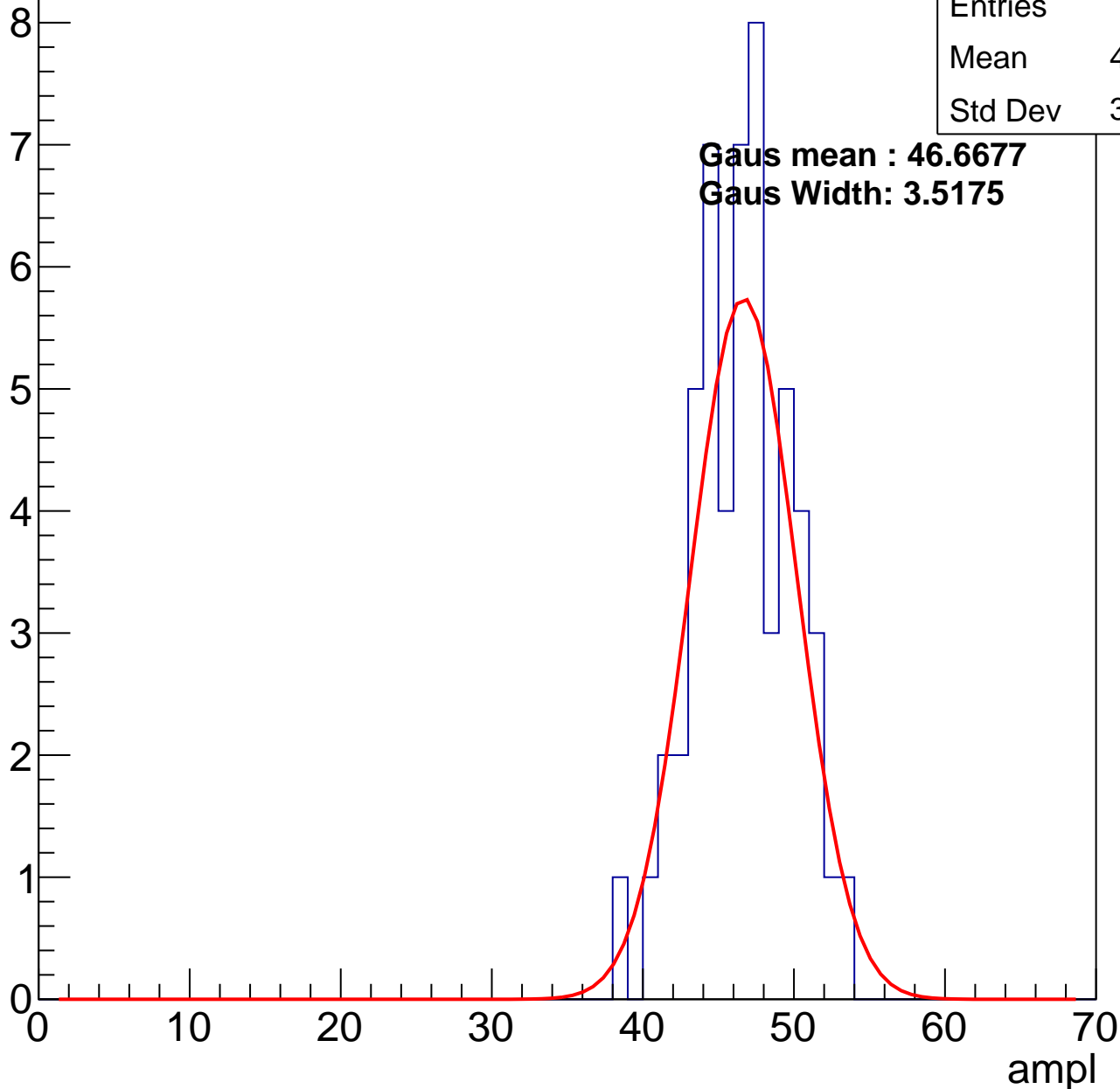
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	46.15
Std Dev	3.182

Gaus mean : 46.6677

Gaus Width: 3.5175



B1L101S, U26-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

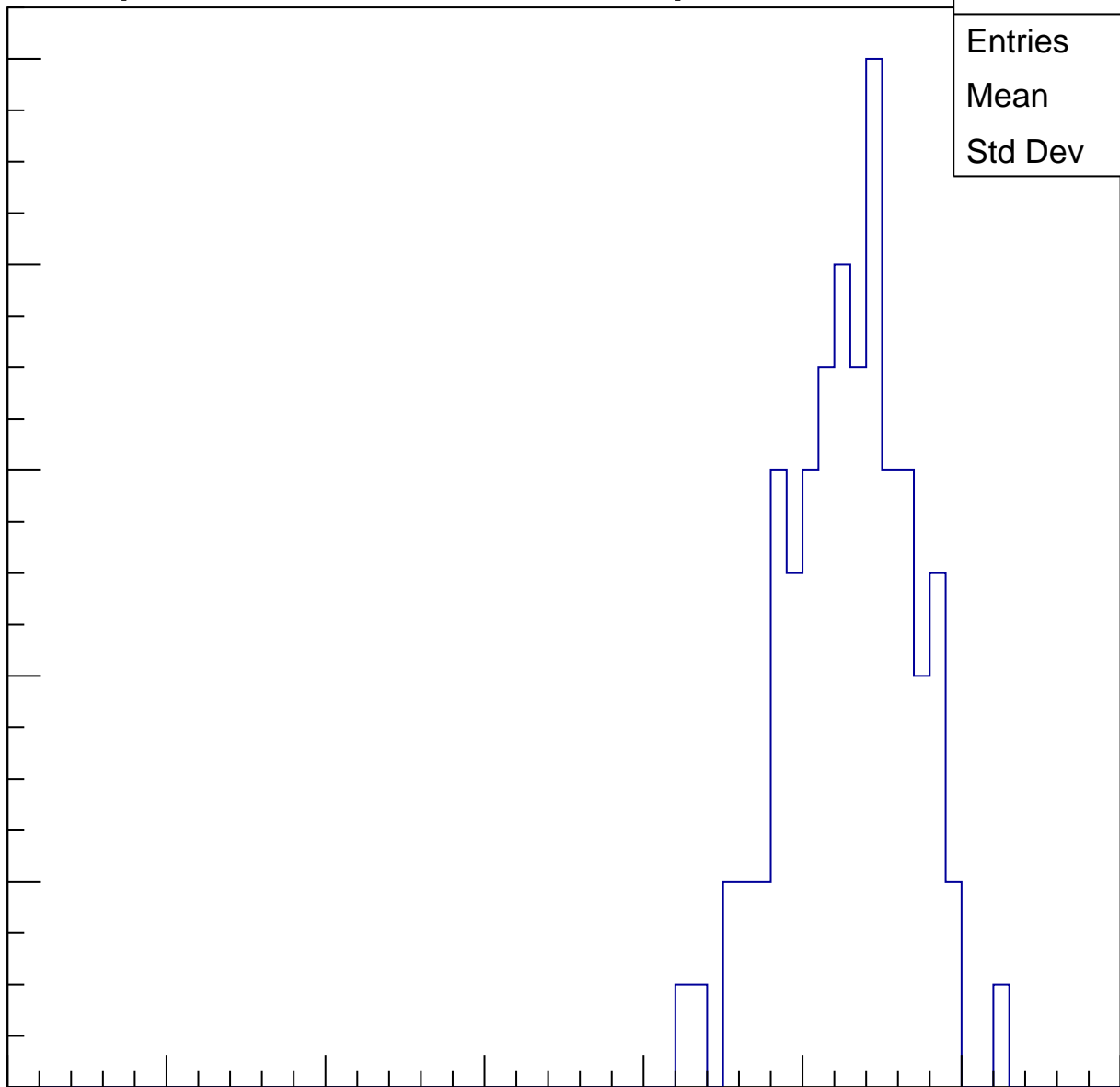
Entries	81
Mean	52.37
Std Dev	3.908

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

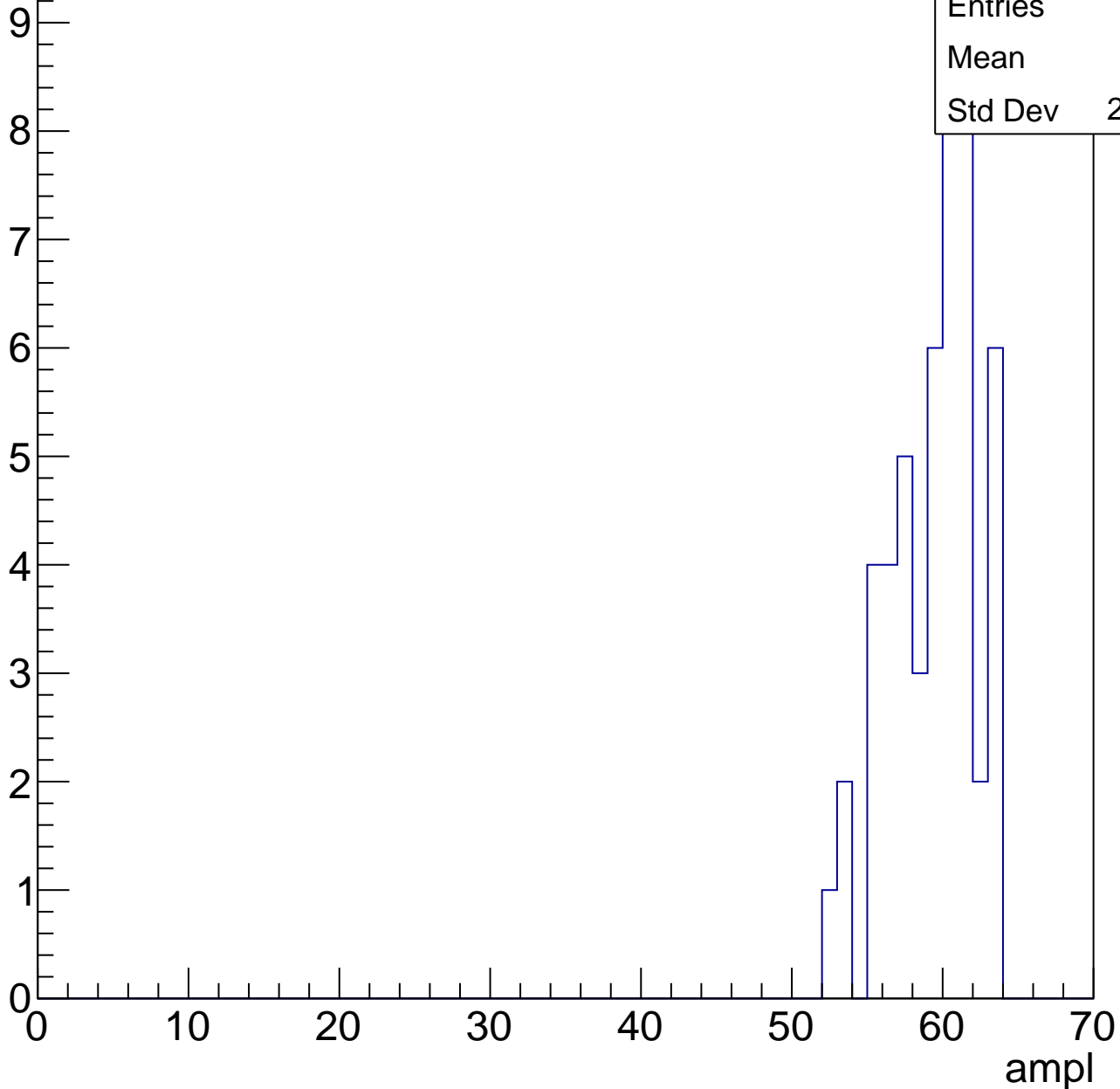


B1L101S, U26-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.9
Std Dev	2.823

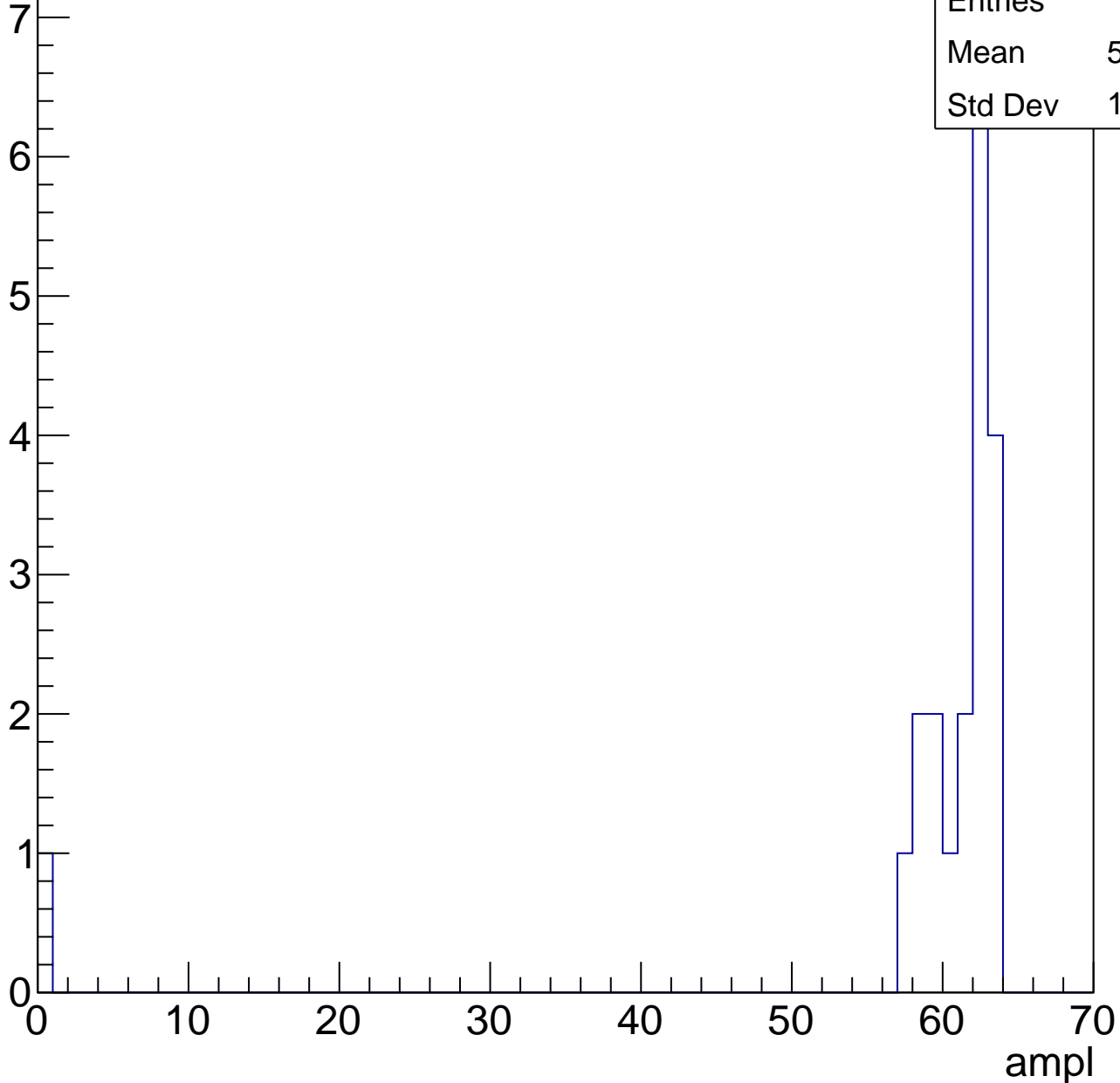


B1L101S, U26-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	57.95
Std Dev	13.42



B1L101S, U26-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch106, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	29.9
Std Dev	3.154

Gaus mean : 30.0800

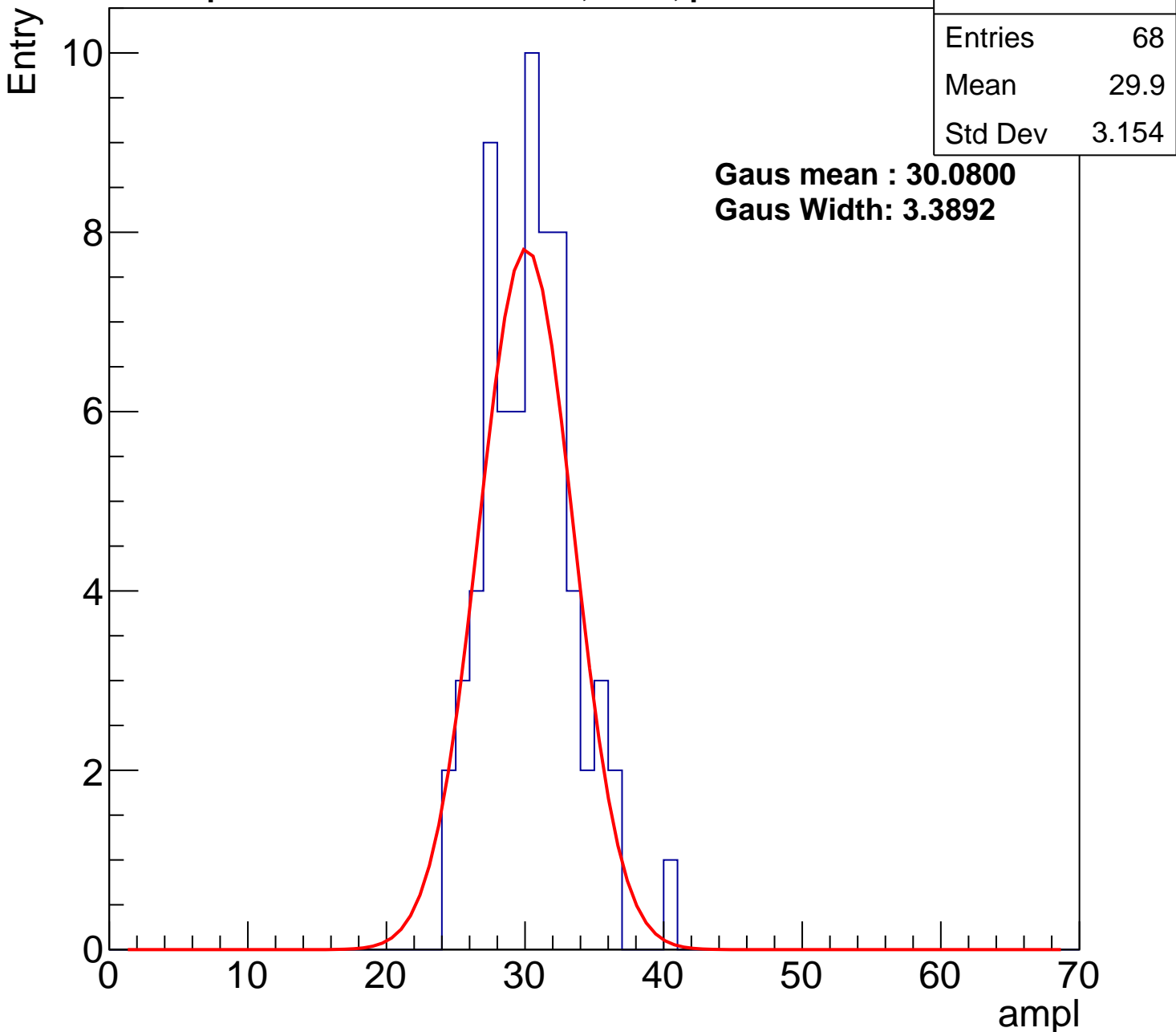
Gaus Width: 3.3892

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch106, adc1

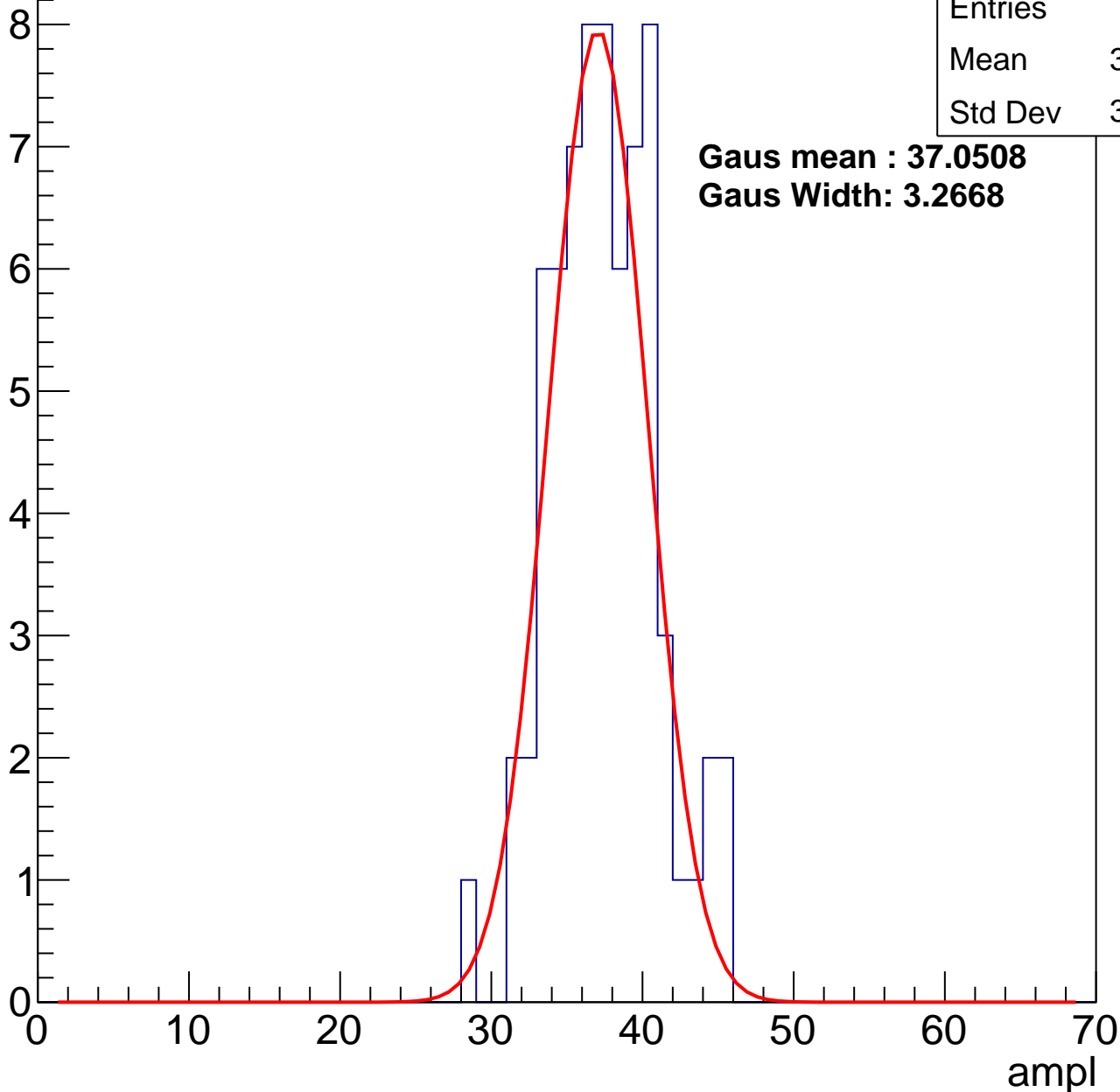
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.03
Std Dev	3.443

Gaus mean : 37.0508

Gaus Width: 3.2668



B1L101S, U26-ch106, adc2

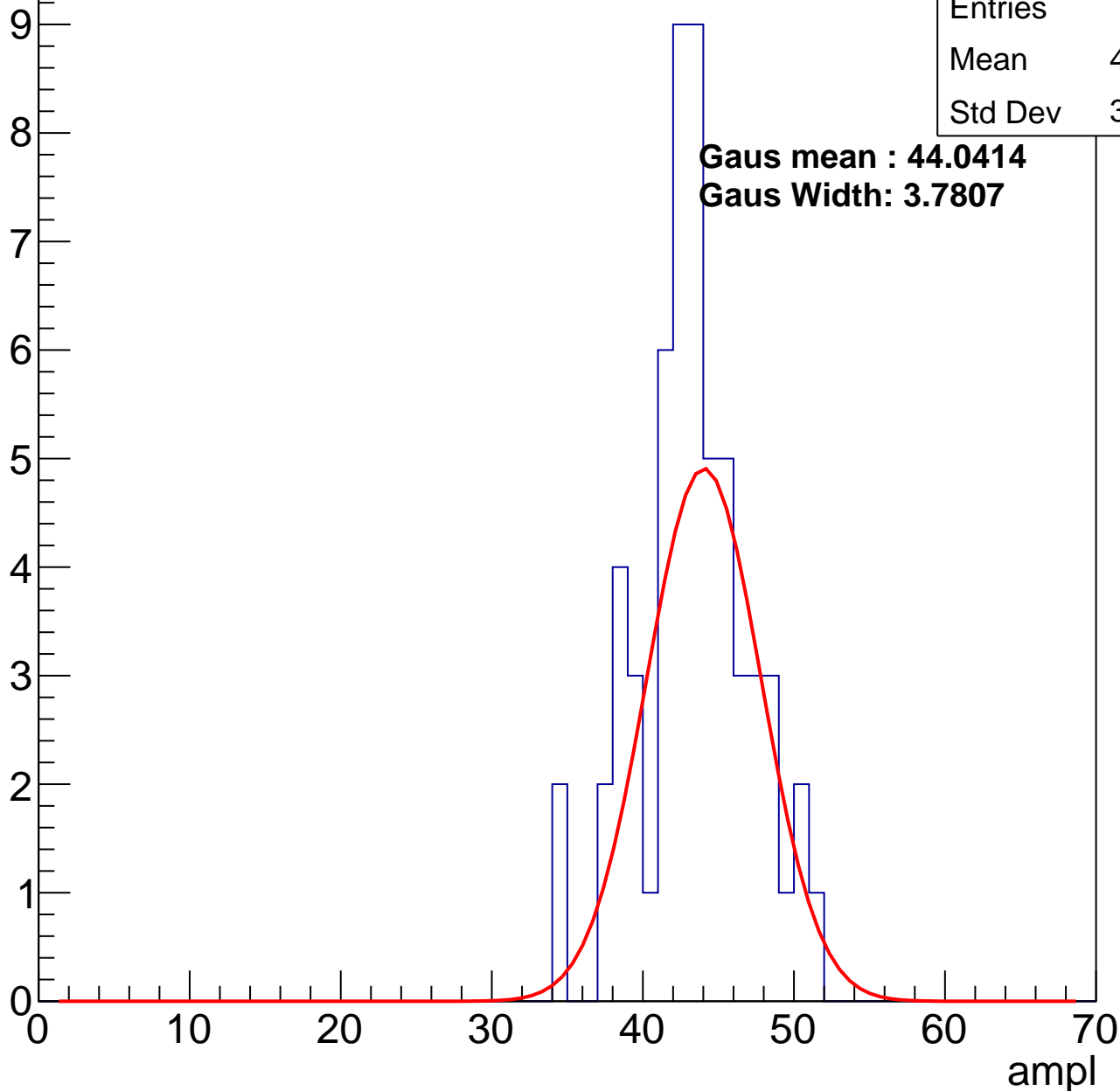
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.88
Std Dev	3.673

Gaus mean : 44.0414

Gaus Width: 3.7807

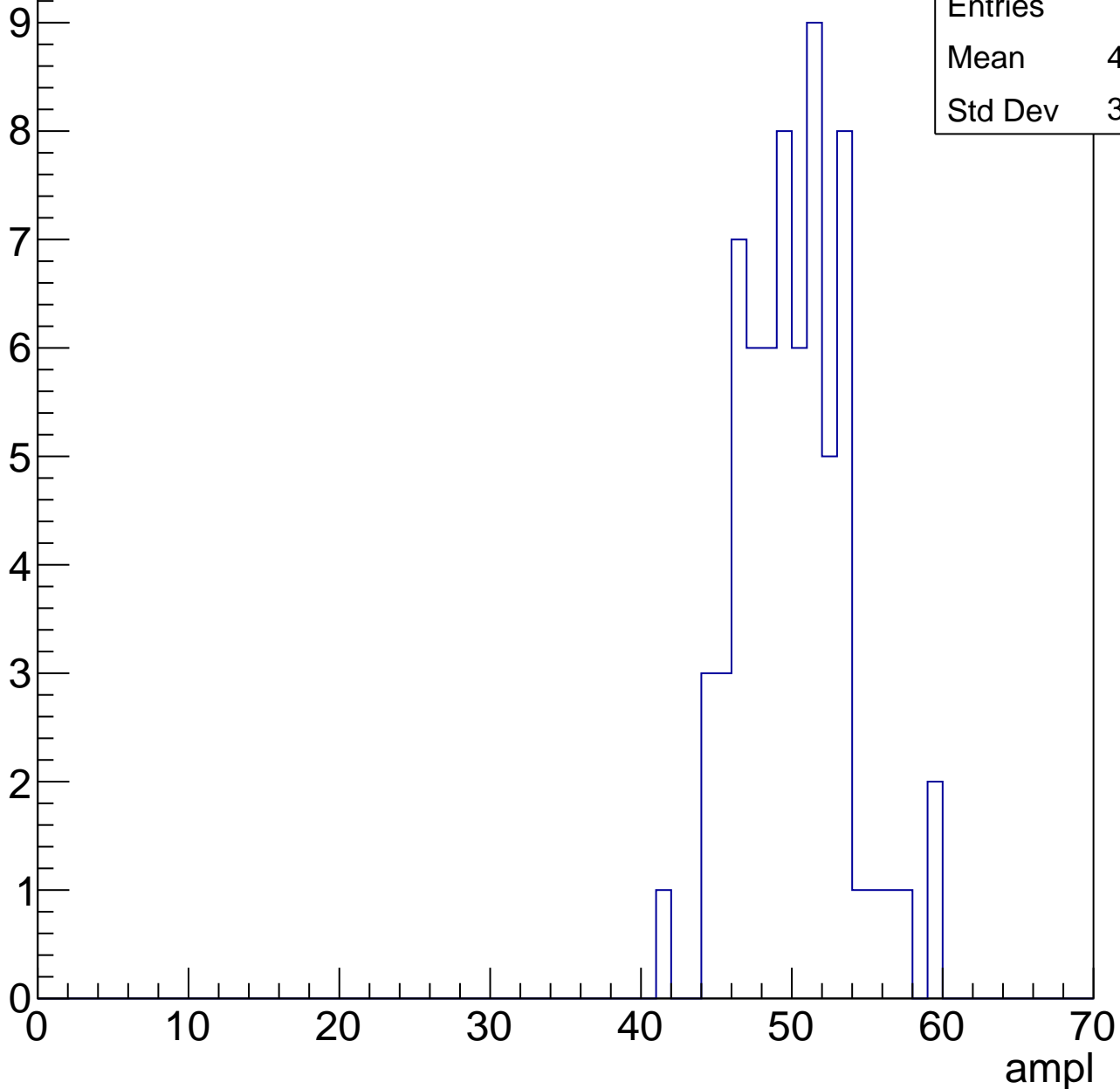


B1L101S, U26-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

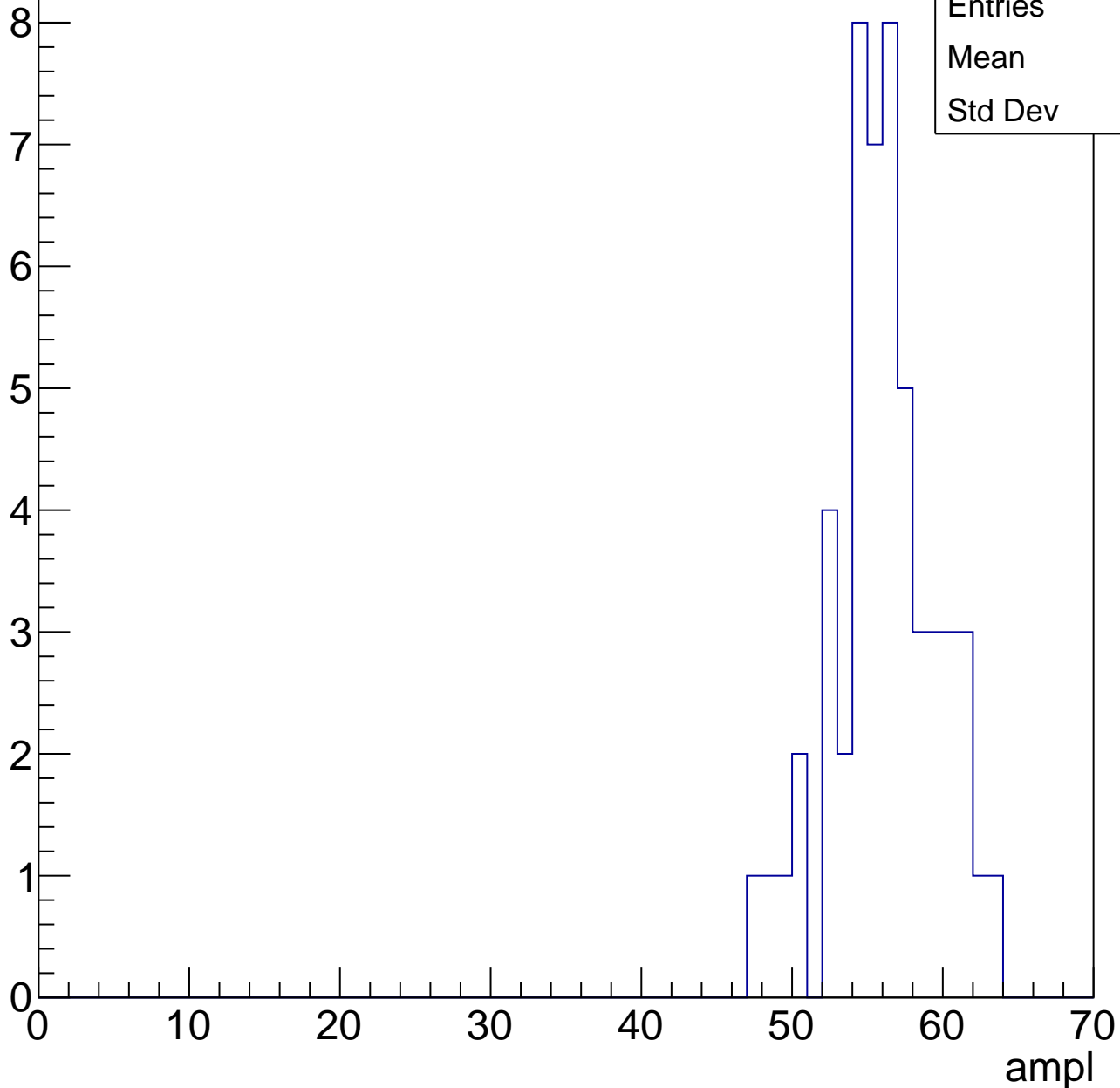
Entries	68
Mean	49.63
Std Dev	3.518



B1L101S, U26-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



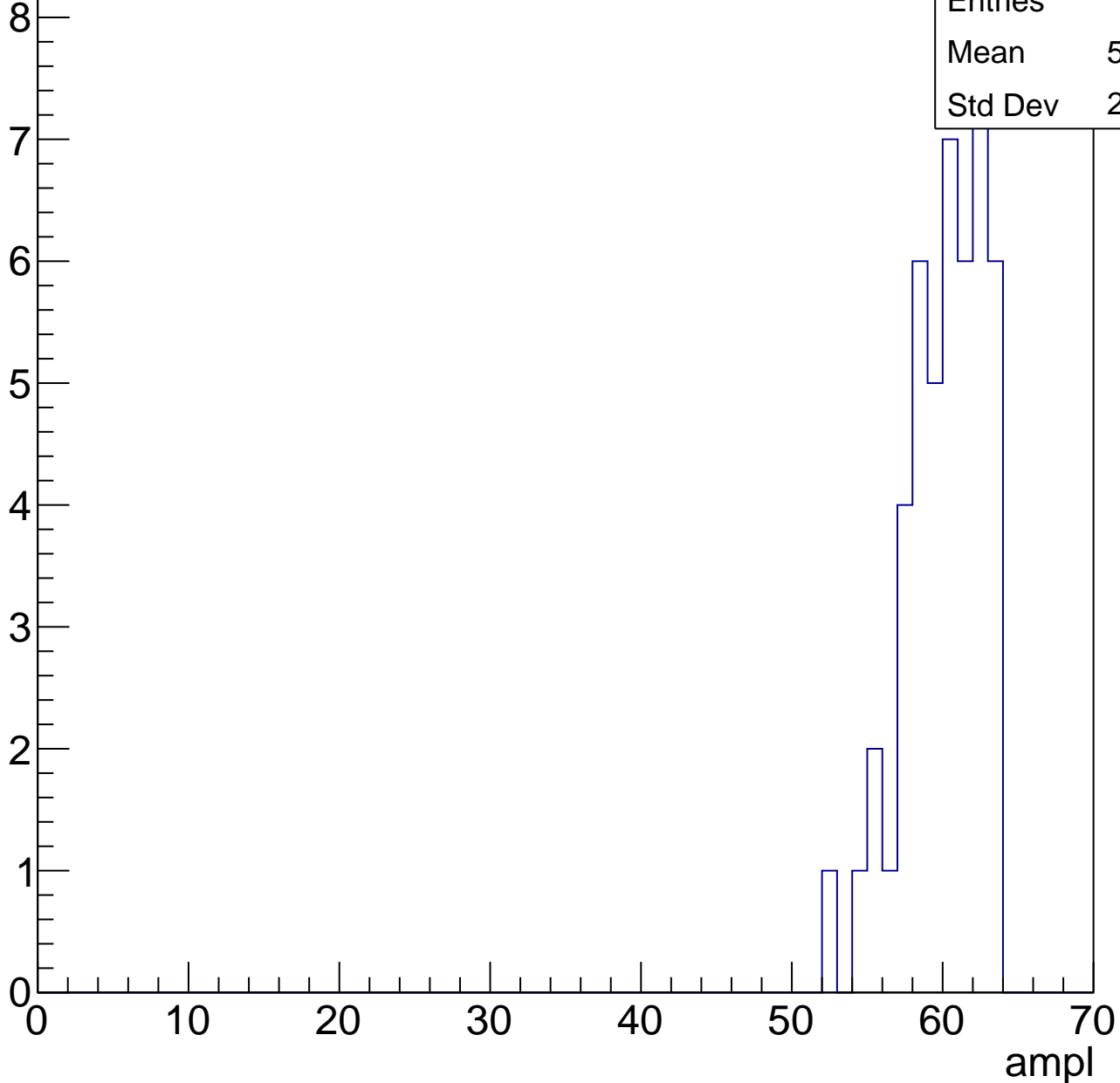
Entries	53
Mean	55.6
Std Dev	3.45

B1L101S, U26-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

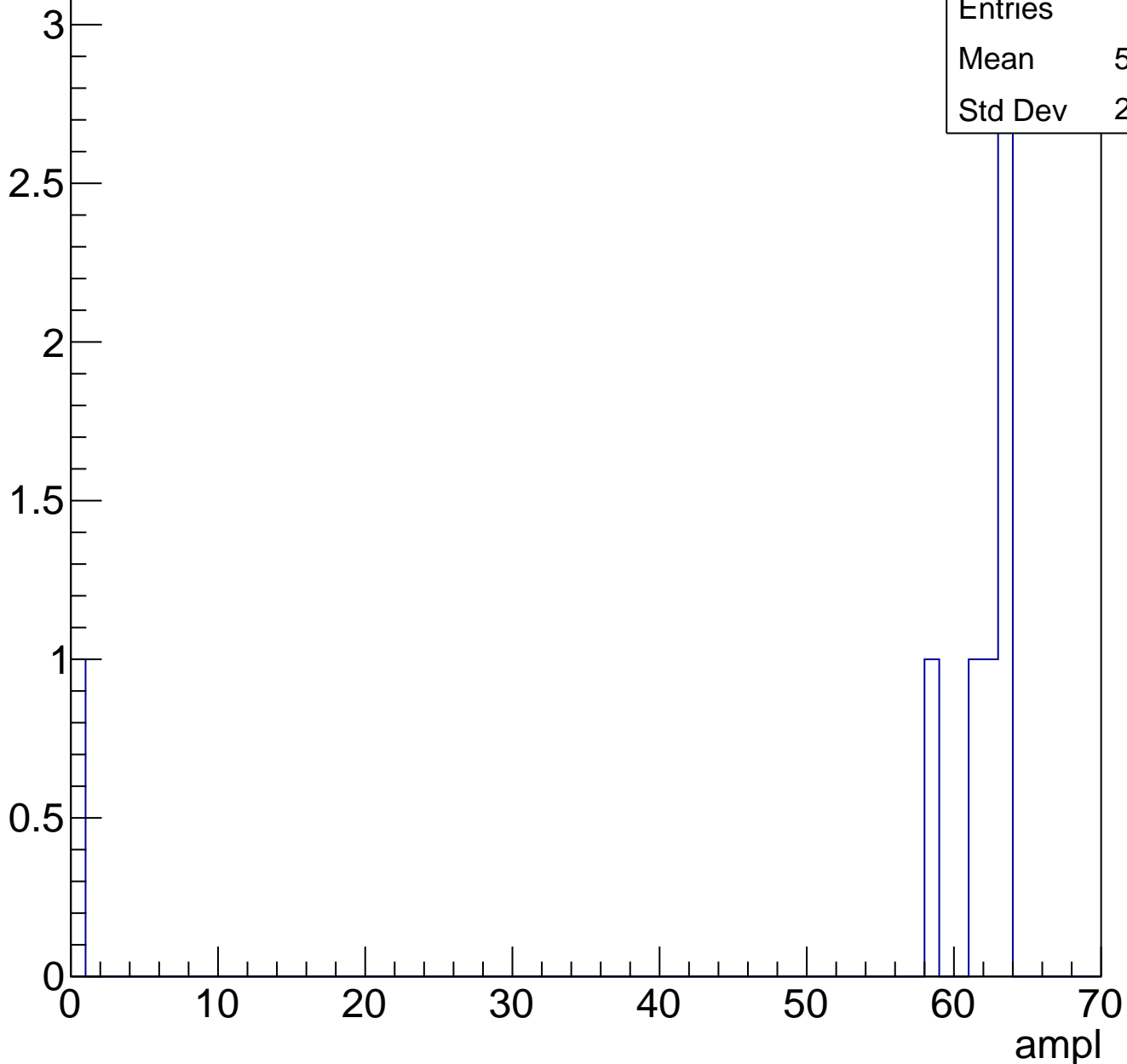
Entries	47
Mean	59.64
Std Dev	2.596



B1L101S, U26-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch107, adc0

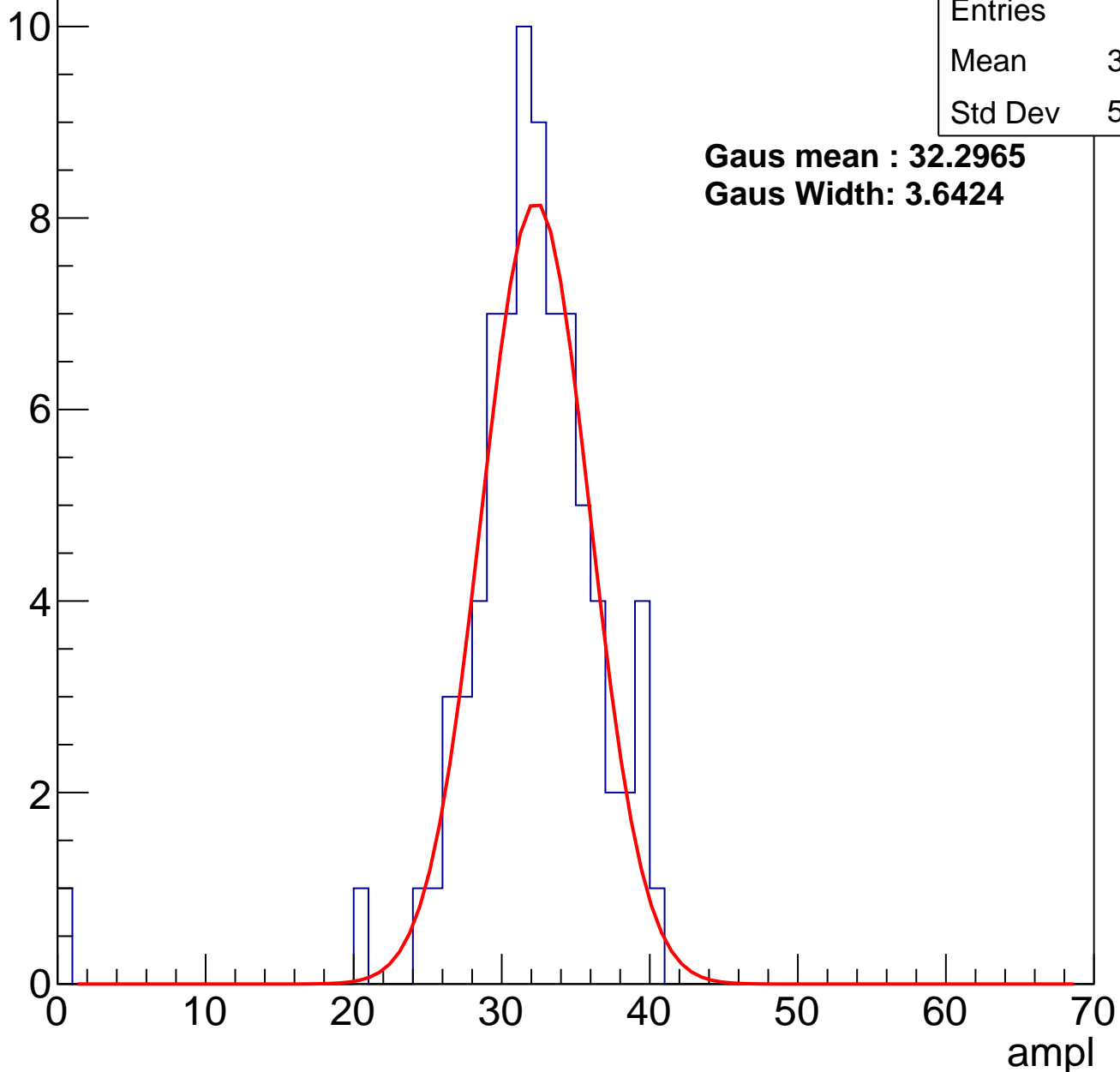
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	31.46
Std Dev	5.194

Gaus mean : 32.2965

Gaus Width: 3.6424

Entry



B1L101S, U26-ch107, adc1

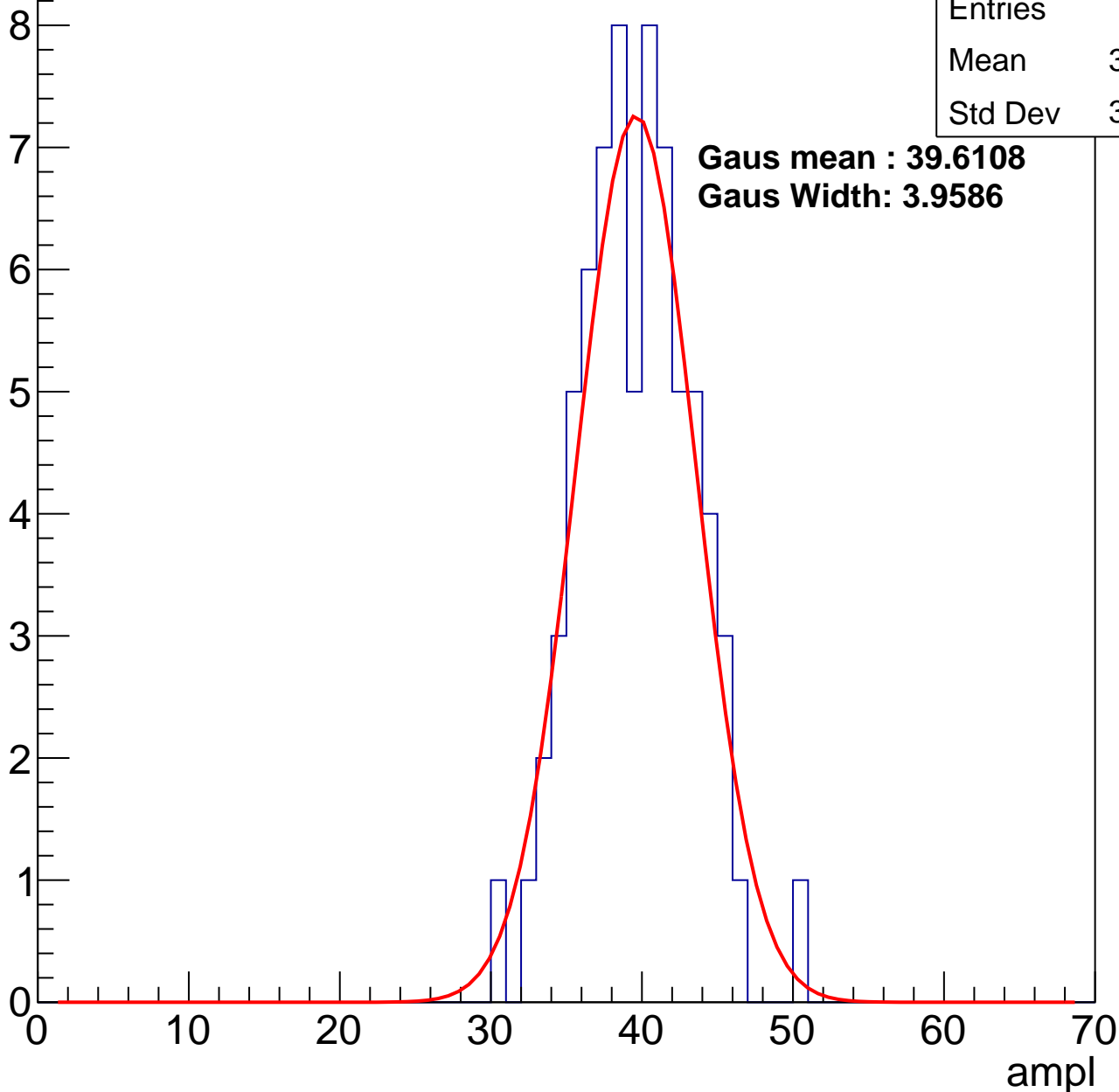
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	39.14
Std Dev	3.698

Gaus mean : 39.6108

Gaus Width: 3.9586



B1L101S, U26-ch107, adc2

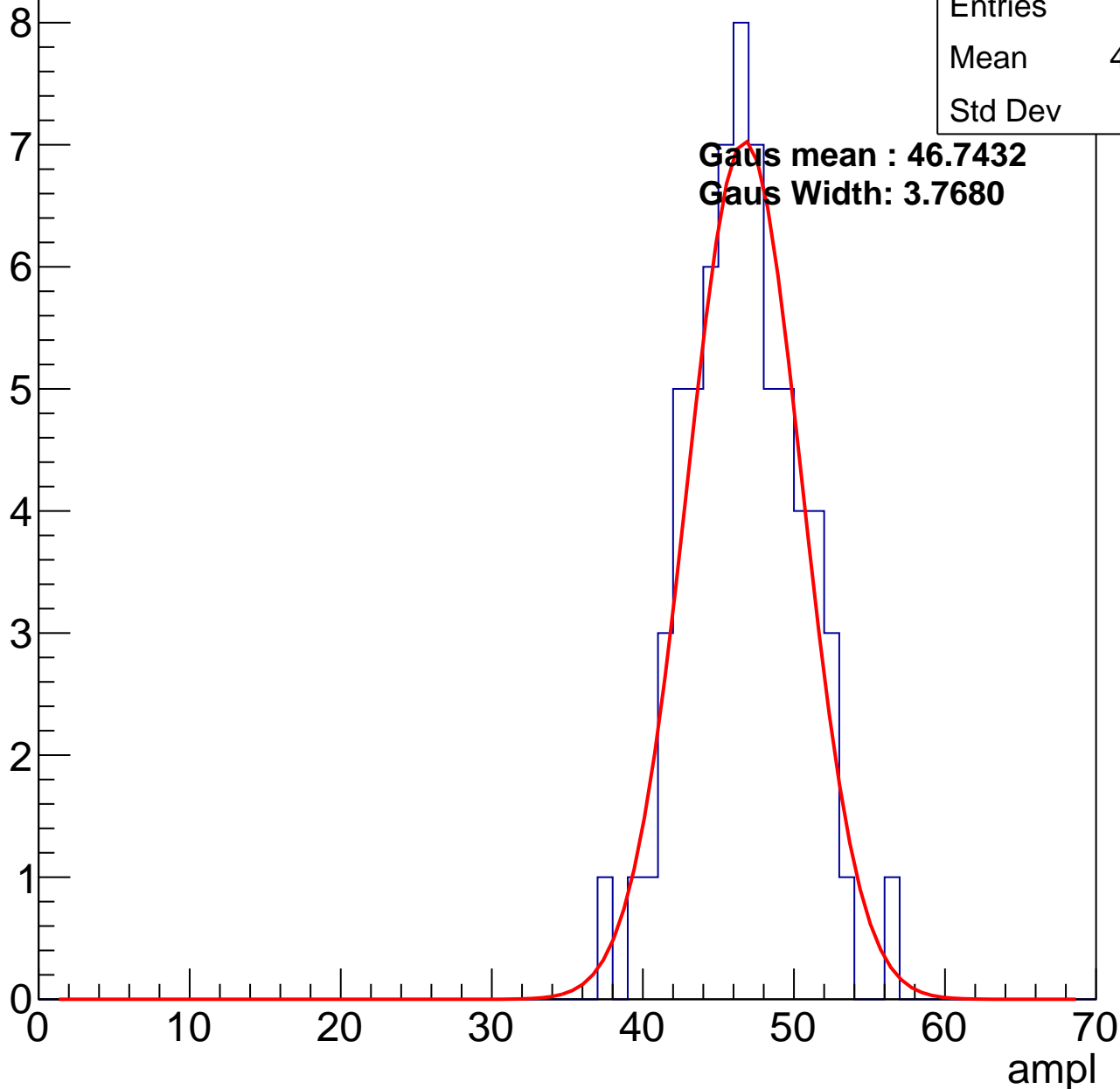
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	46.18
Std Dev	3.64

Gaus mean : 46.7432

Gaus Width: 3.7680



B1L101S, U26-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	52.57
Std Dev	3.187

Entry

10

8

6

4

2

0

0

10

20

30

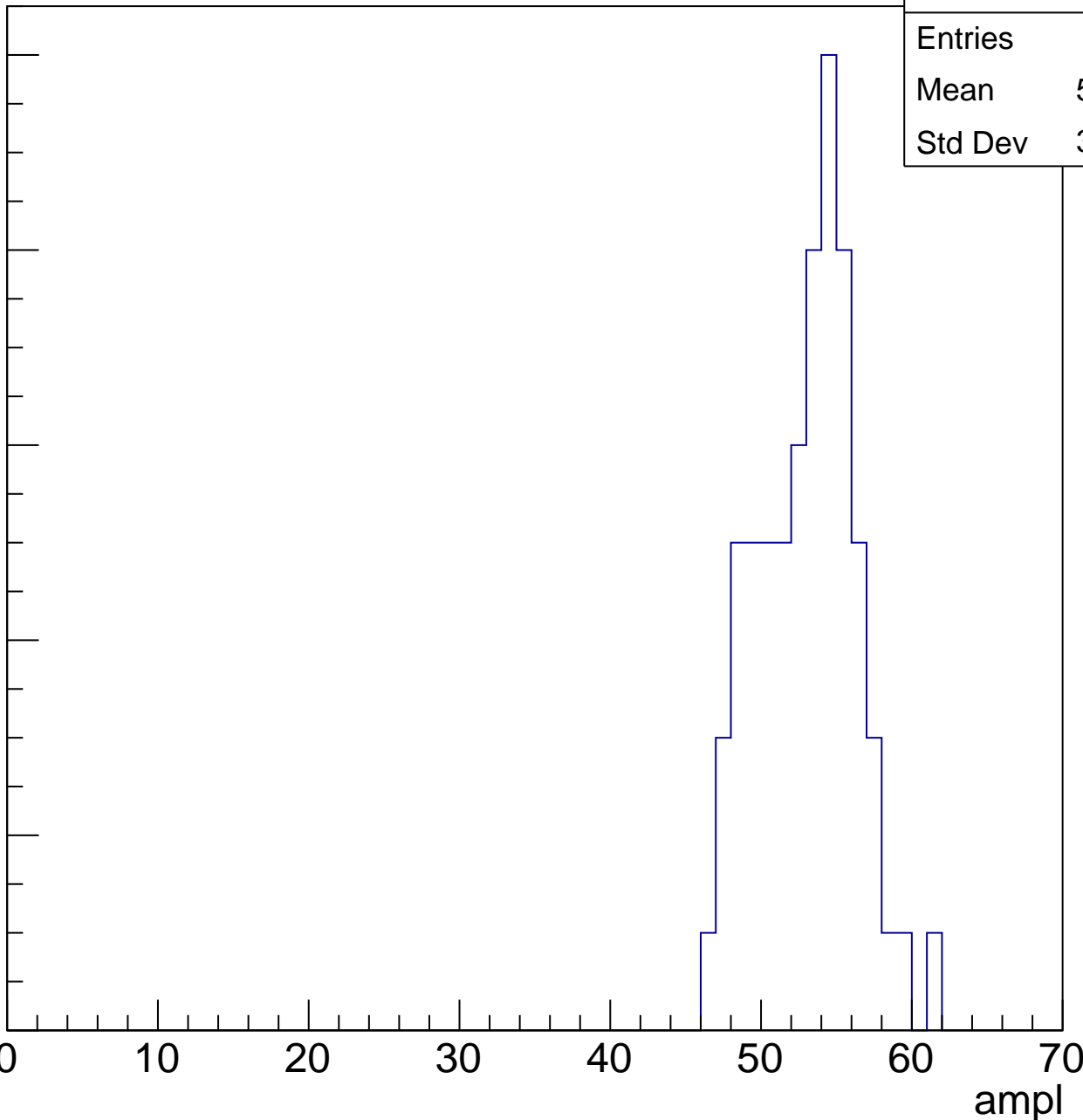
40

50

60

70

ampl

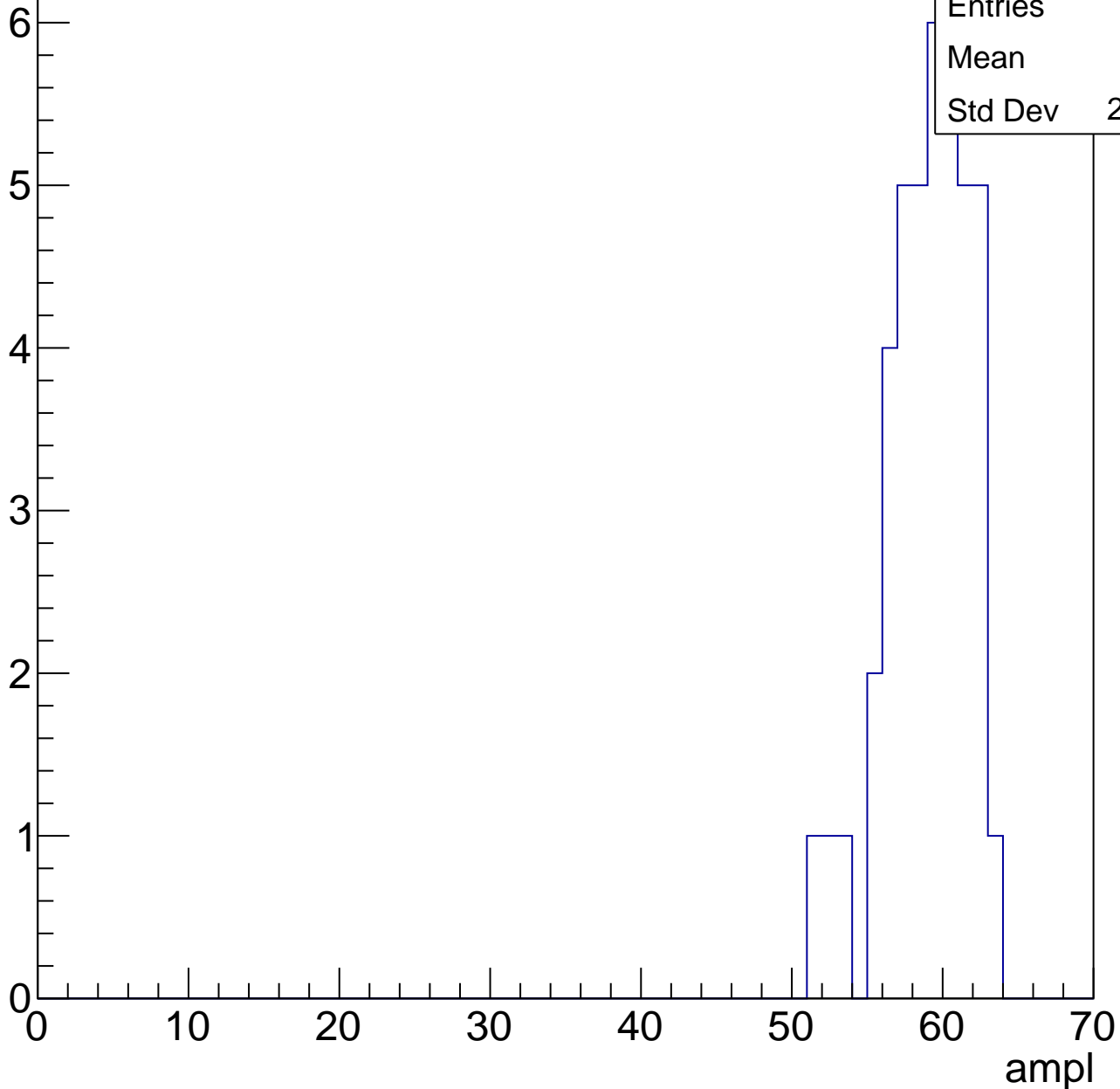


B1L101S, U26-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.5
Std Dev	2.754

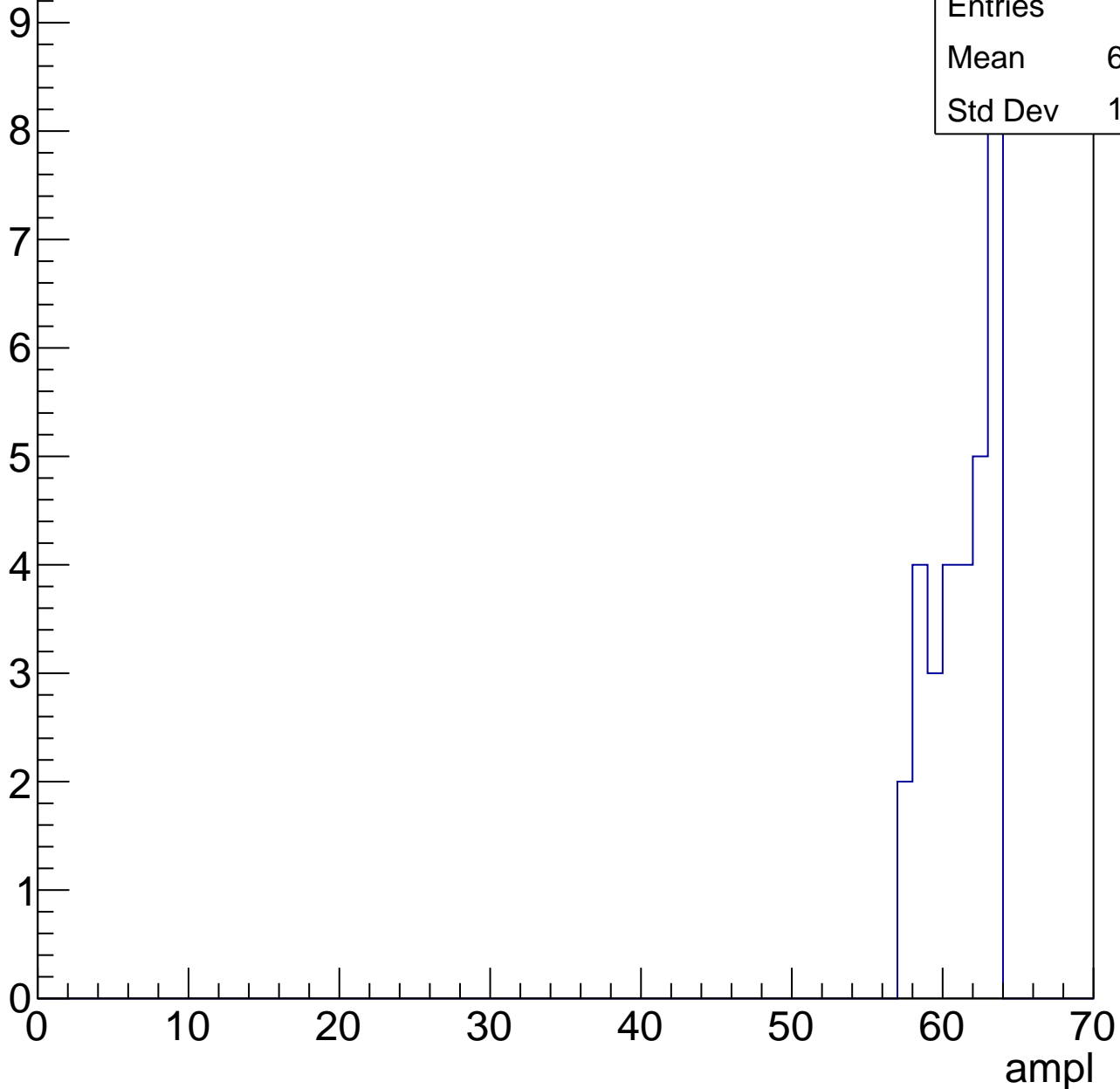


B1L101S, U26-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.77
Std Dev	1.995



B1L101S, U26-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch108, adc0

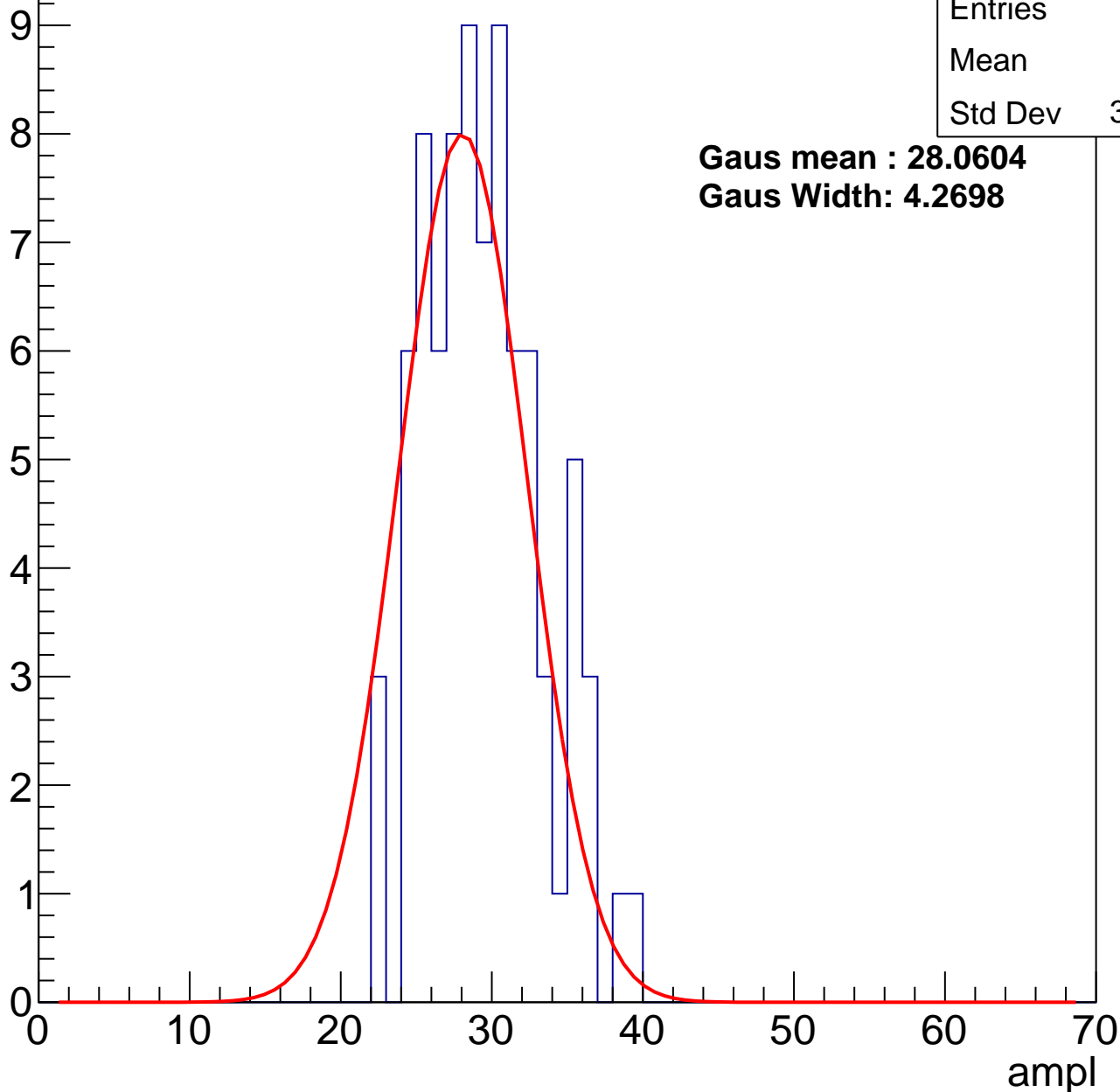
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	29
Std Dev	3.806

Gaus mean : 28.0604

Gaus Width: 4.2698



B1L101S, U26-ch108, adc1

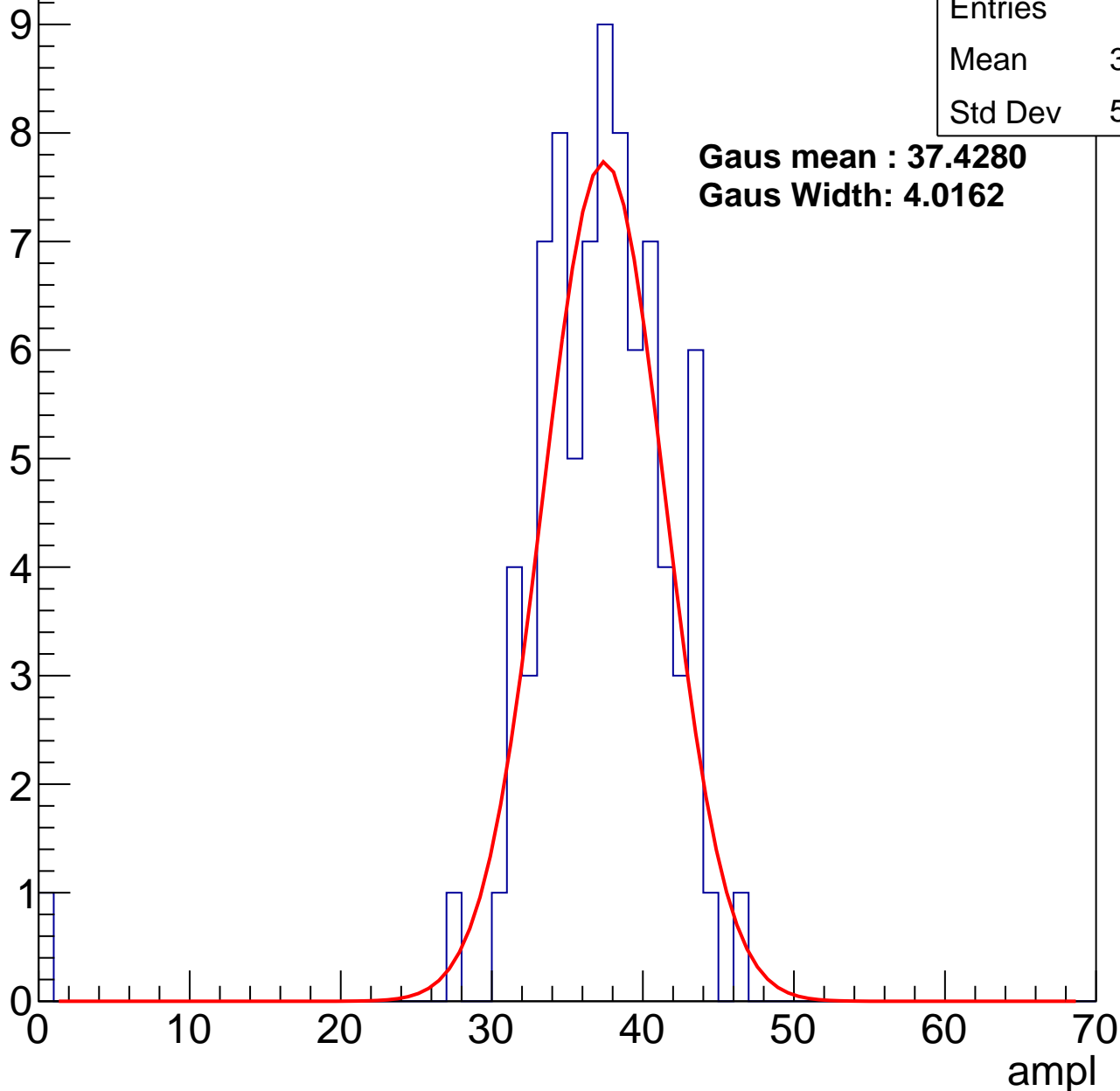
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.54
Std Dev	5.533

Gaus mean : 37.4280

Gaus Width: 4.0162



B1L101S, U26-ch108, adc2

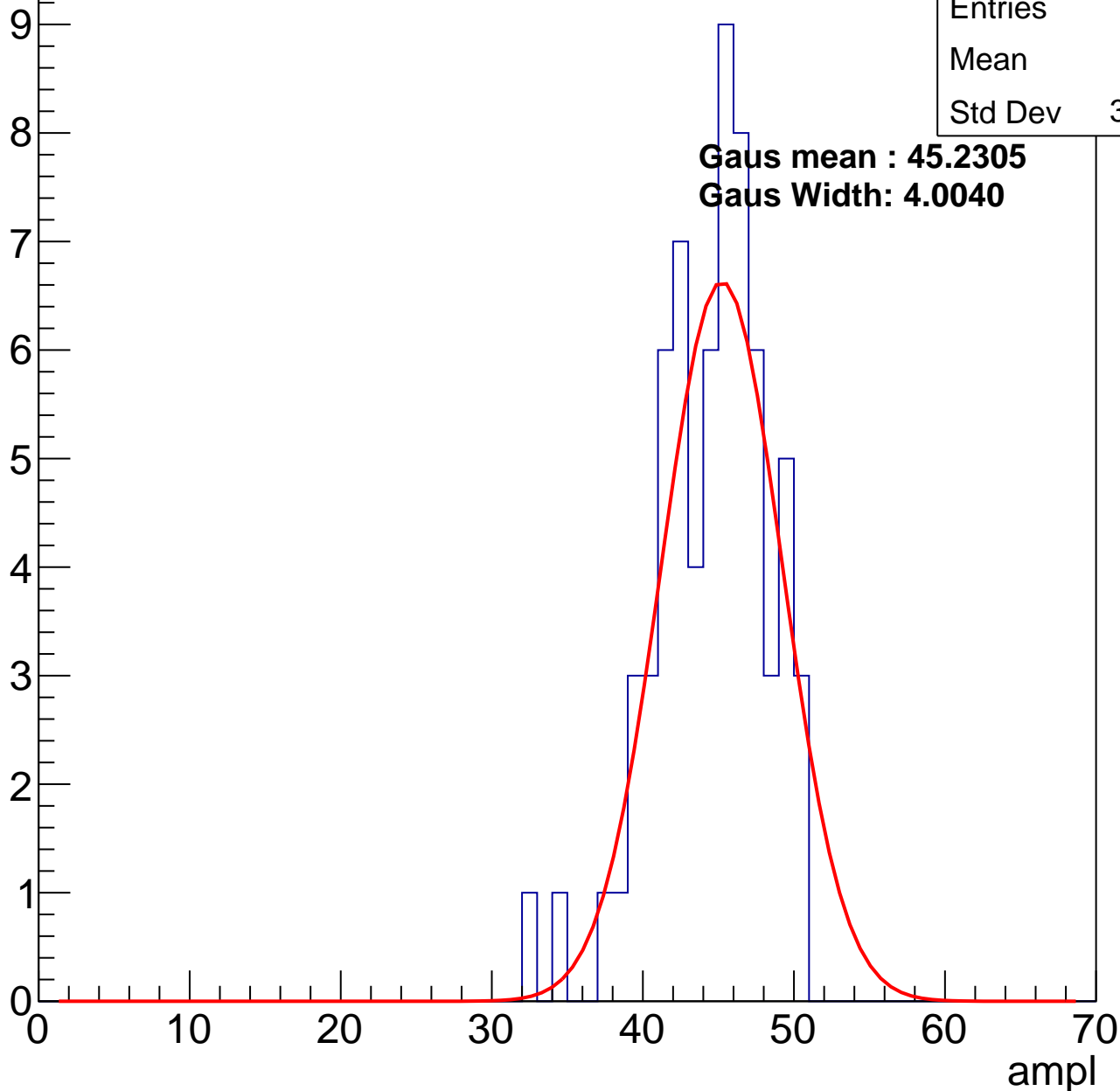
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44
Std Dev	3.693

Gaus mean : 45.2305

Gaus Width: 4.0040

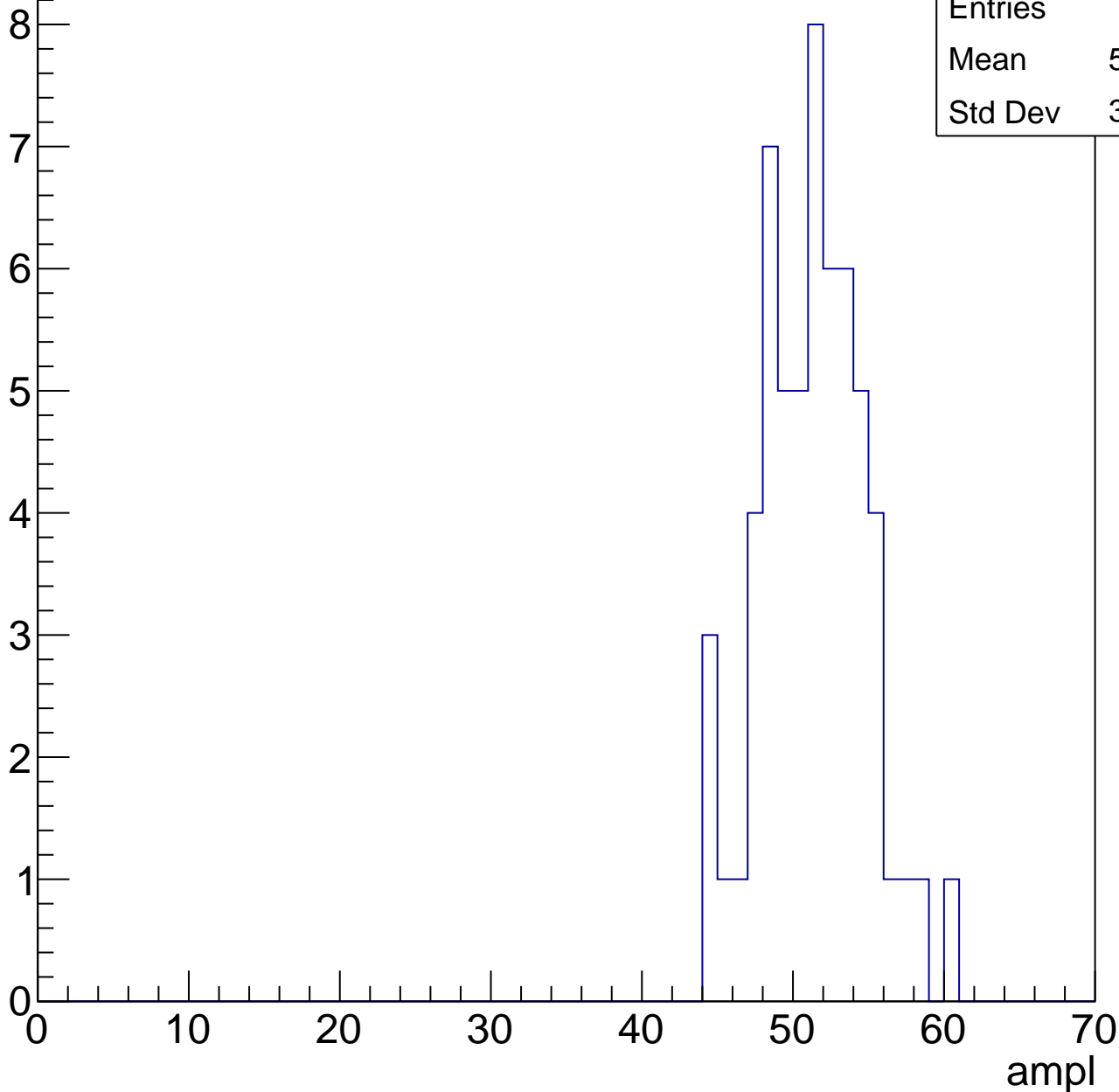


B1L101S, U26-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

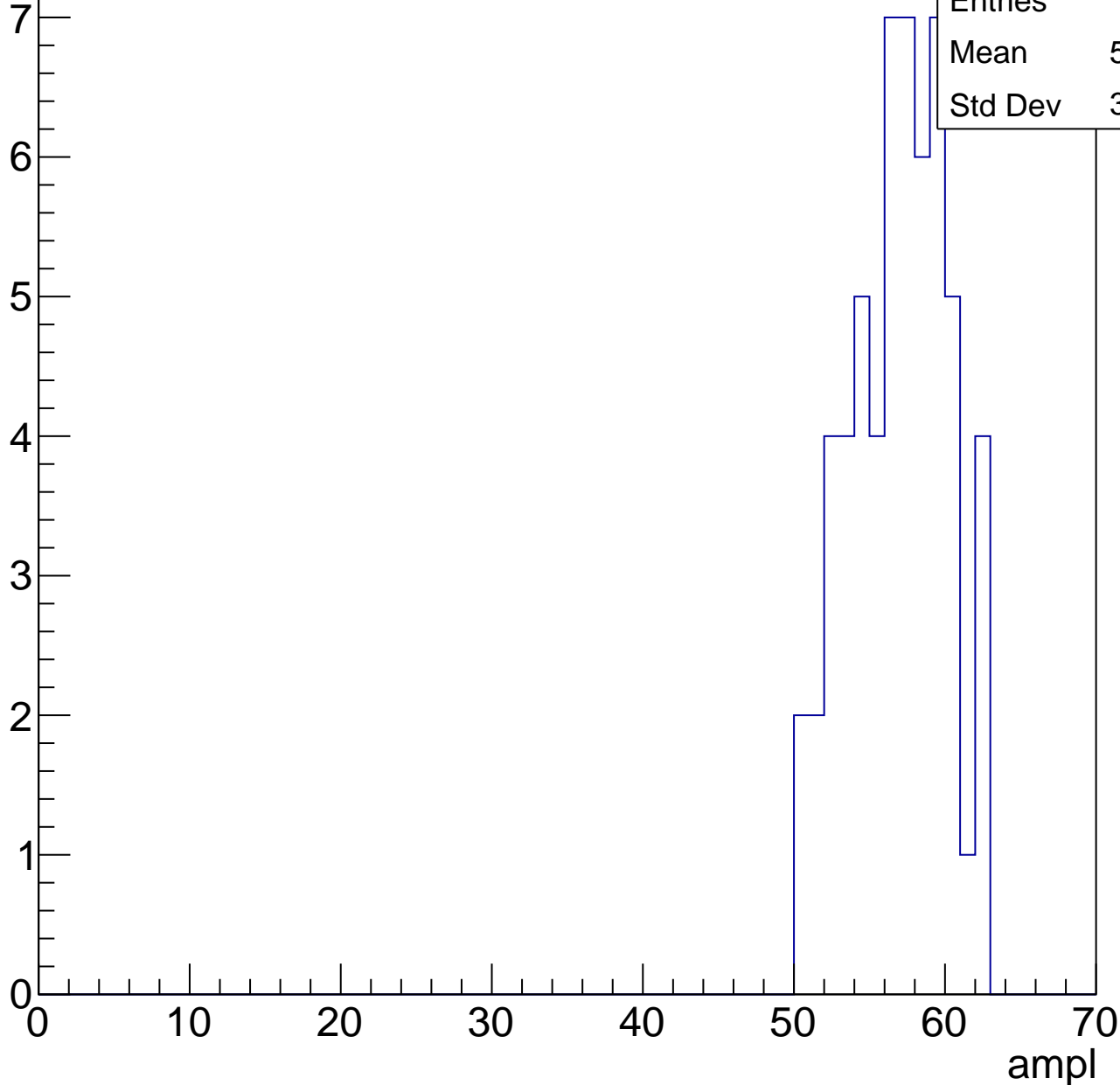
Entries	59
Mean	50.86
Std Dev	3.417



B1L101S, U26-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



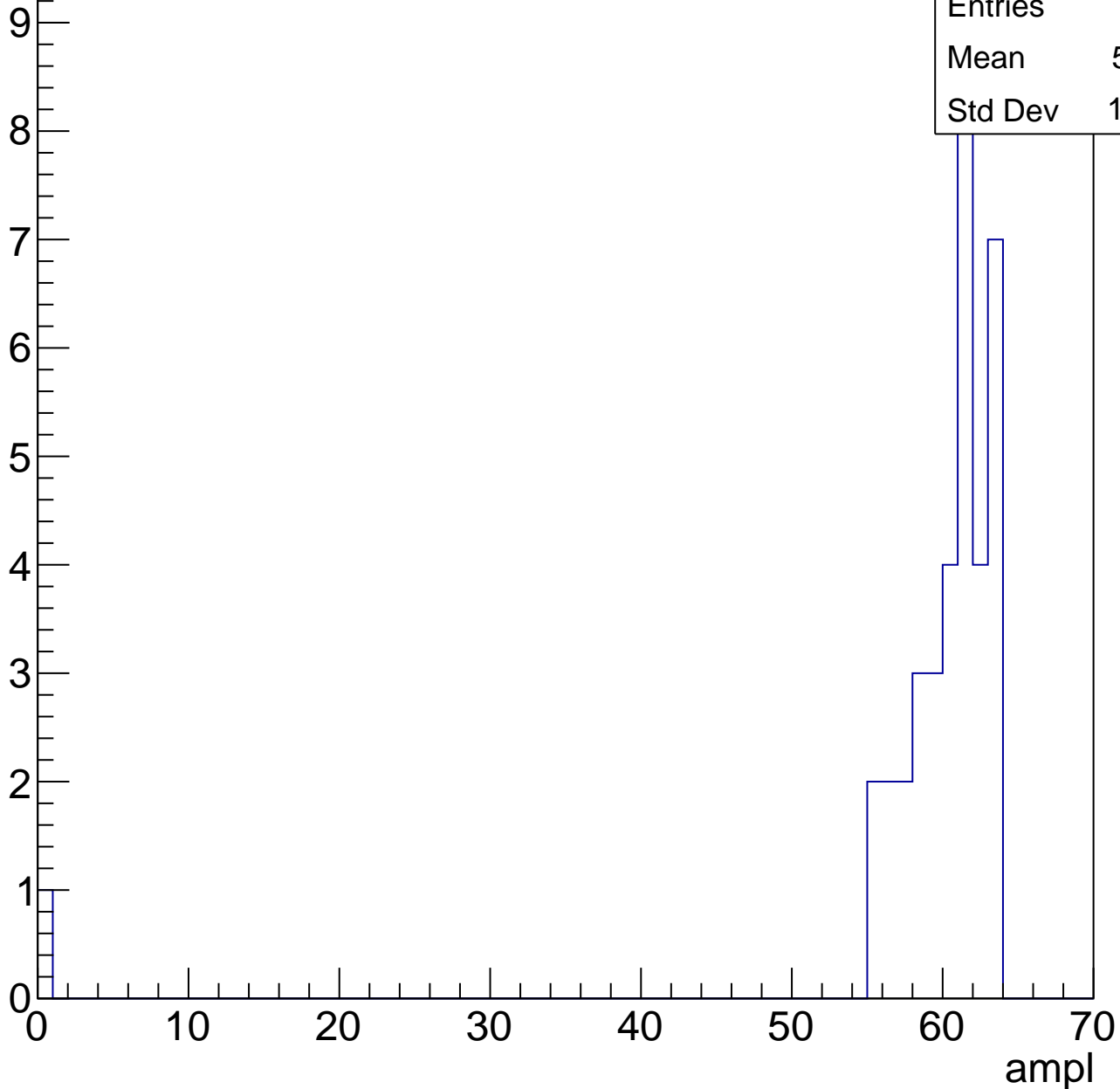
Entries	58
Mean	56.43
Std Dev	3.157

B1L101S, U26-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.51
Std Dev	10.03



B1L101S, U26-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

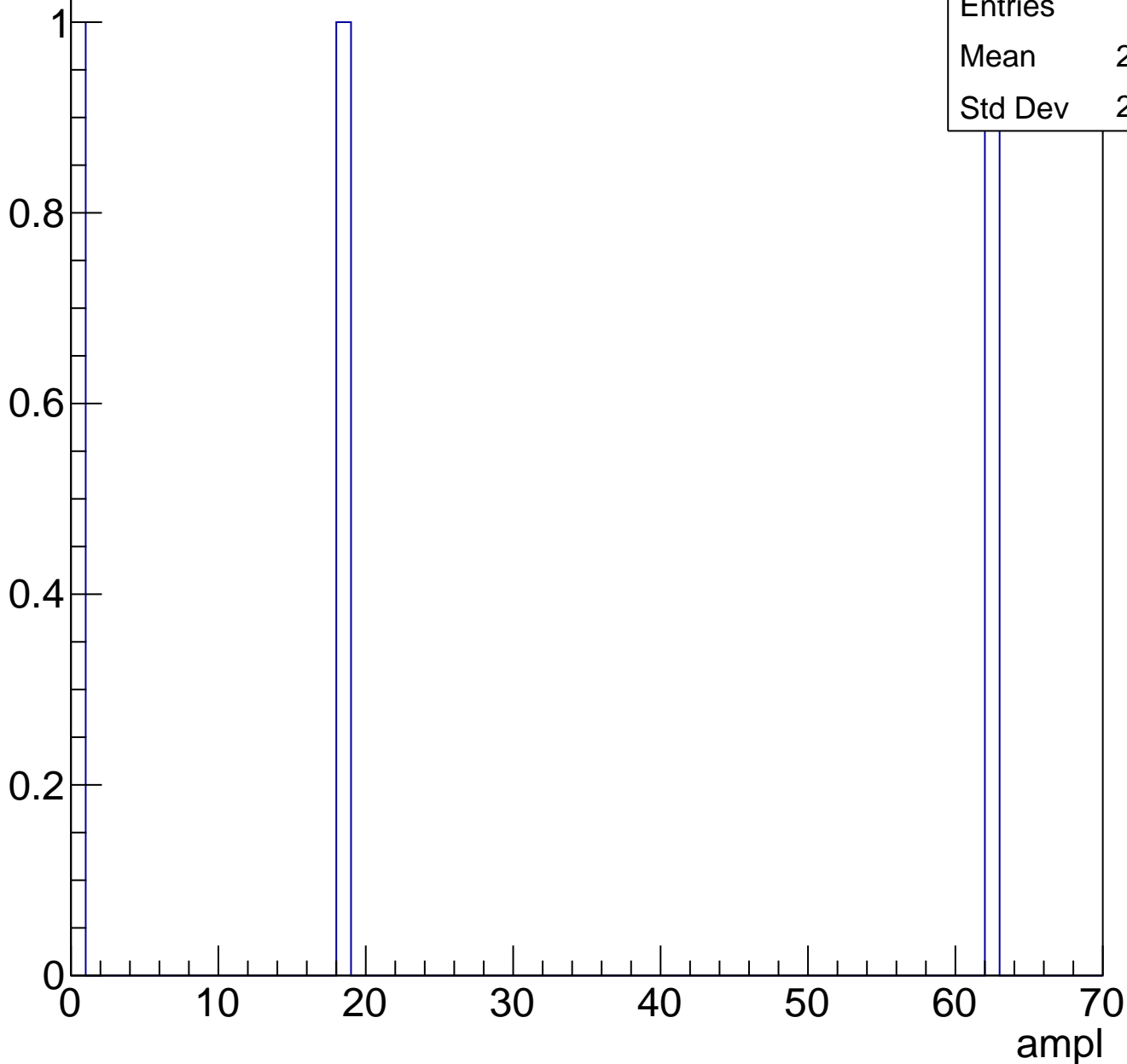
Entry



B1L101S, U26-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch109, adc0

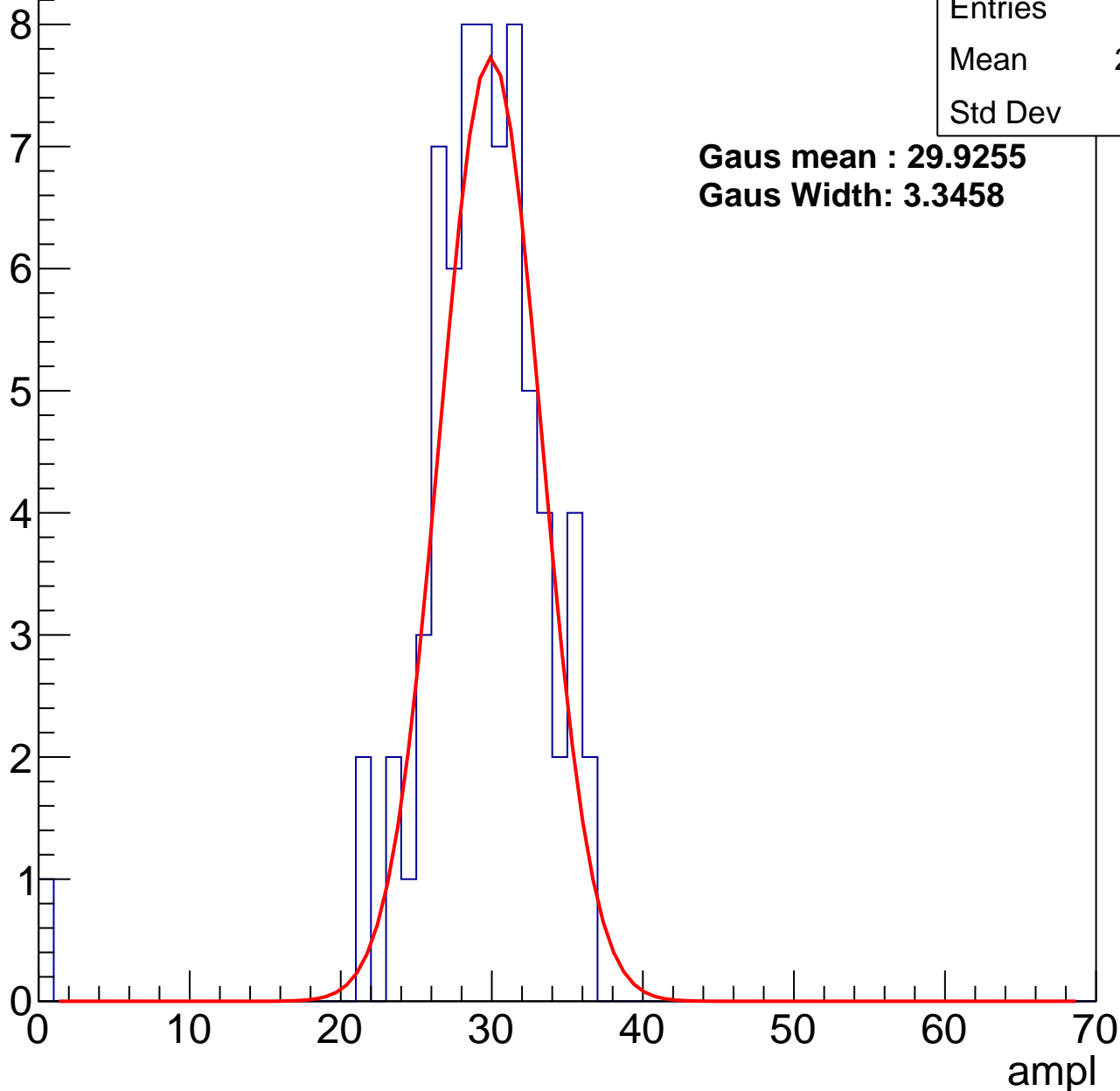
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.81
Std Dev	4.85

Gaus mean : 29.9255

Gaus Width: 3.3458



B1L101S, U26-ch109, adc1

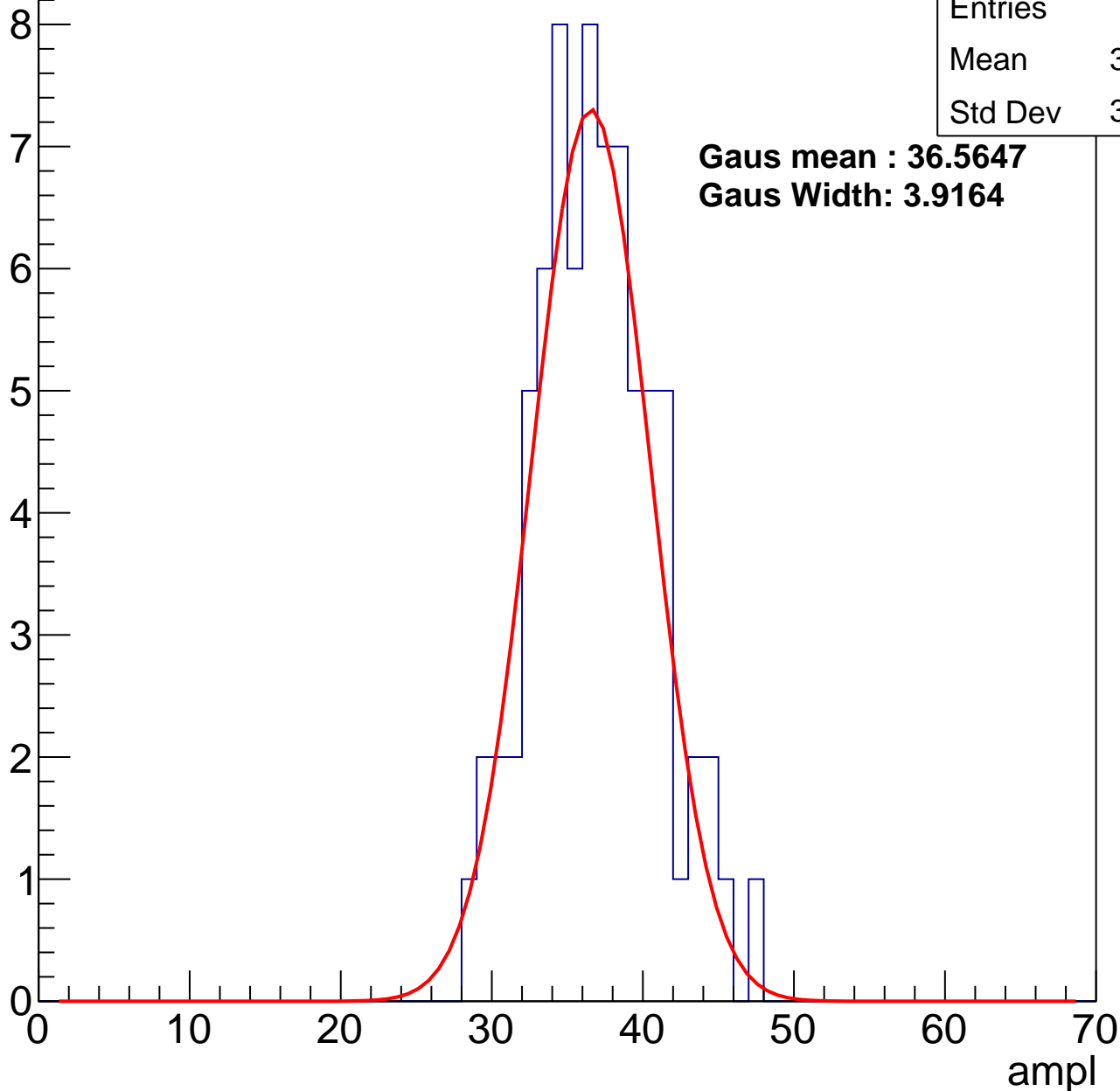
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.43
Std Dev	3.958

Gaus mean : 36.5647

Gaus Width: 3.9164



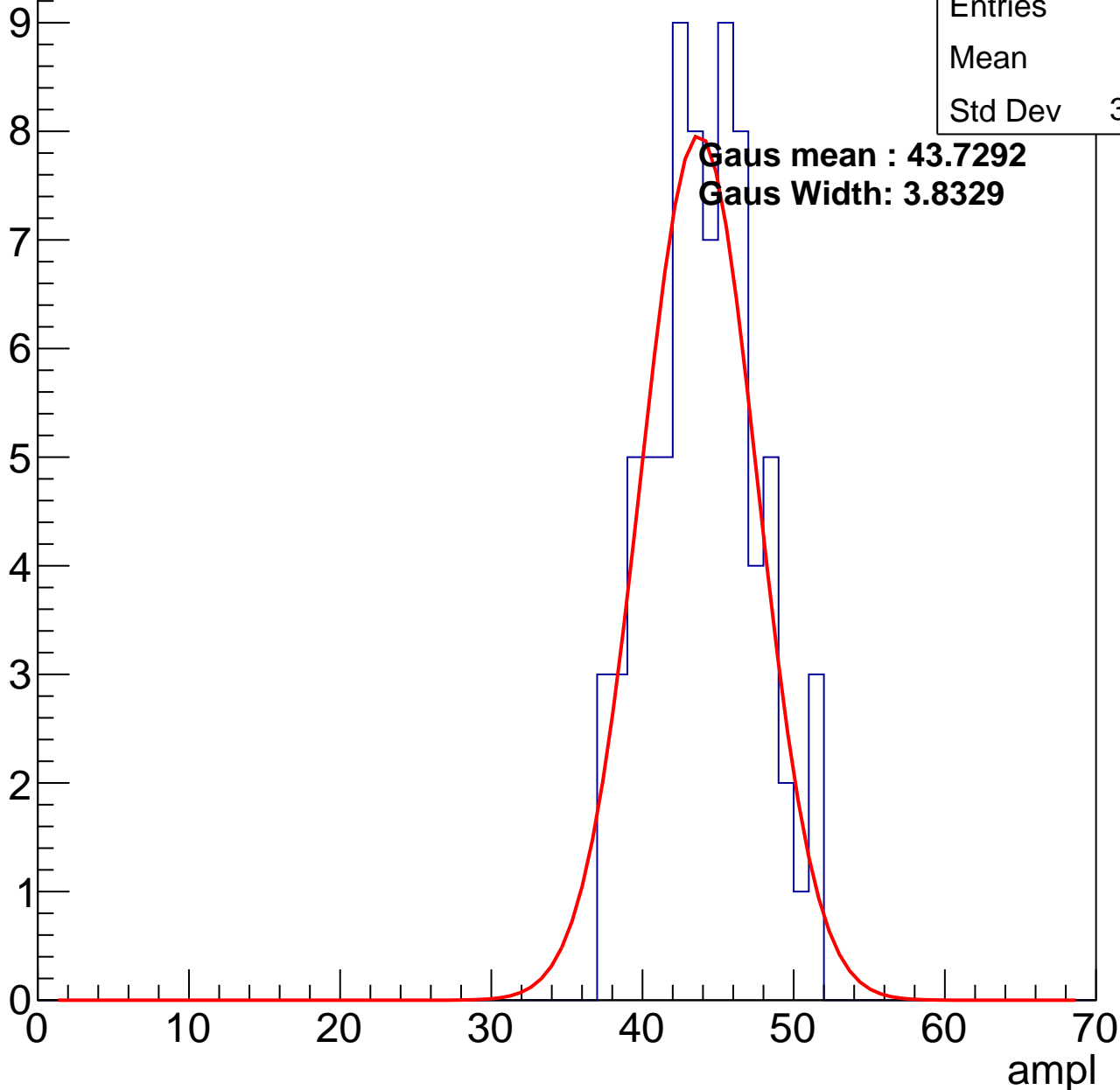
B1L101S, U26-ch109, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.6
Std Dev	3.469

Gaus mean : 43.7292
Gaus Width: 3.8329

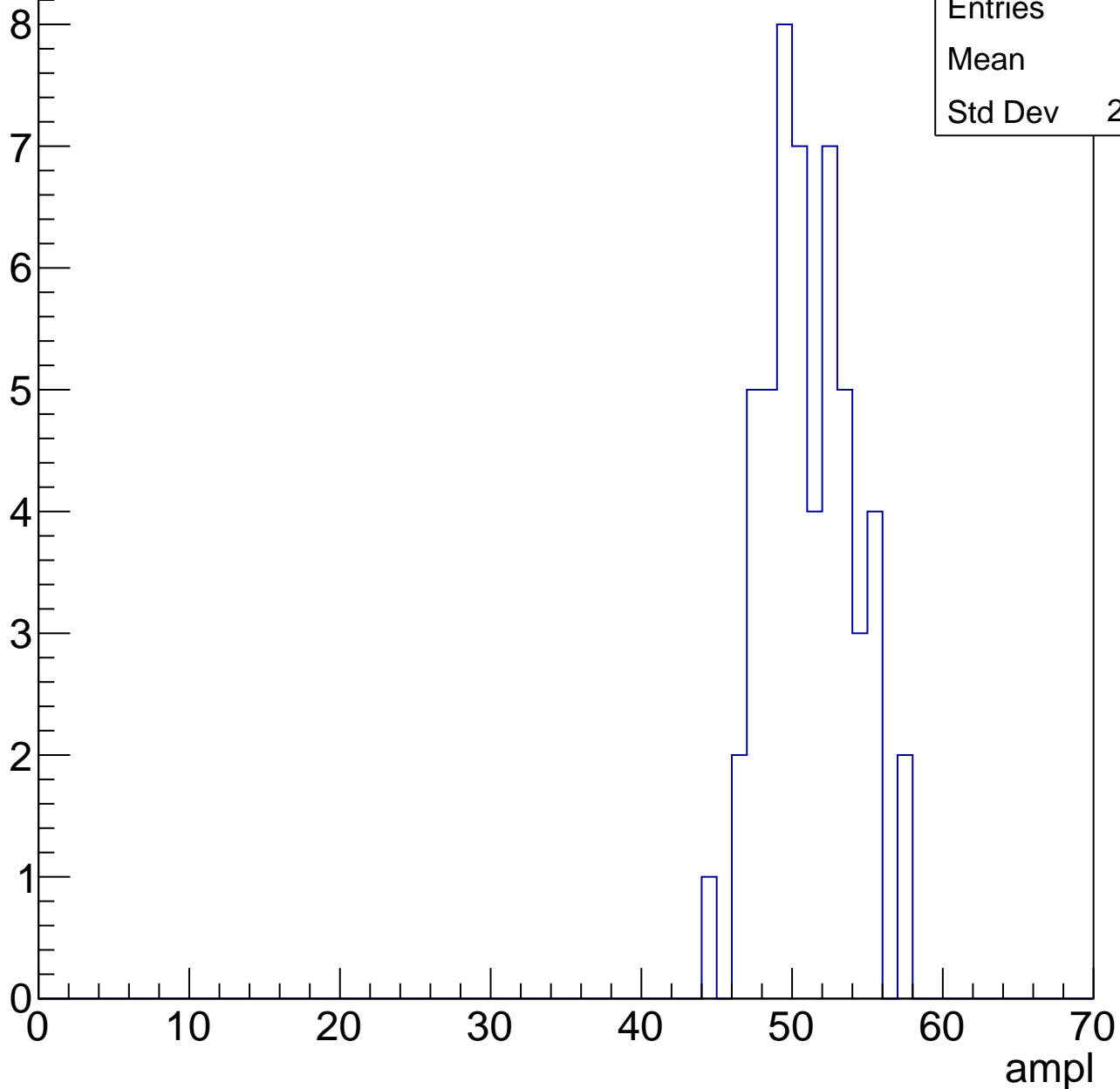


B1L101S, U26-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	50.6
Std Dev	2.897

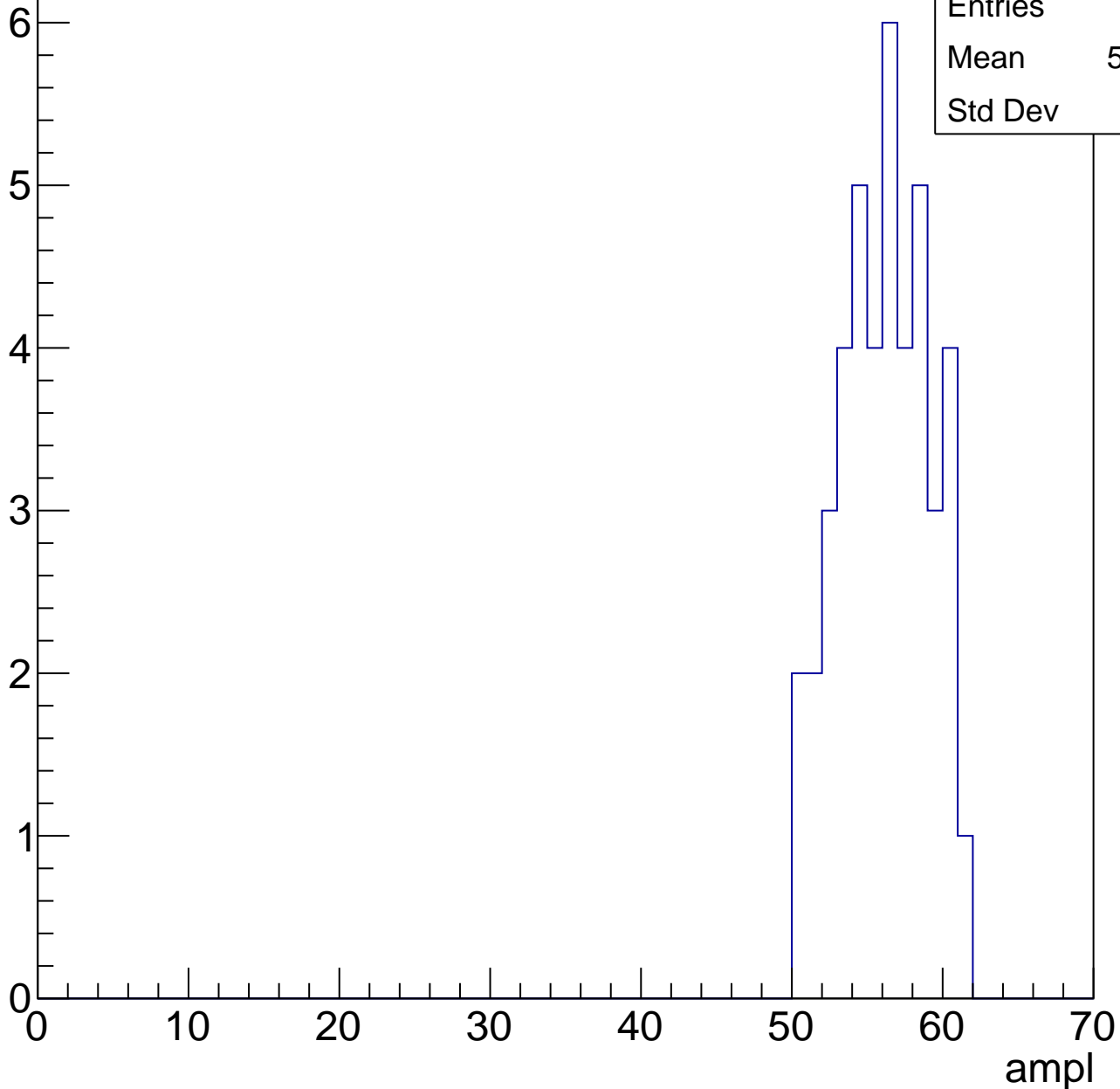


B1L101S, U26-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	55.63
Std Dev	2.91

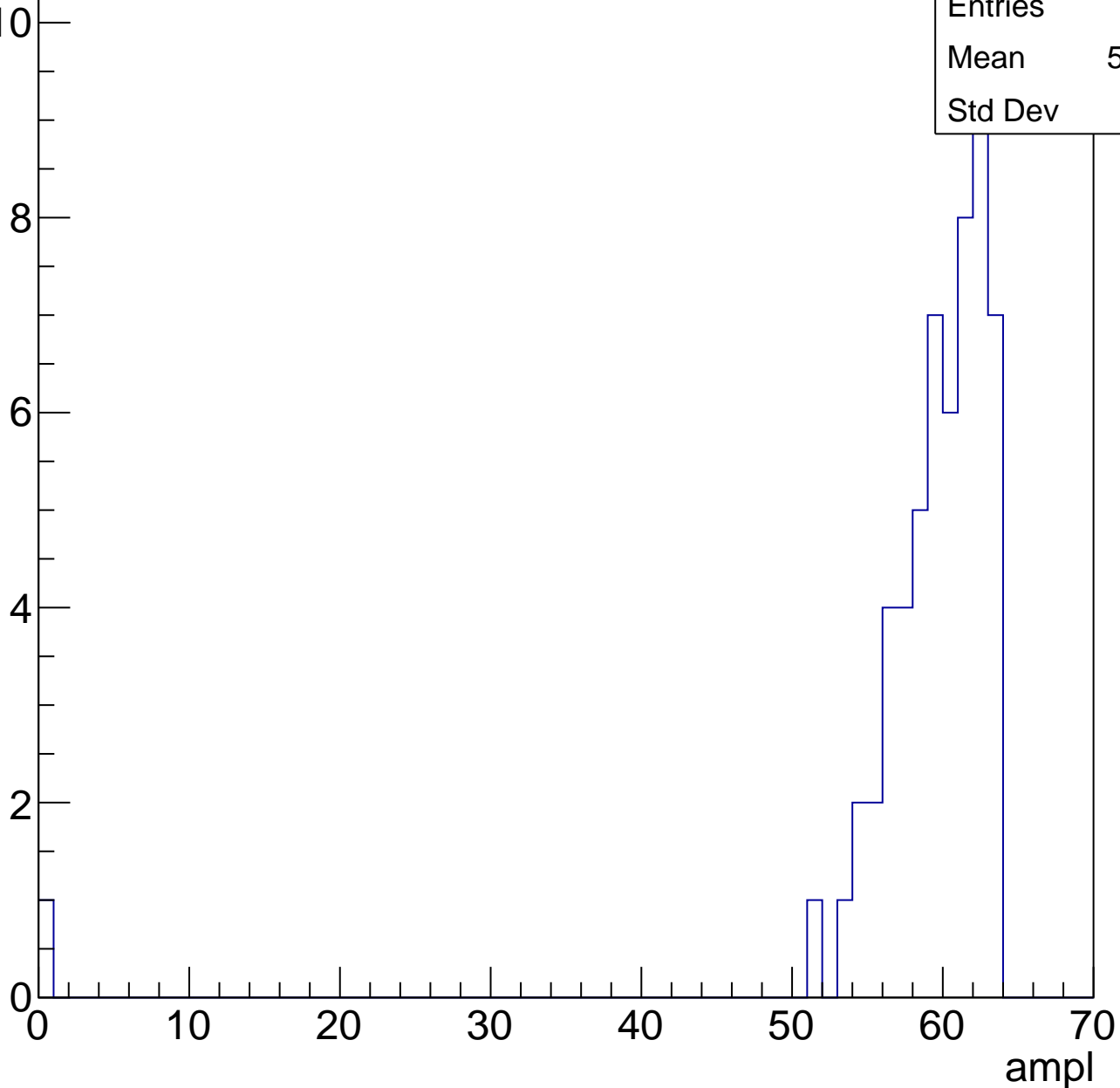


B1L101S, U26-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	58.38
Std Dev	8.24



B1L101S, U26-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch110, adc0

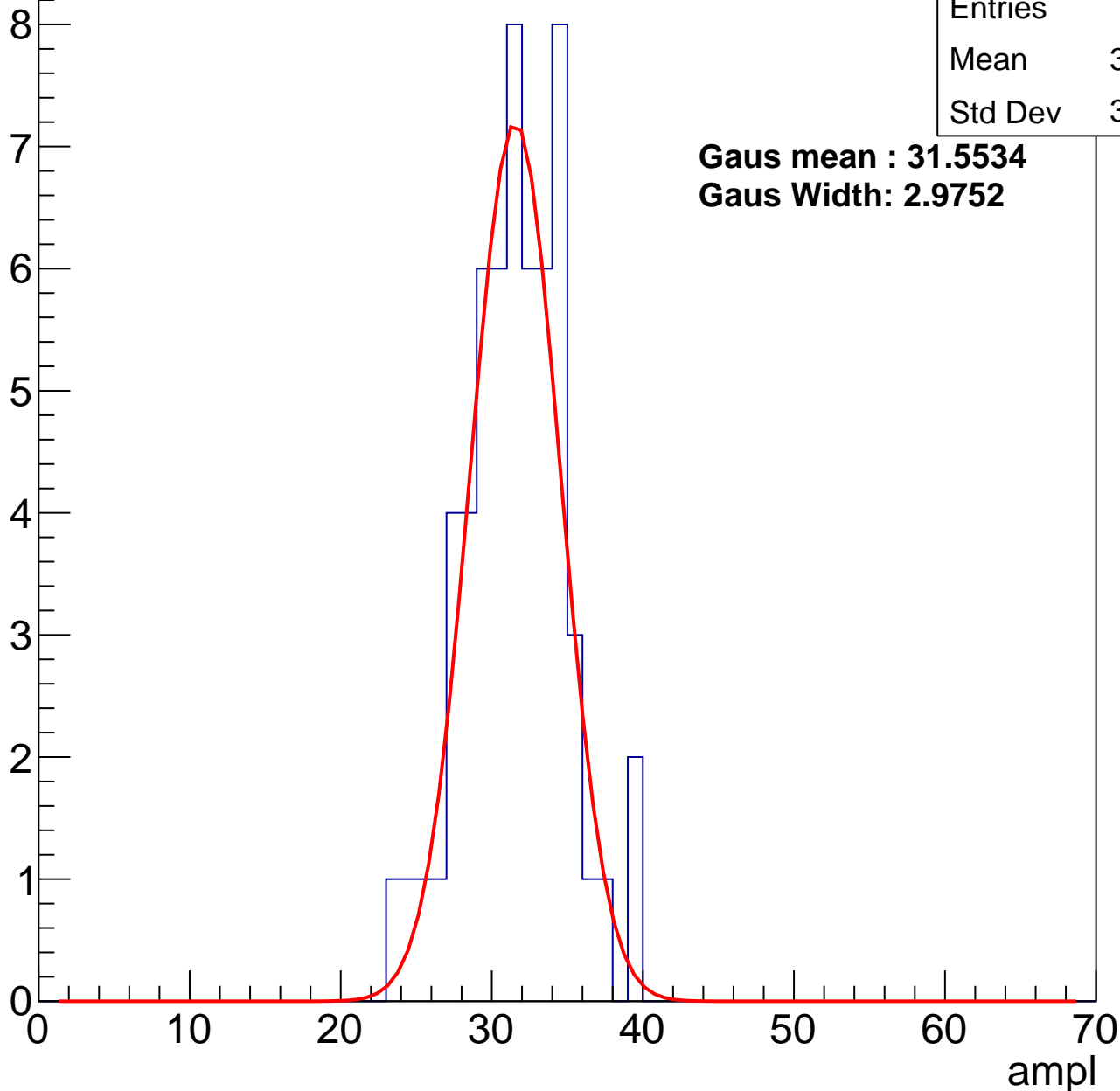
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	31.15
Std Dev	3.298

Gaus mean : 31.5534

Gaus Width: 2.9752



B1L101S, U26-ch110, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	38.27
Std Dev	3.666

Gaus mean : 39.3812

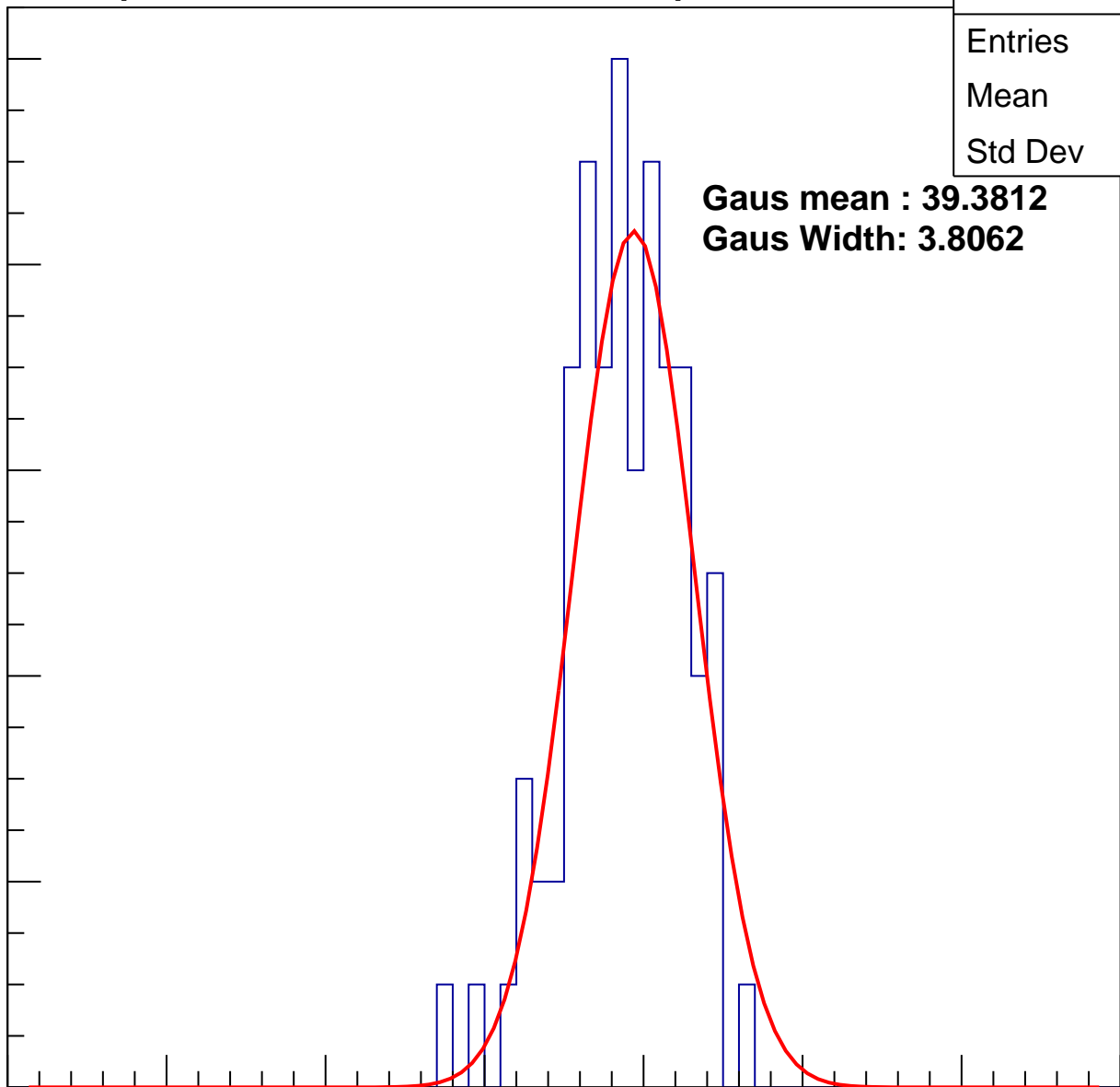
Gaus Width: 3.8062

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch110, adc2

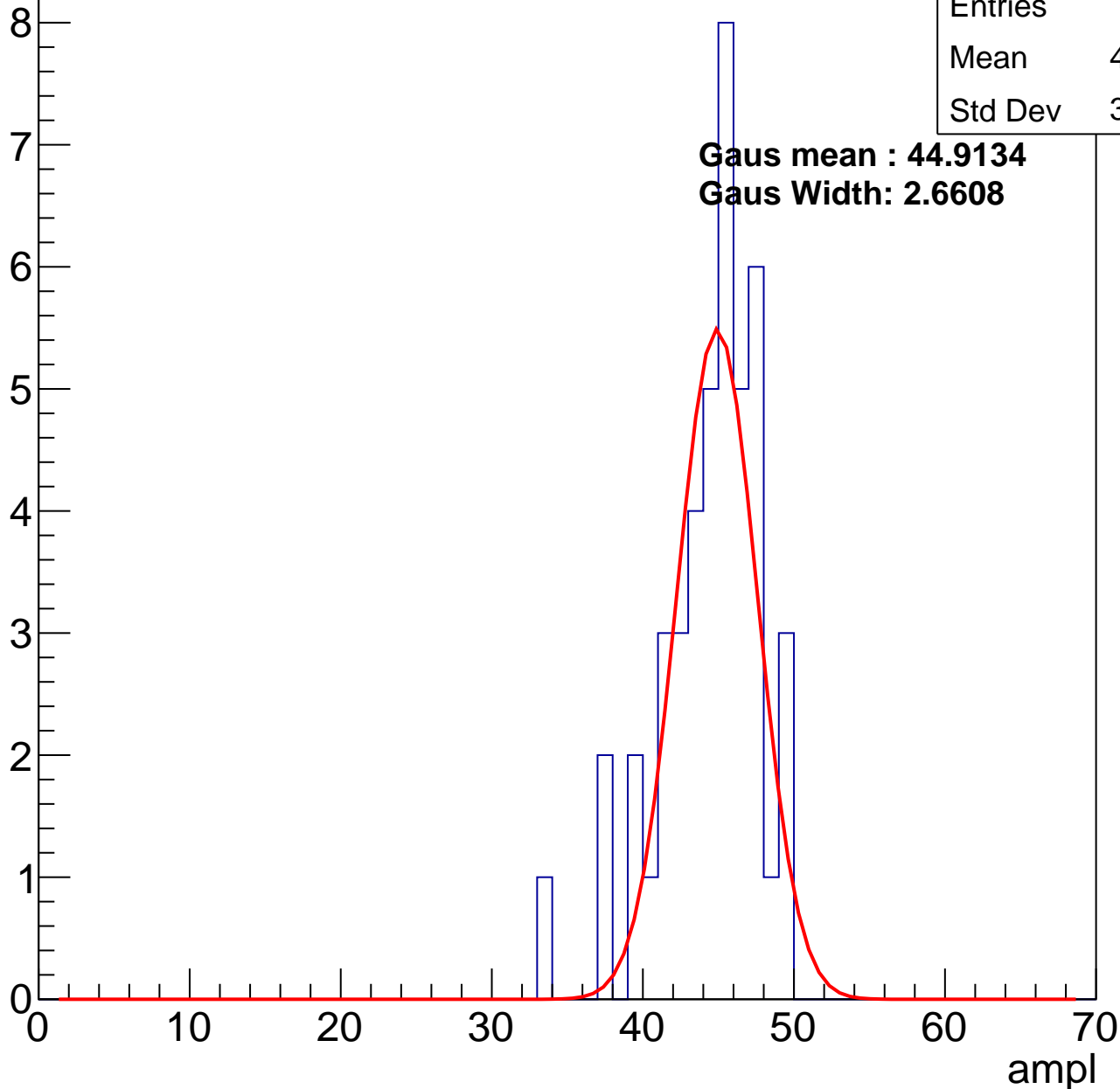
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	43.93
Std Dev	3.374

Gaus mean : 44.9134

Gaus Width: 2.6608

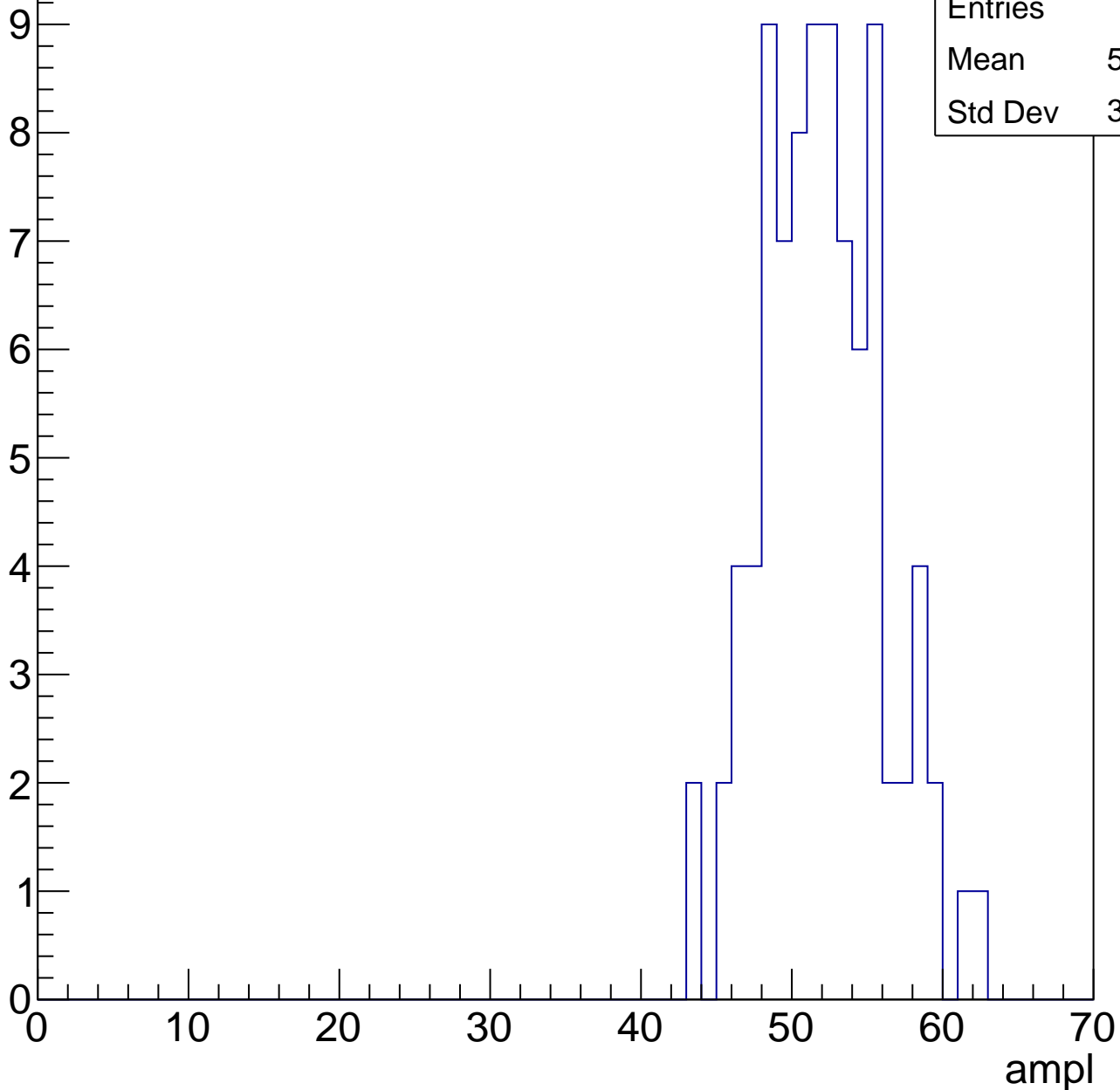


B1L101S, U26-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	51.58
Std Dev	3.922

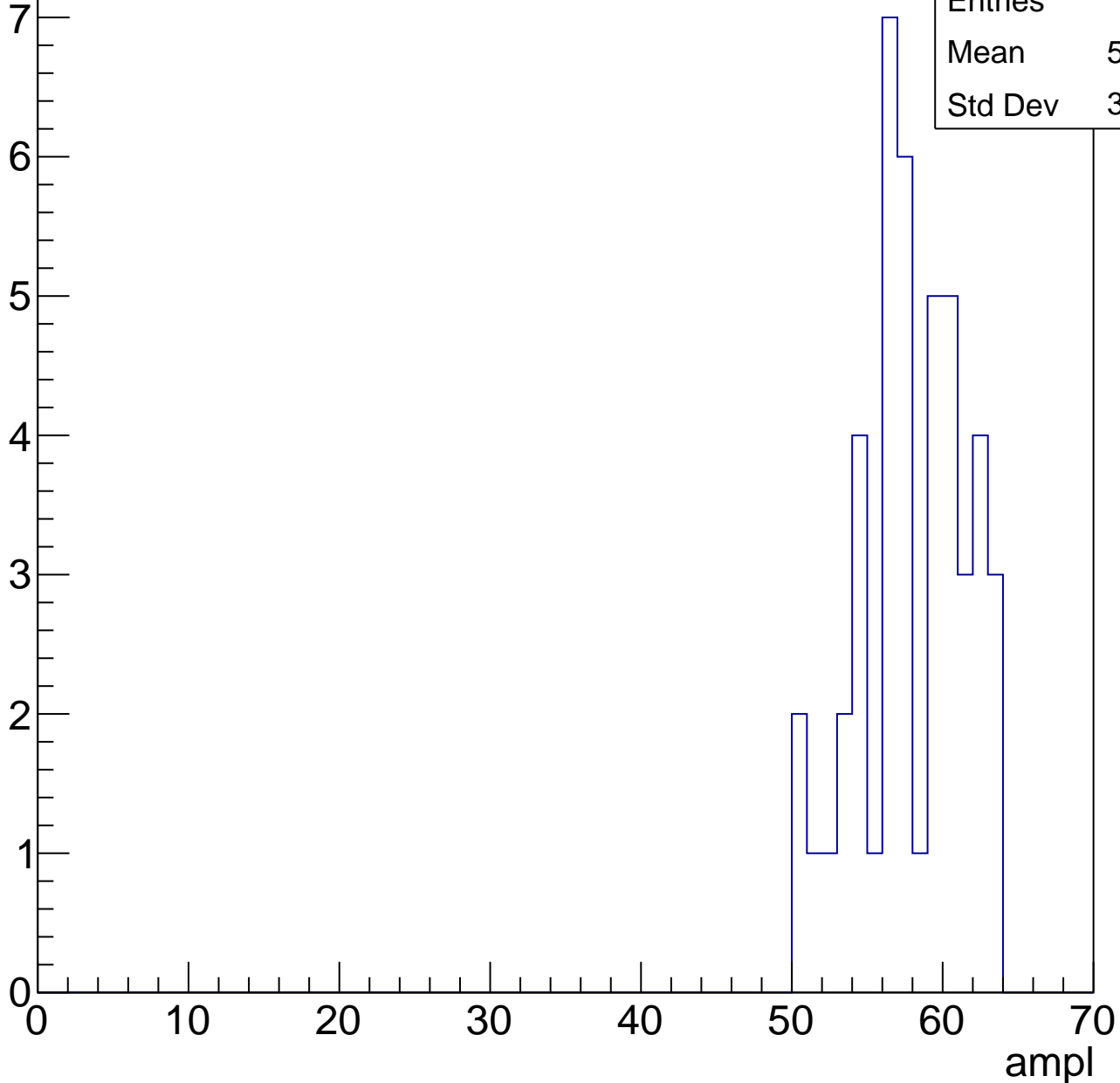


B1L101S, U26-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.49
Std Dev	3.494

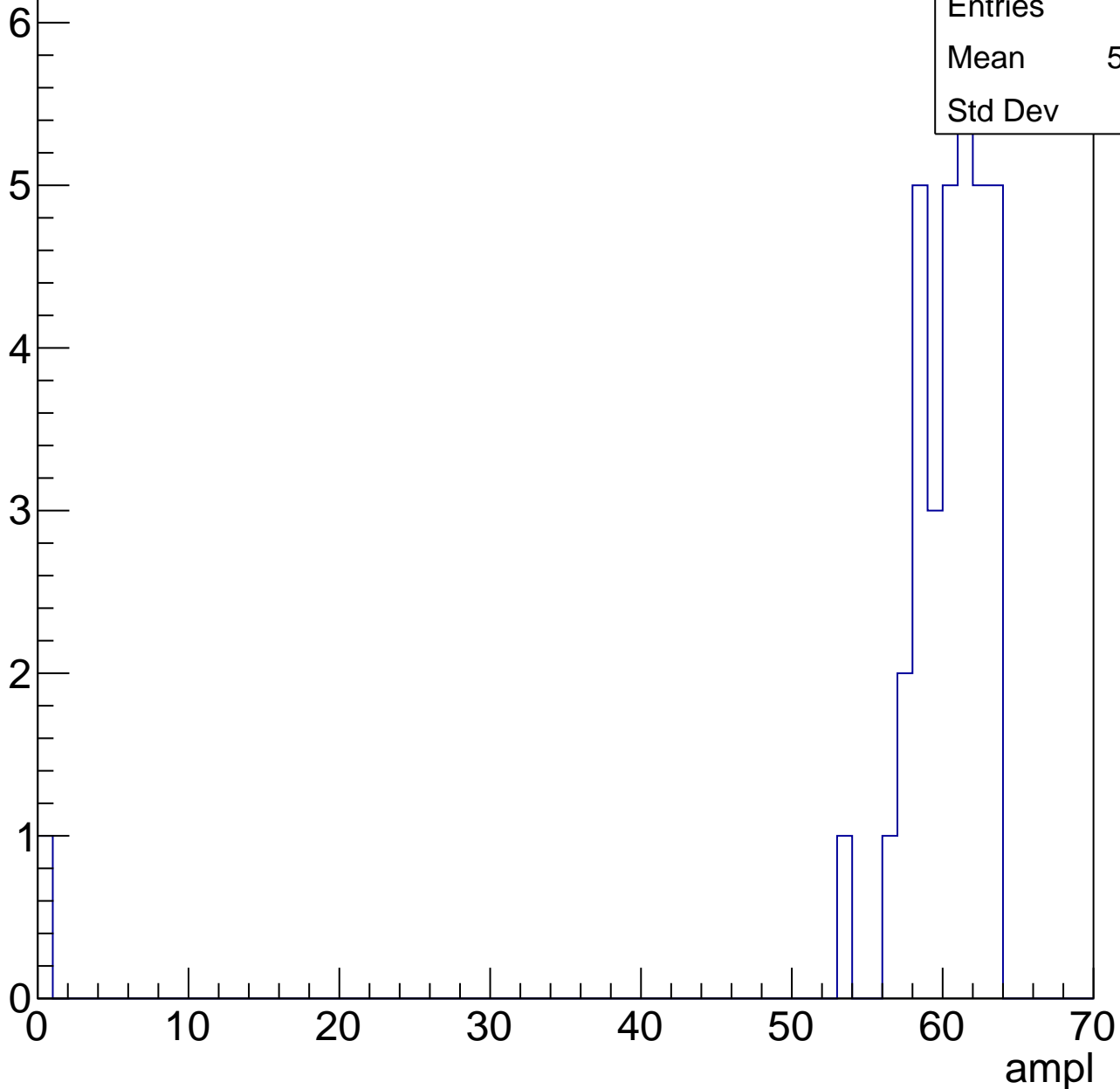


B1L101S, U26-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.26
Std Dev	10.4



B1L101S, U26-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L101S, U26-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch111, adc0

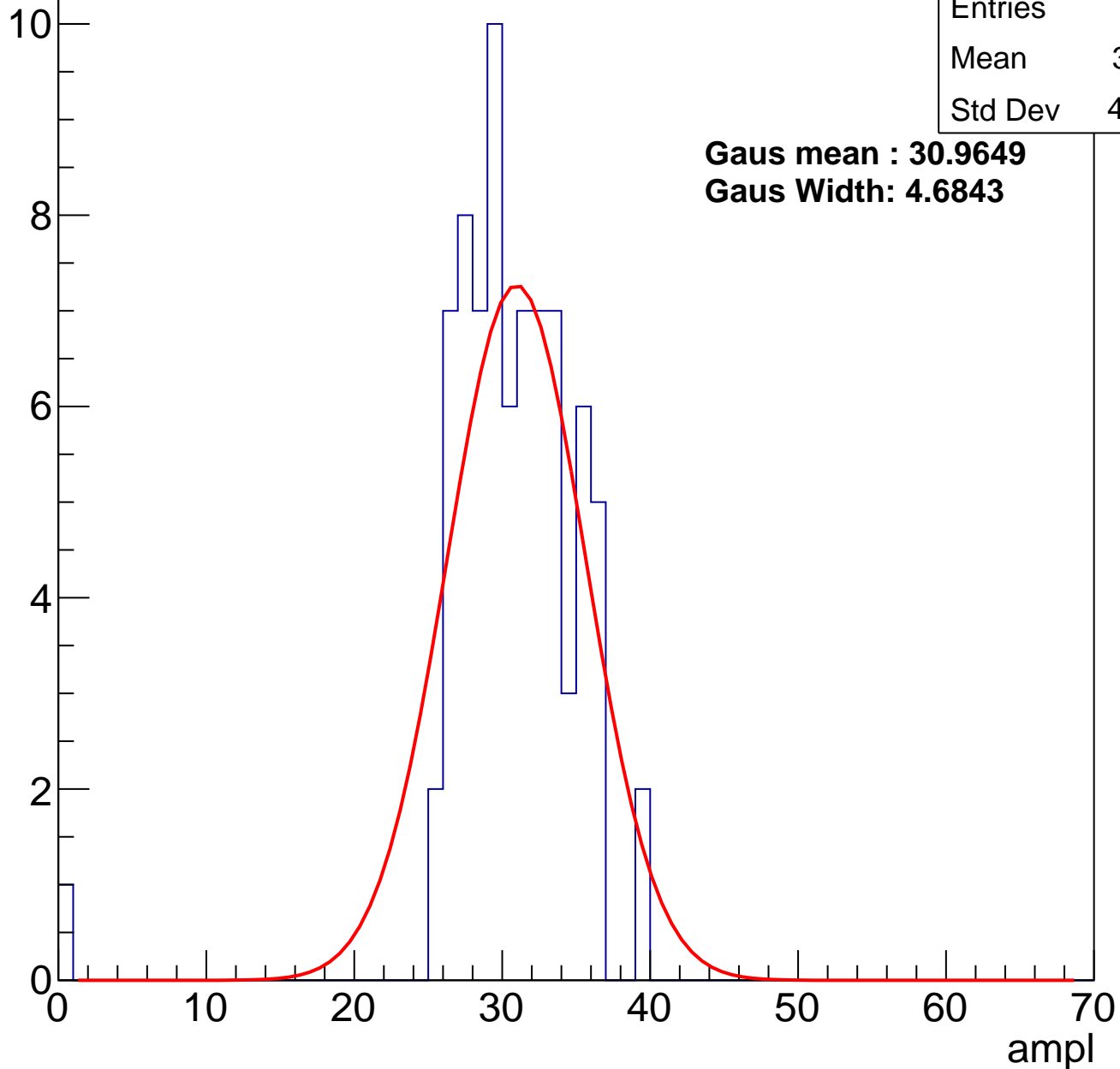
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.21
Std Dev	4.813

Gaus mean : 30.9649

Gaus Width: 4.6843

Entry



B1L101S, U26-ch111, adc1

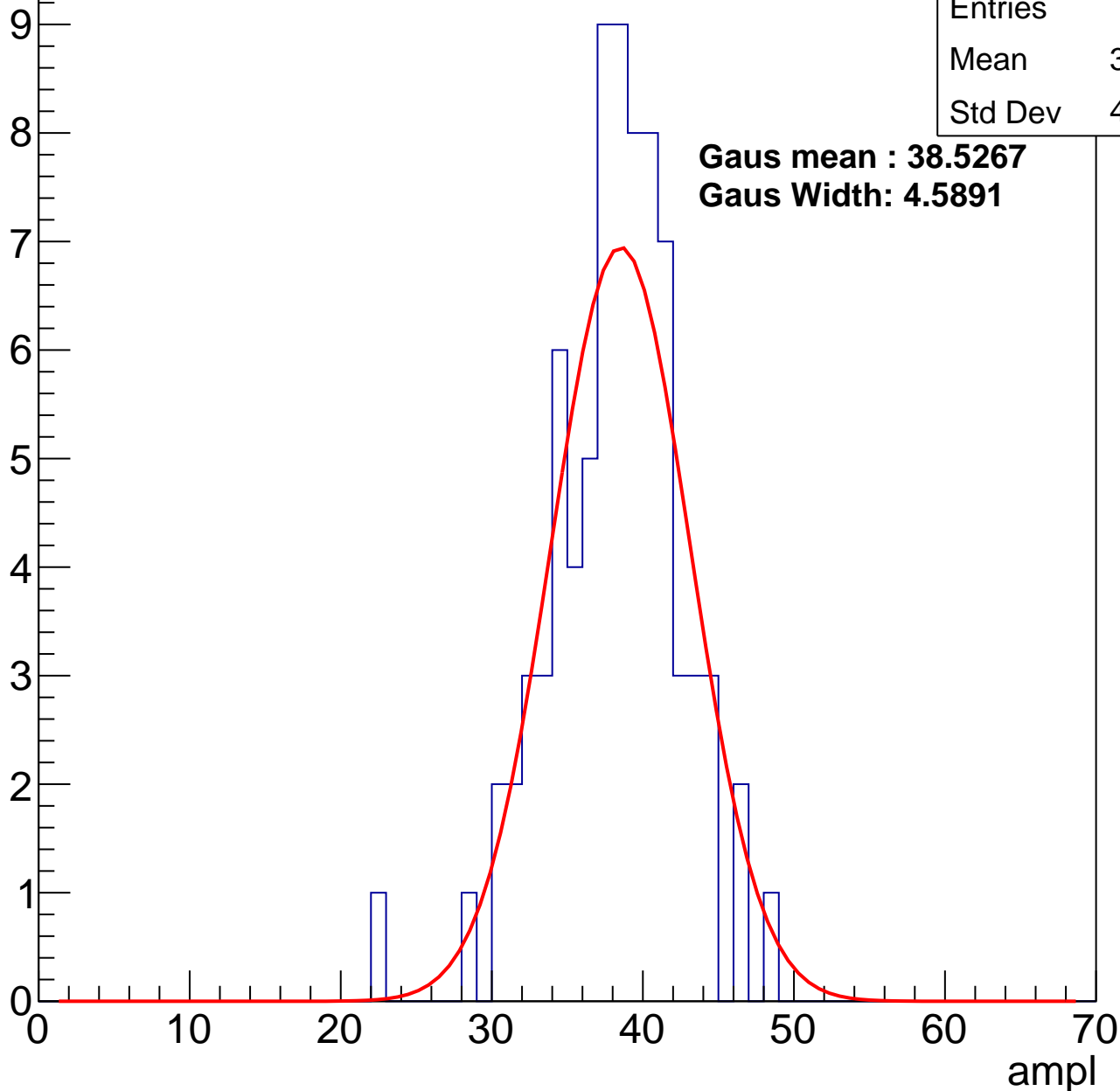
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	37.65
Std Dev	4.299

Gaus mean : 38.5267

Gaus Width: 4.5891



B1L101S, U26-ch111, adc2

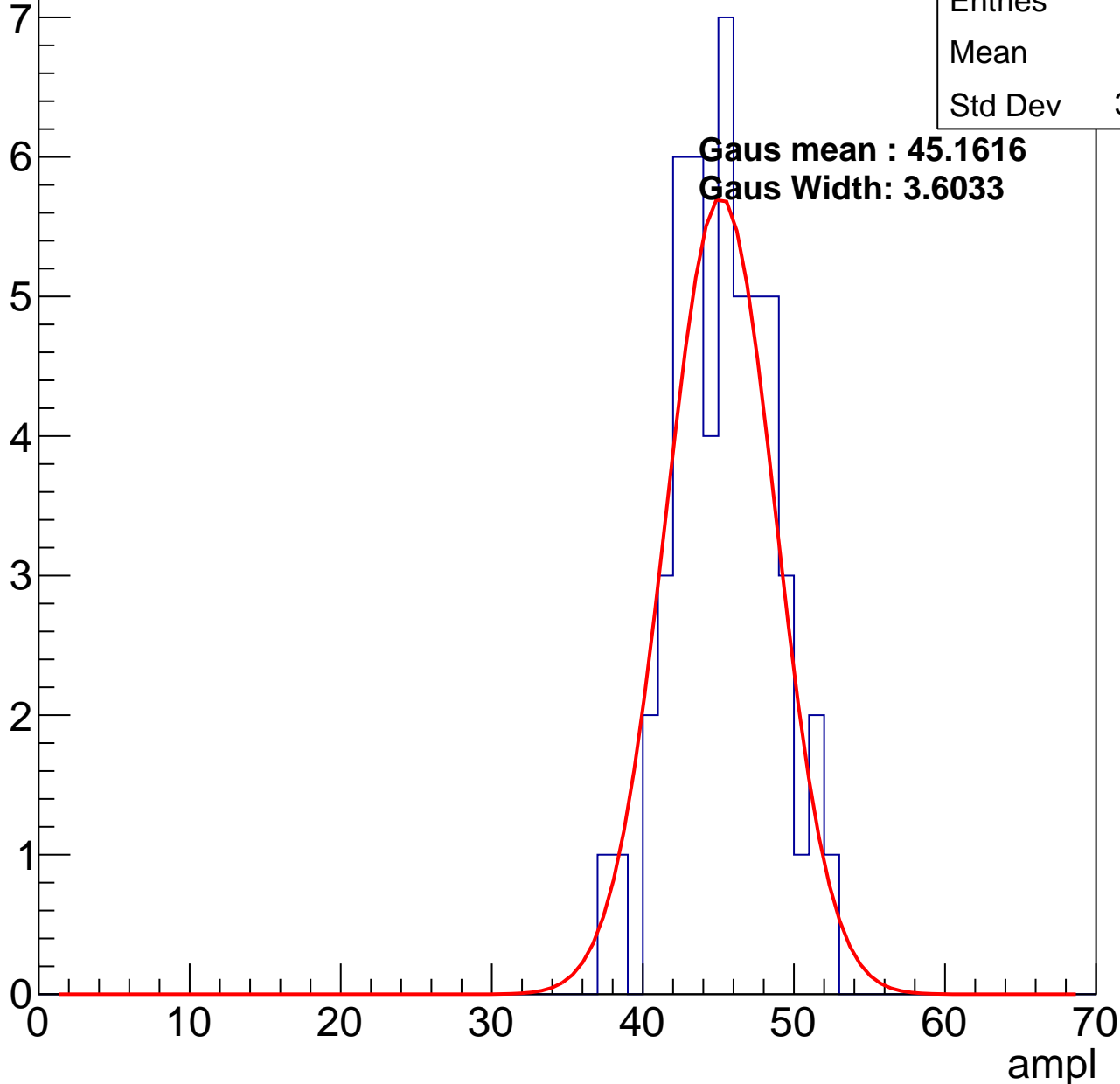
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.9
Std Dev	3.271

Gaus mean : 45.1616

Gaus Width: 3.6033

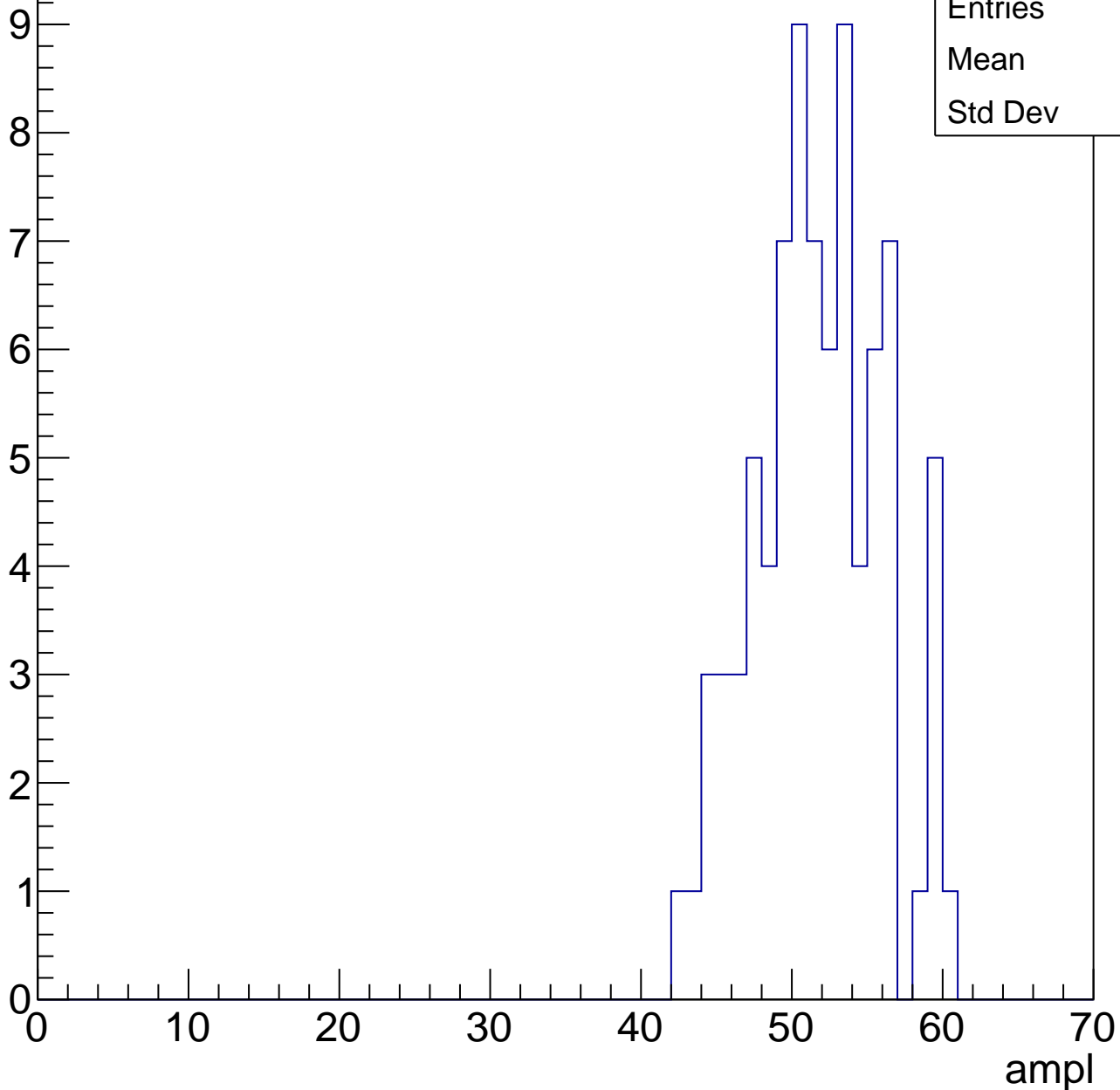


B1L101S, U26-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

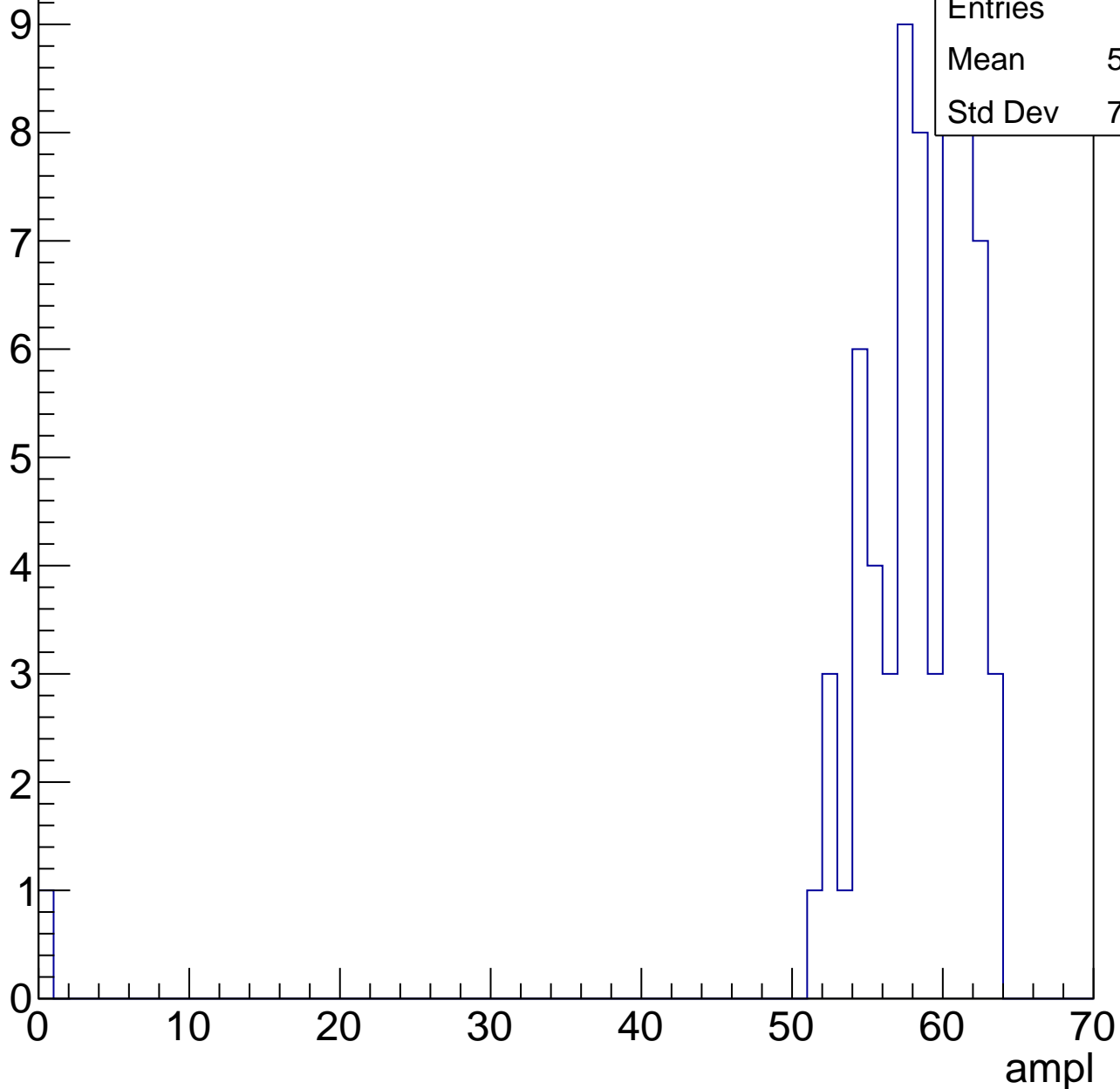
Entries	82
Mean	51.3
Std Dev	4.16



B1L101S, U26-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

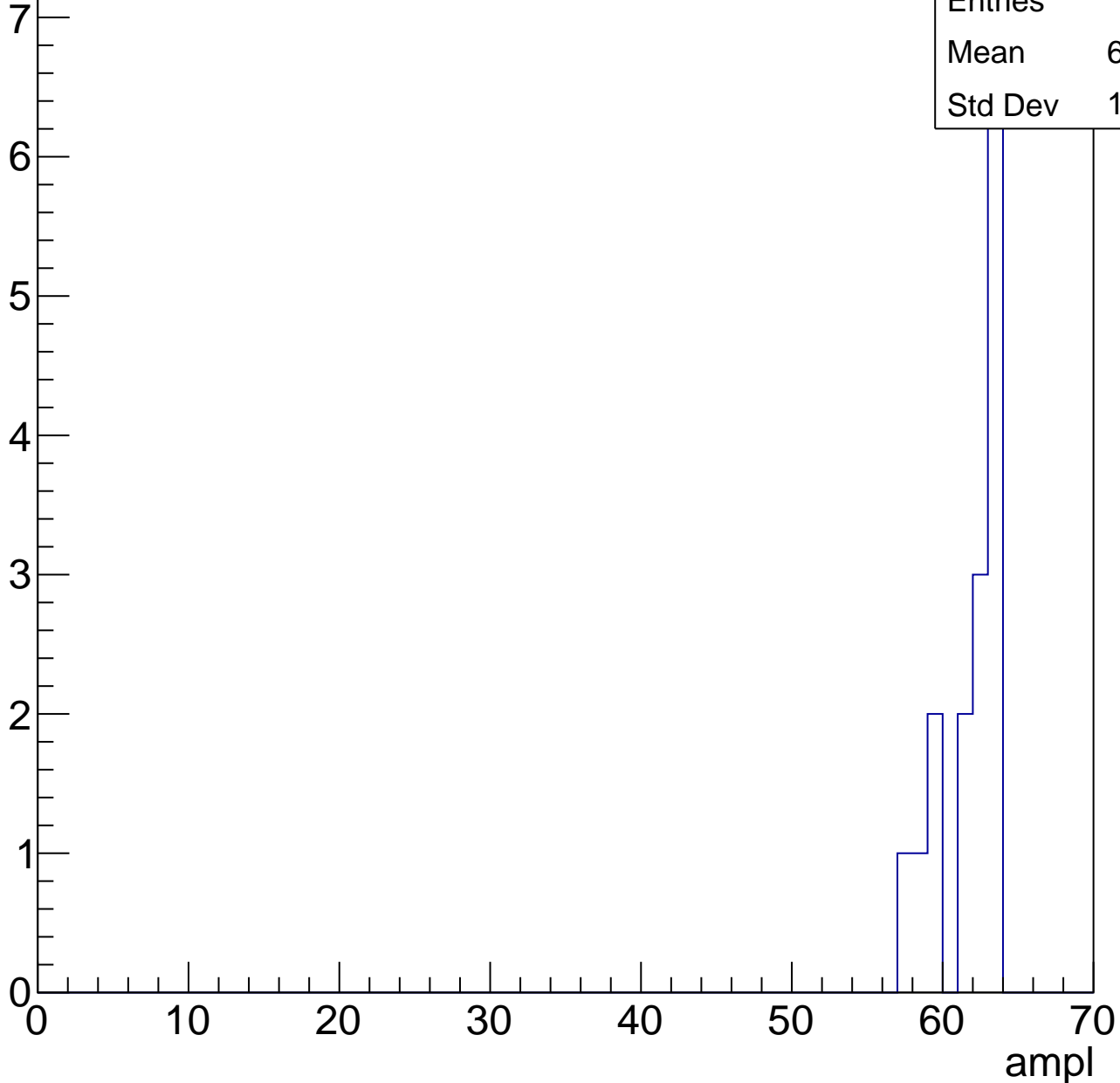


B1L101S, U26-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.38
Std Dev	1.965



B1L101S, U26-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch112, adc0

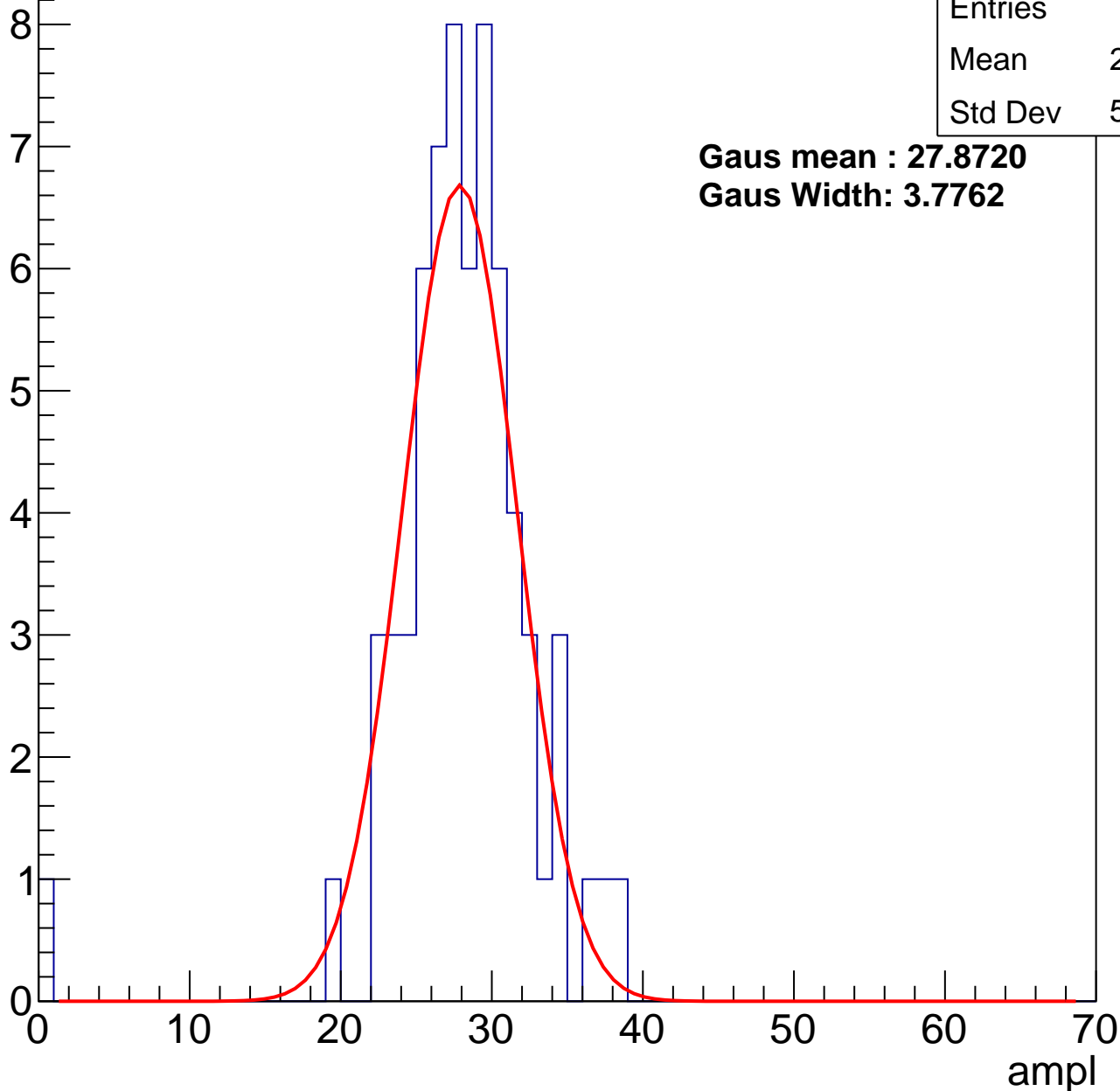
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.58
Std Dev	5.027

Gaus mean : 27.8720

Gaus Width: 3.7762



B1L101S, U26-ch112, adc1

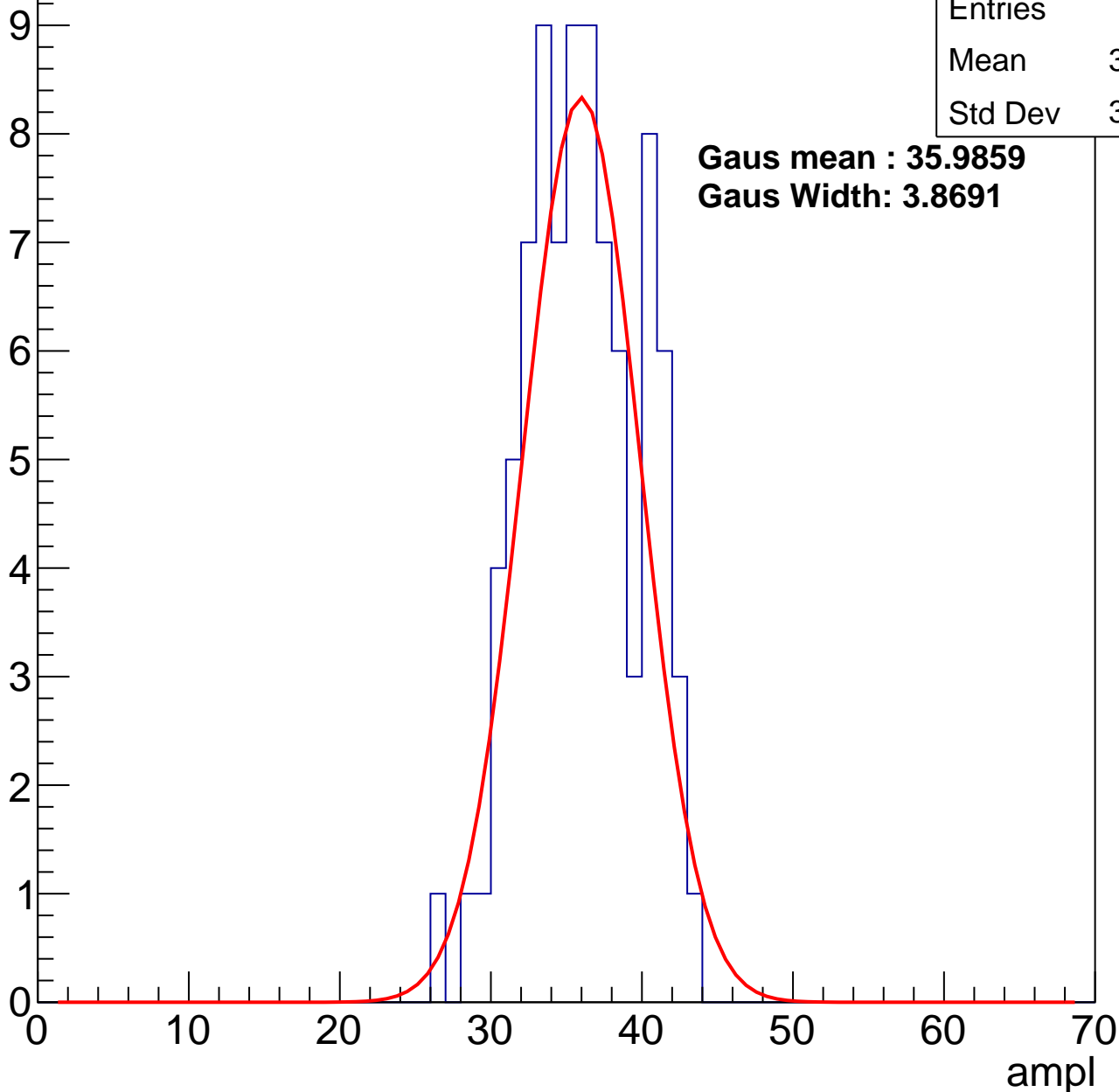
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	35.57
Std Dev	3.697

Gaus mean : 35.9859

Gaus Width: 3.8691



B1L101S, U26-ch112, adc2

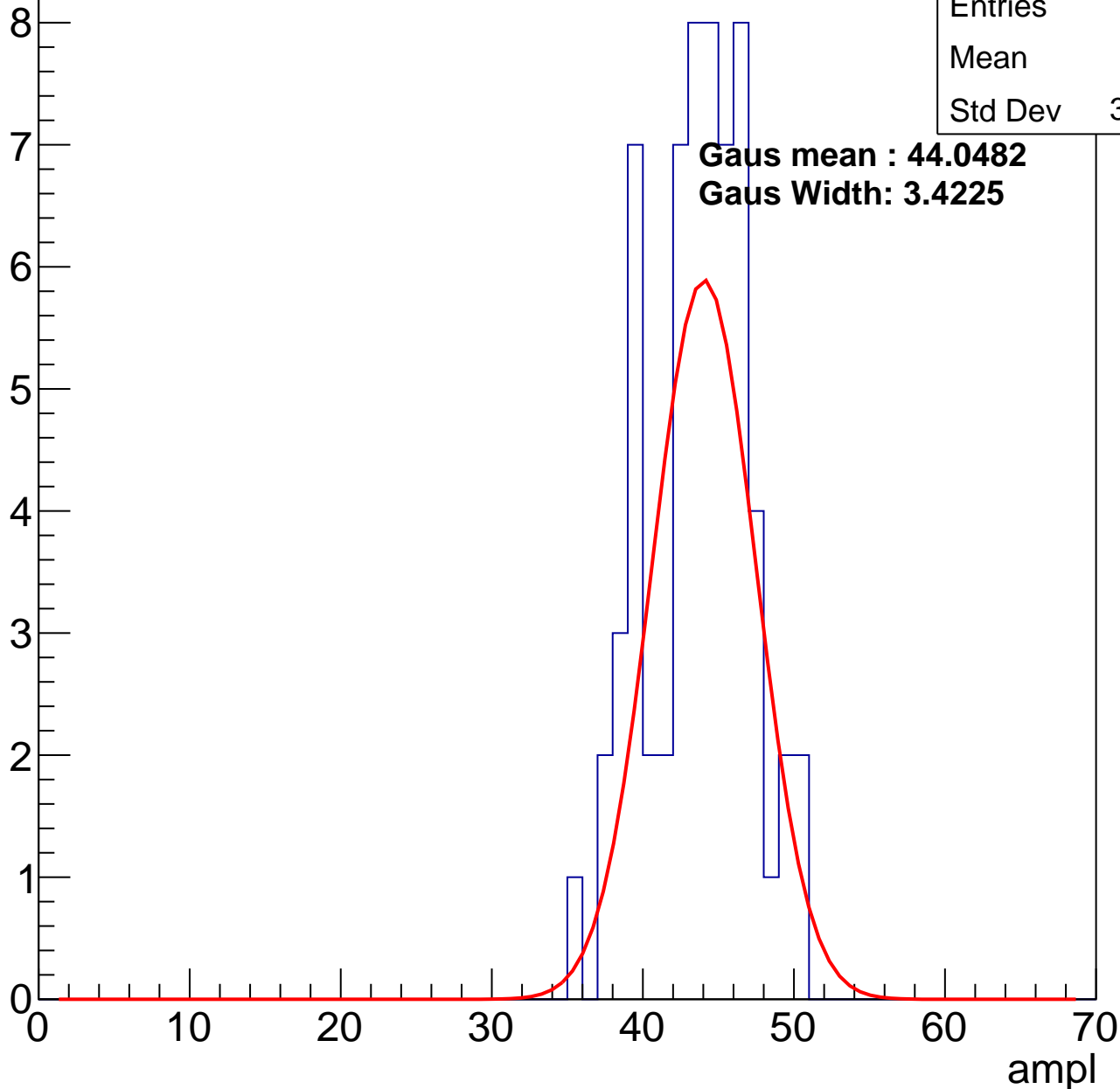
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.2
Std Dev	3.355

Gaus mean : 44.0482

Gaus Width: 3.4225

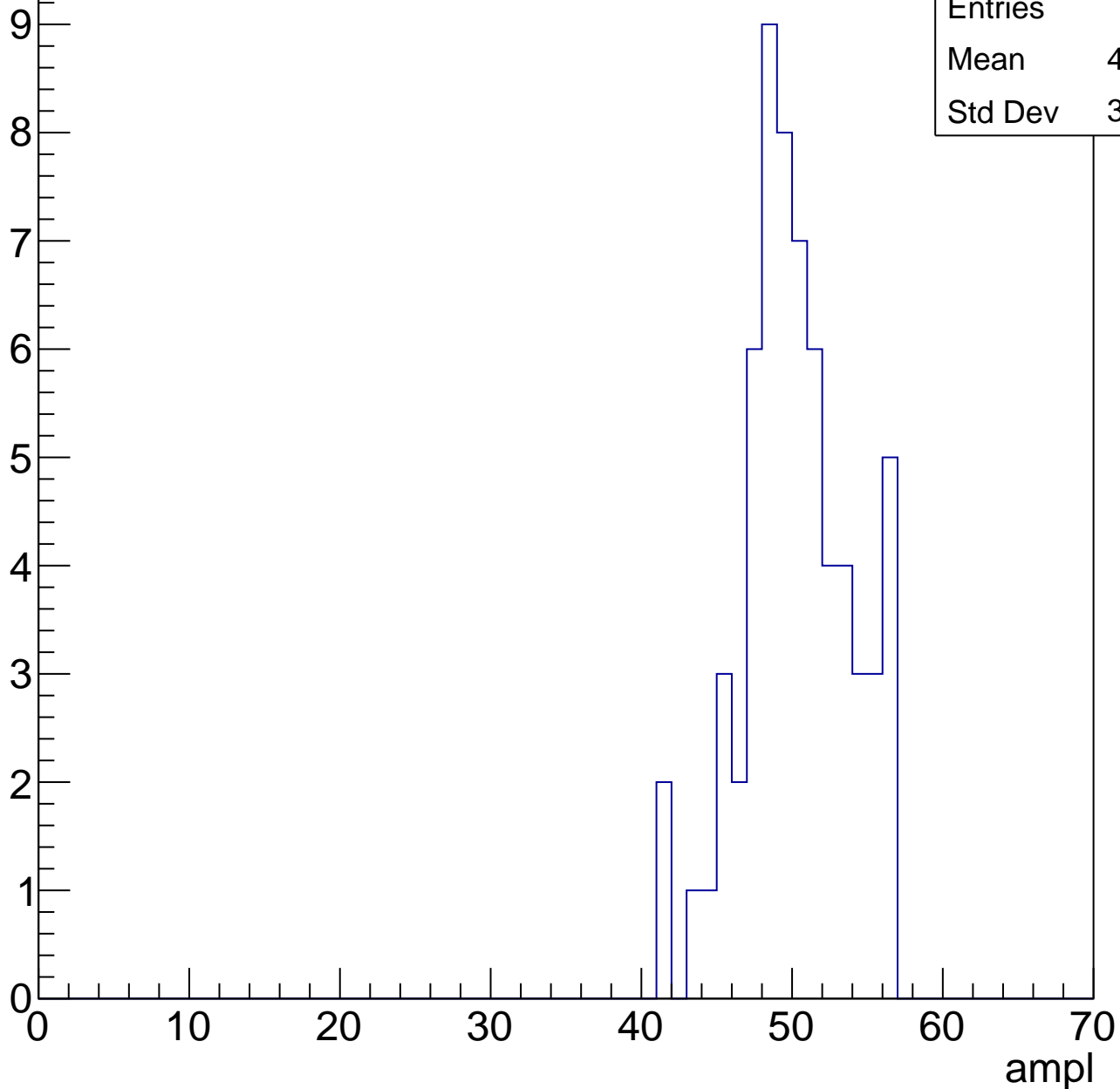


B1L101S, U26-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.77
Std Dev	3.556



B1L101S, U26-ch112, adc4

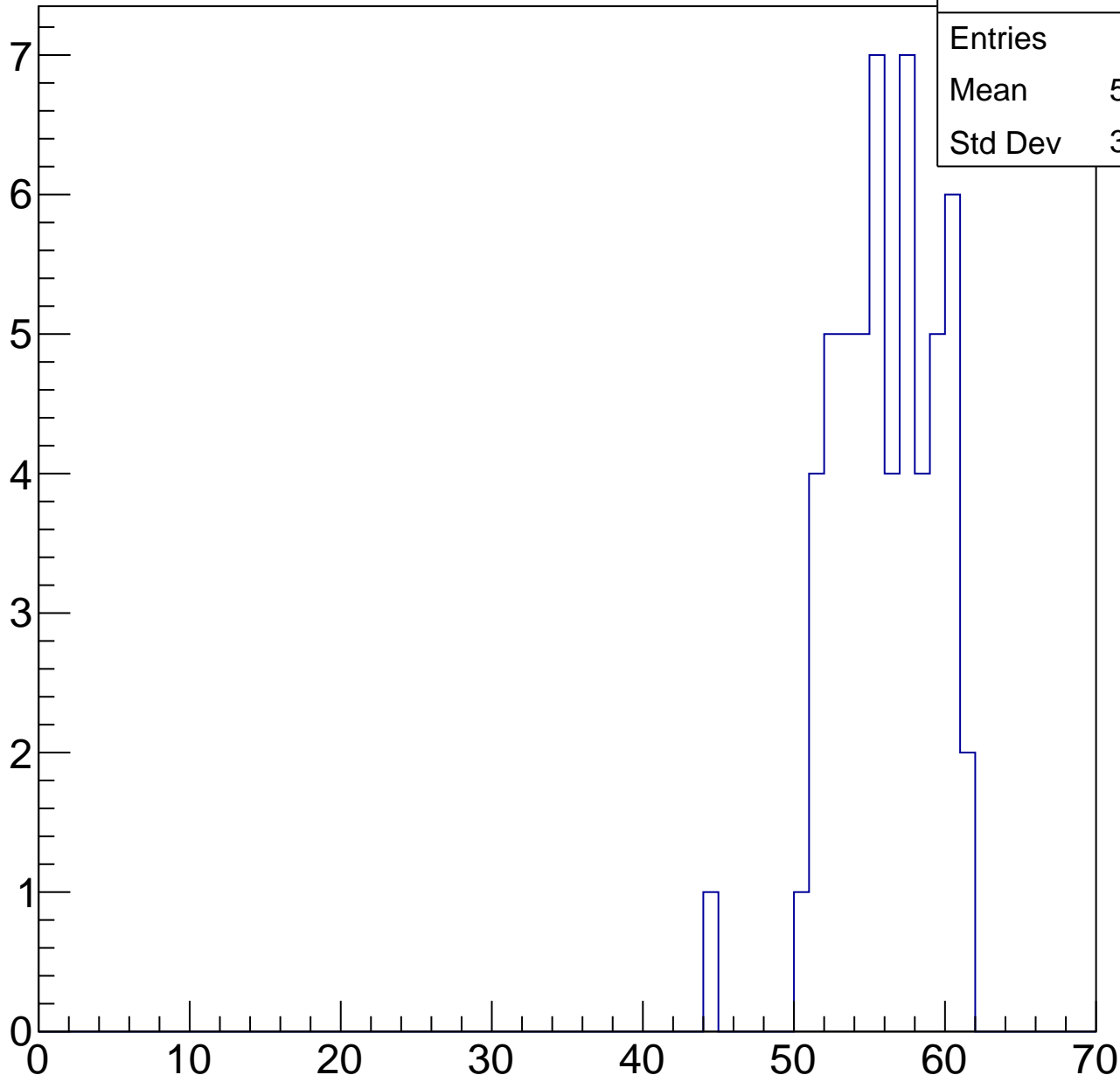
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	56
Mean	55.54
Std Dev	3.365

ampl

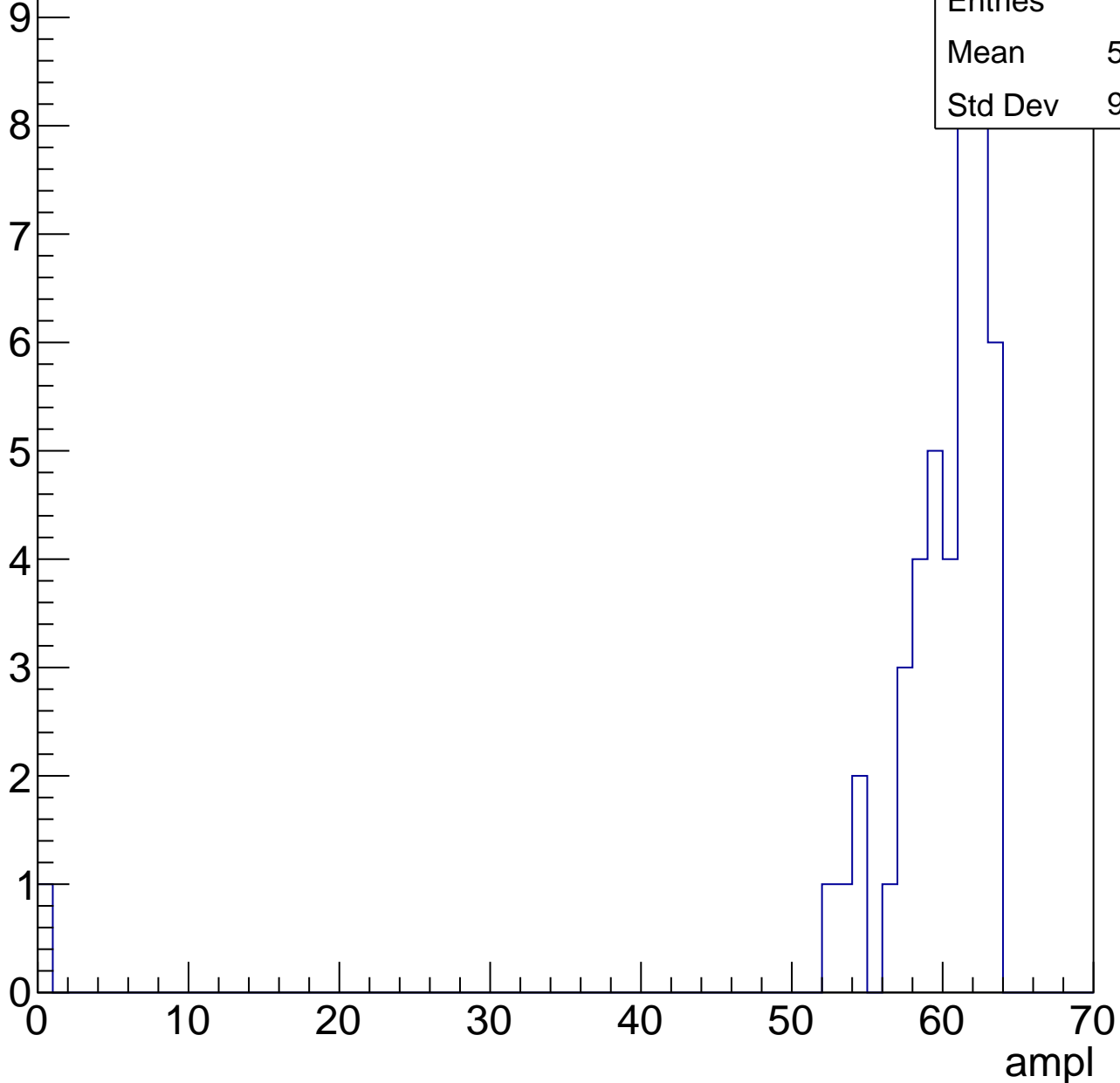


B1L101S, U26-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

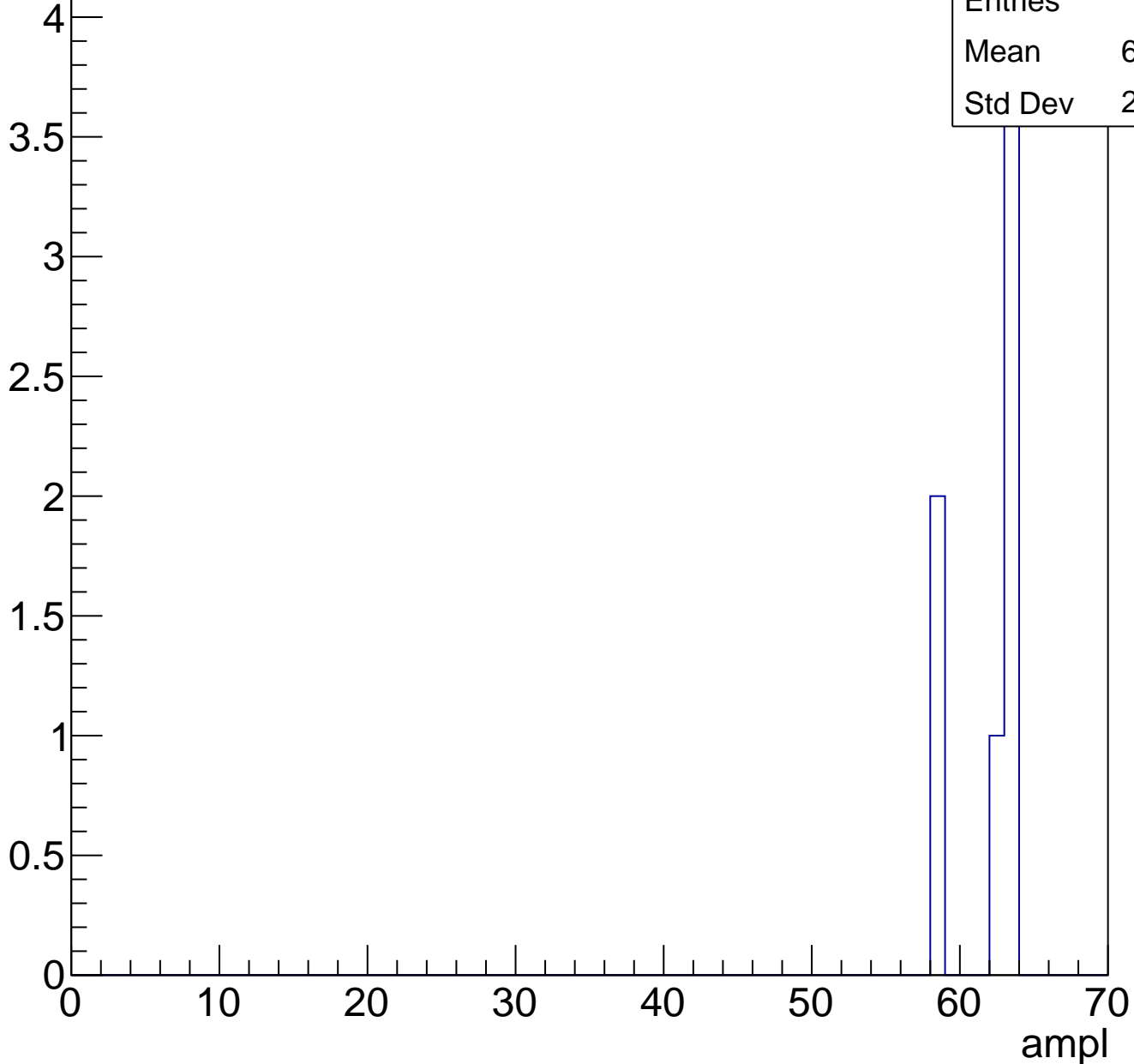
Entries	45
Mean	58.47
Std Dev	9.239



B1L101S, U26-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch113, adc0

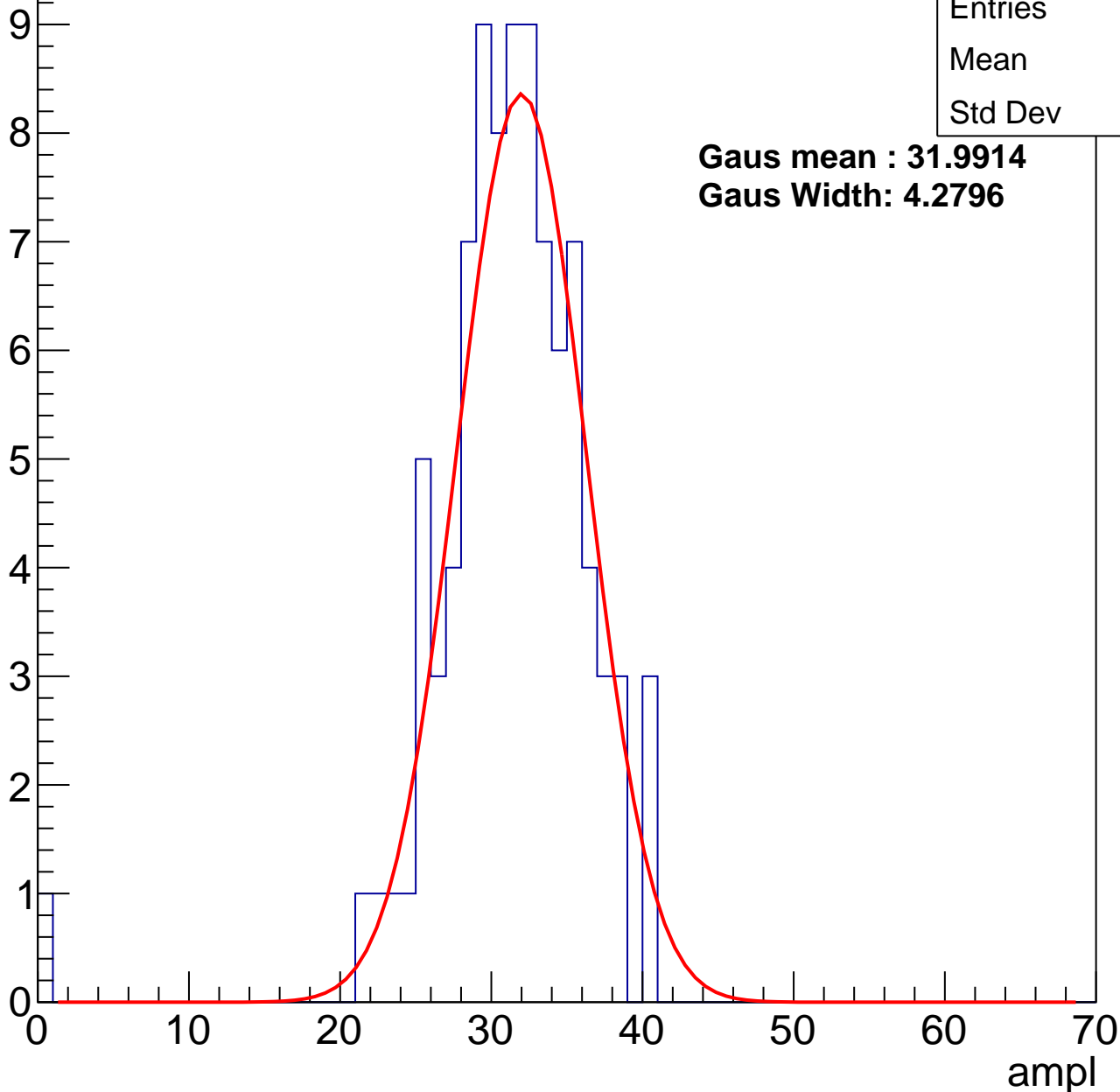
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	92
Mean	30.8
Std Dev	5.19

Gaus mean : 31.9914

Gaus Width: 4.2796



B1L101S, U26-ch113, adc1

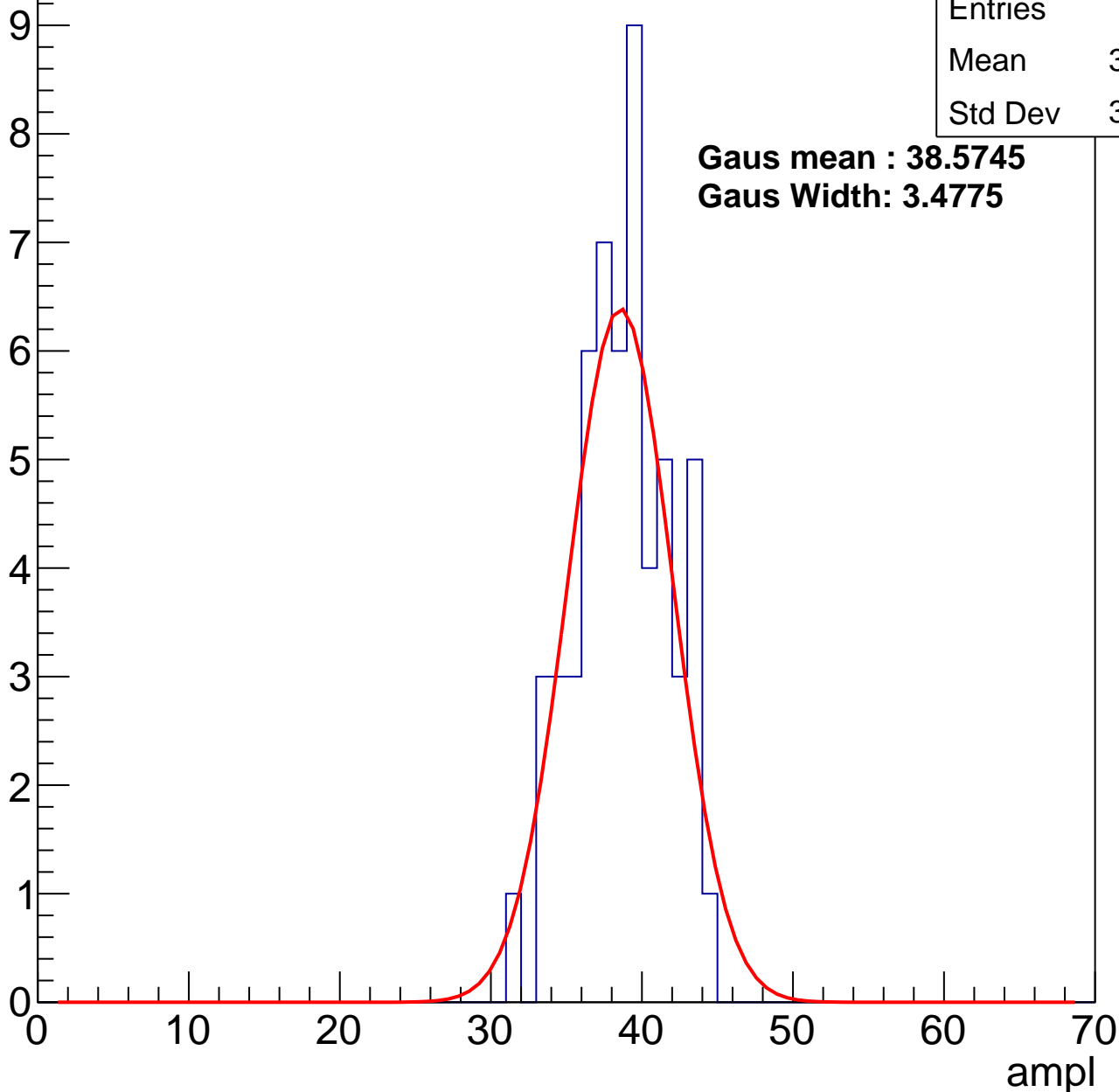
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	38.23
Std Dev	3.006

Gaus mean : 38.5745

Gaus Width: 3.4775



B1L101S, U26-ch113, adc2

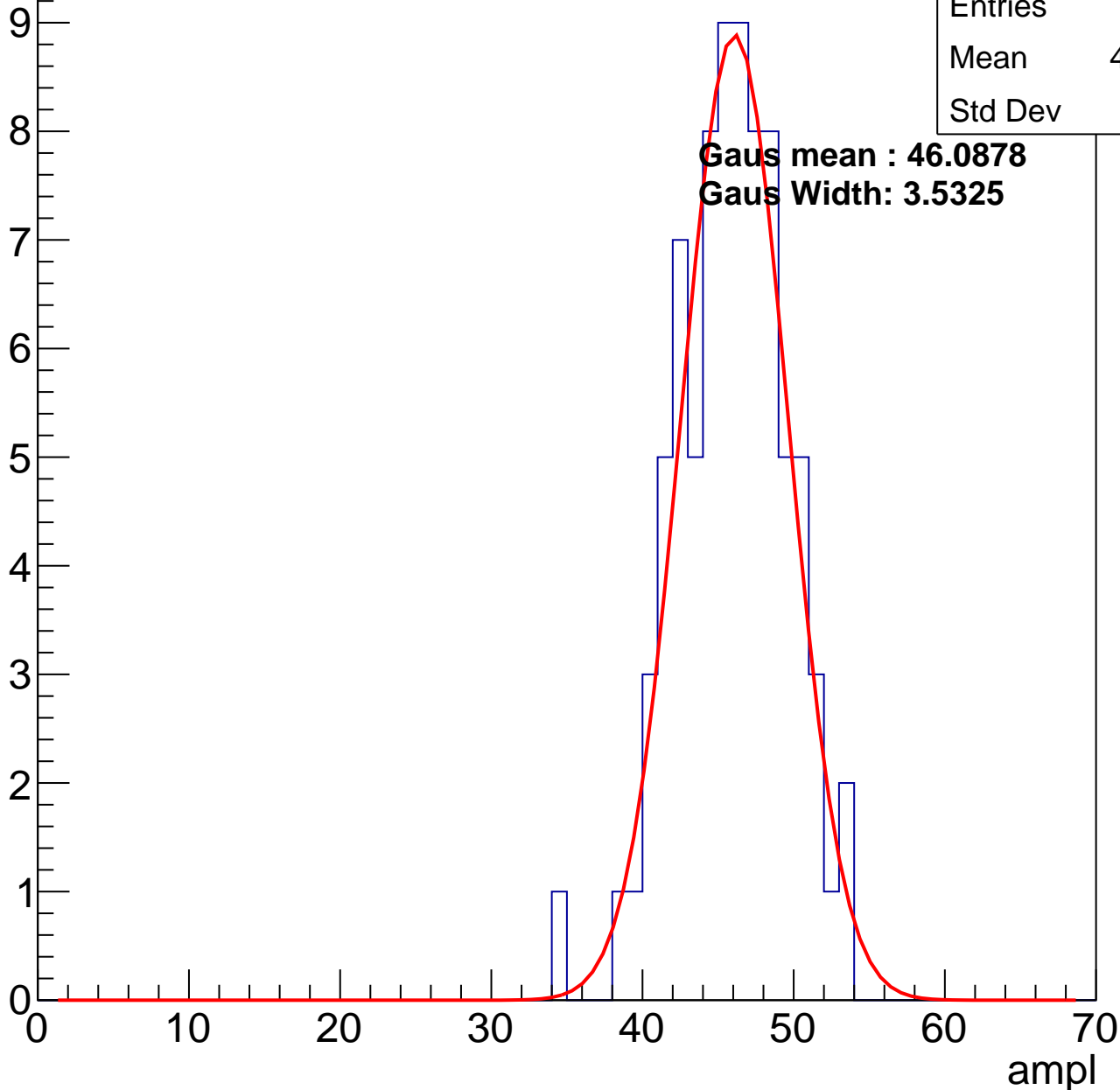
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	45.46
Std Dev	3.58

Gaus mean : 46.0878

Gaus Width: 3.5325



B1L101S, U26-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

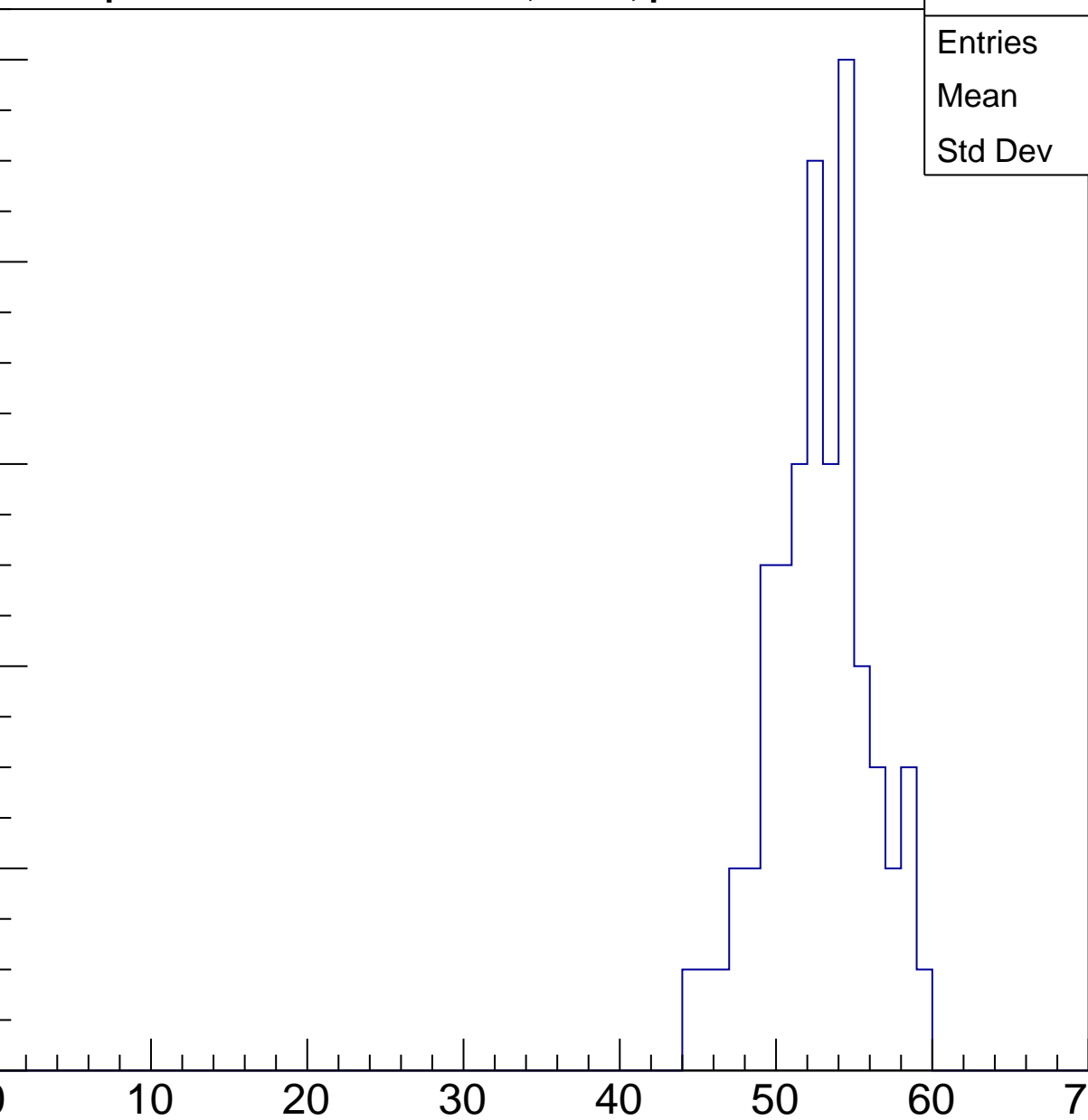
Entries	61
Mean	52.25
Std Dev	3.227

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

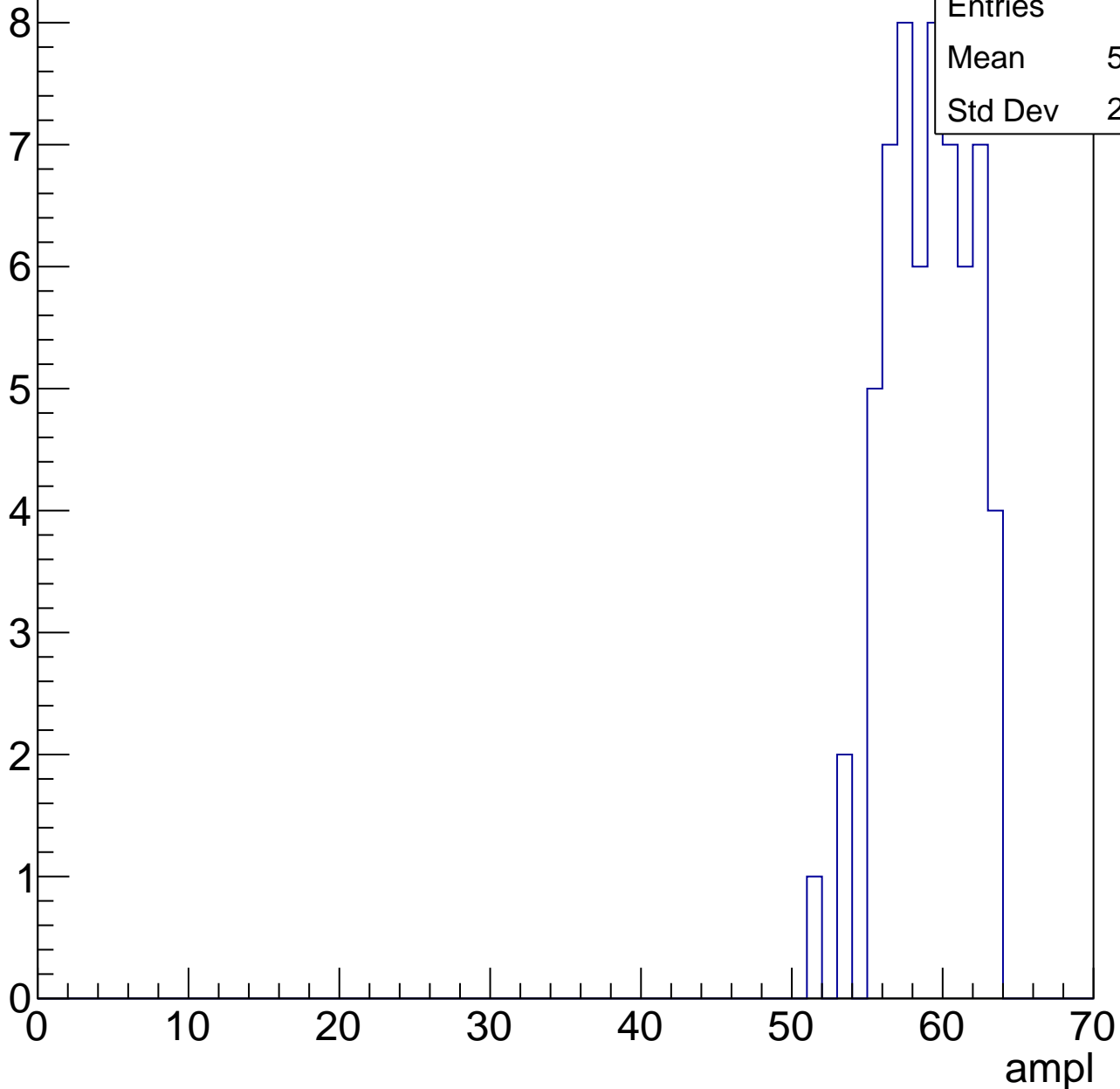


B1L101S, U26-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	58.56
Std Dev	2.755

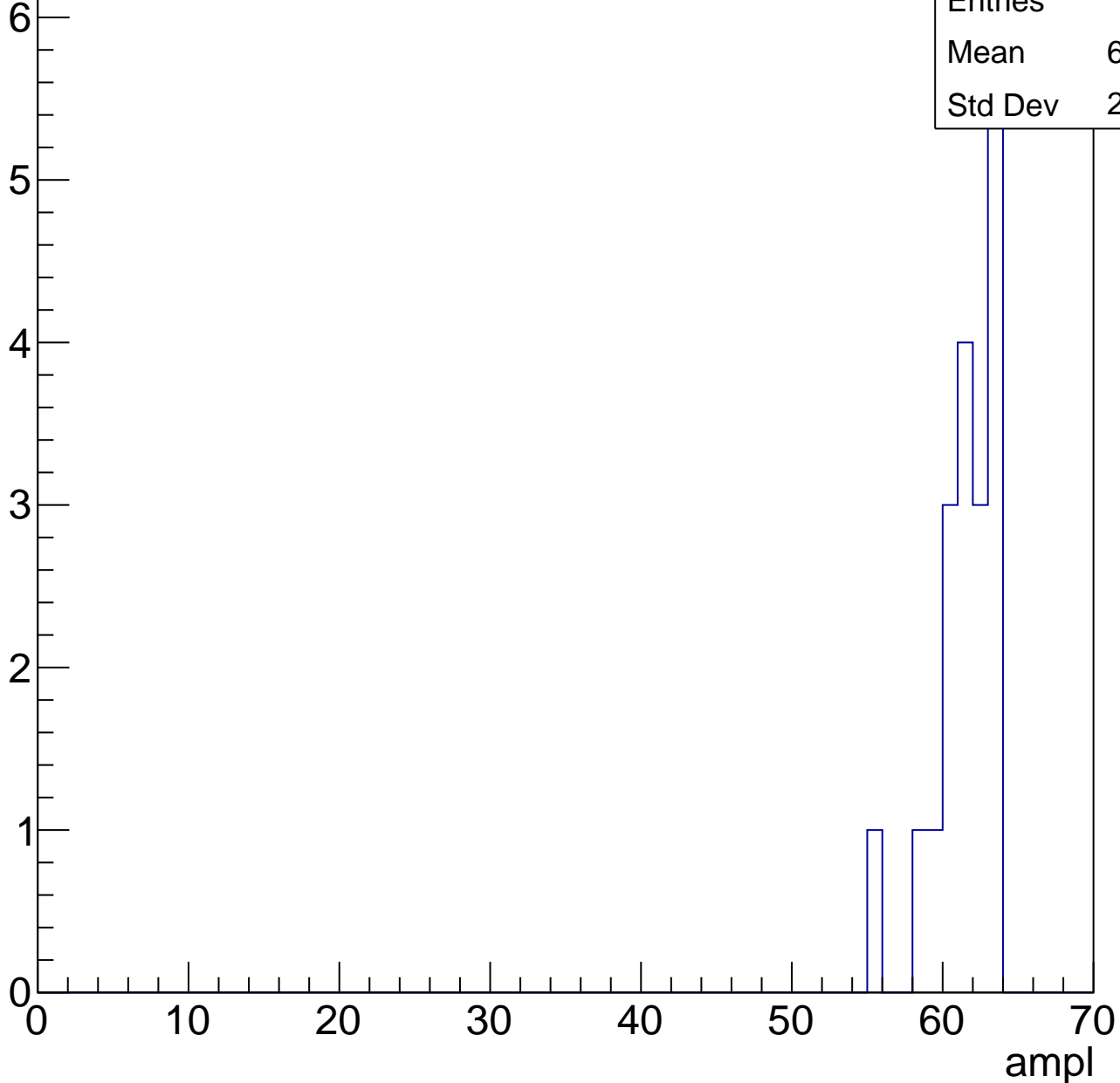


B1L101S, U26-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.05
Std Dev	2.038



B1L101S, U26-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch114, adc0

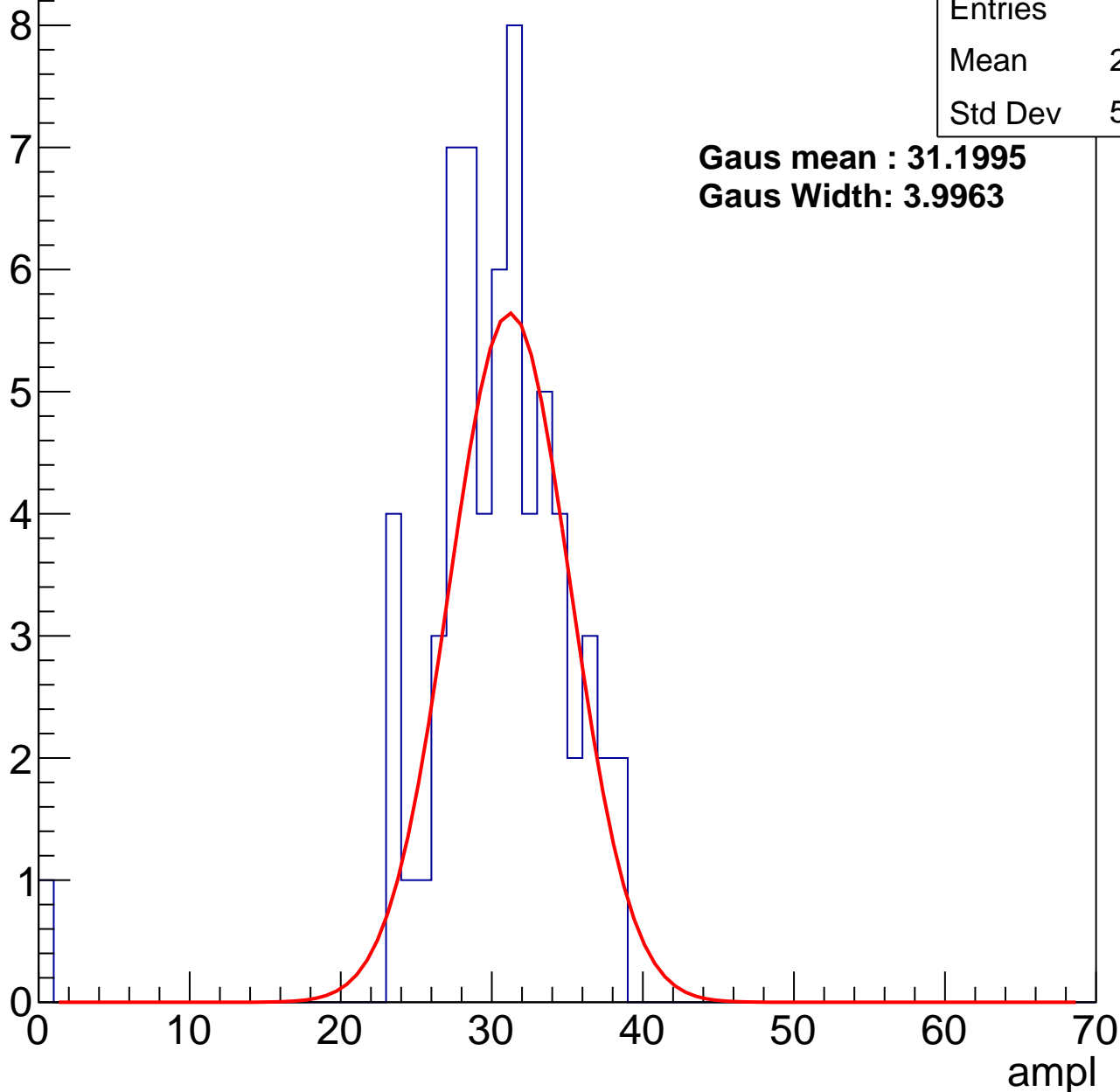
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.77
Std Dev	5.323

Gaus mean : 31.1995

Gaus Width: 3.9963



B1L101S, U26-ch114, adc1

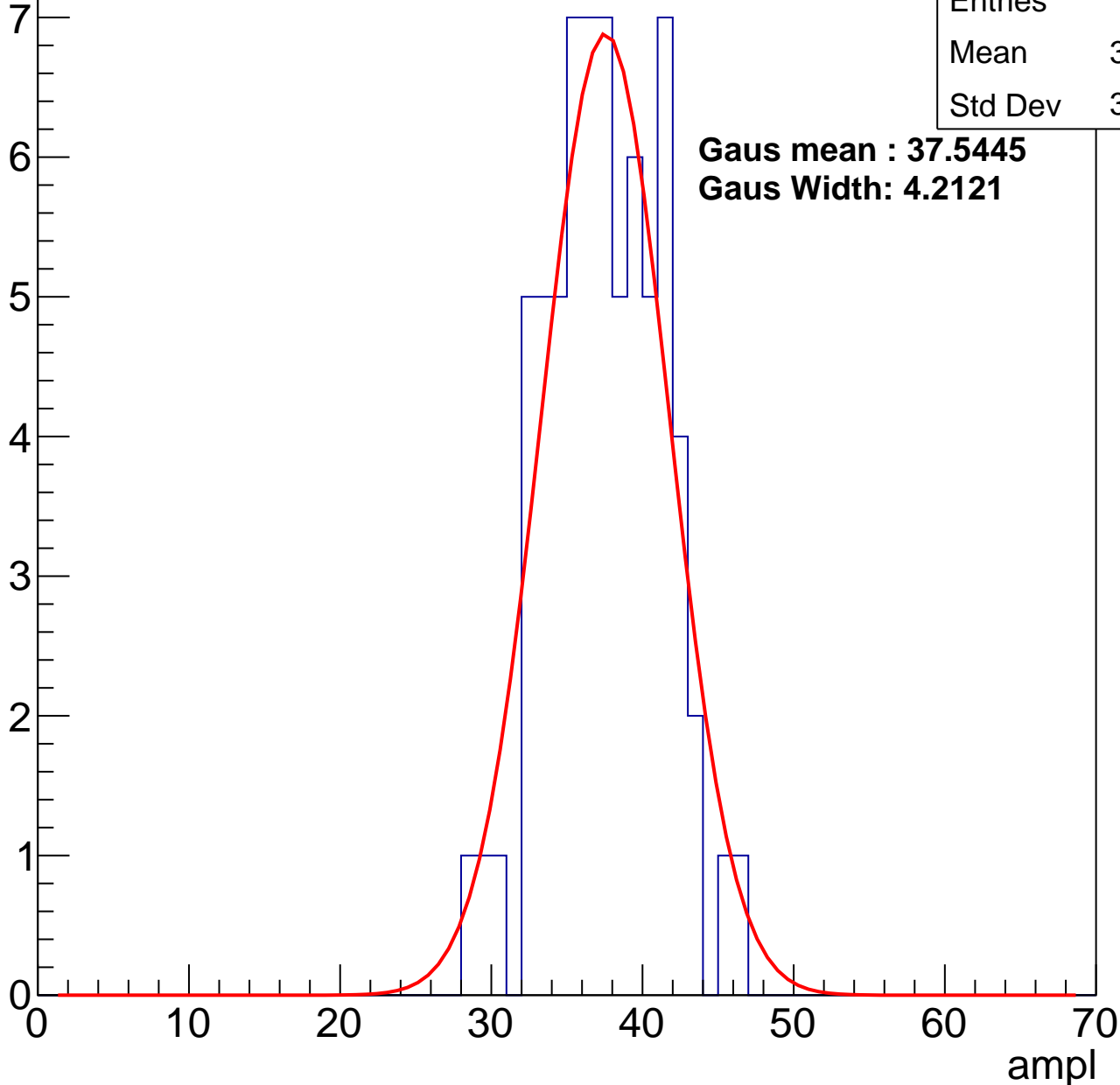
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.06
Std Dev	3.745

Gaus mean : 37.5445

Gaus Width: 4.2121



B1L101S, U26-ch114, adc2

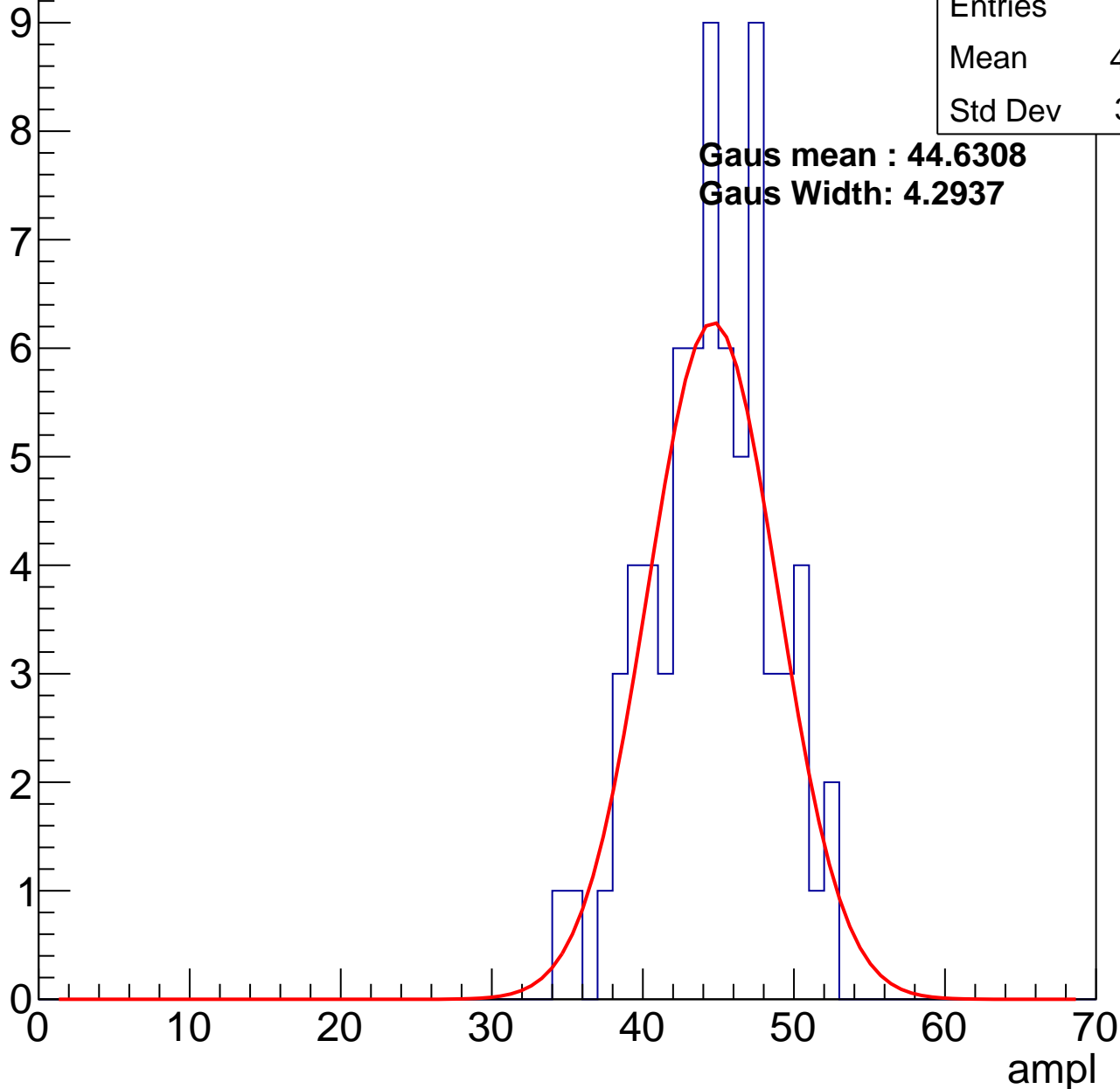
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.14
Std Dev	3.951

Gaus mean : 44.6308

Gaus Width: 4.2937

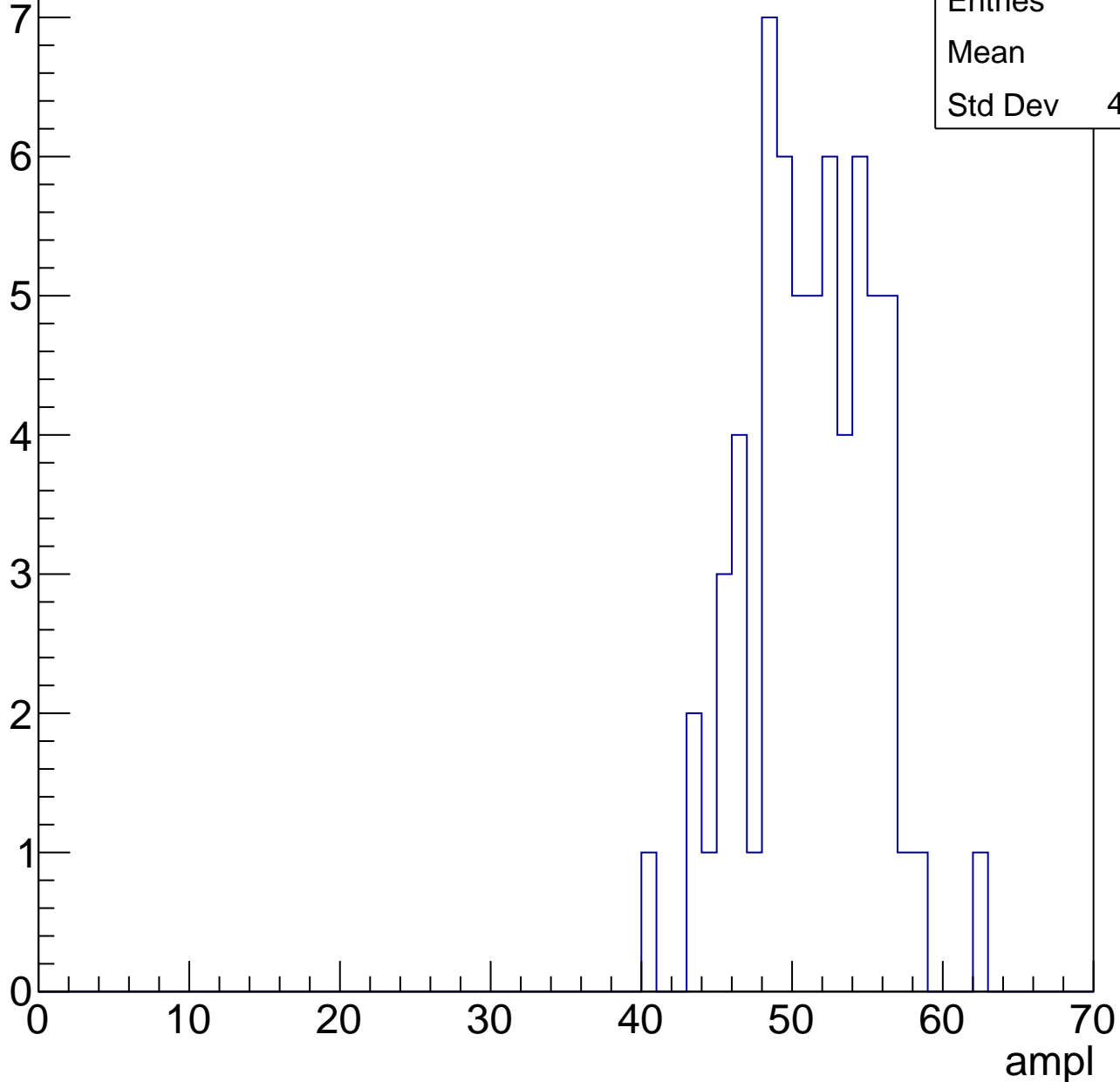


B1L101S, U26-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.8
Std Dev	4.143

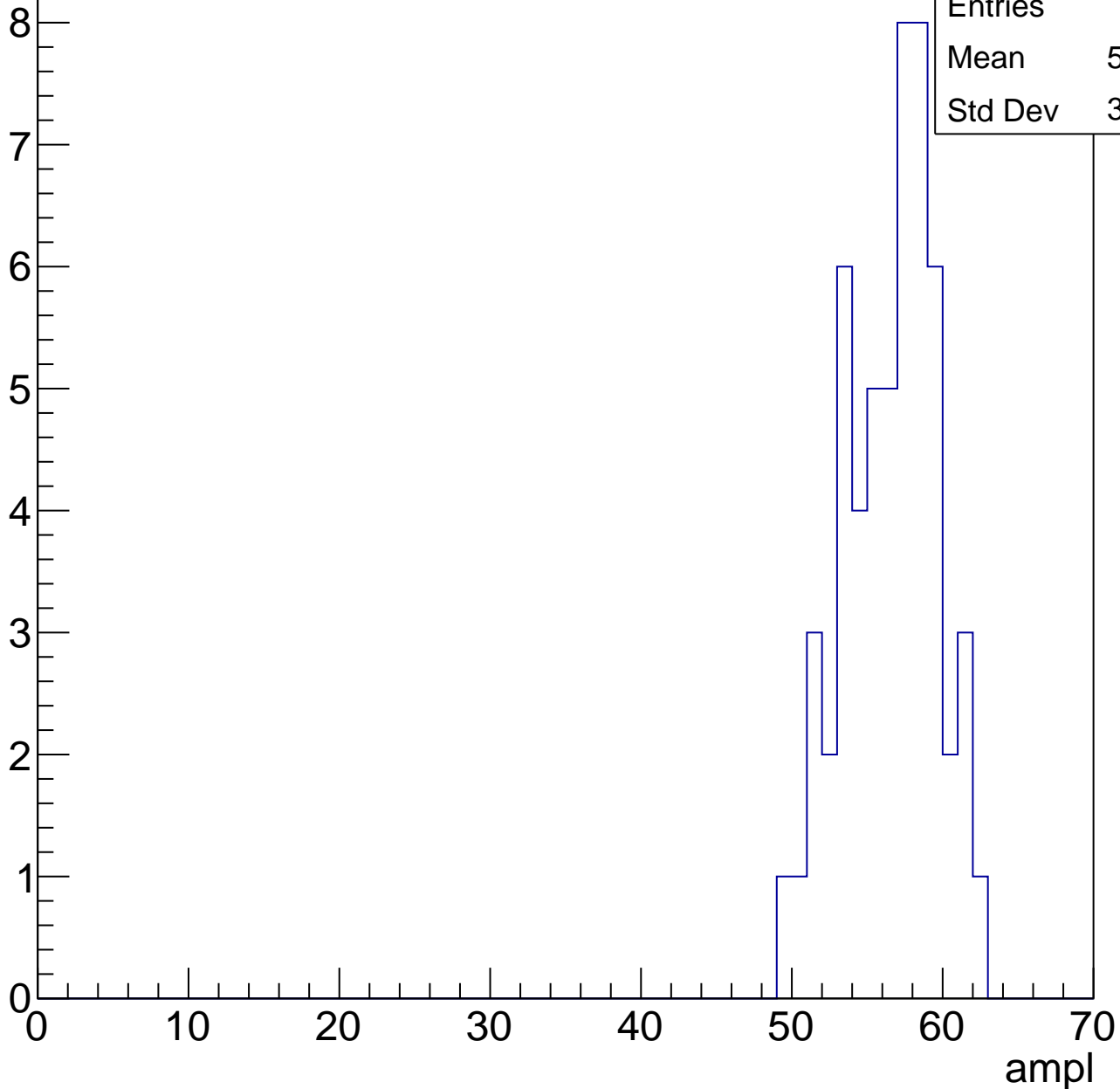


B1L101S, U26-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.07
Std Dev	3.026

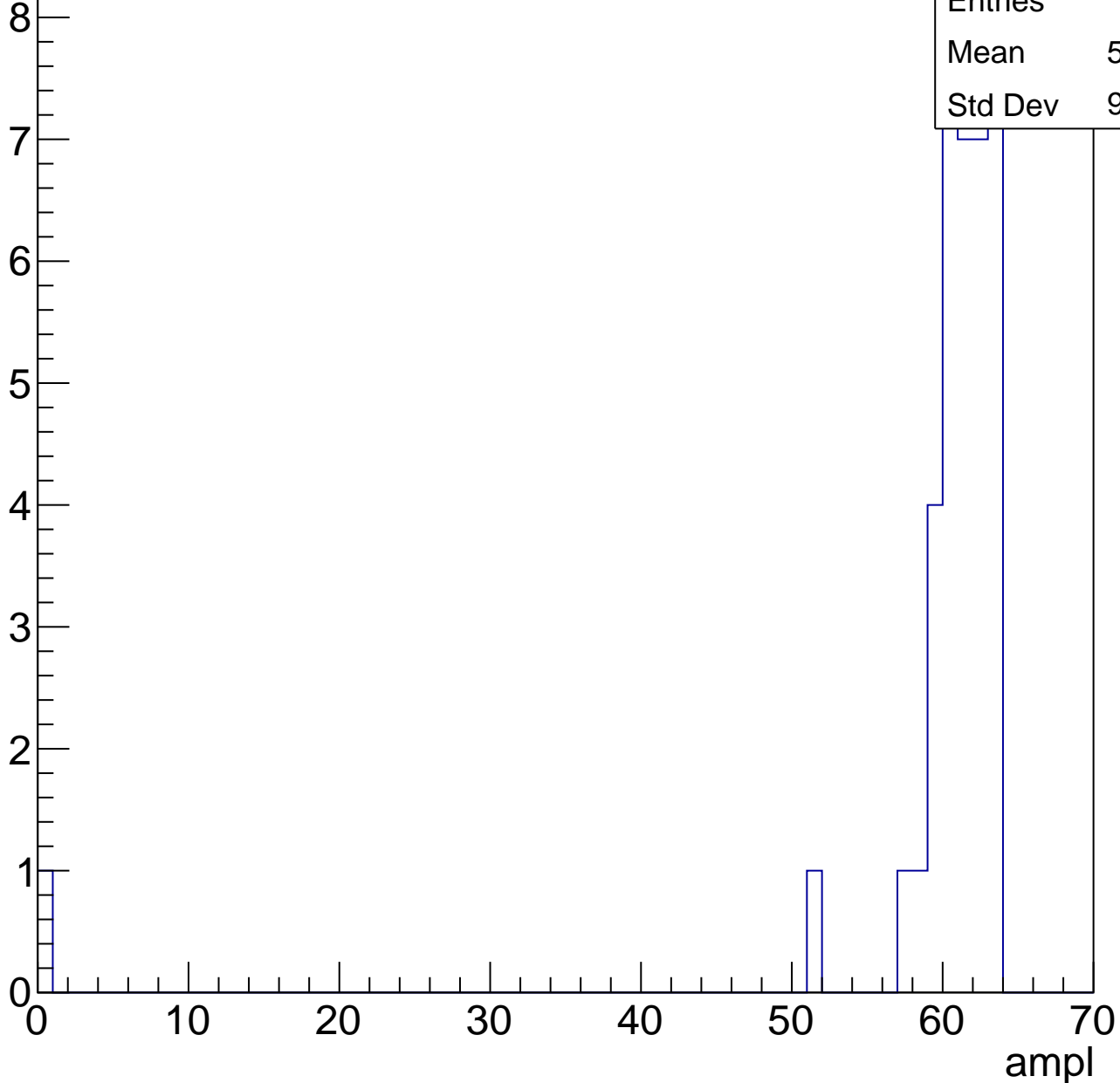


B1L101S, U26-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59.13
Std Dev	9.969



B1L101S, U26-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

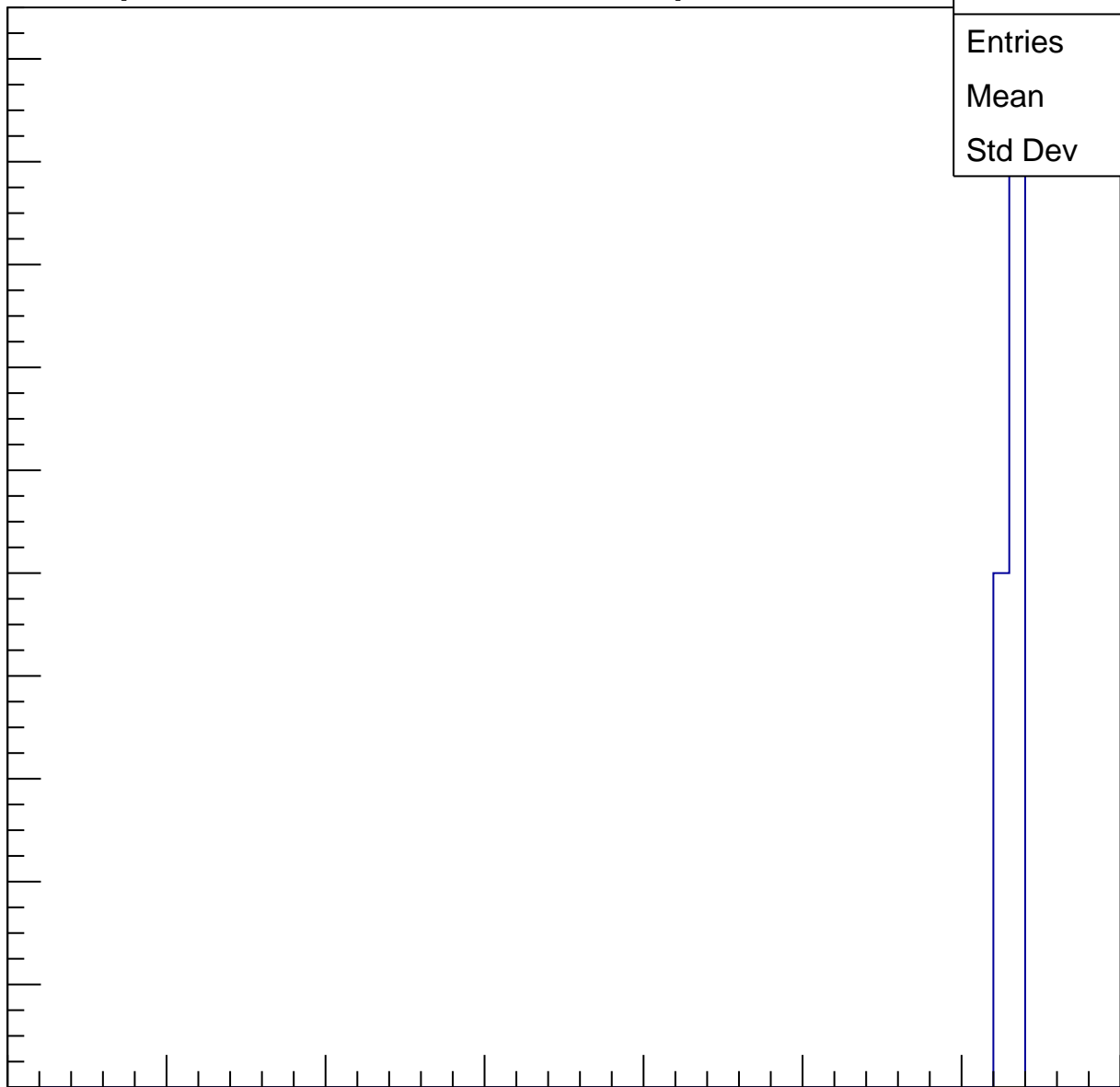
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L101S, U26-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L101S, U26-ch115, adc0

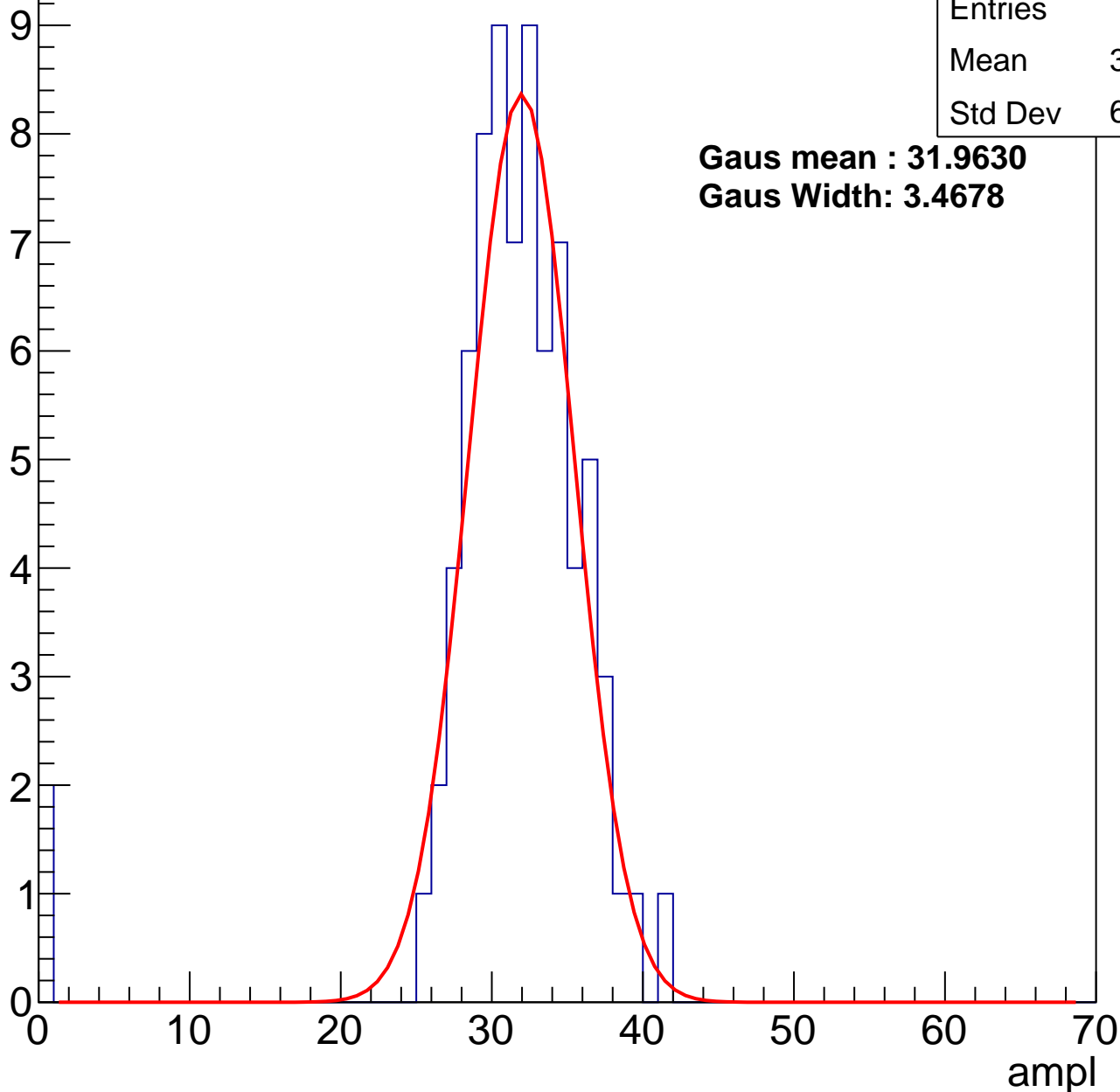
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.86
Std Dev	6.039

Gaus mean : 31.9630

Gaus Width: 3.4678



B1L101S, U26-ch115, adc1

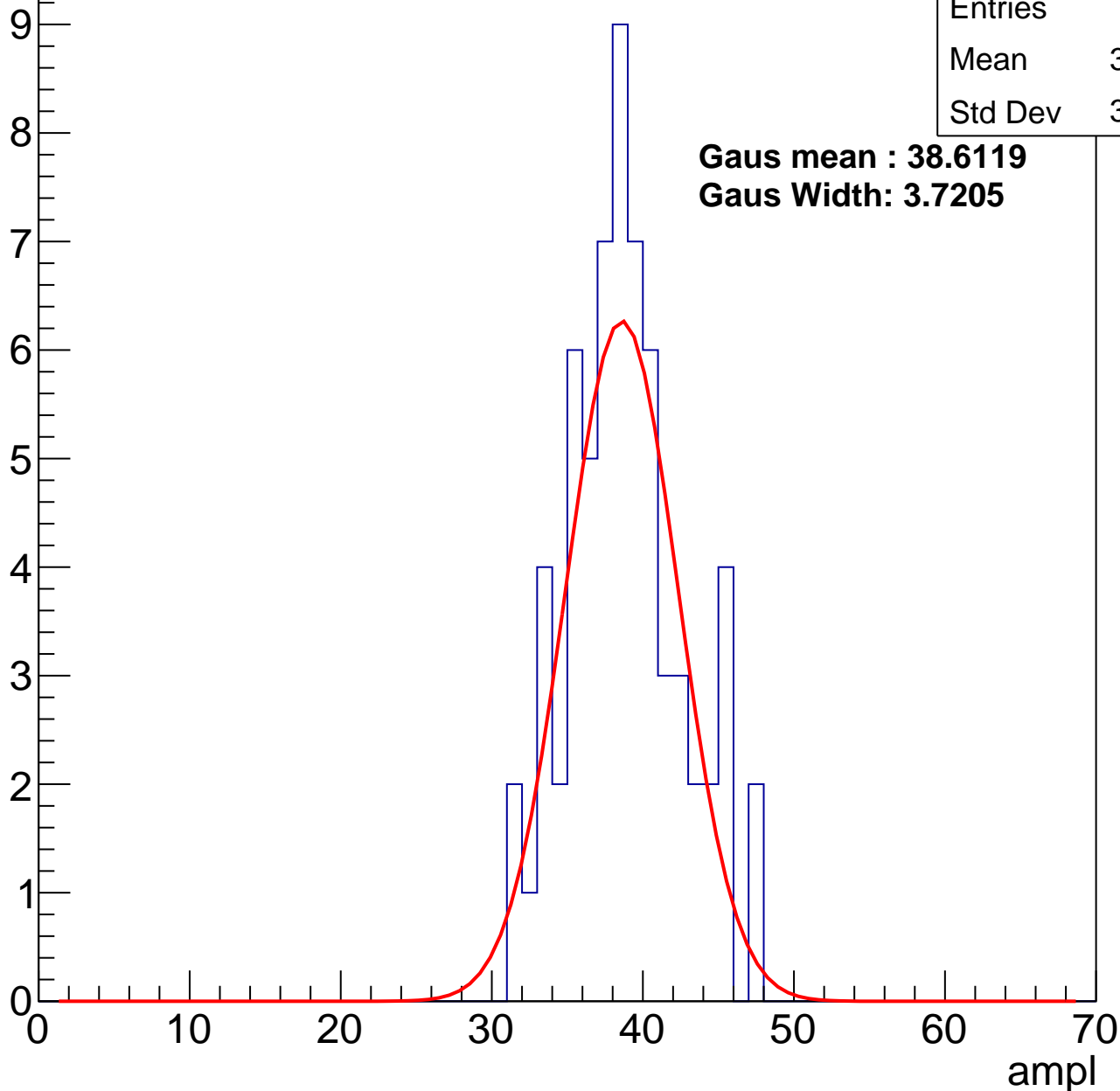
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	38.38
Std Dev	3.769

Gaus mean : 38.6119

Gaus Width: 3.7205



B1L101S, U26-ch115, adc2

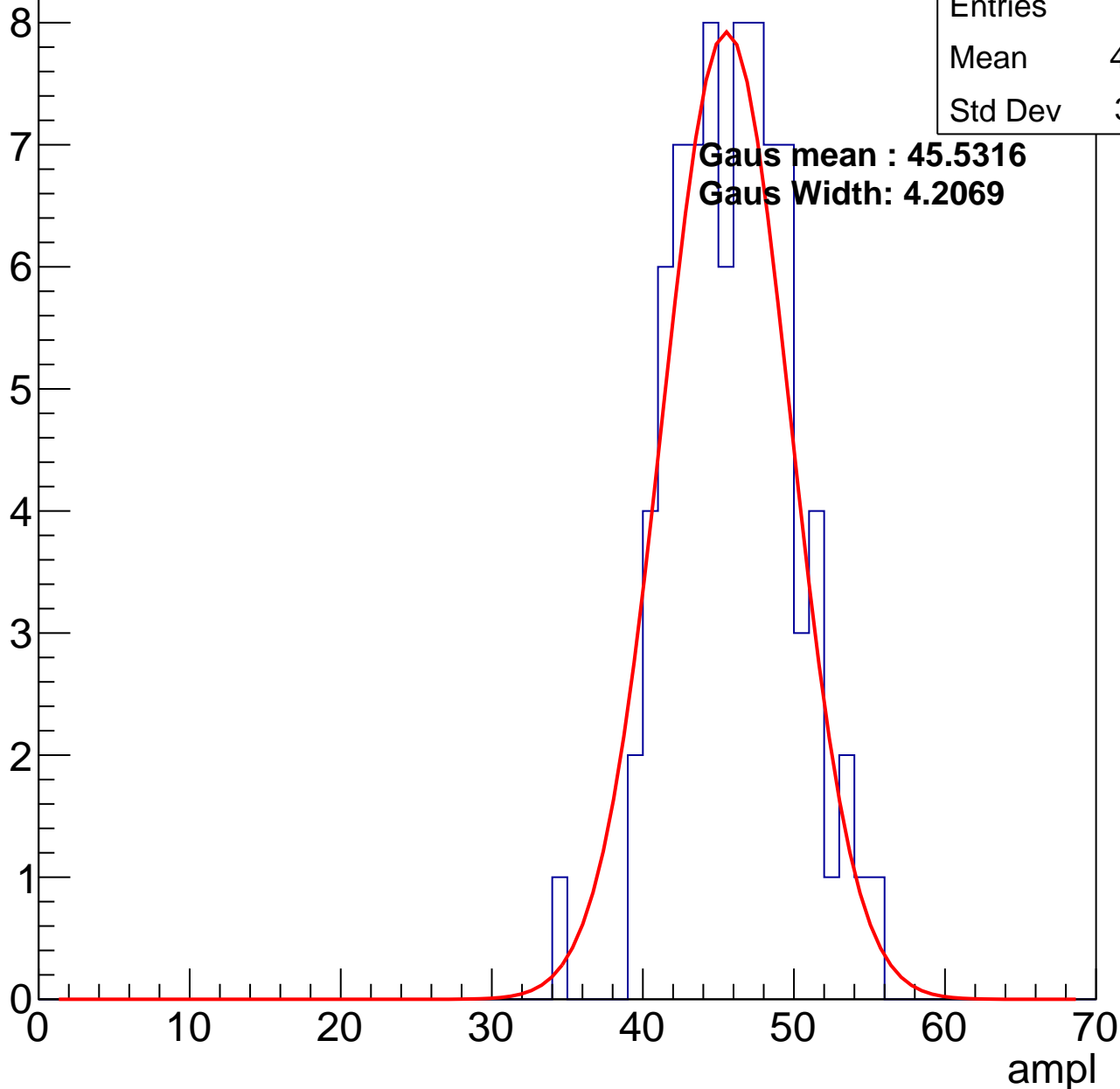
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	45.53
Std Dev	3.891

Gaus mean : 45.5316

Gaus Width: 4.2069

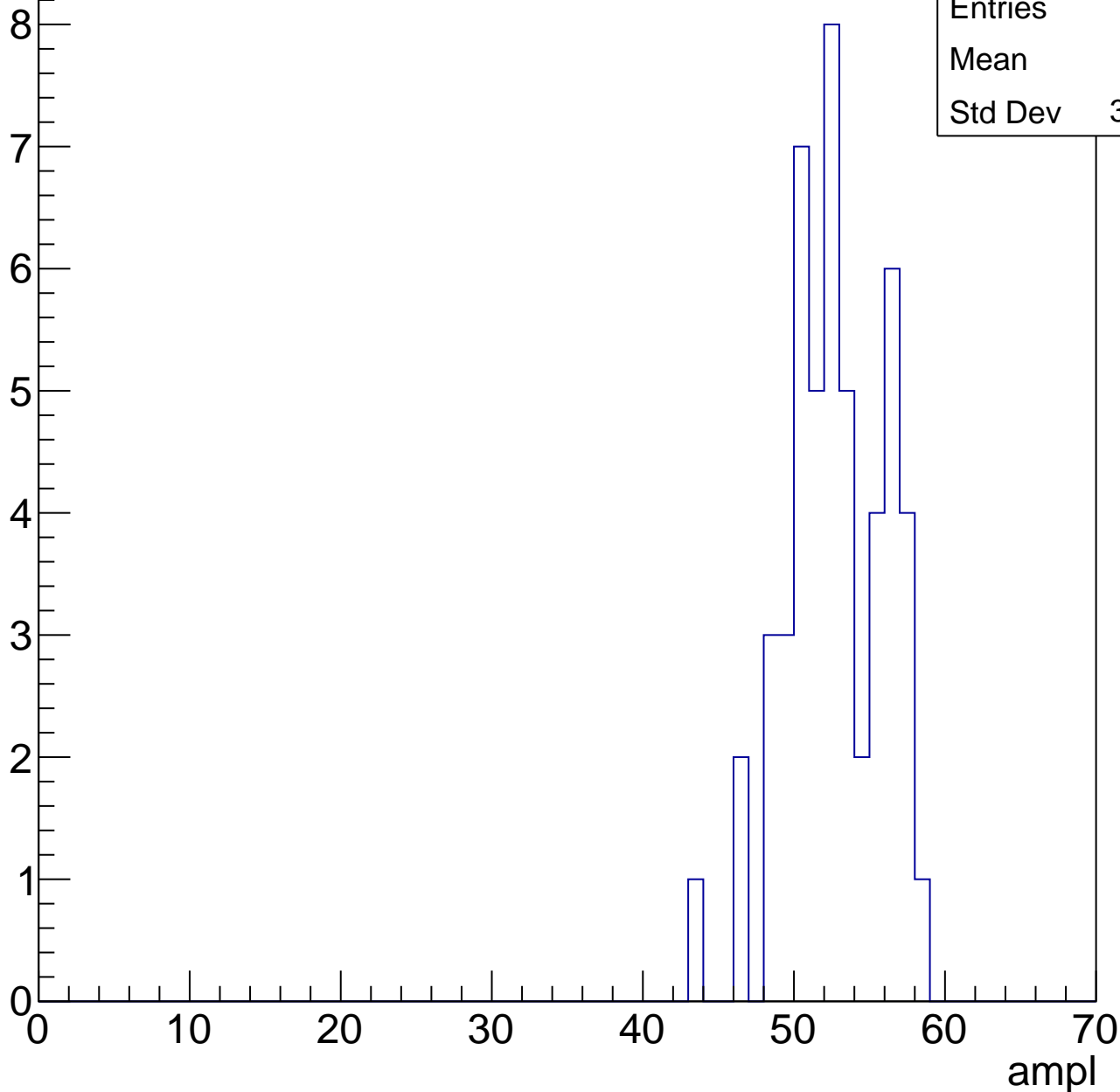


B1L101S, U26-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	52.2
Std Dev	3.254

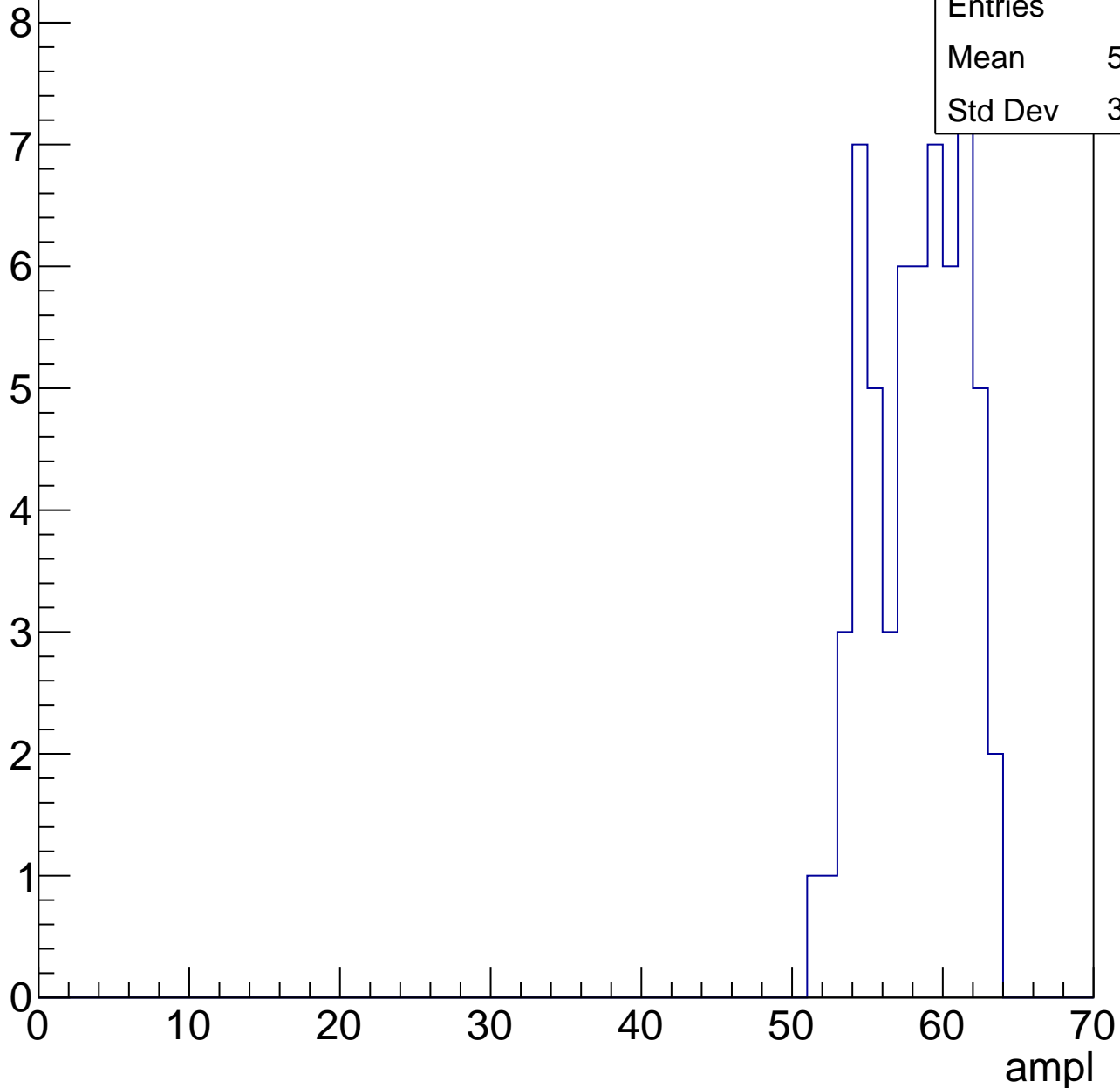


B1L101S, U26-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.83
Std Dev	3.072

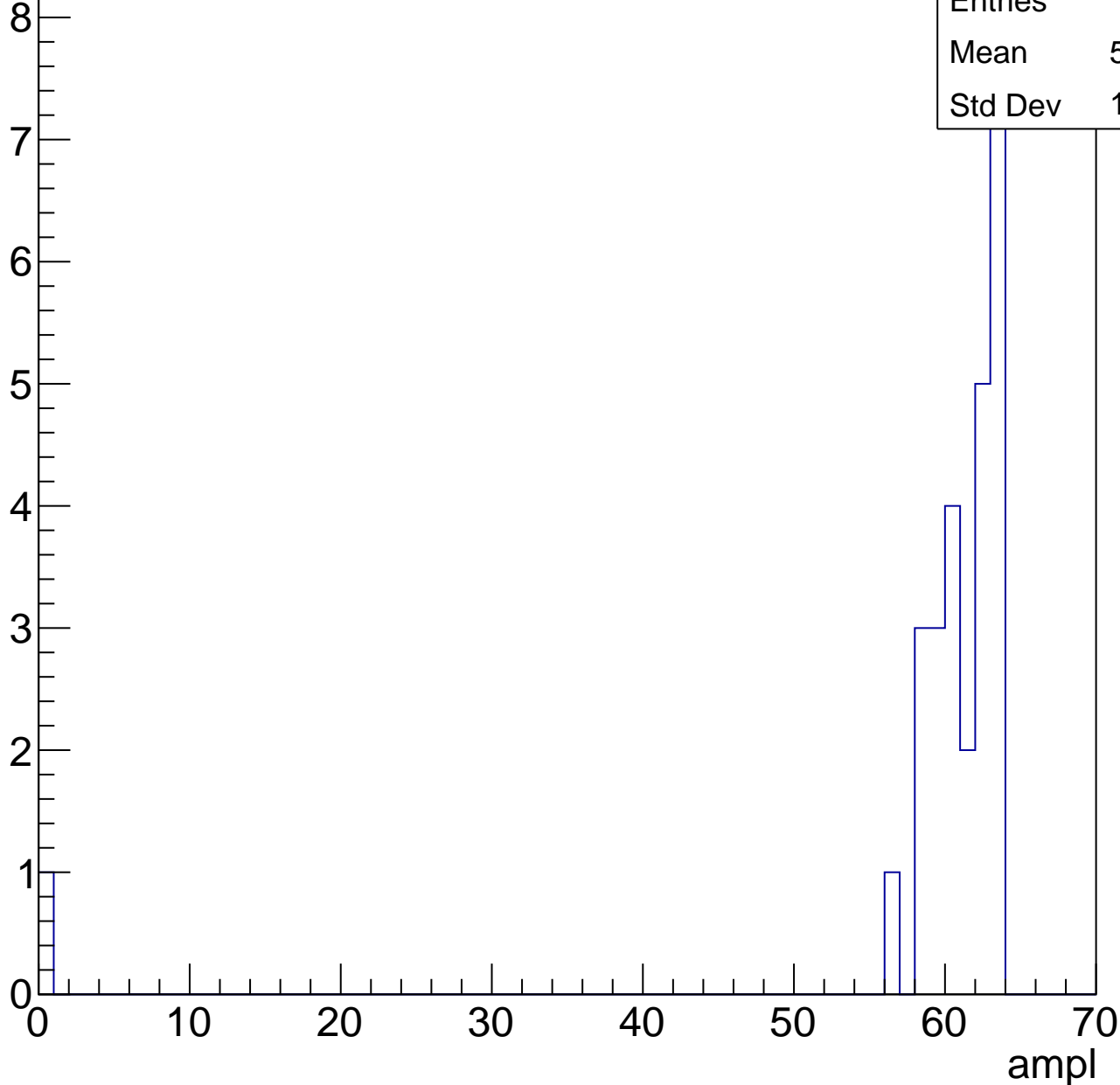


B1L101S, U26-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.63
Std Dev	11.67



B1L101S, U26-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch116, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	31.2
Std Dev	5.035

Gaus mean : 31.7187

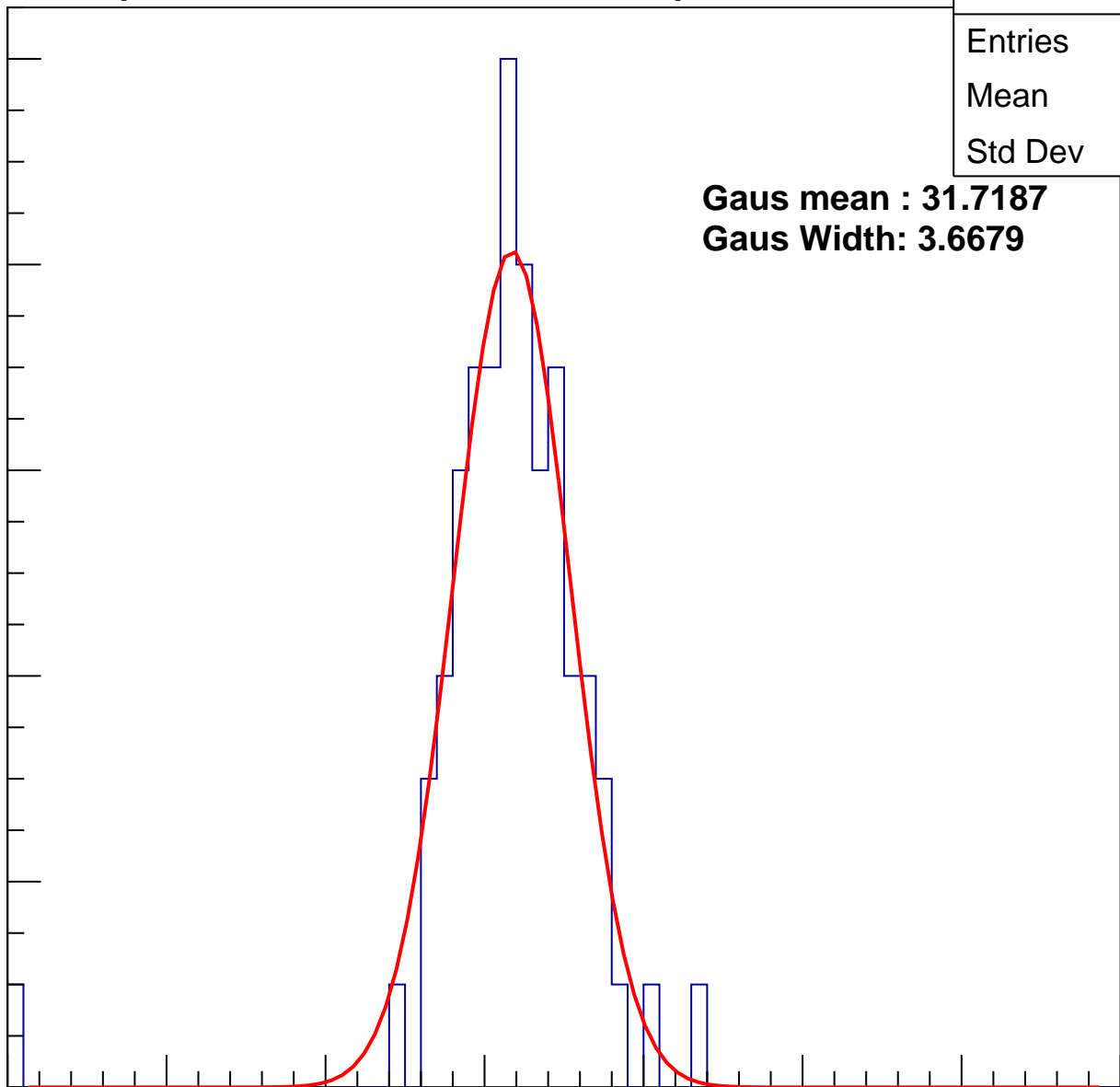
Gaus Width: 3.6679

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch116, adc1

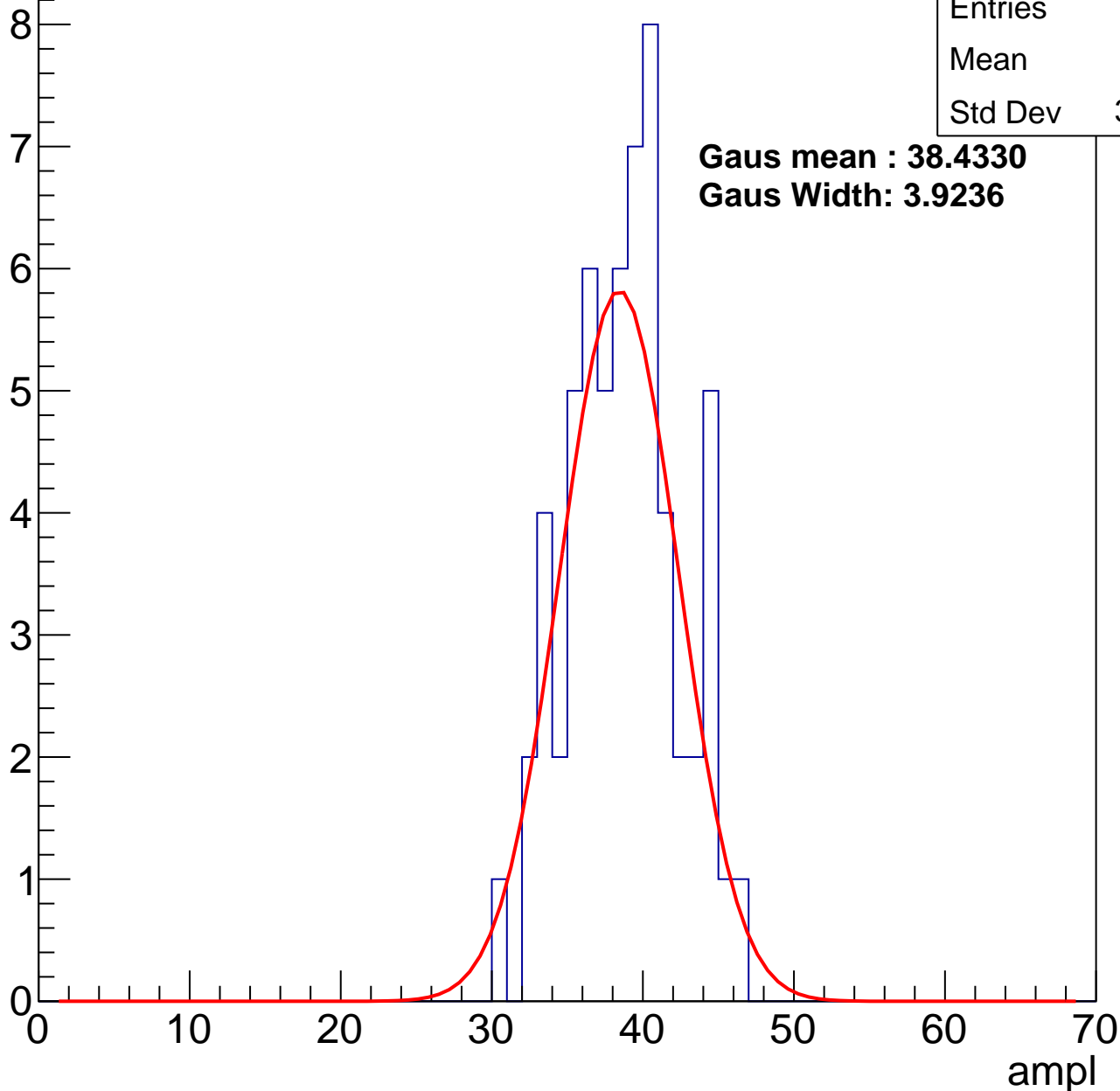
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	38.3
Std Dev	3.591

Gaus mean : 38.4330

Gaus Width: 3.9236



B1L101S, U26-ch116, adc2

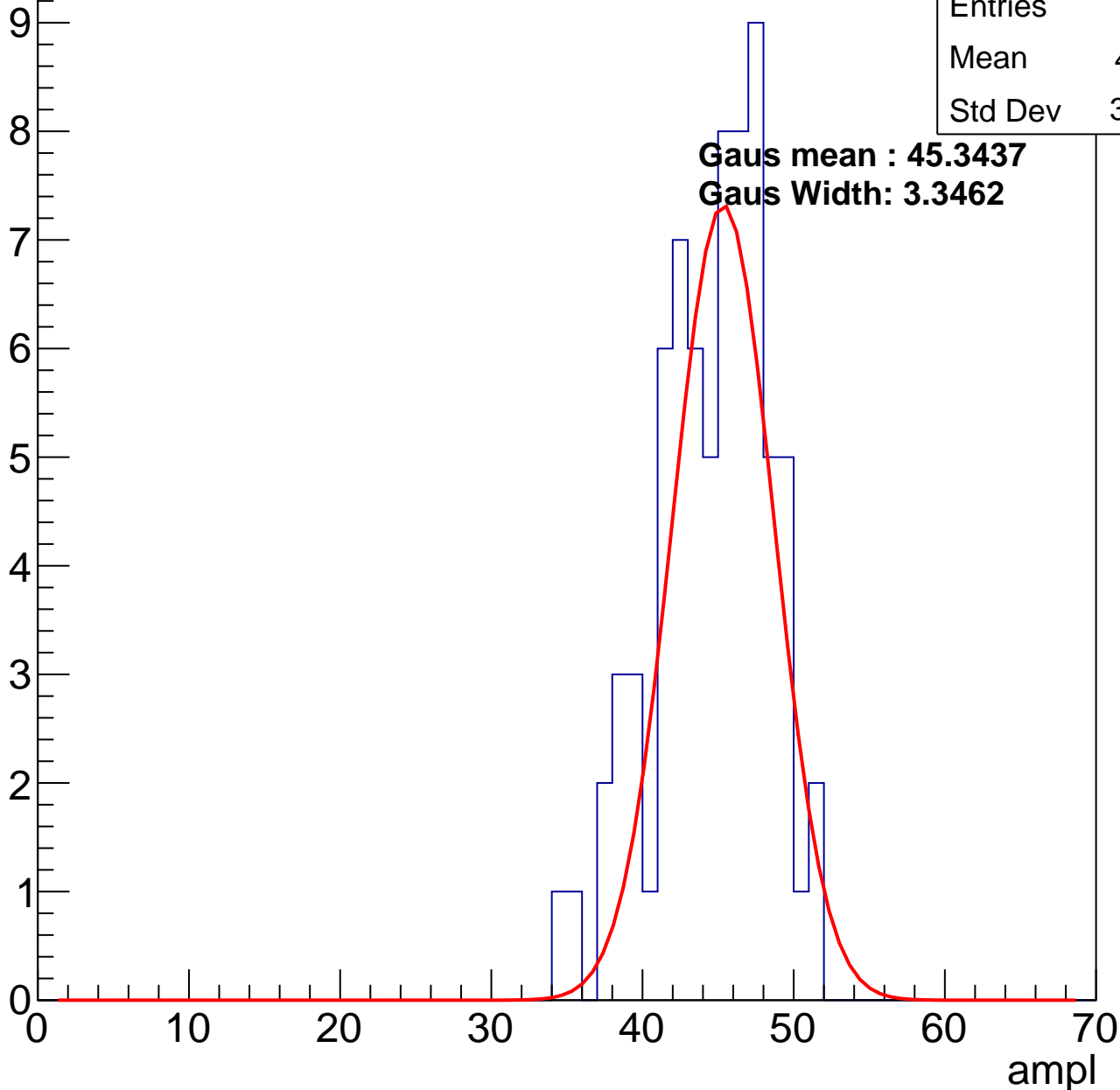
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.11
Std Dev	3.747

Gaus mean : 45.3437

Gaus Width: 3.3462

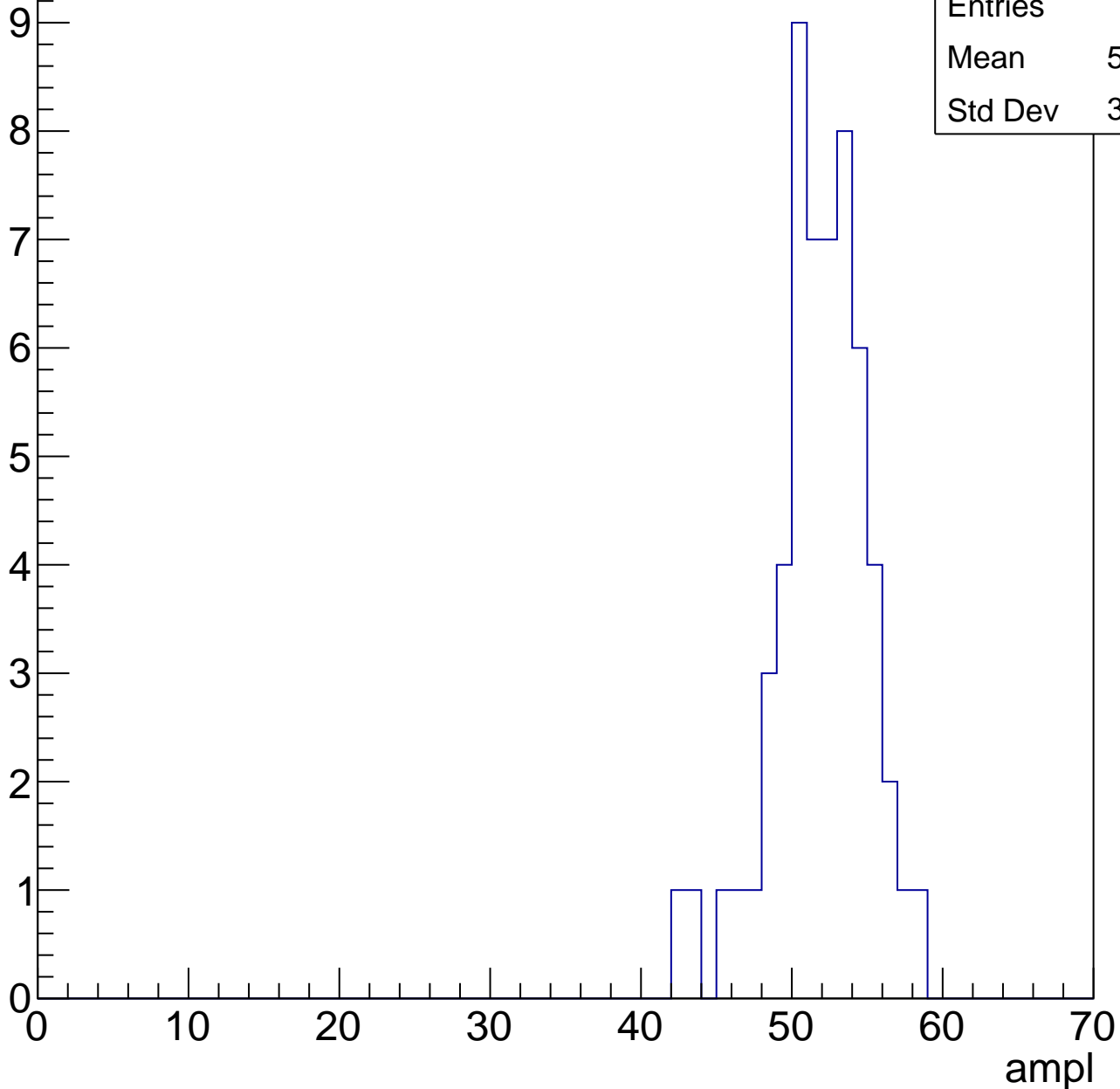


B1L101S, U26-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	51.39
Std Dev	3.127

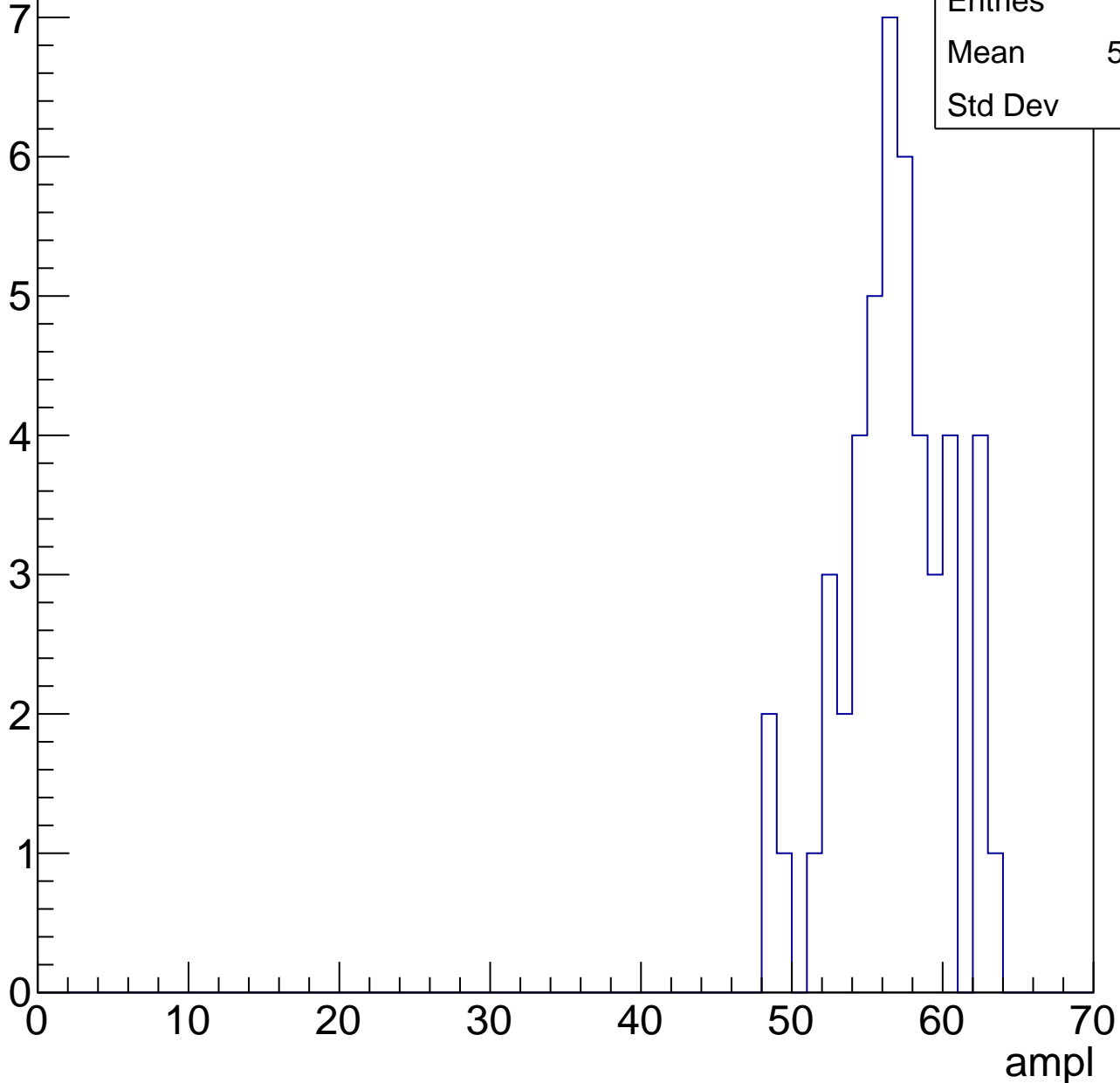


B1L101S, U26-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56.23
Std Dev	3.55

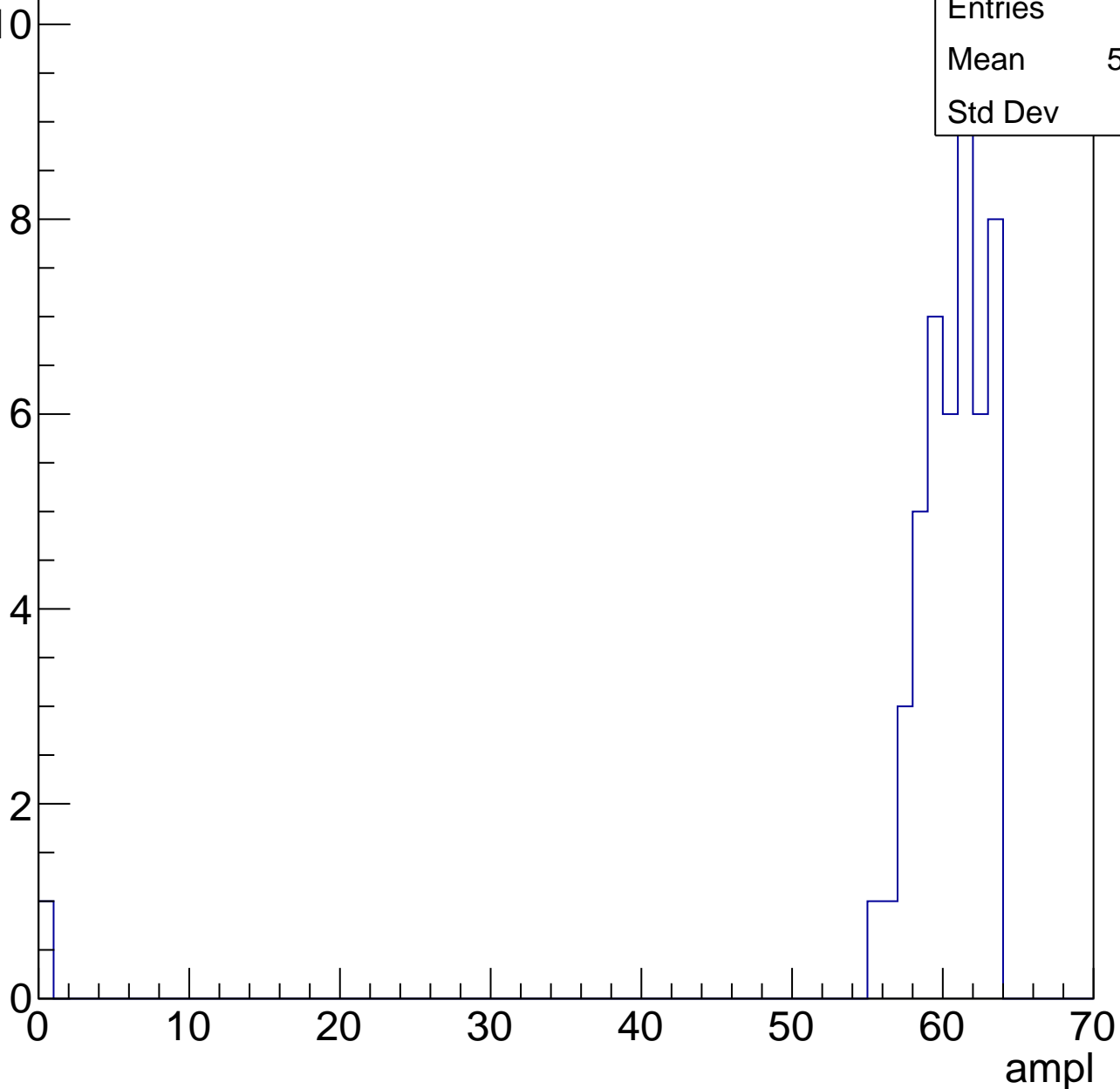


B1L101S, U26-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.98
Std Dev	8.84



B1L101S, U26-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch117, adc0

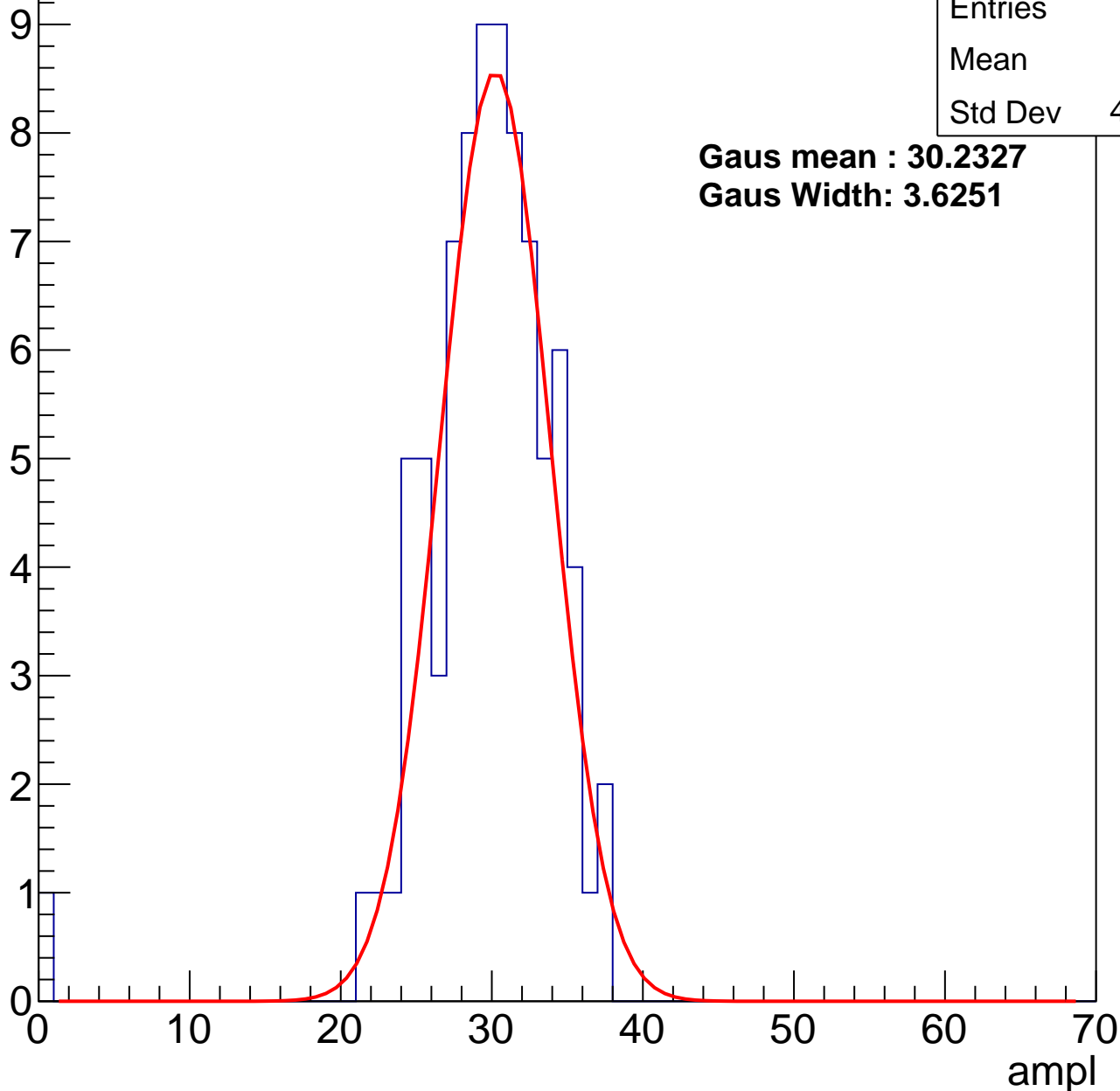
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	29.2
Std Dev	4.796

Gaus mean : 30.2327

Gaus Width: 3.6251



B1L101S, U26-ch117, adc1

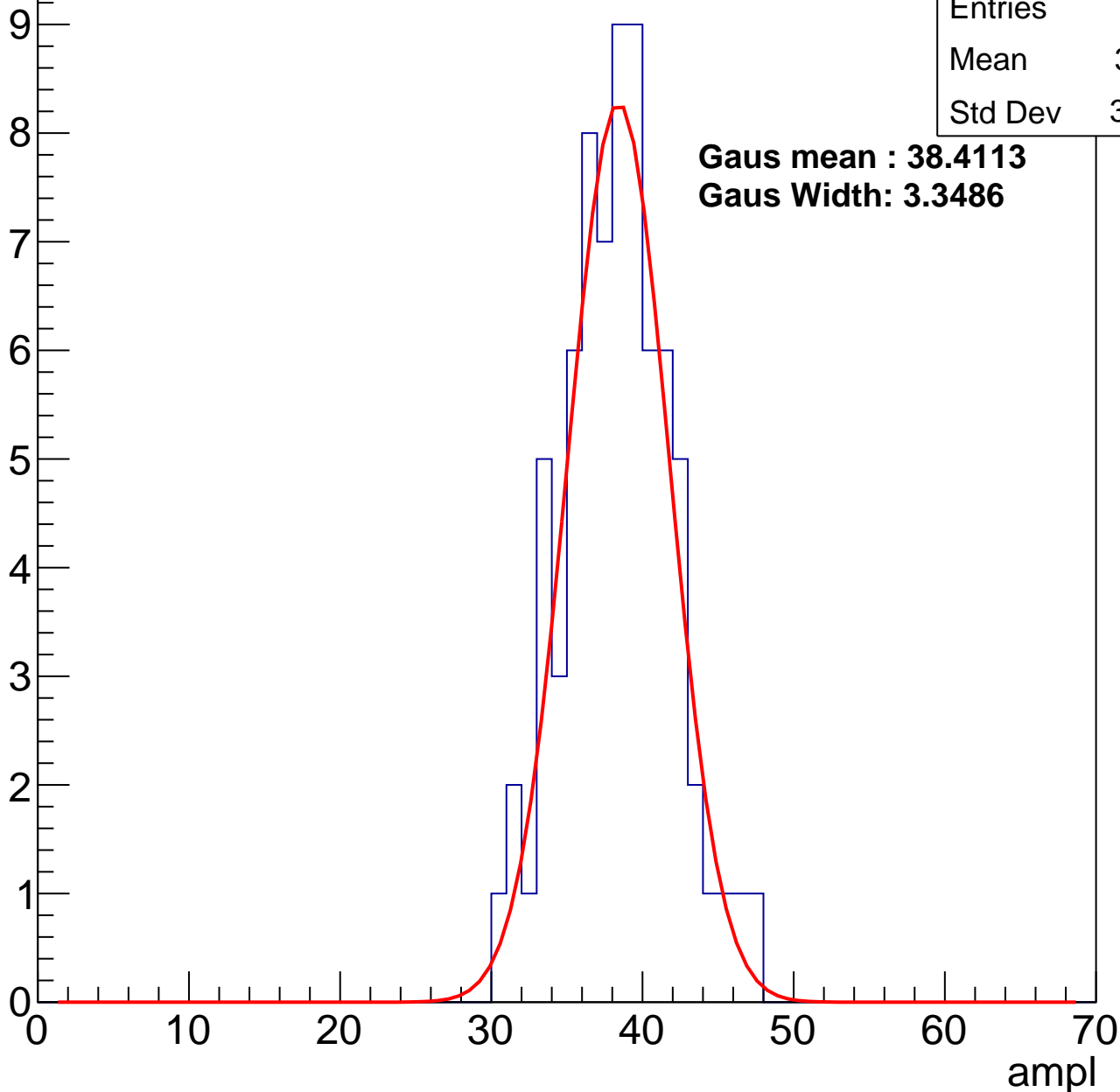
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.91
Std Dev	3.507

Gaus mean : 38.4113

Gaus Width: 3.3486



B1L101S, U26-ch117, adc2

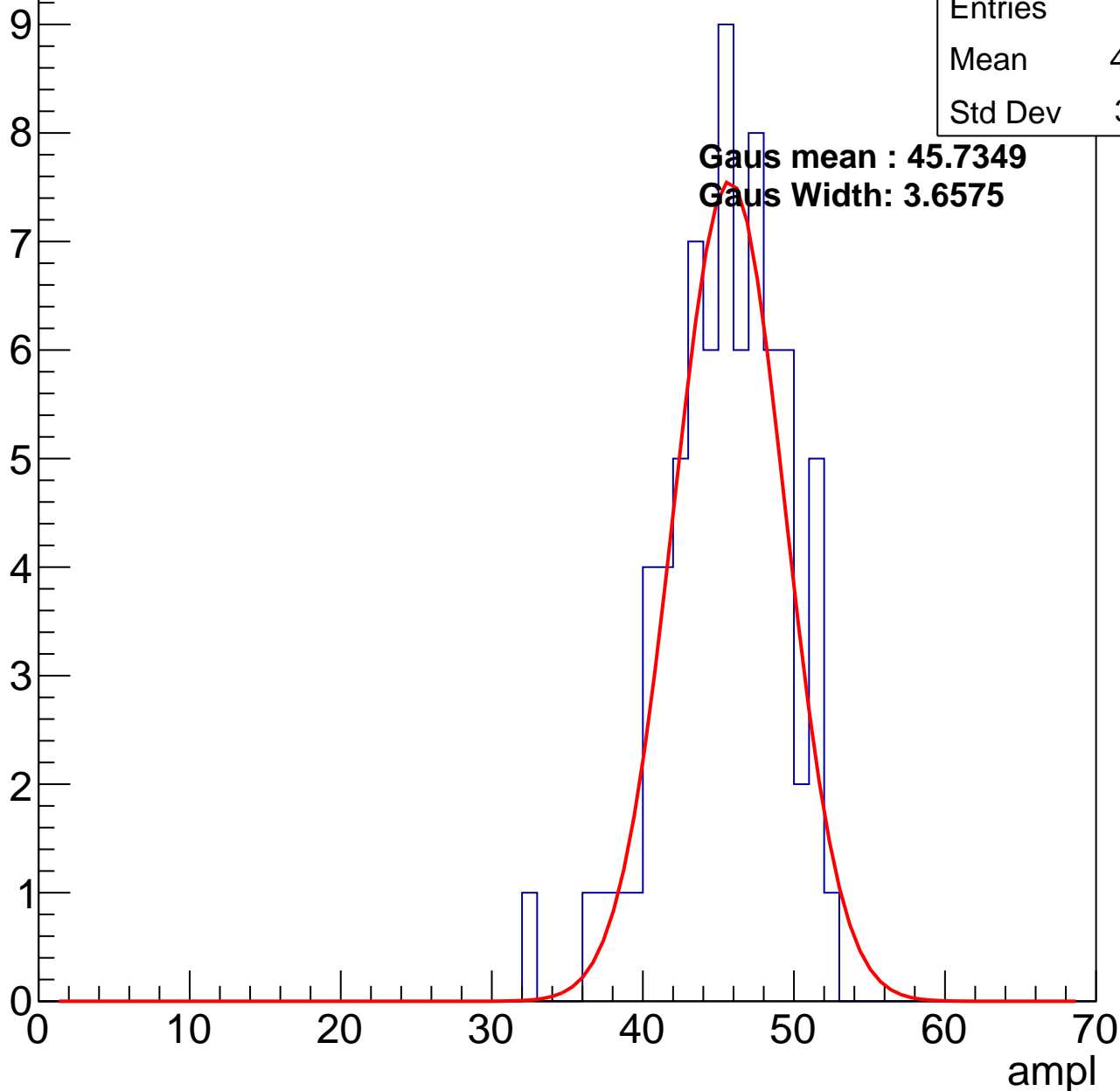
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.96
Std Dev	3.871

Gaus mean : 45.7349

Gaus Width: 3.6575

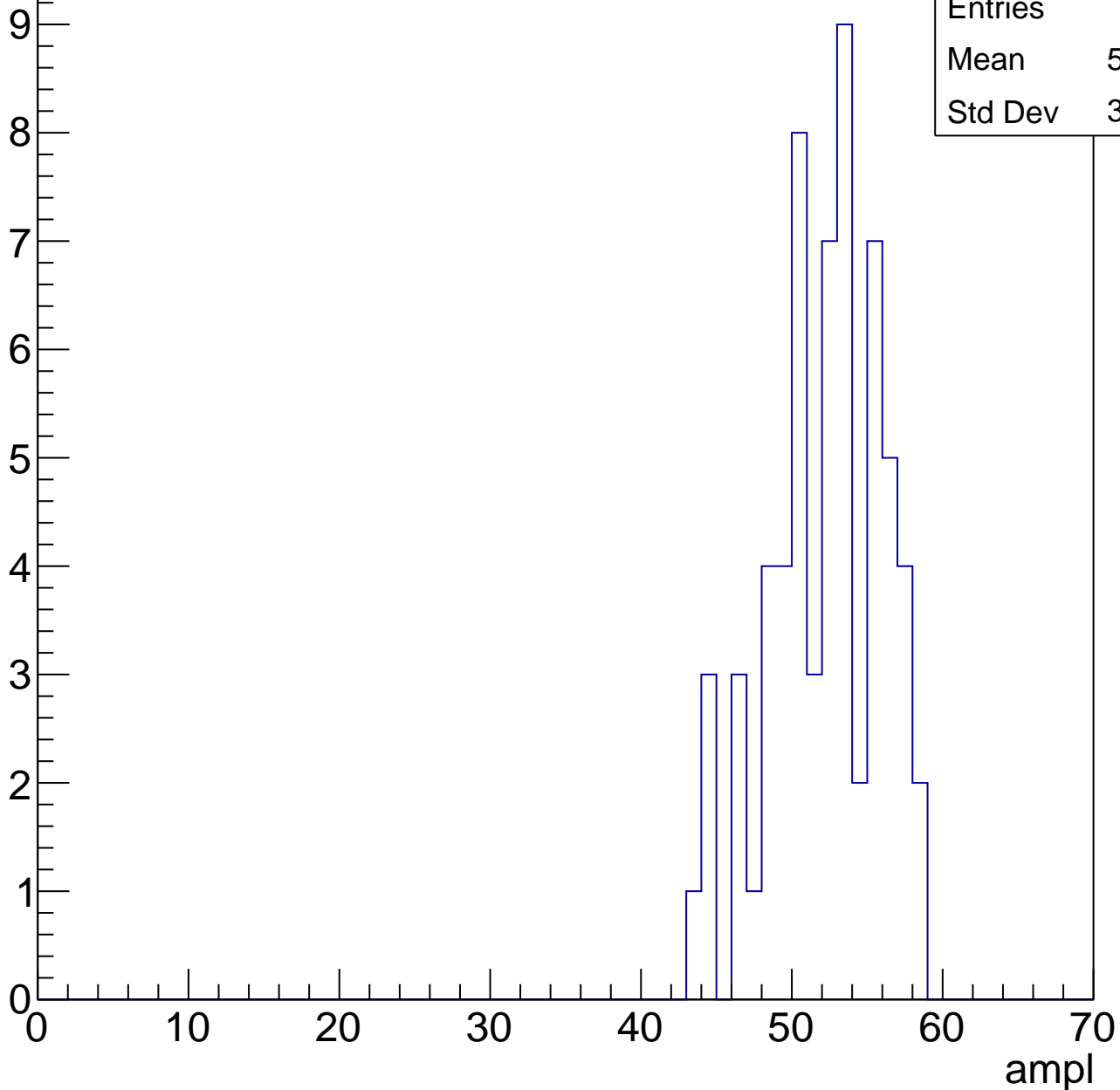


B1L101S, U26-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	51.73
Std Dev	3.713

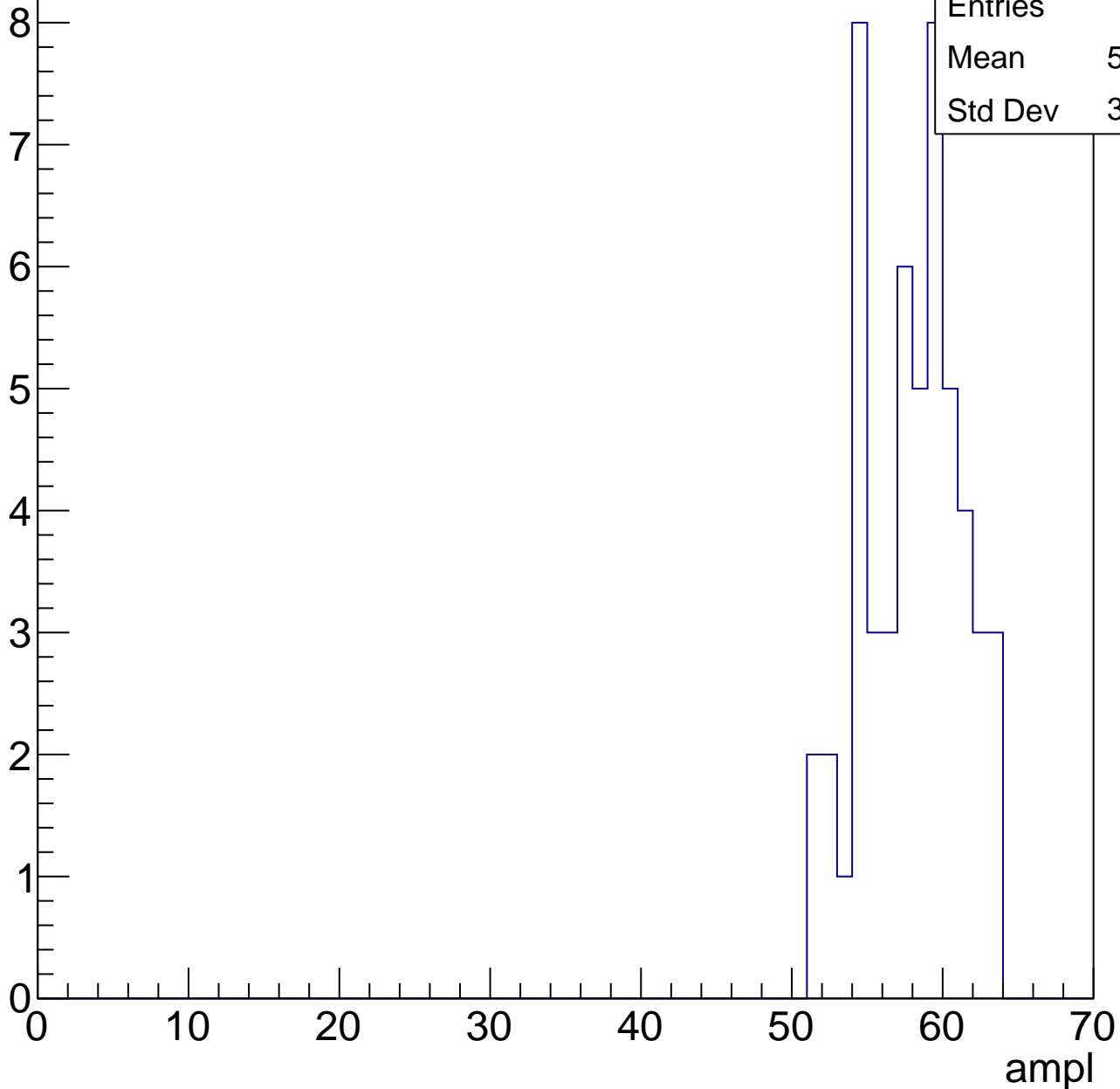


B1L101S, U26-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.49
Std Dev	3.196

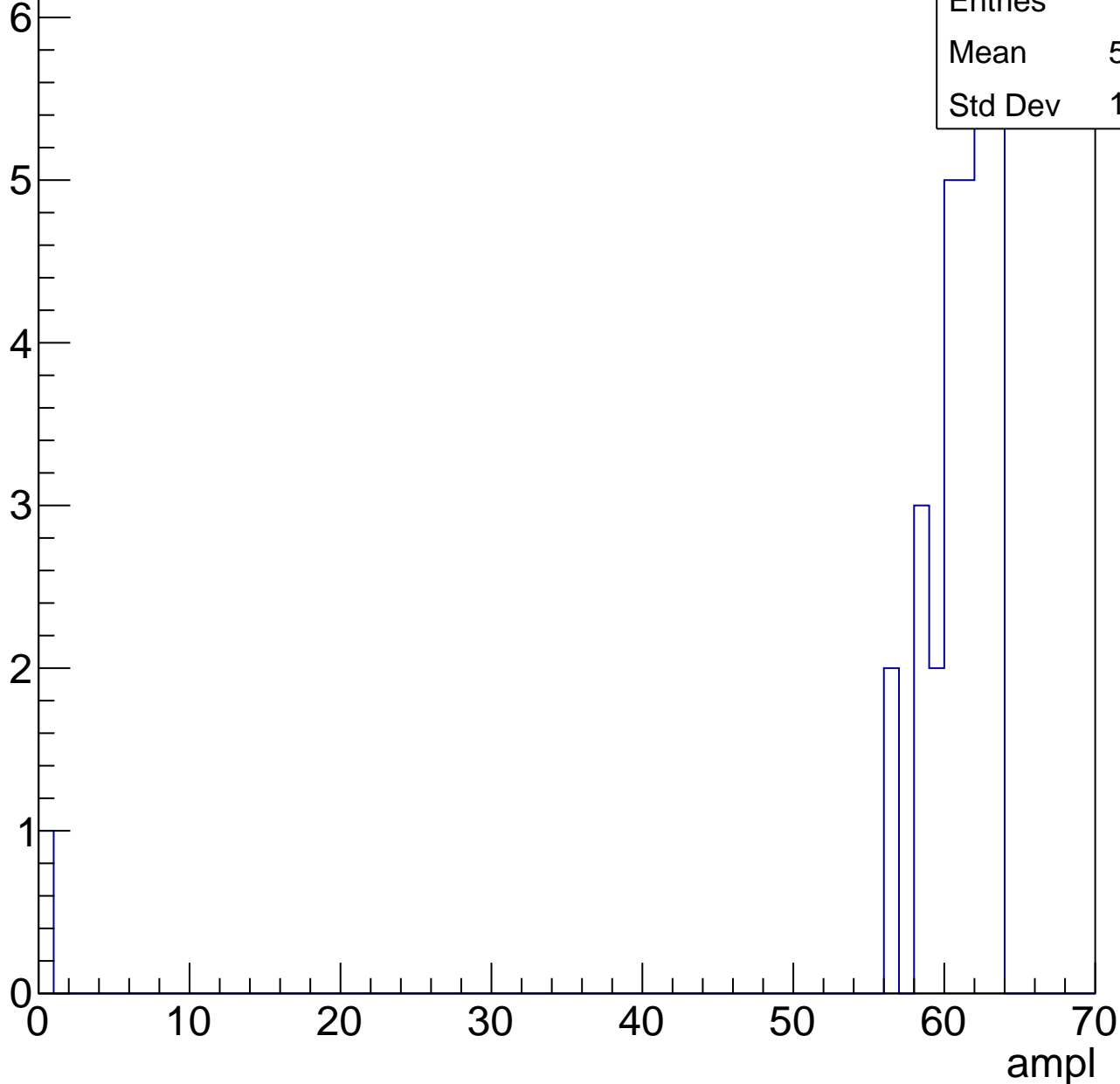


B1L101S, U26-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

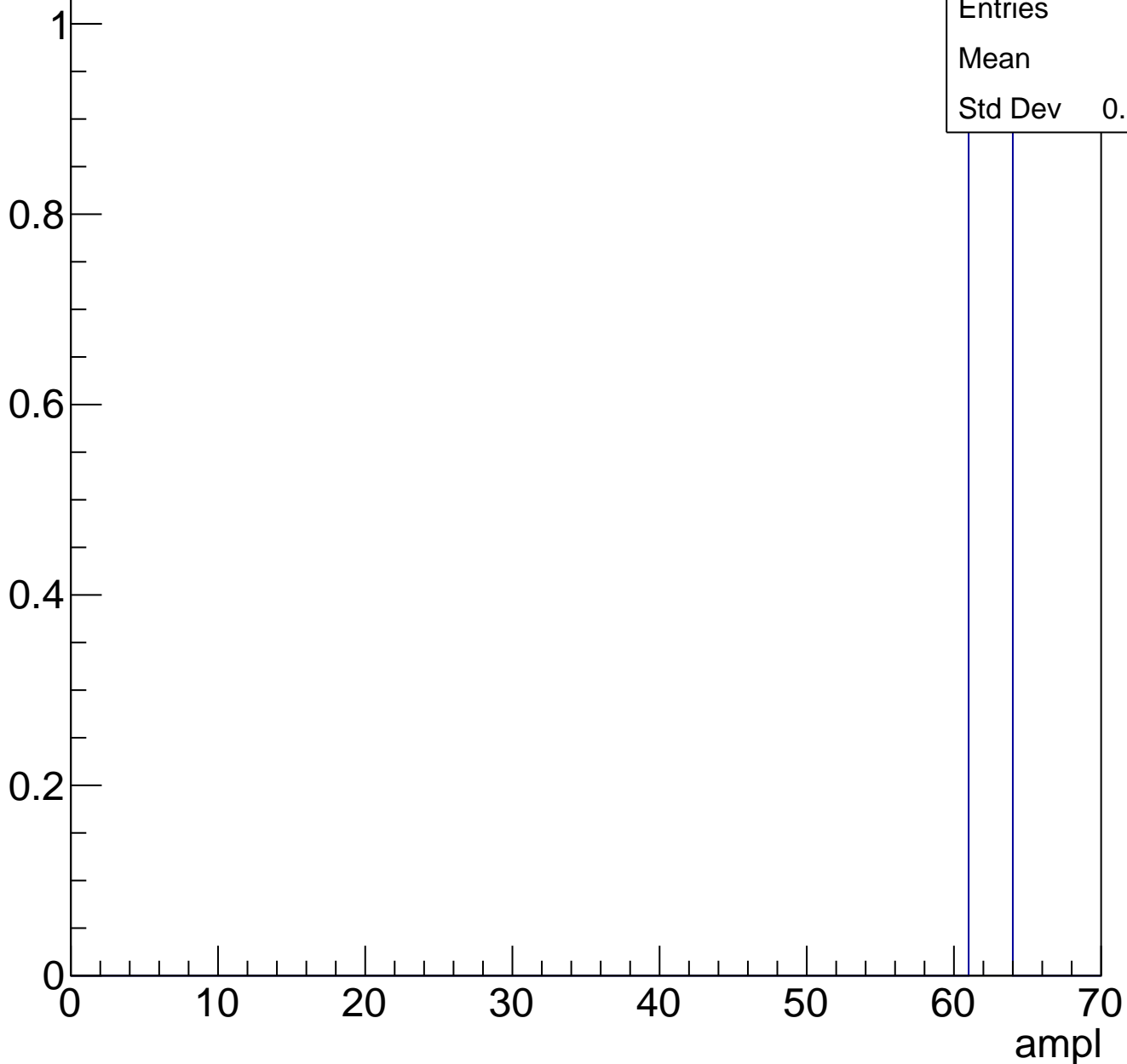
Entries	30
Mean	58.63
Std Dev	11.06



B1L101S, U26-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch118, adc0

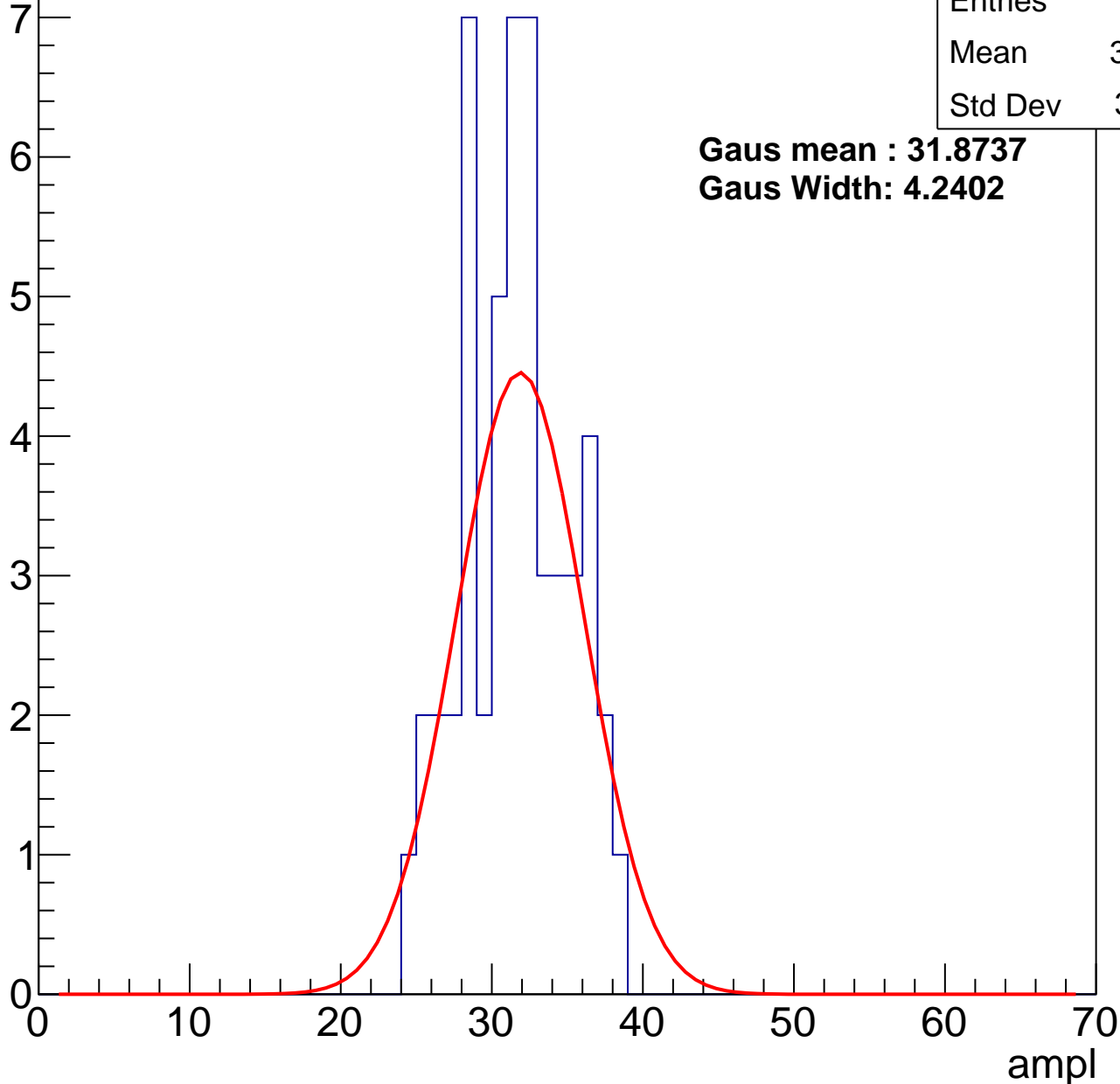
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	31.12
Std Dev	3.411

Gaus mean : 31.8737

Gaus Width: 4.2402



B1L101S, U26-ch118, adc1

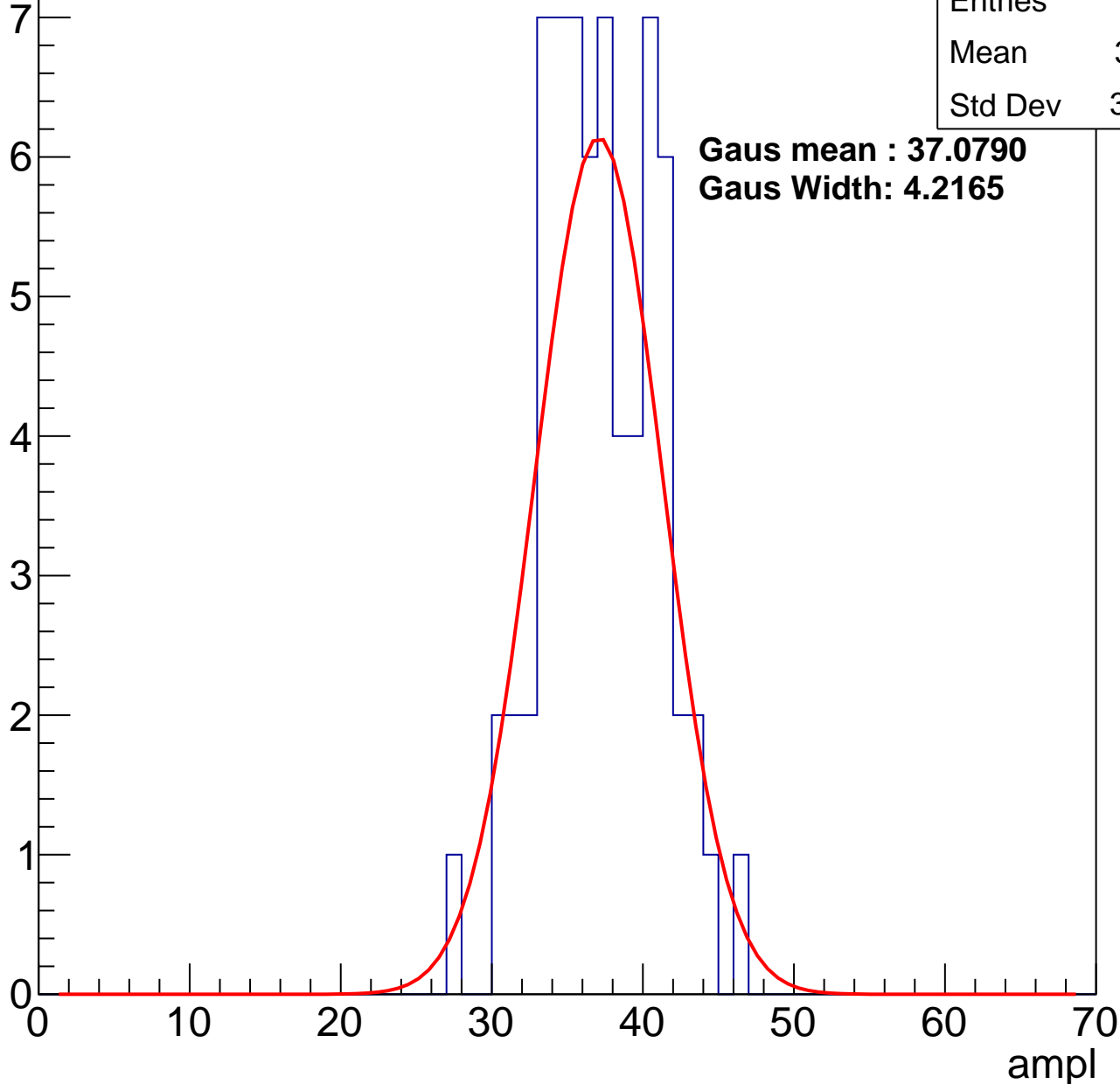
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.71
Std Dev	3.746

Gaus mean : 37.0790

Gaus Width: 4.2165



B1L101S, U26-ch118, adc2

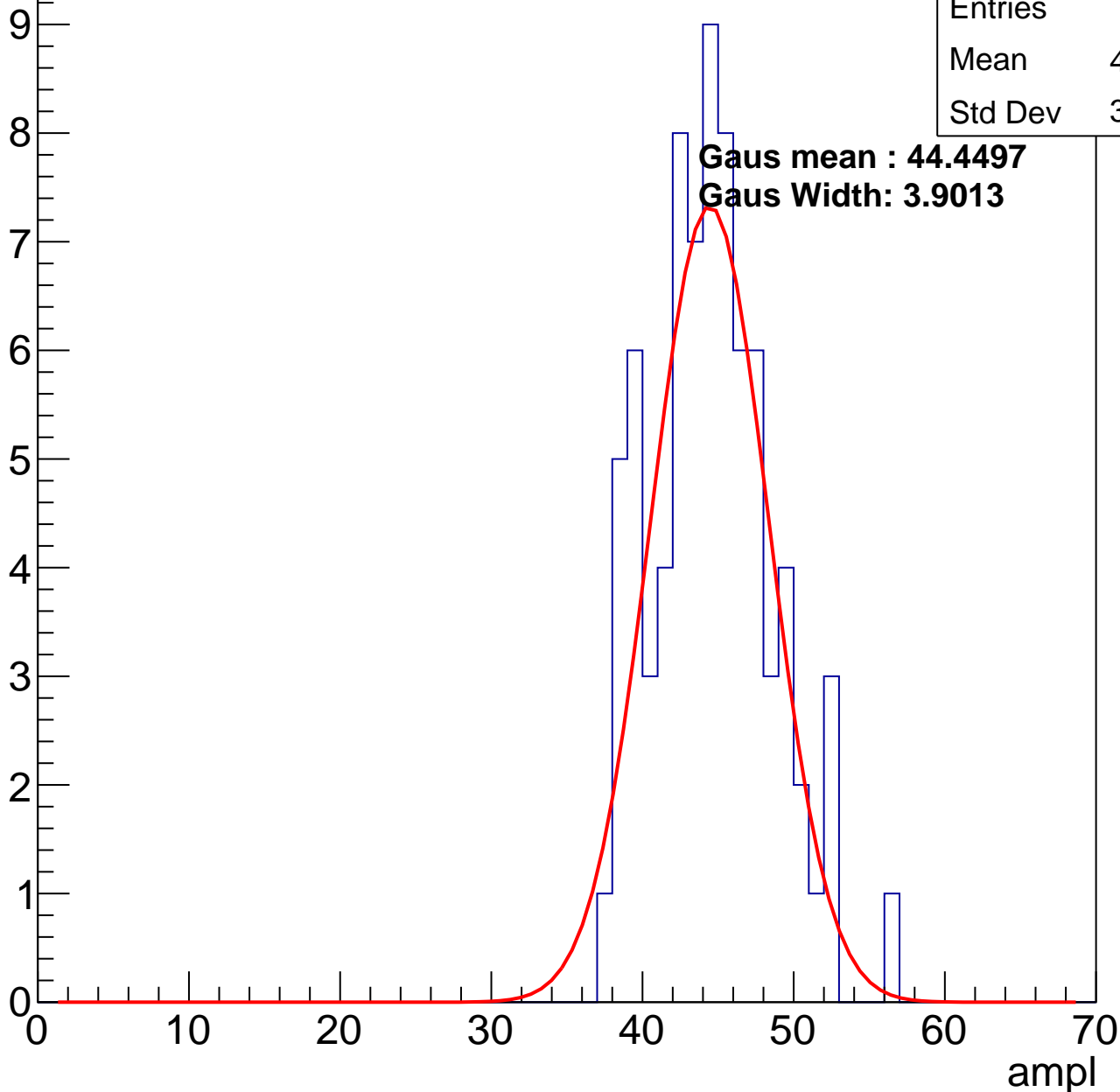
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44.14
Std Dev	3.934

Gaus mean : 44.4497

Gaus Width: 3.9013

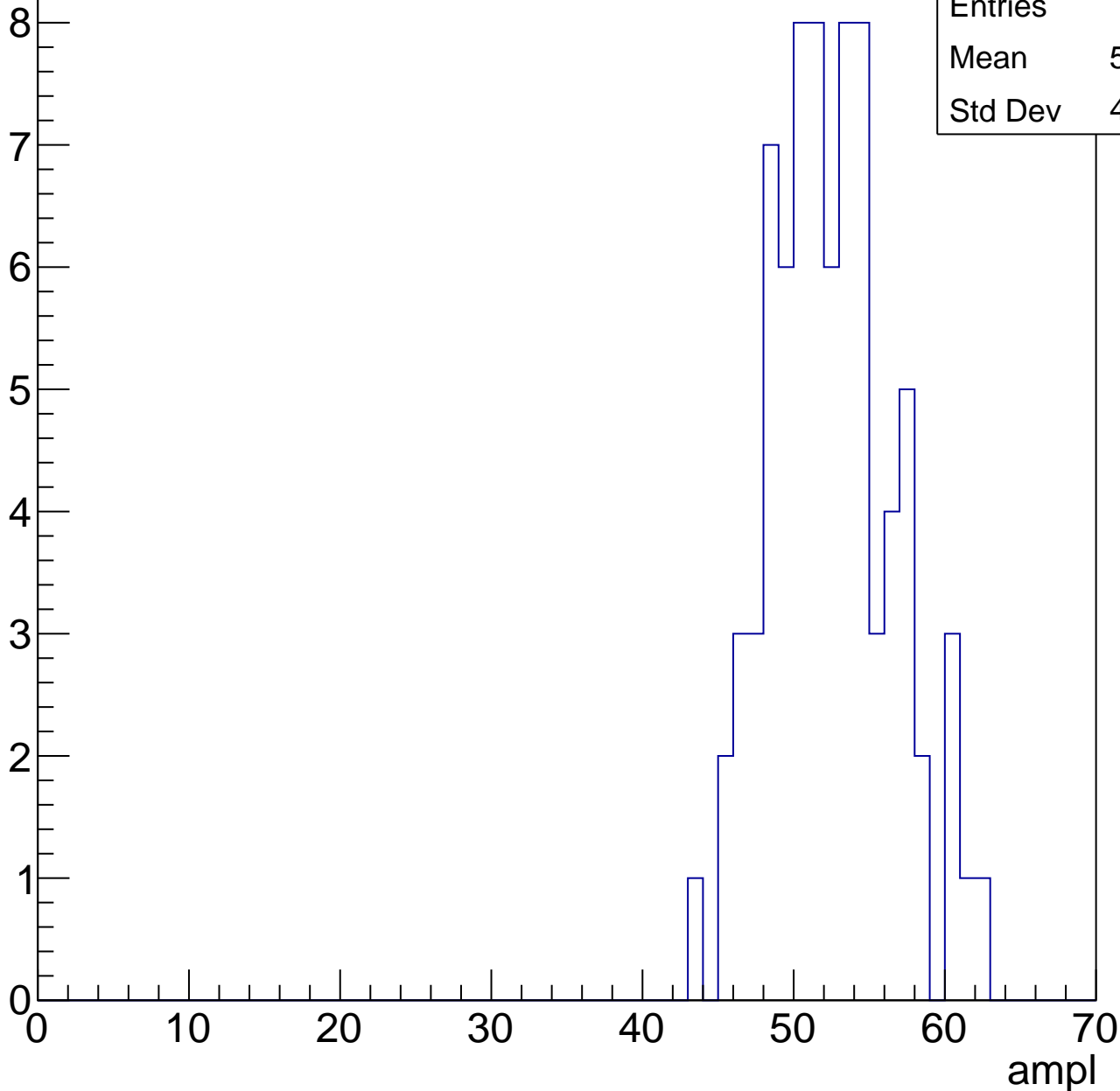


B1L101S, U26-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	52.04
Std Dev	4.008

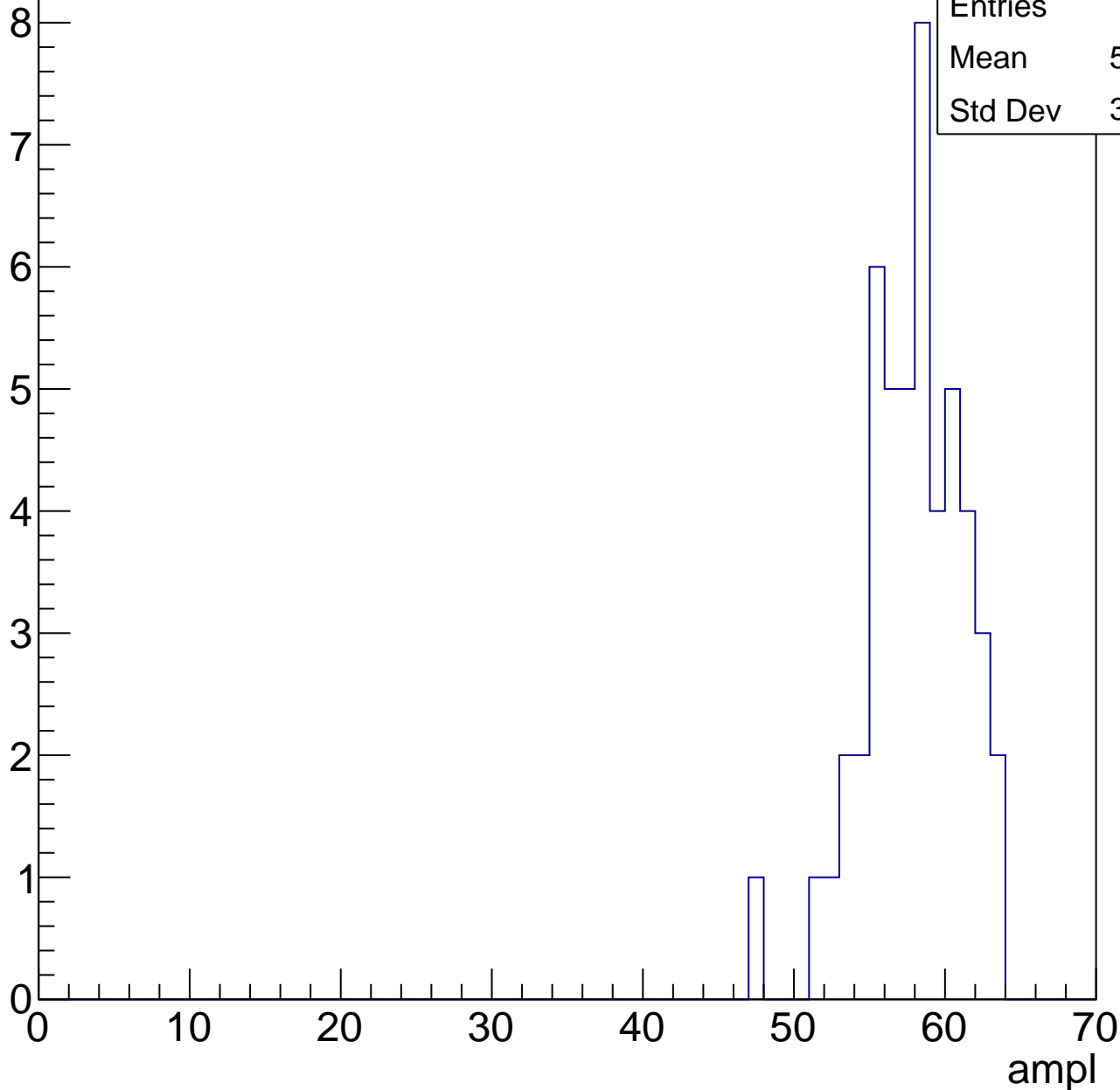


B1L101S, U26-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

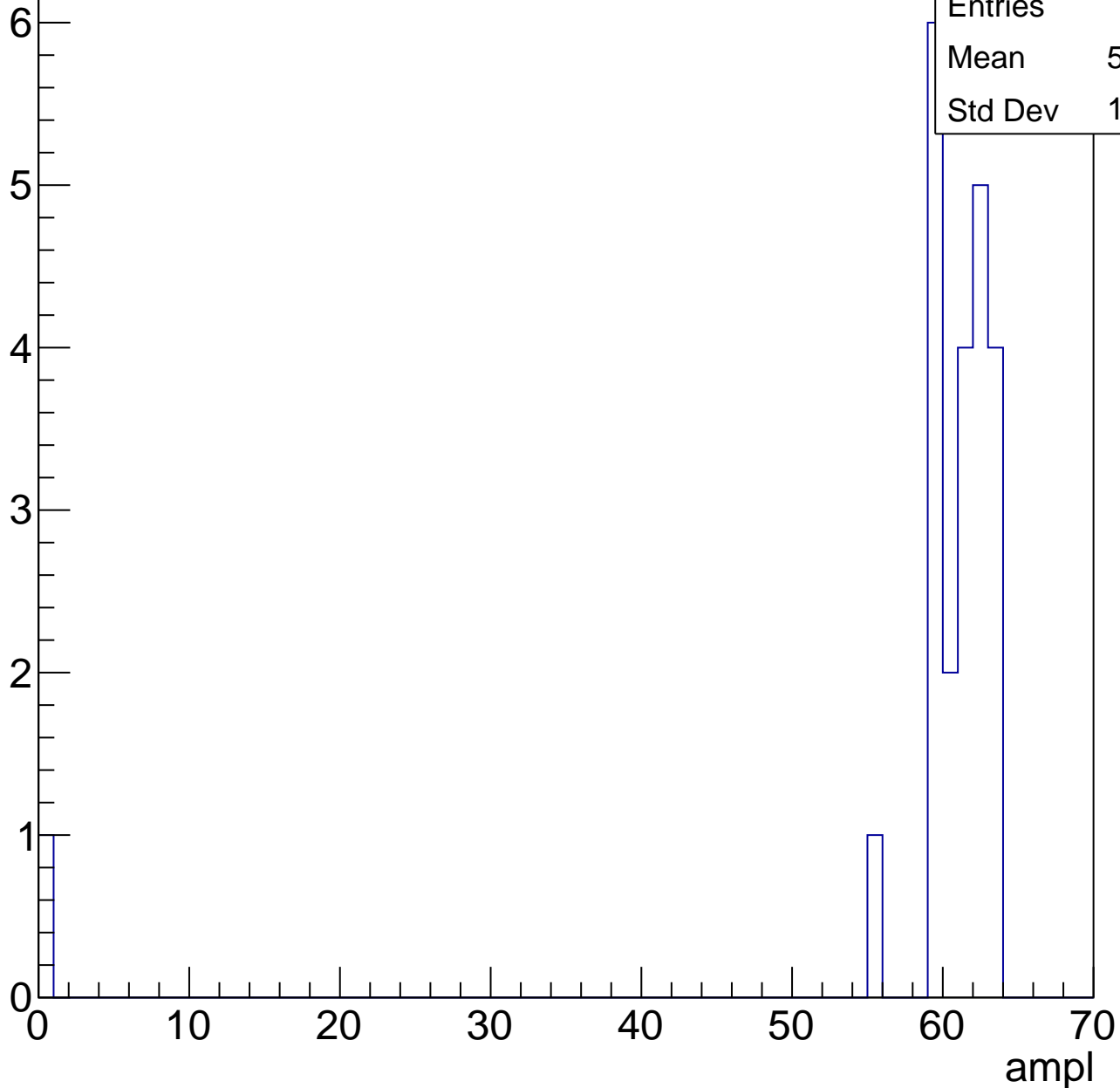
Entries	49
Mean	57.45
Std Dev	3.227



B1L101S, U26-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	62.5
Std Dev	0.7638

B1L101S, U26-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch119, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	31.3
Std Dev	3.984

Gaus mean : 31.7459

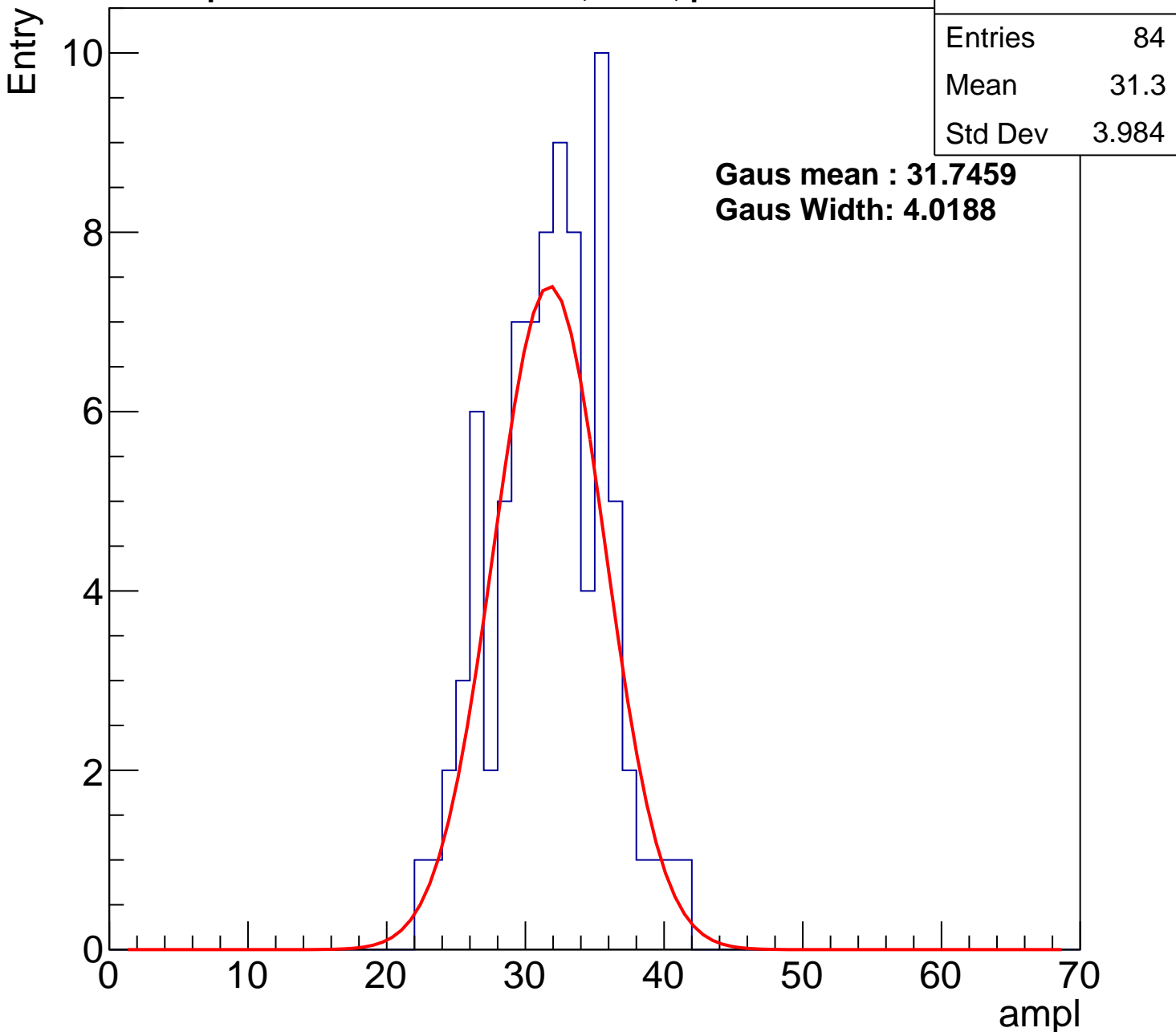
Gaus Width: 4.0188

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch119, adc1

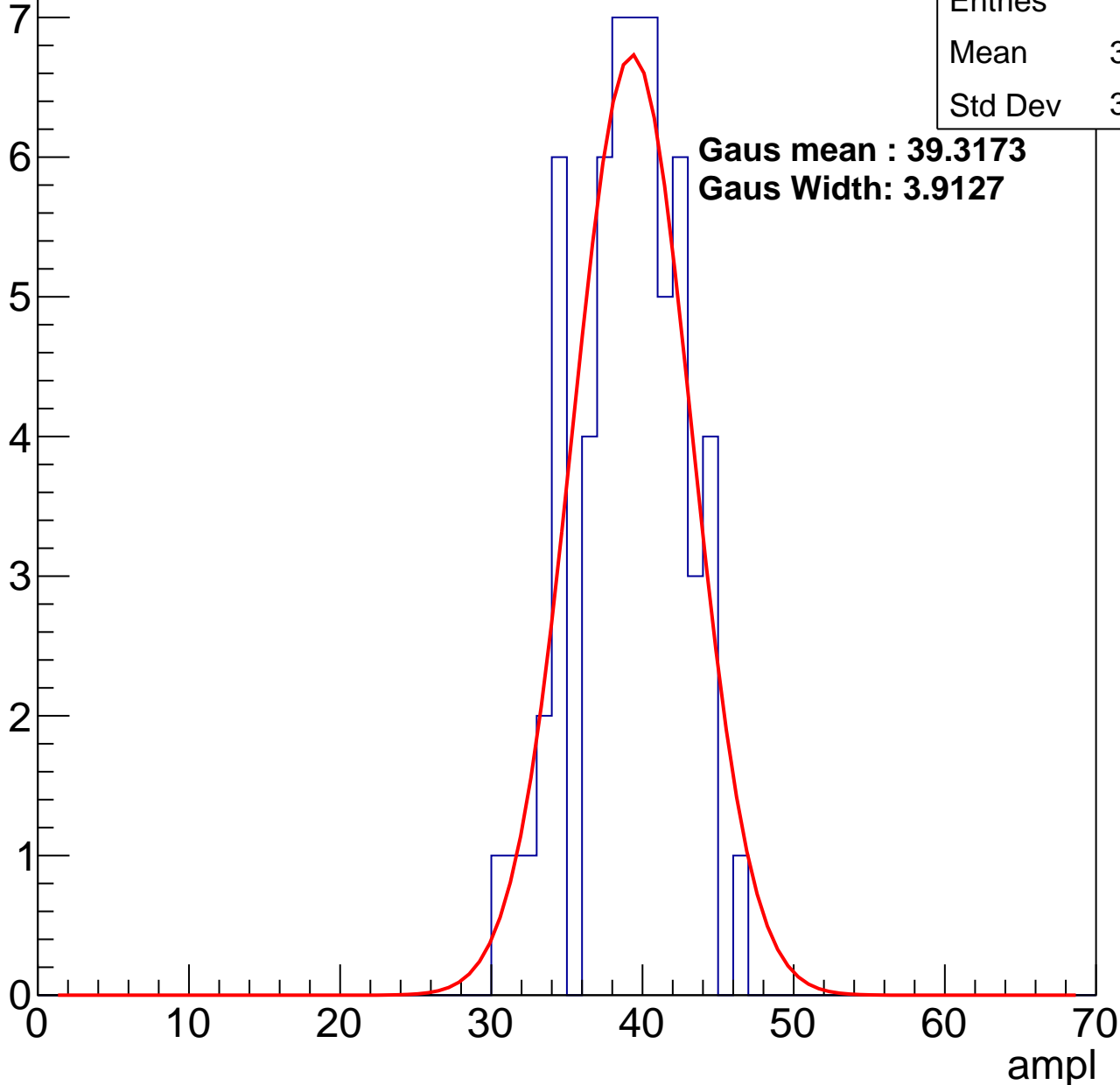
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	38.62
Std Dev	3.512

Gaus mean : 39.3173

Gaus Width: 3.9127



B1L101S, U26-ch119, adc2

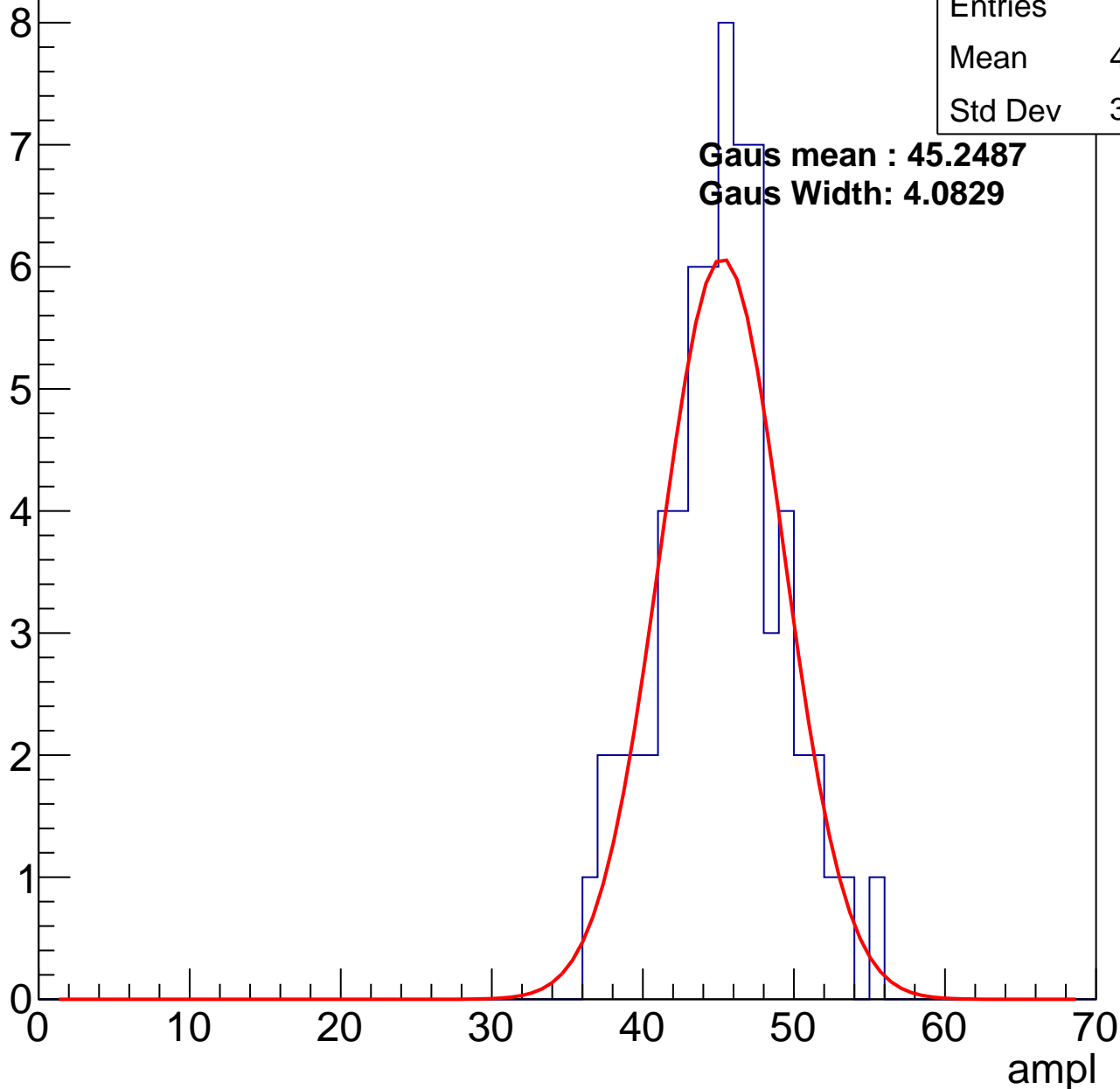
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.78
Std Dev	3.955

Gaus mean : 45.2487

Gaus Width: 4.0829

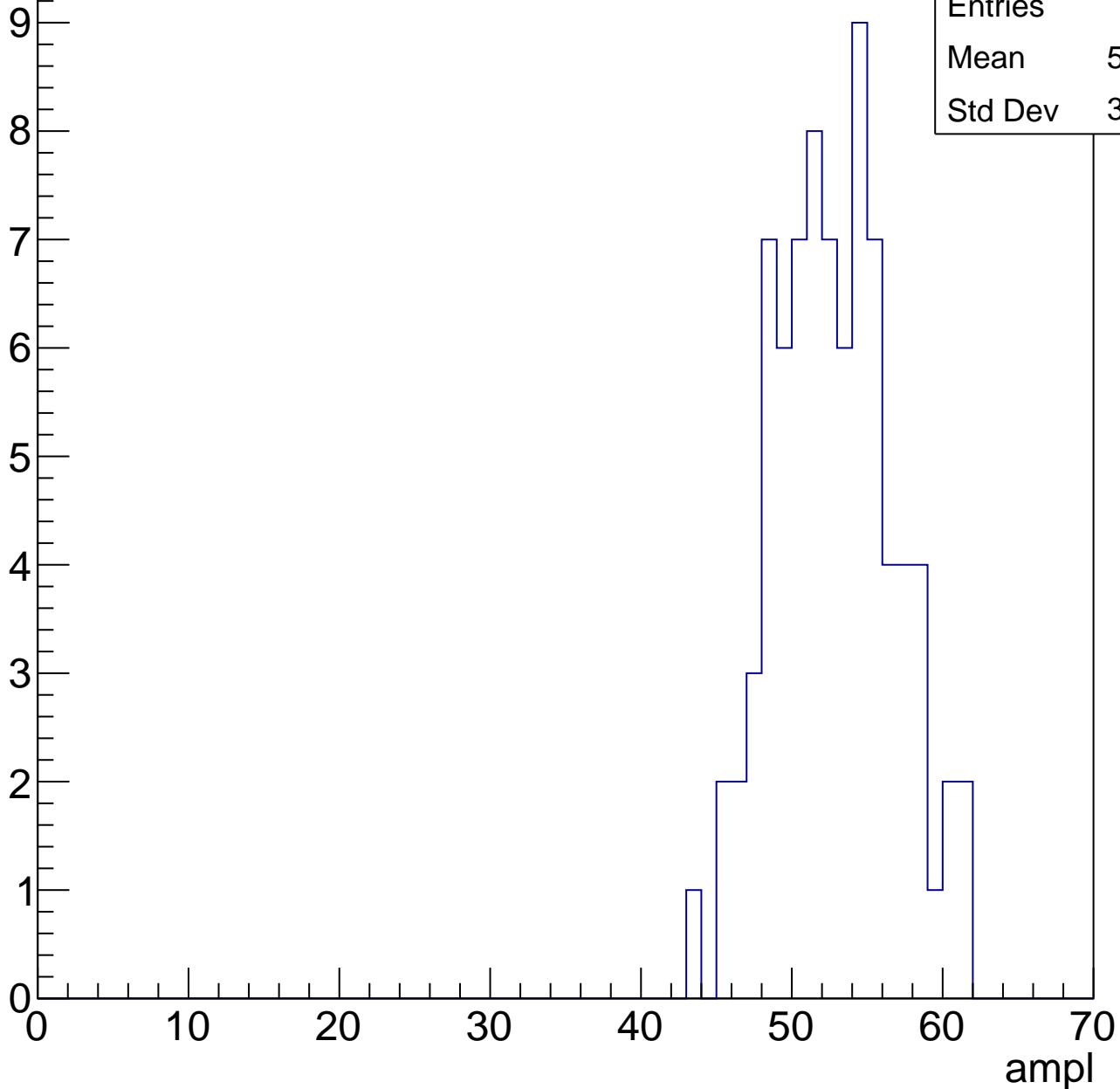


B1L101S, U26-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	52.34
Std Dev	3.936

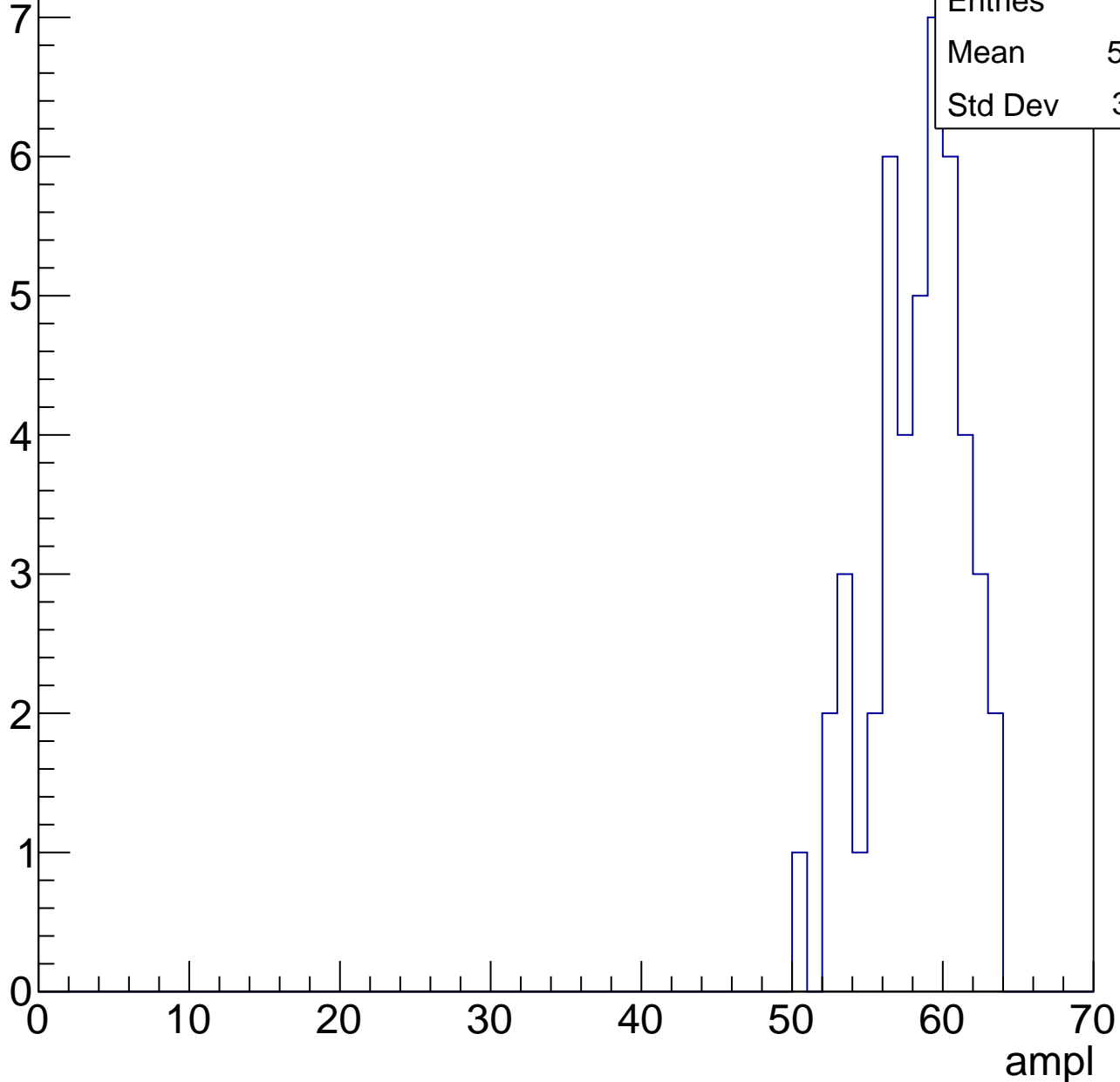


B1L101S, U26-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.83
Std Dev	3.081

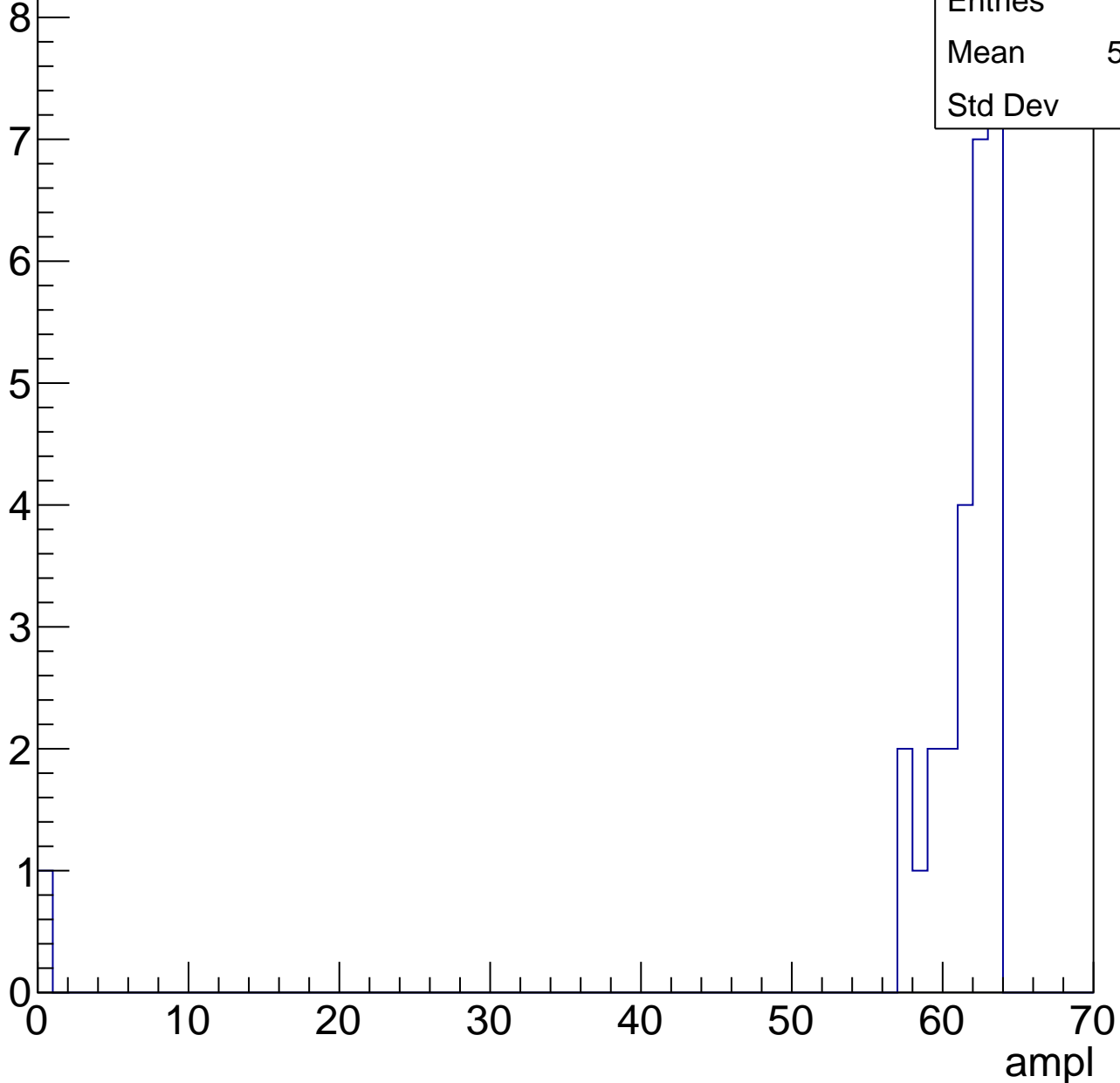


B1L101S, U26-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.96
Std Dev	11.7



B1L101S, U26-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

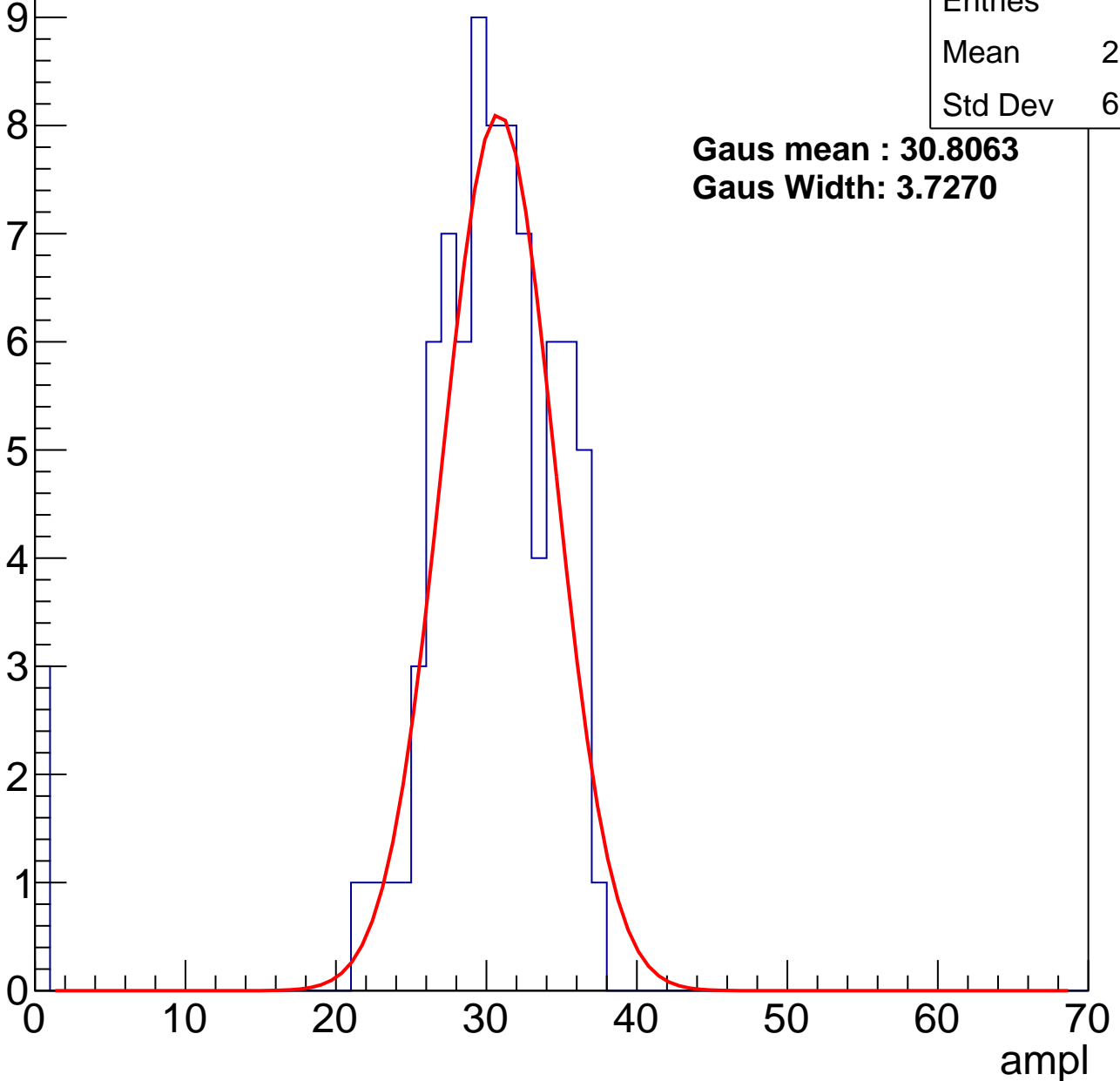
B1L101S, U26-ch120, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	29.08
Std Dev	6.654

Gaus mean : 30.8063
Gaus Width: 3.7270



B1L101S, U26-ch120, adc1

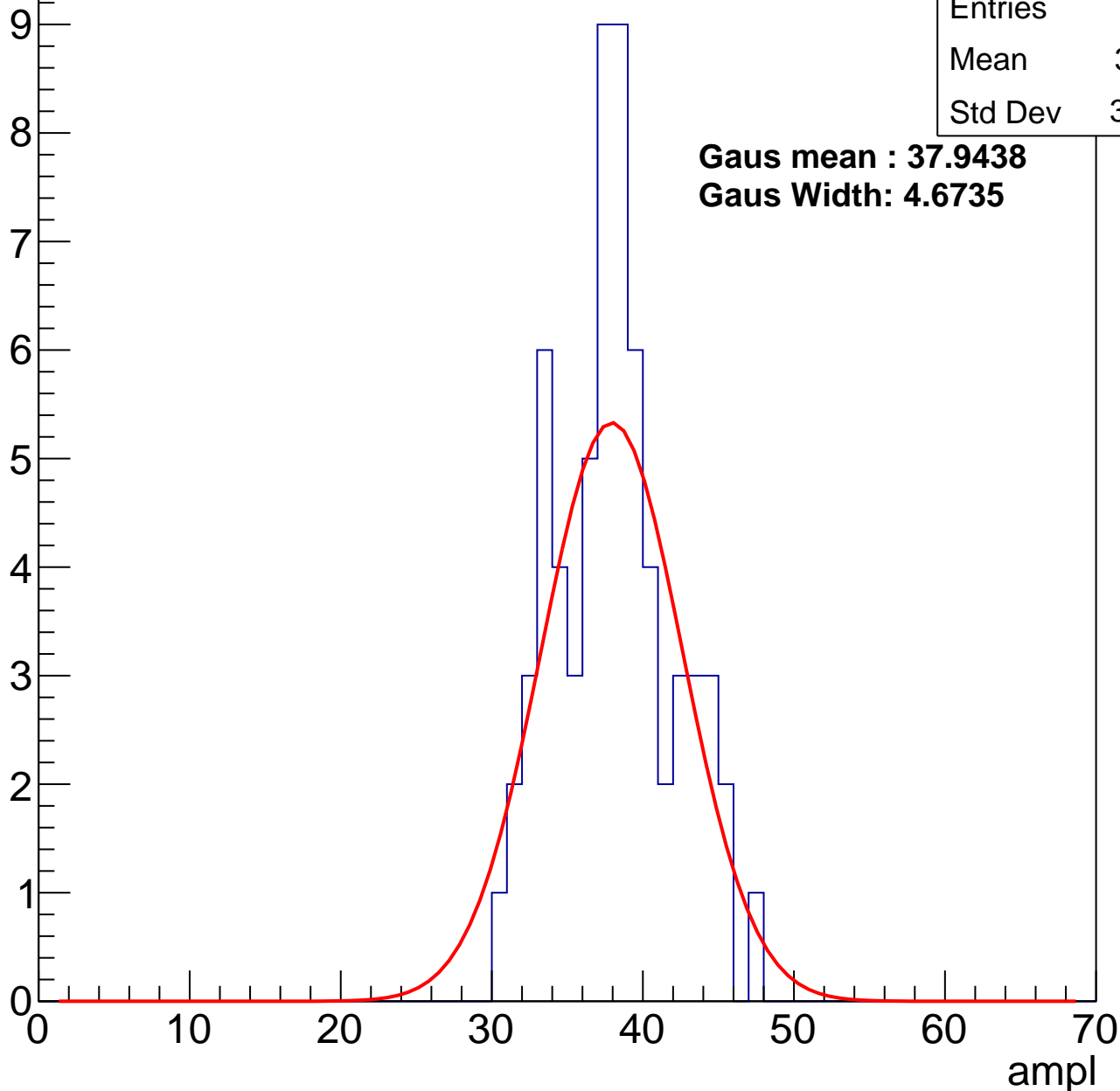
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.61
Std Dev	3.853

Gaus mean : 37.9438

Gaus Width: 4.6735

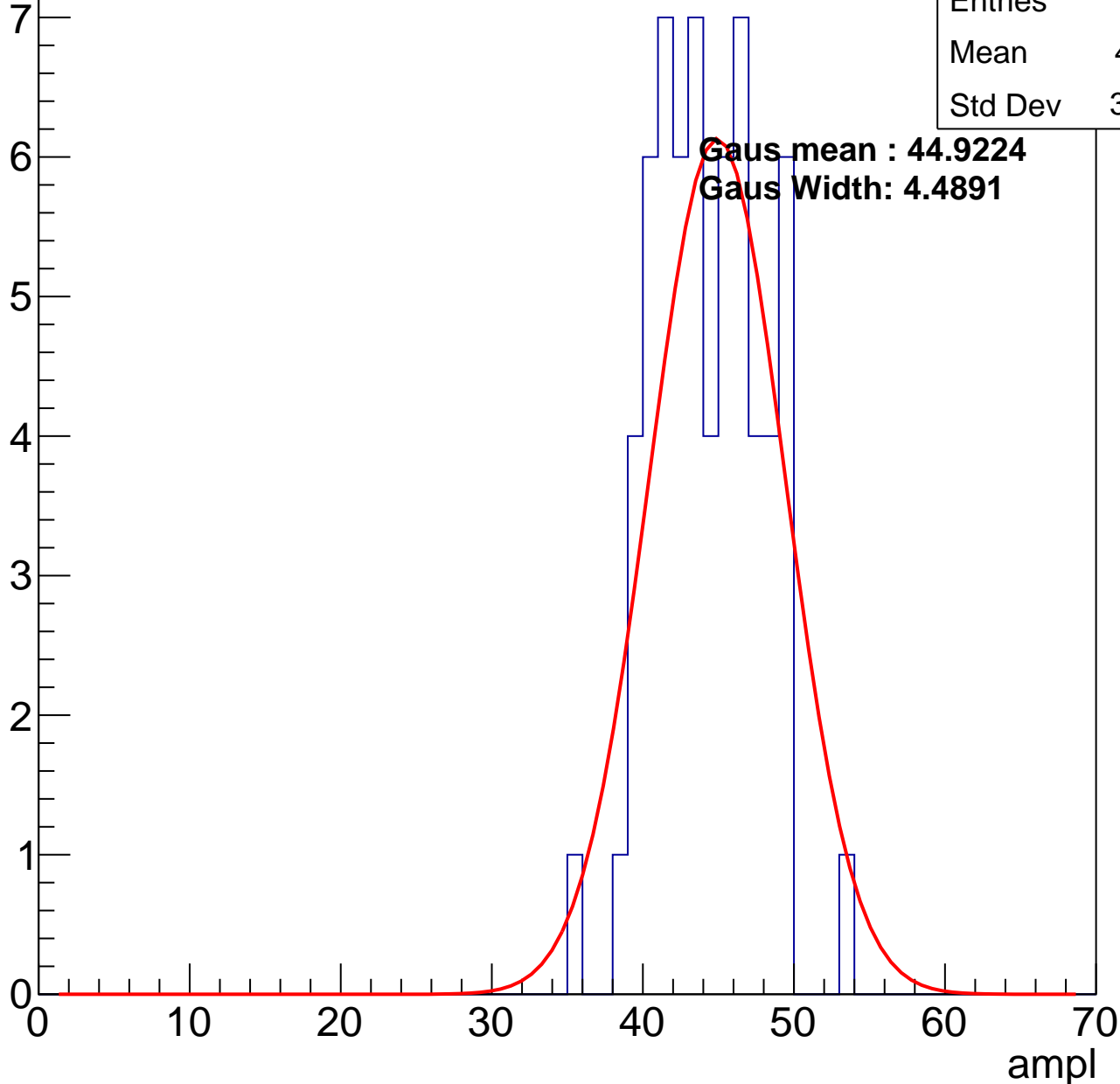


B1L101S, U26-ch120, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.81
Std Dev	3.468



B1L101S, U26-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	50.77
Std Dev	3.849

Entry

10

8

6

4

2

0

0

10

20

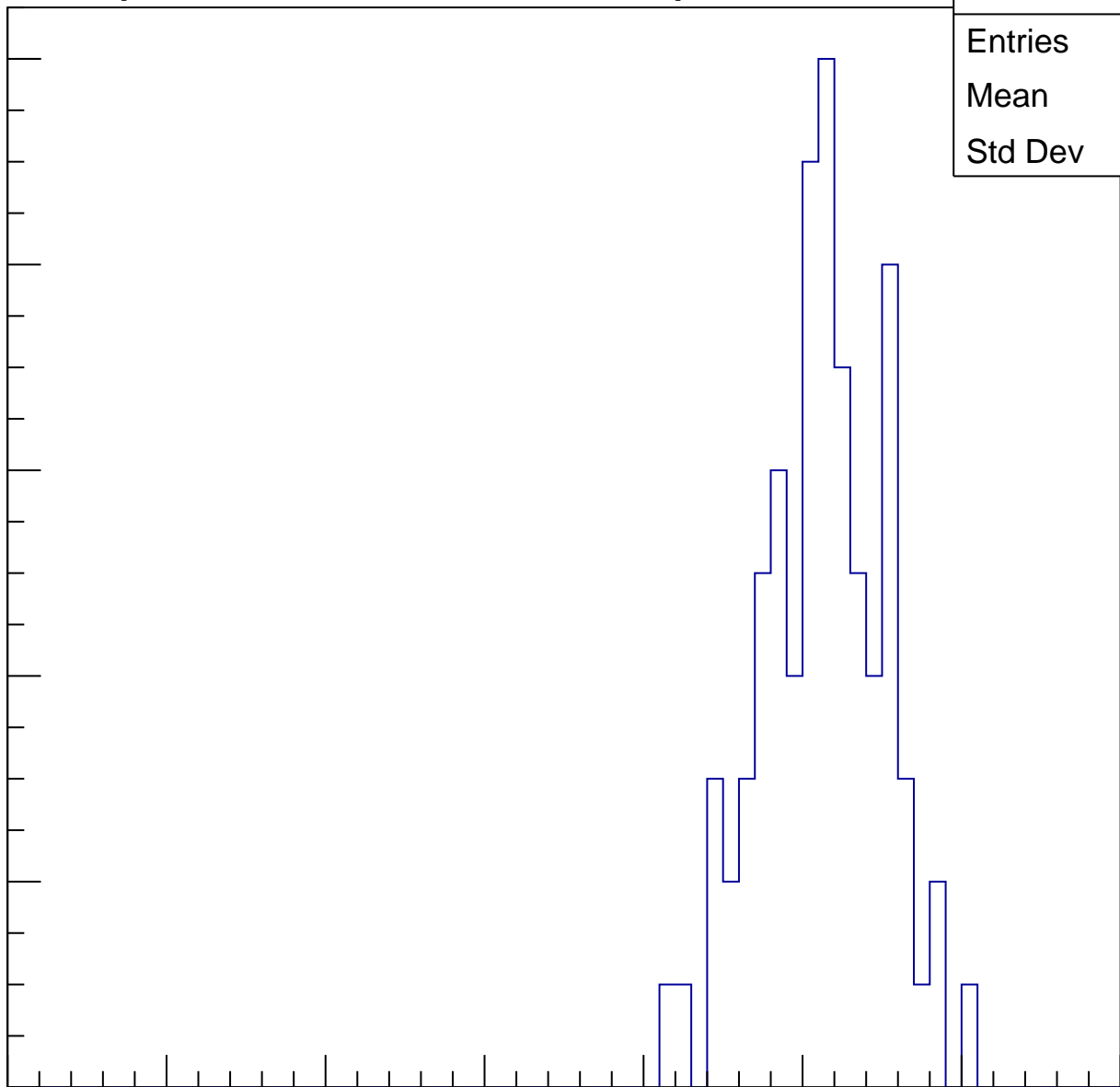
30

40

50

60

ampl

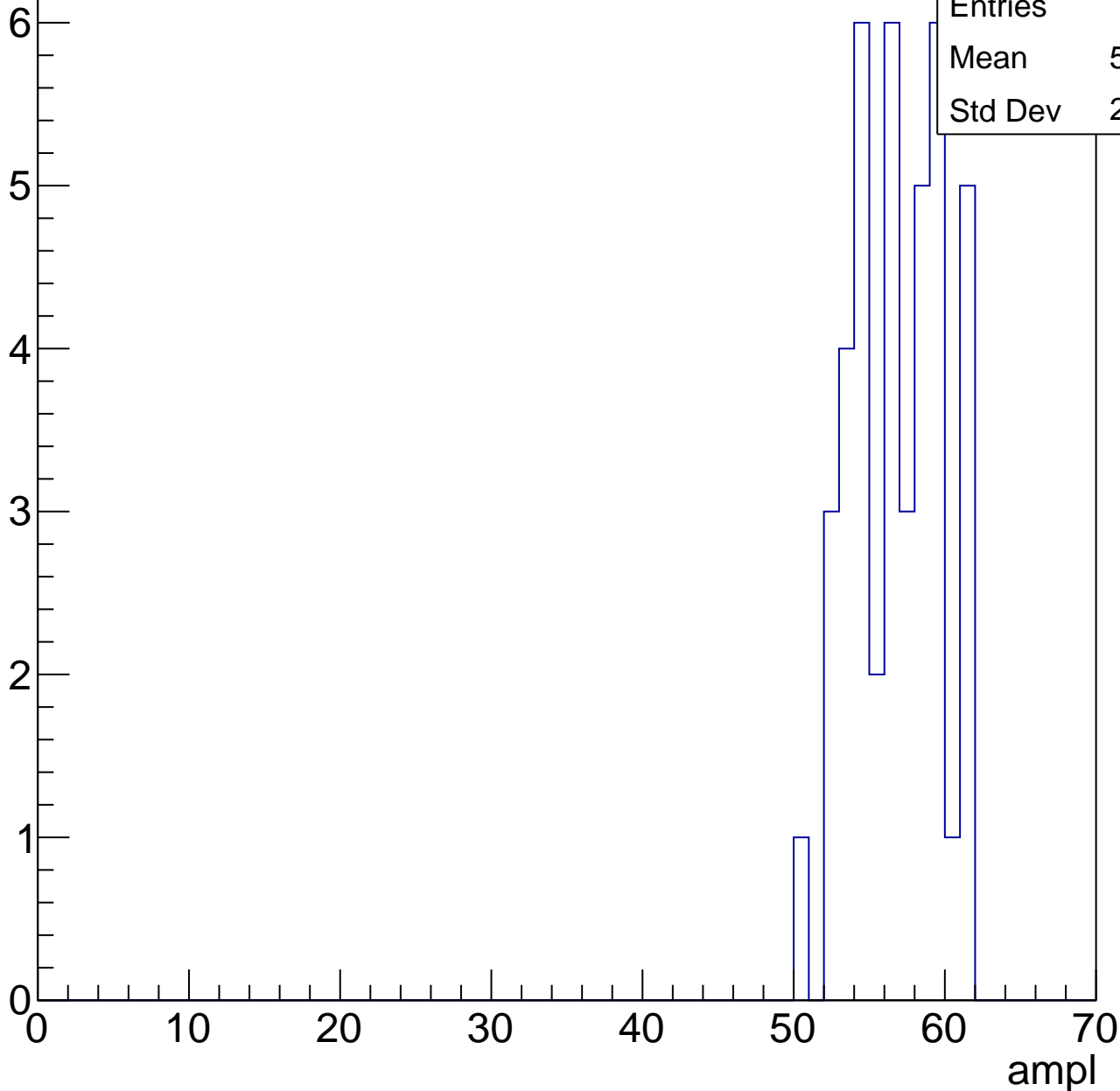


B1L101S, U26-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	56.38
Std Dev	2.919

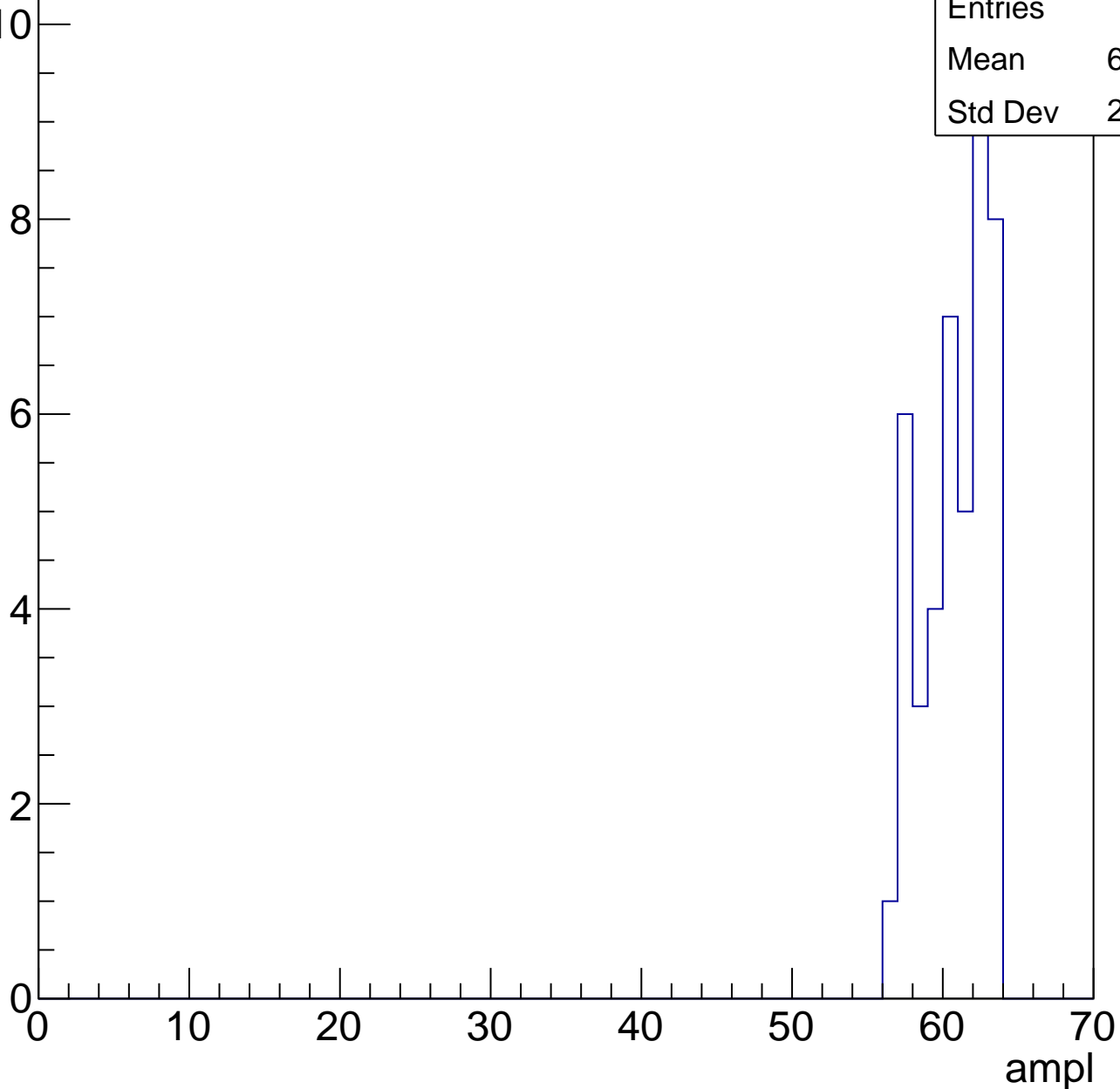


B1L101S, U26-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

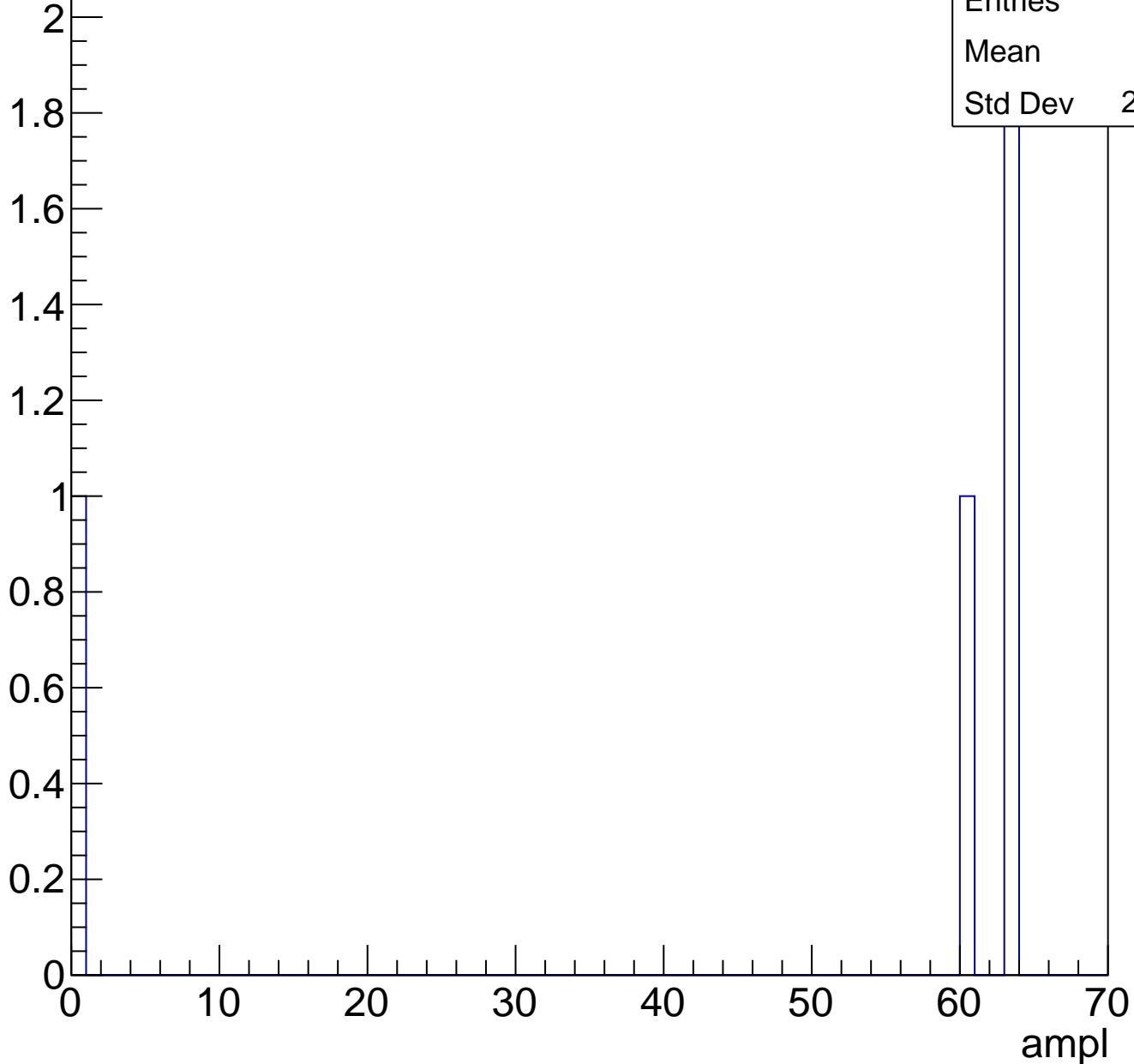
Entries	44
Mean	60.39
Std Dev	2.113



B1L101S, U26-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

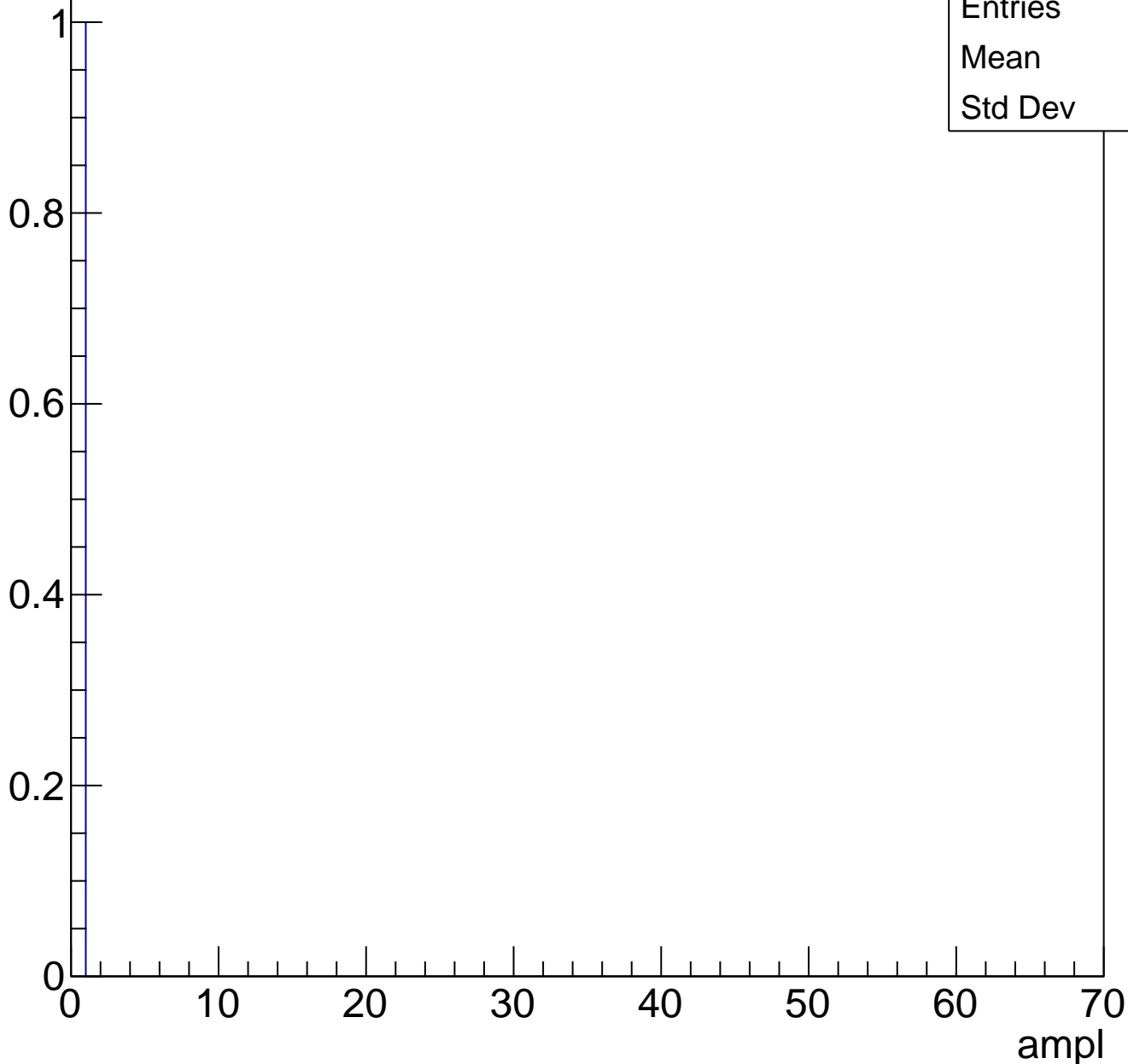
Entry



B1L101S, U26-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch121, adc0

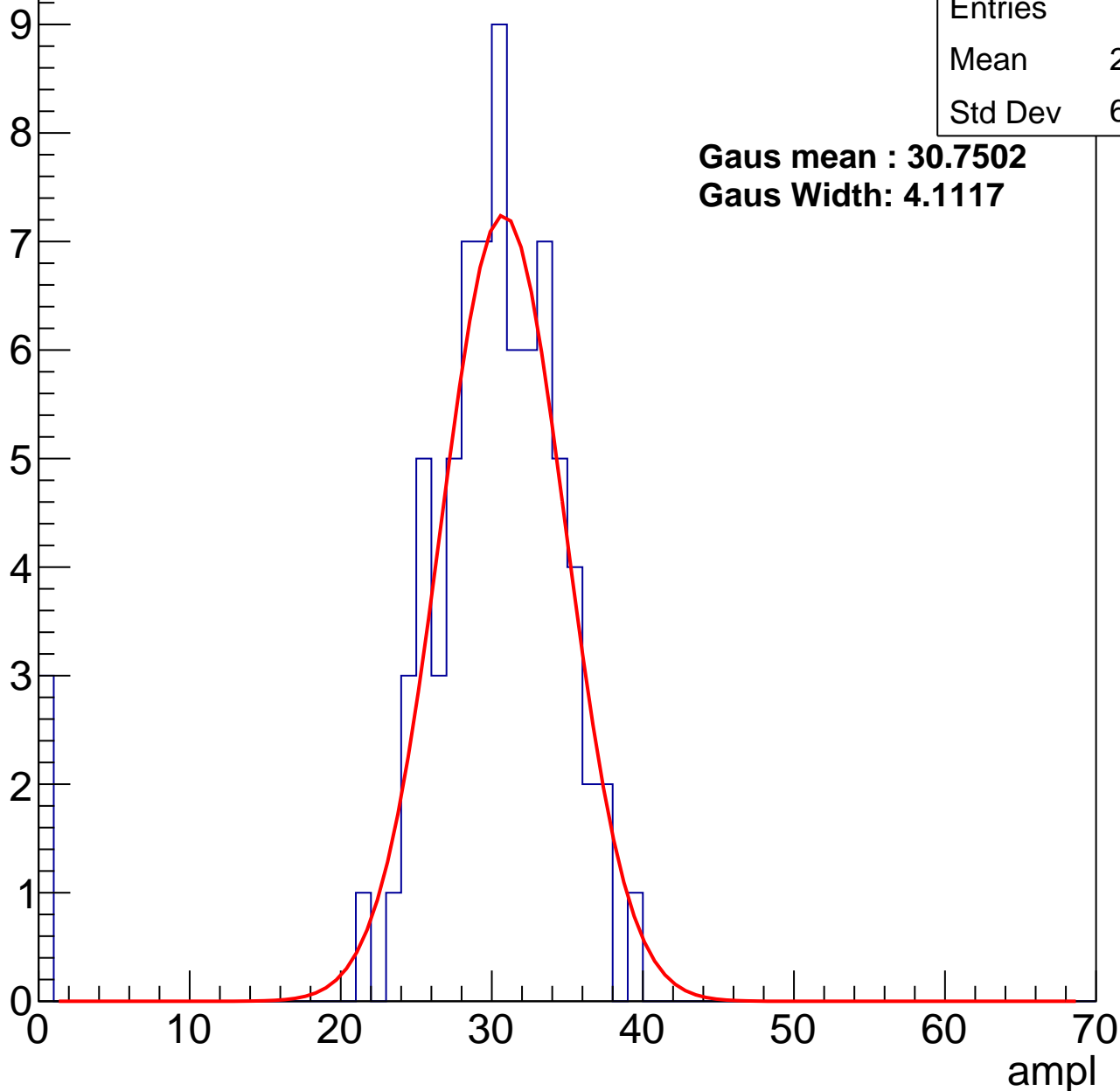
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.92
Std Dev	6.862

Gaus mean : 30.7502

Gaus Width: 4.1117



B1L101S, U26-ch121, adc1

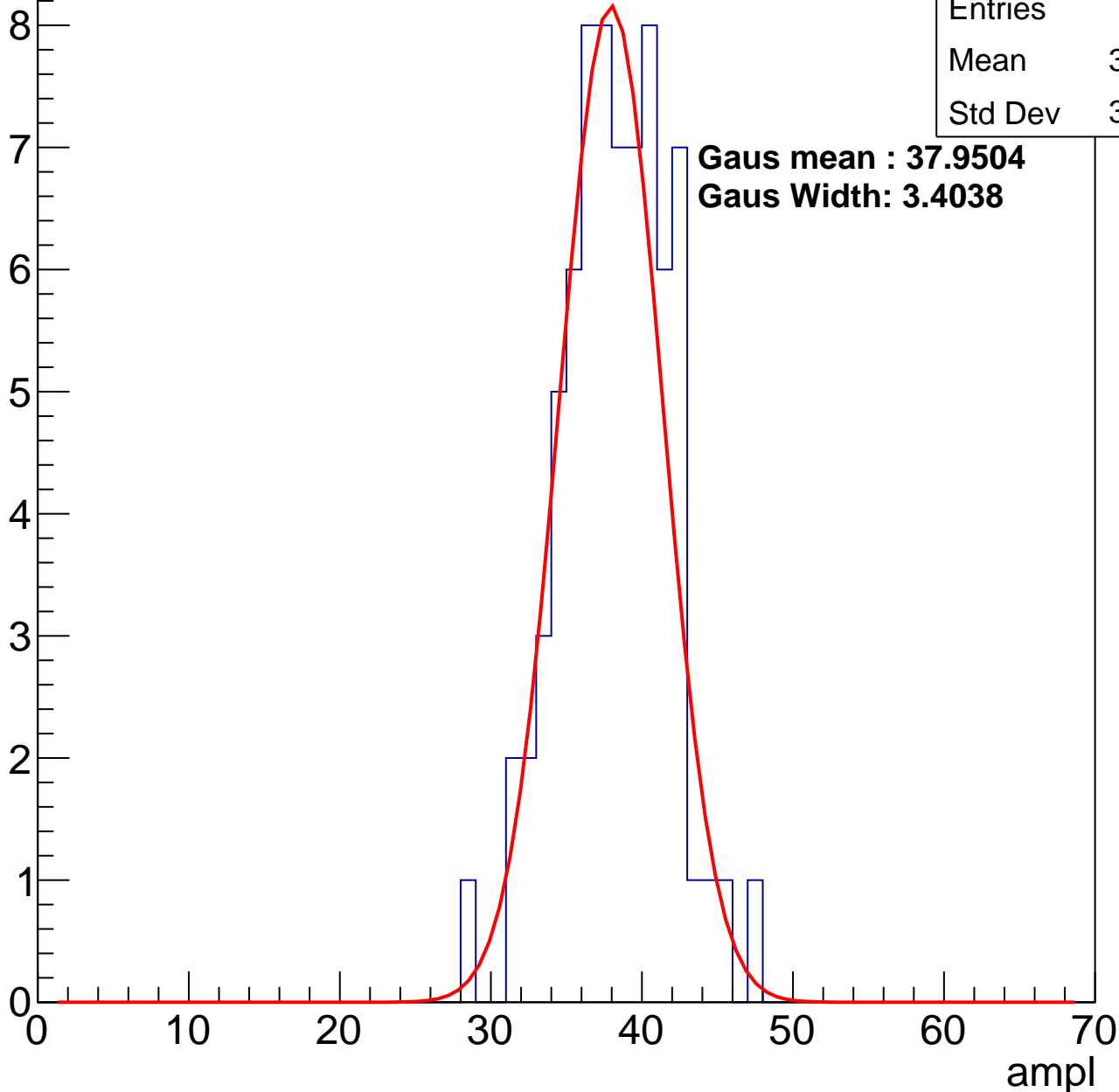
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.77
Std Dev	3.509

Gaus mean : 37.9504

Gaus Width: 3.4038



B1L101S, U26-ch121, adc2

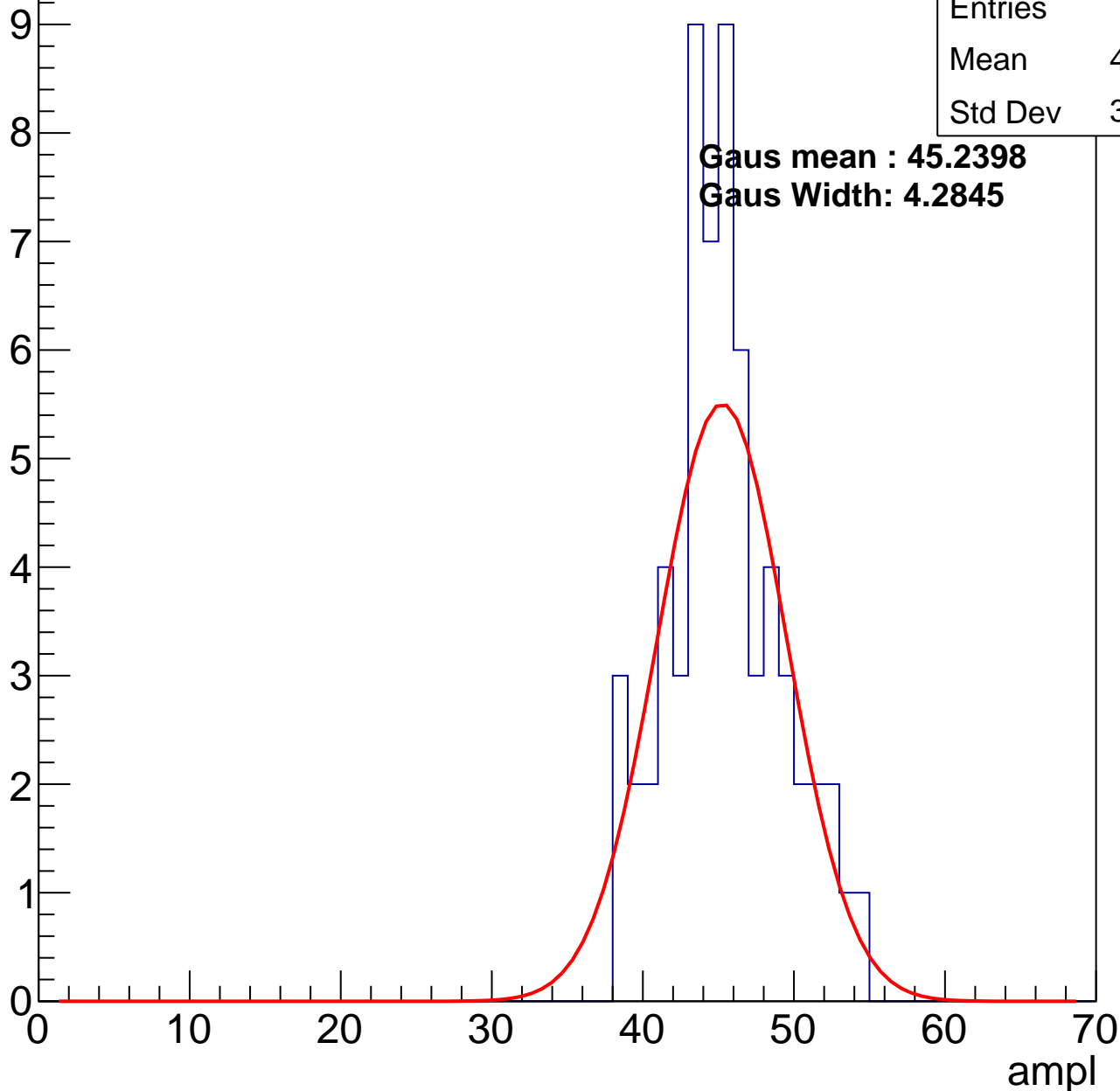
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.94
Std Dev	3.716

Gaus mean : 45.2398

Gaus Width: 4.2845

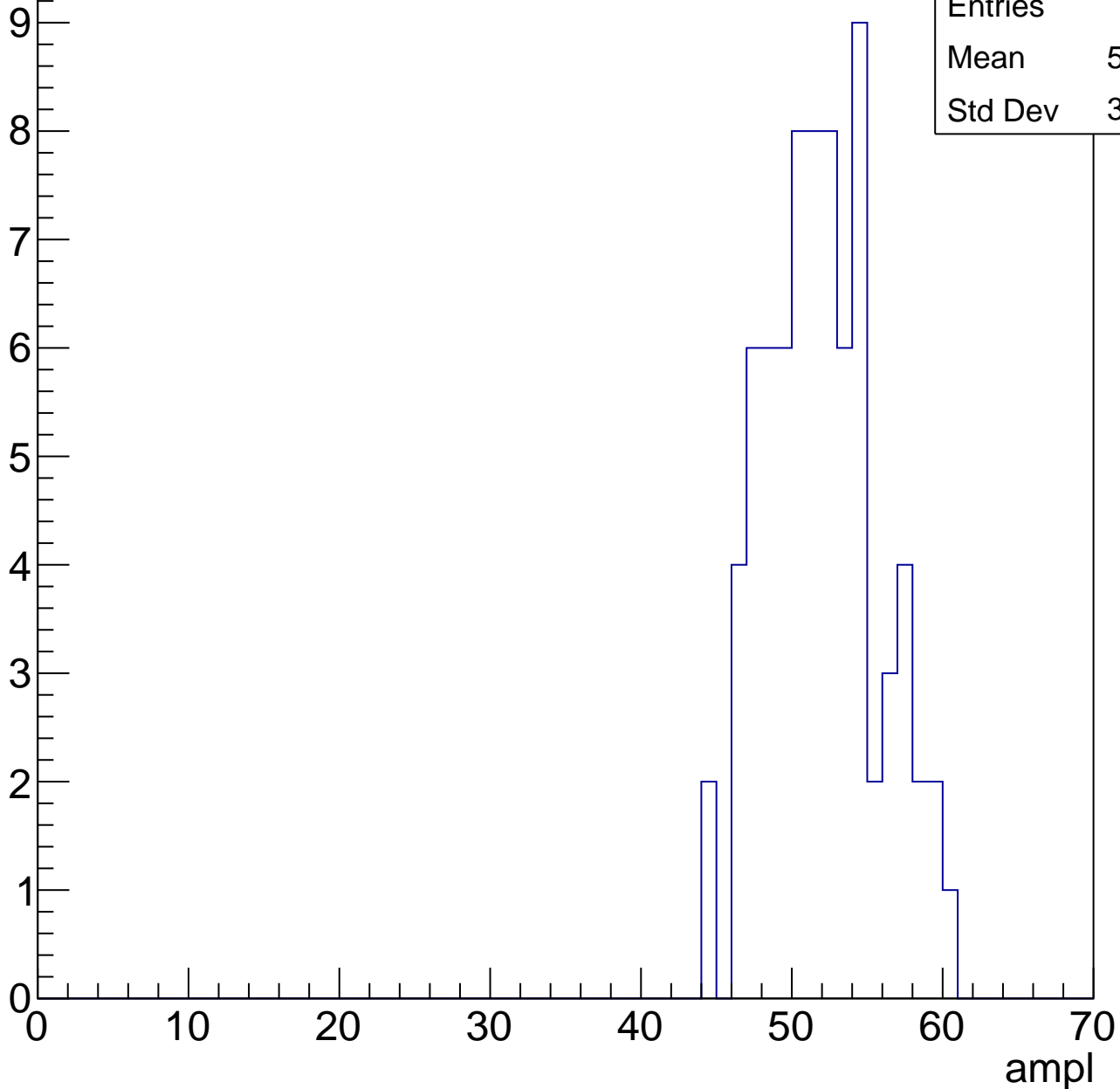


B1L101S, U26-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	51.48
Std Dev	3.663

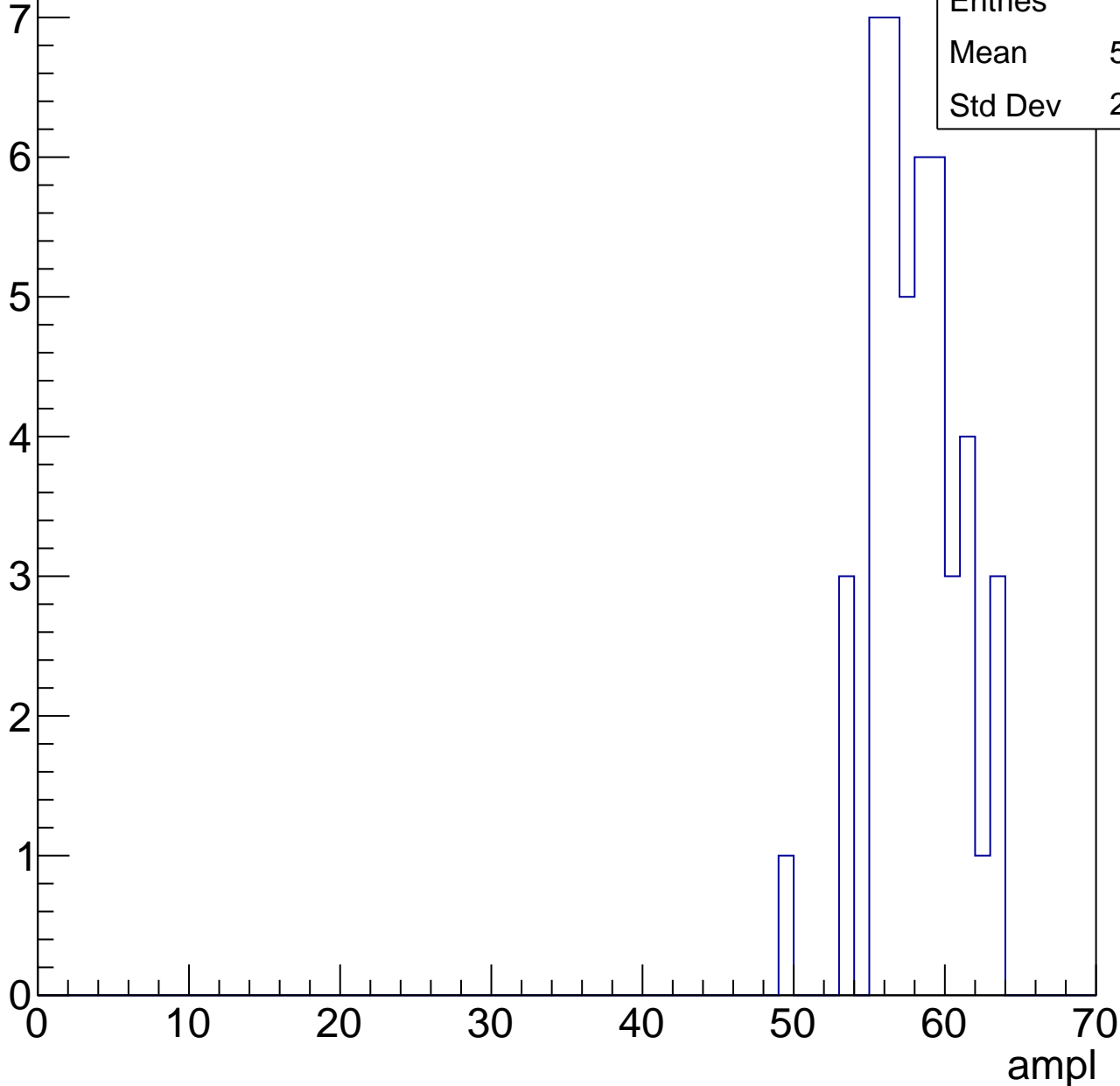


B1L101S, U26-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.54
Std Dev	2.902

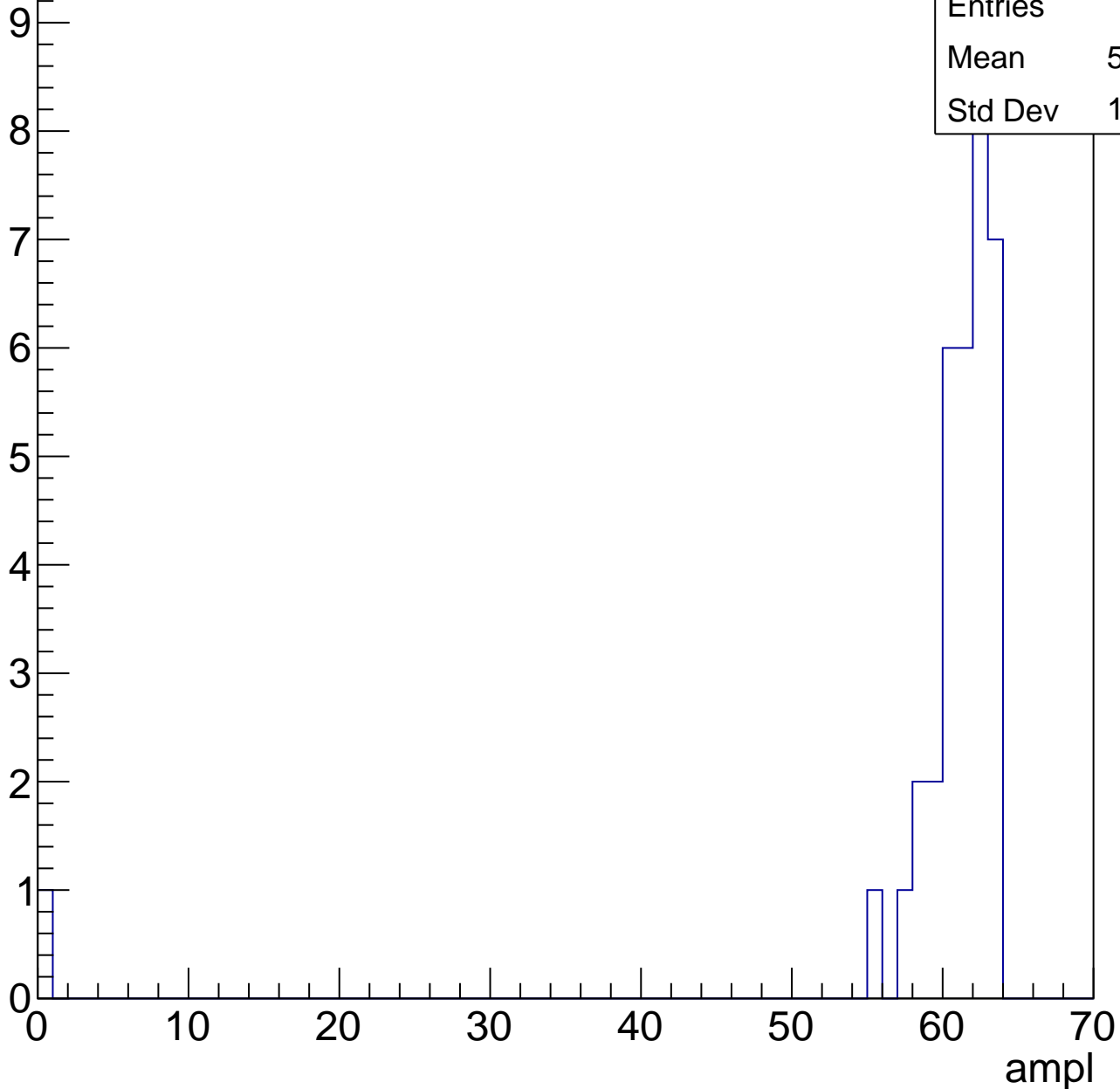


B1L101S, U26-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	59.17
Std Dev	10.32



B1L101S, U26-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch122, adc0

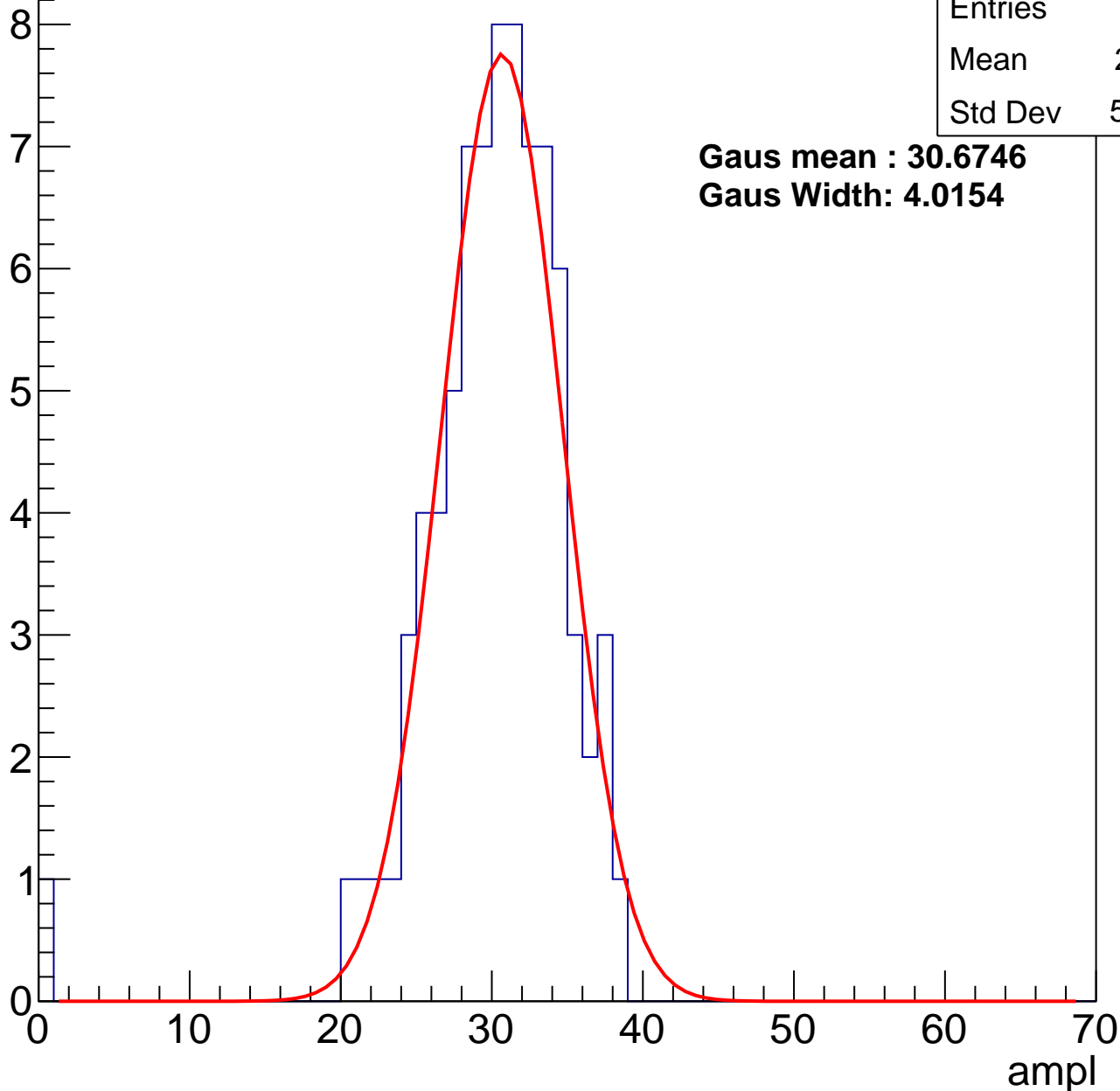
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	29.61
Std Dev	5.108

Gaus mean : 30.6746

Gaus Width: 4.0154



B1L101S, U26-ch122, adc1

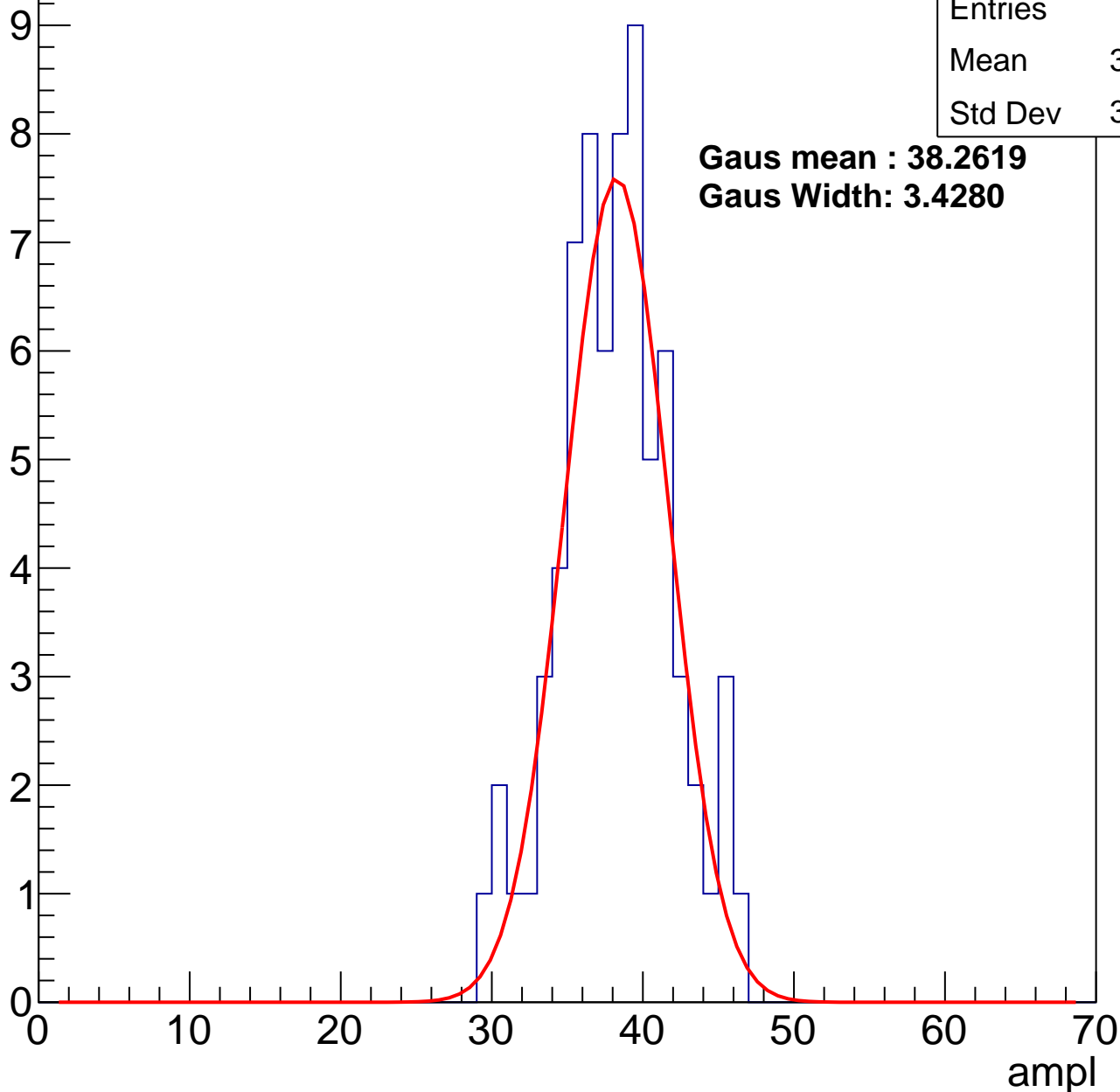
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.75
Std Dev	3.672

Gaus mean : 38.2619

Gaus Width: 3.4280



B1L101S, U26-ch122, adc2

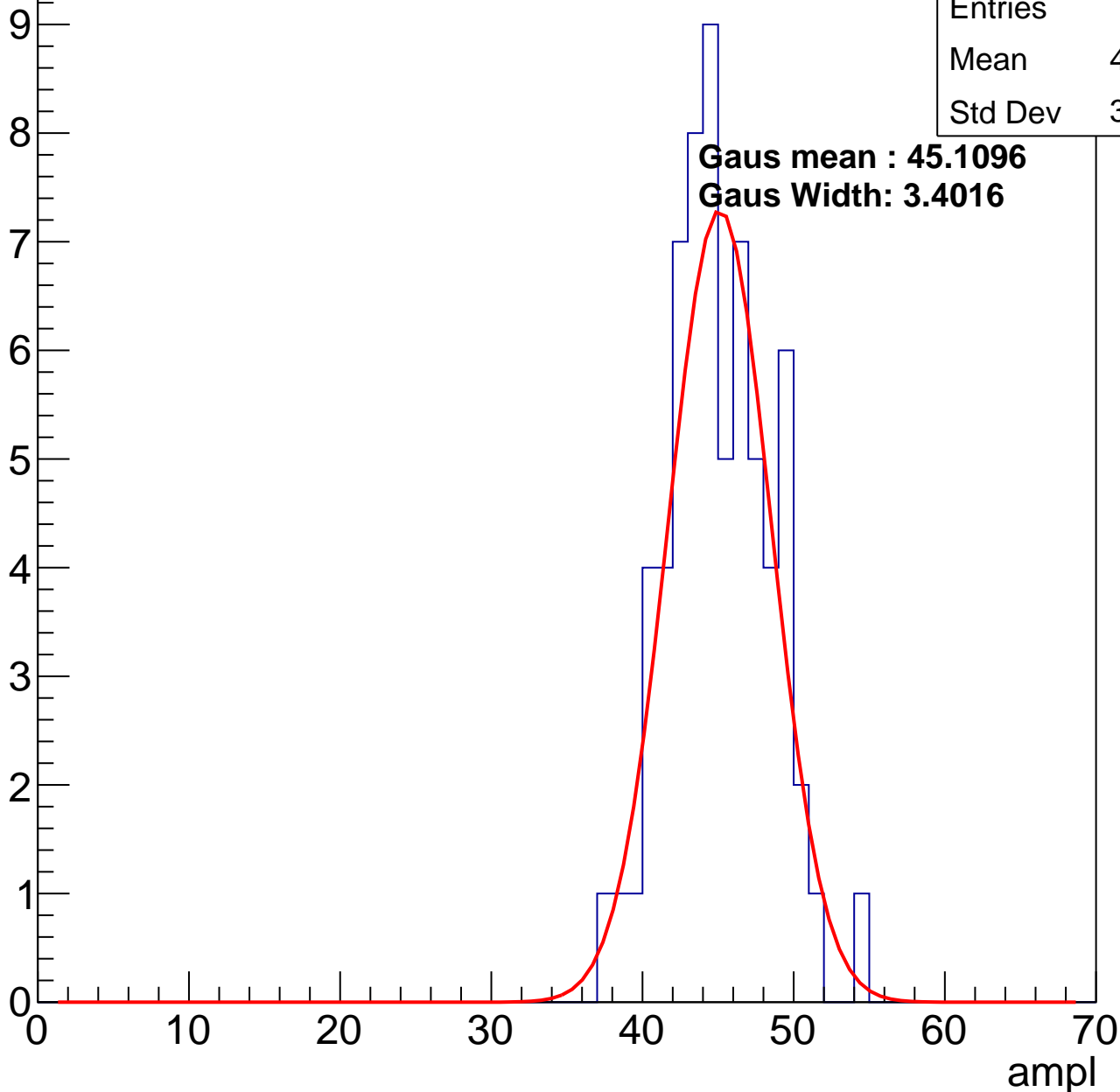
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.62
Std Dev	3.334

Gaus mean : 45.1096

Gaus Width: 3.4016

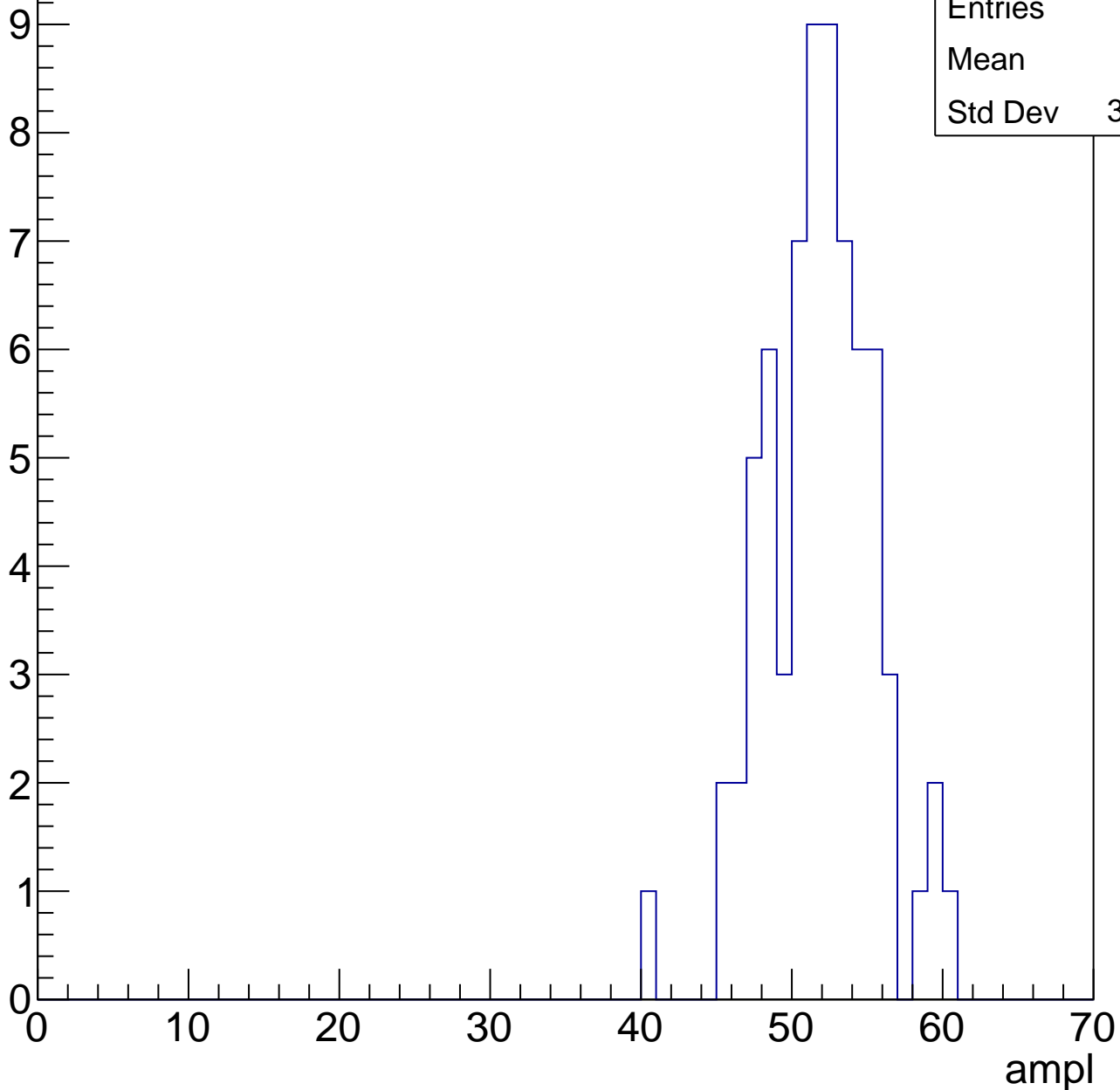


B1L101S, U26-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	51.4
Std Dev	3.595

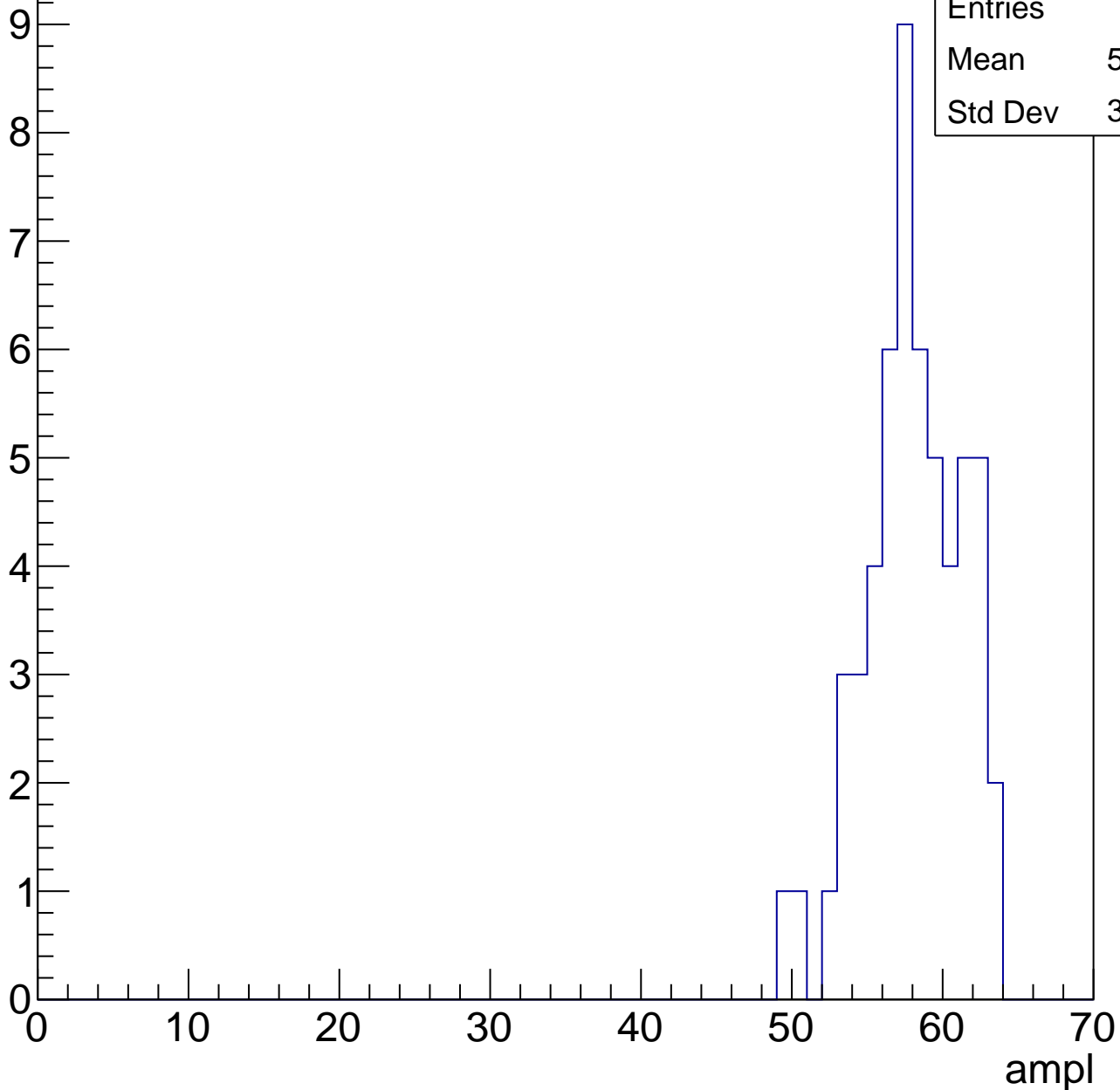


B1L101S, U26-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.55
Std Dev	3.184

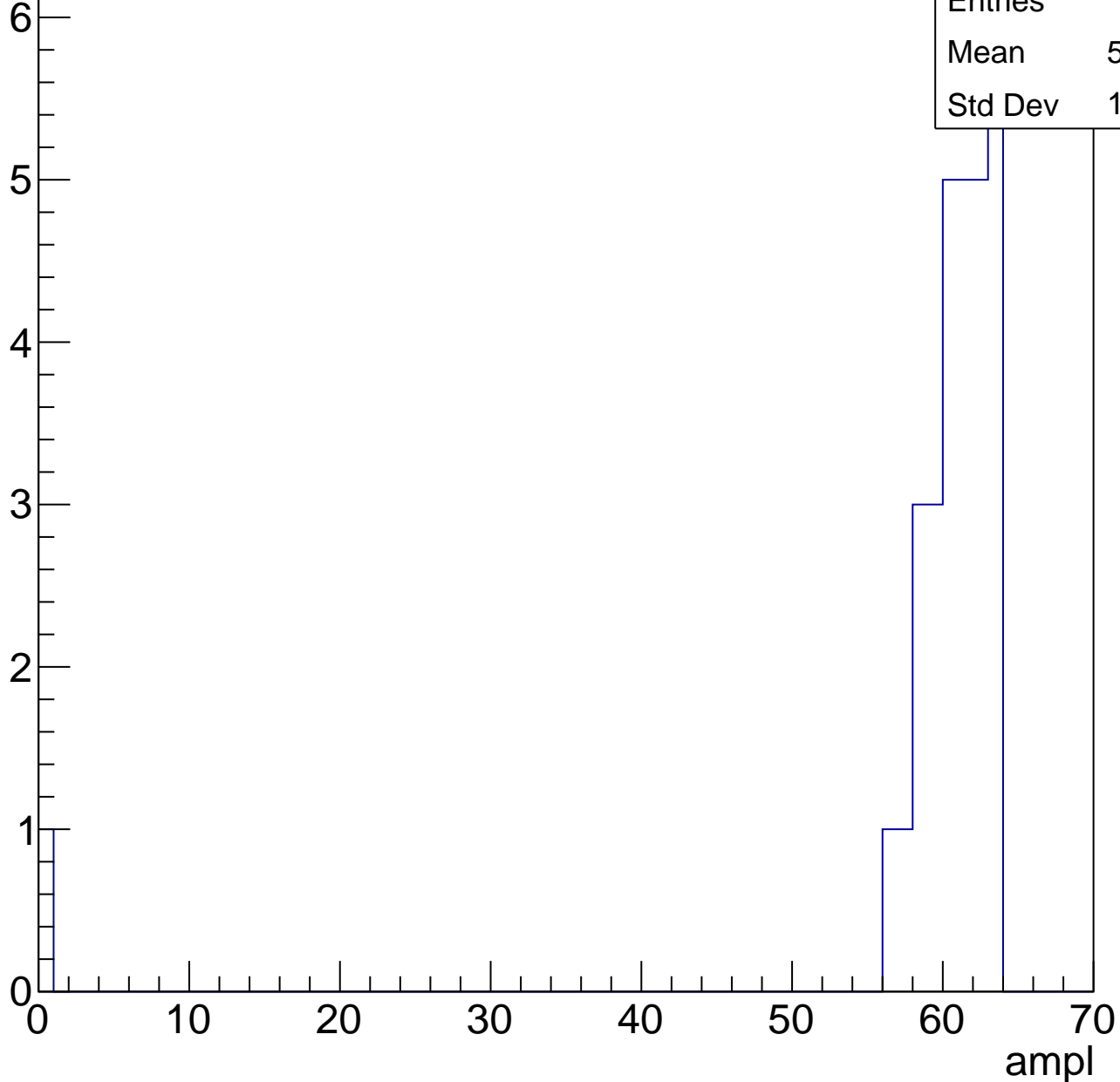


B1L101S, U26-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	58.57
Std Dev	11.04



B1L101S, U26-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U26-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch123, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	29.45
Std Dev	3.325

Gaus mean : 30.0864

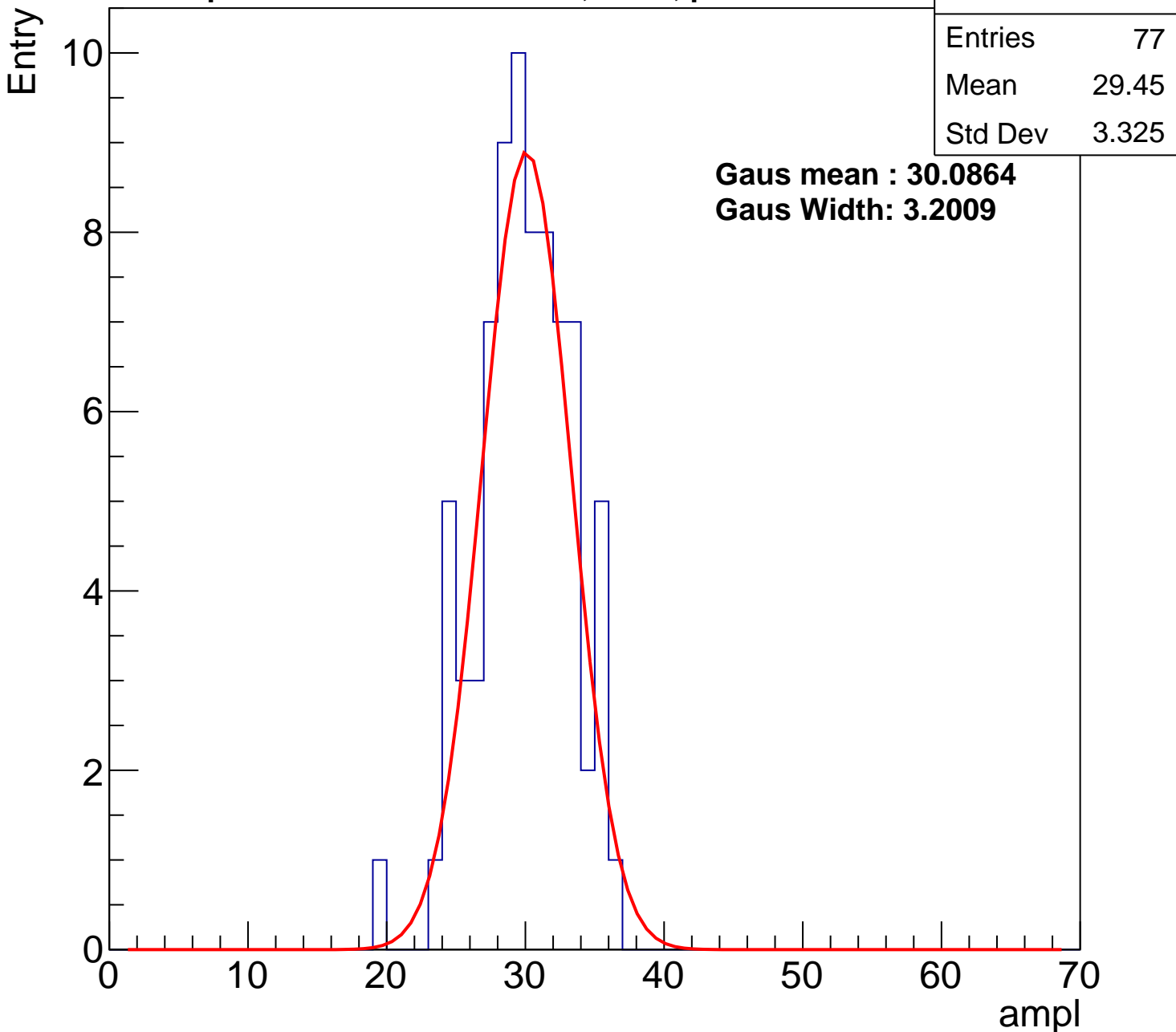
Gaus Width: 3.2009

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch123, adc1

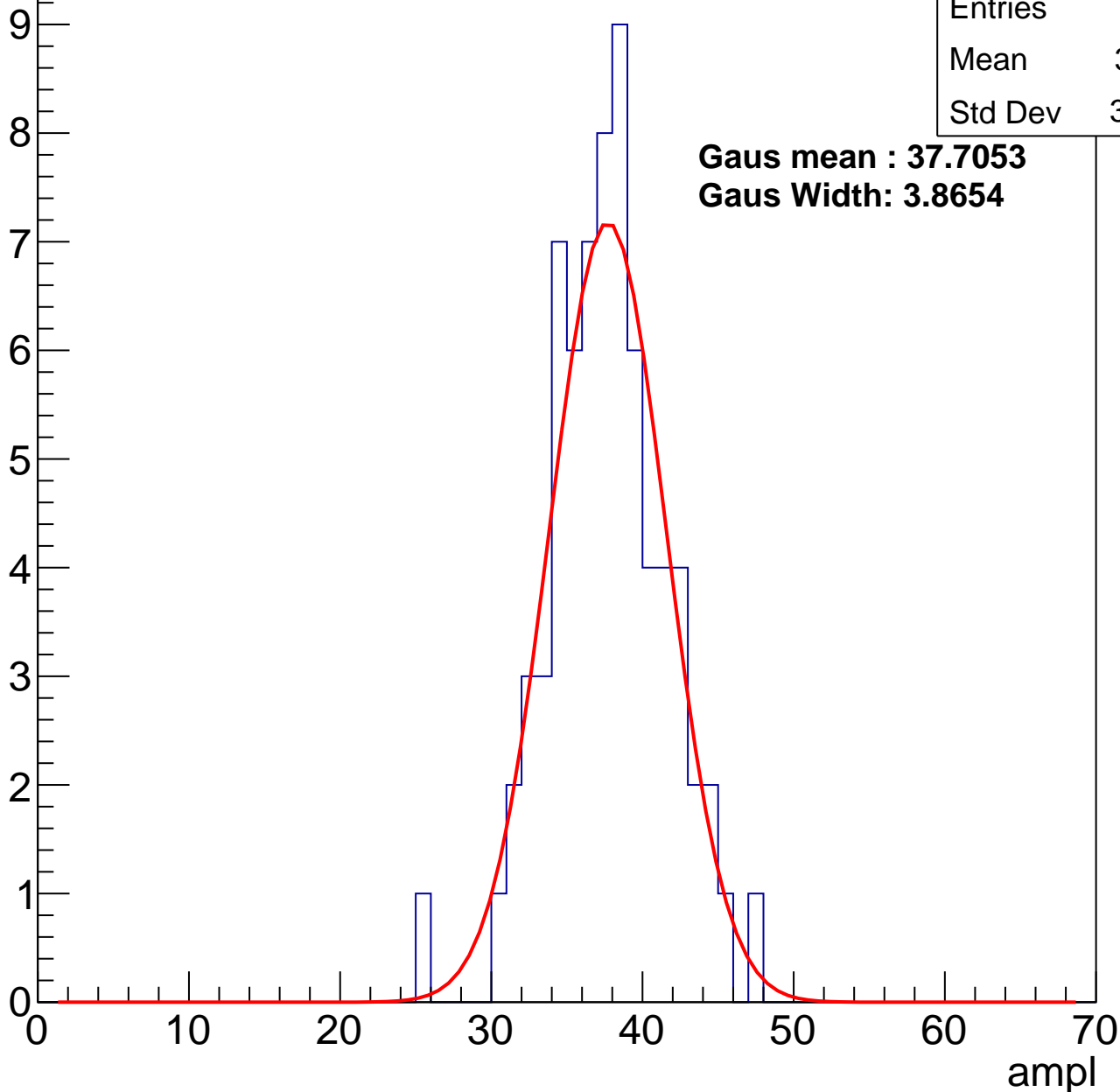
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.21
Std Dev	3.838

Gaus mean : 37.7053

Gaus Width: 3.8654

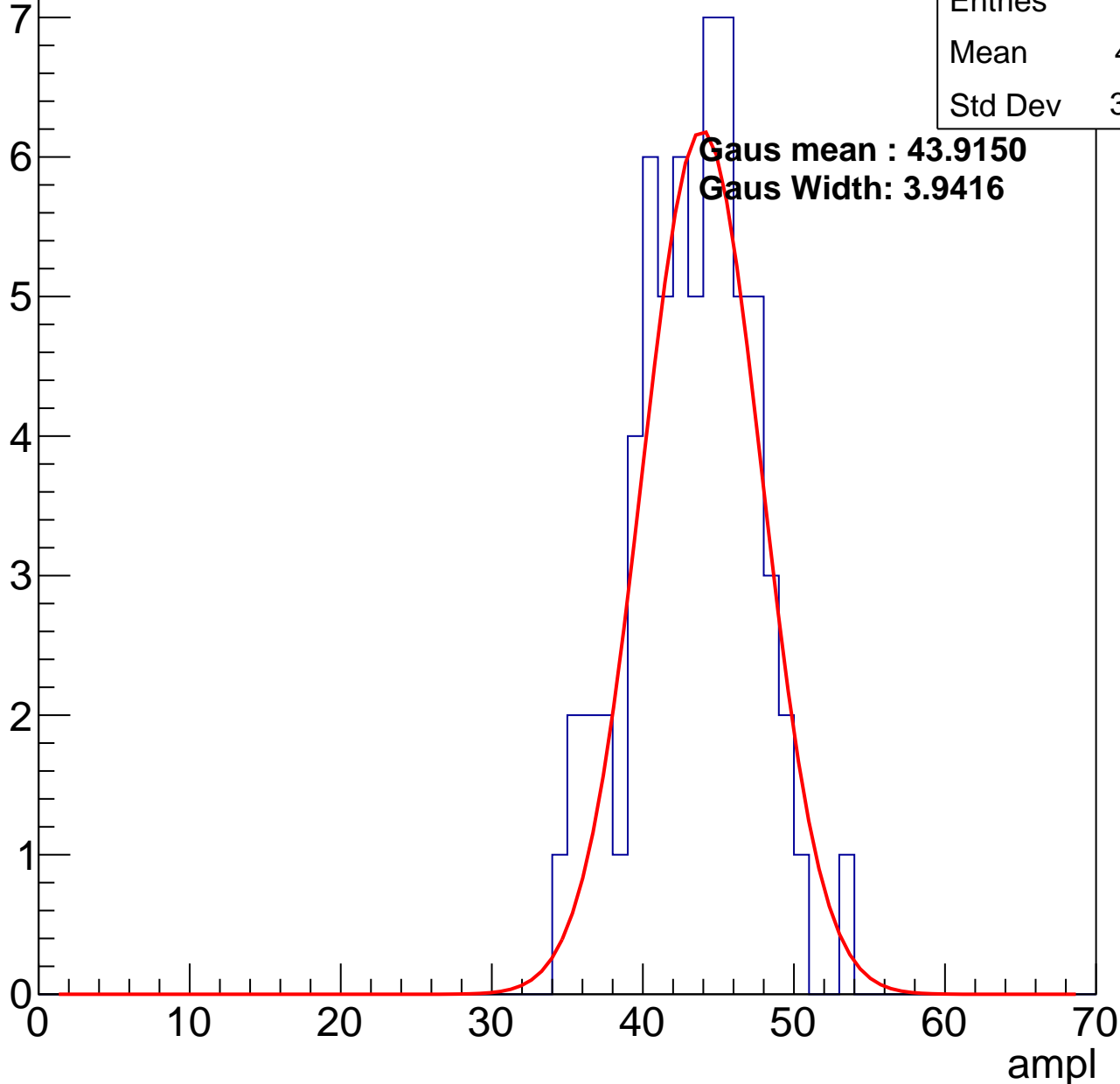


B1L101S, U26-ch123, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.91
Std Dev	3.929

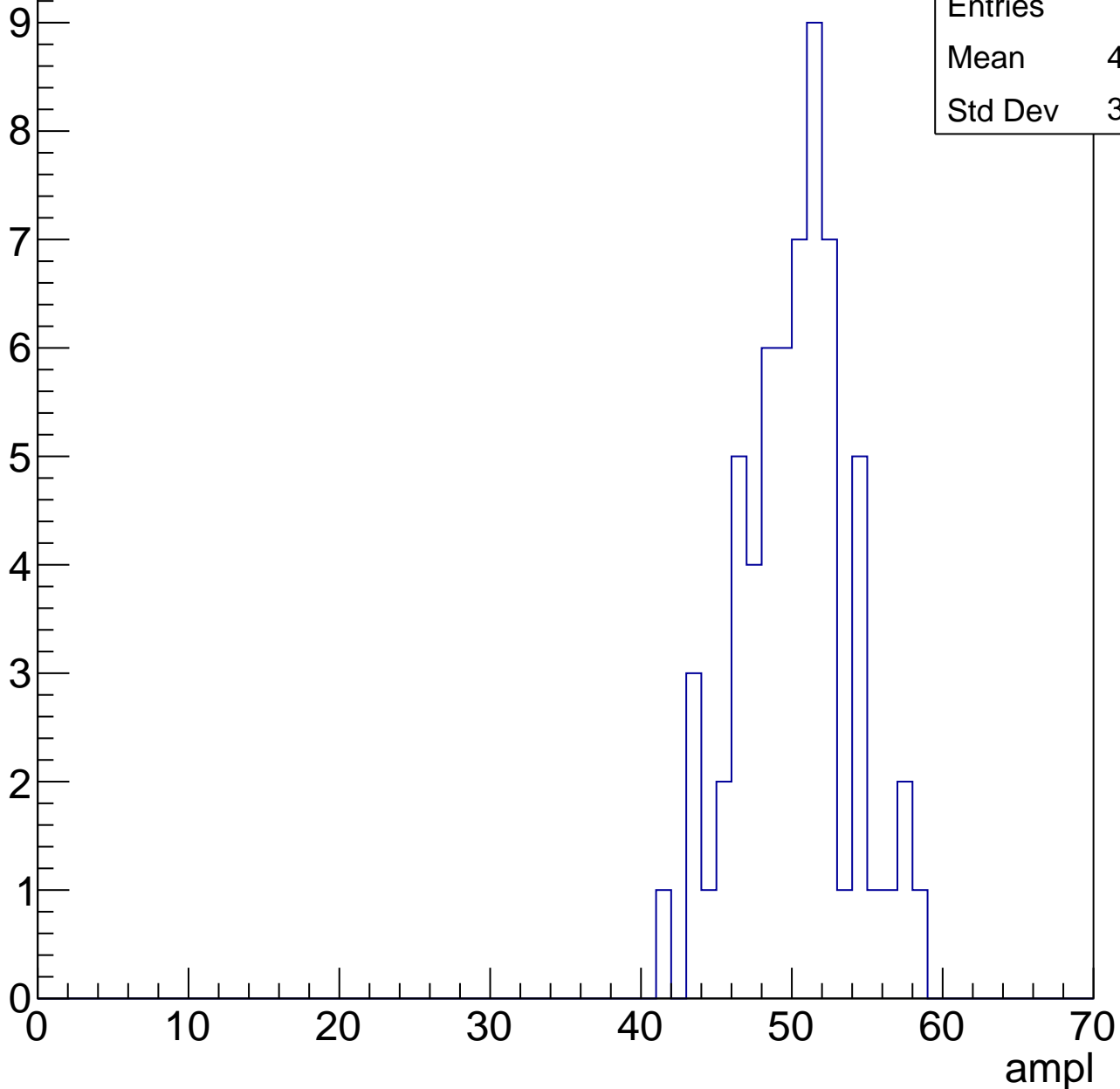


B1L101S, U26-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.73
Std Dev	3.602

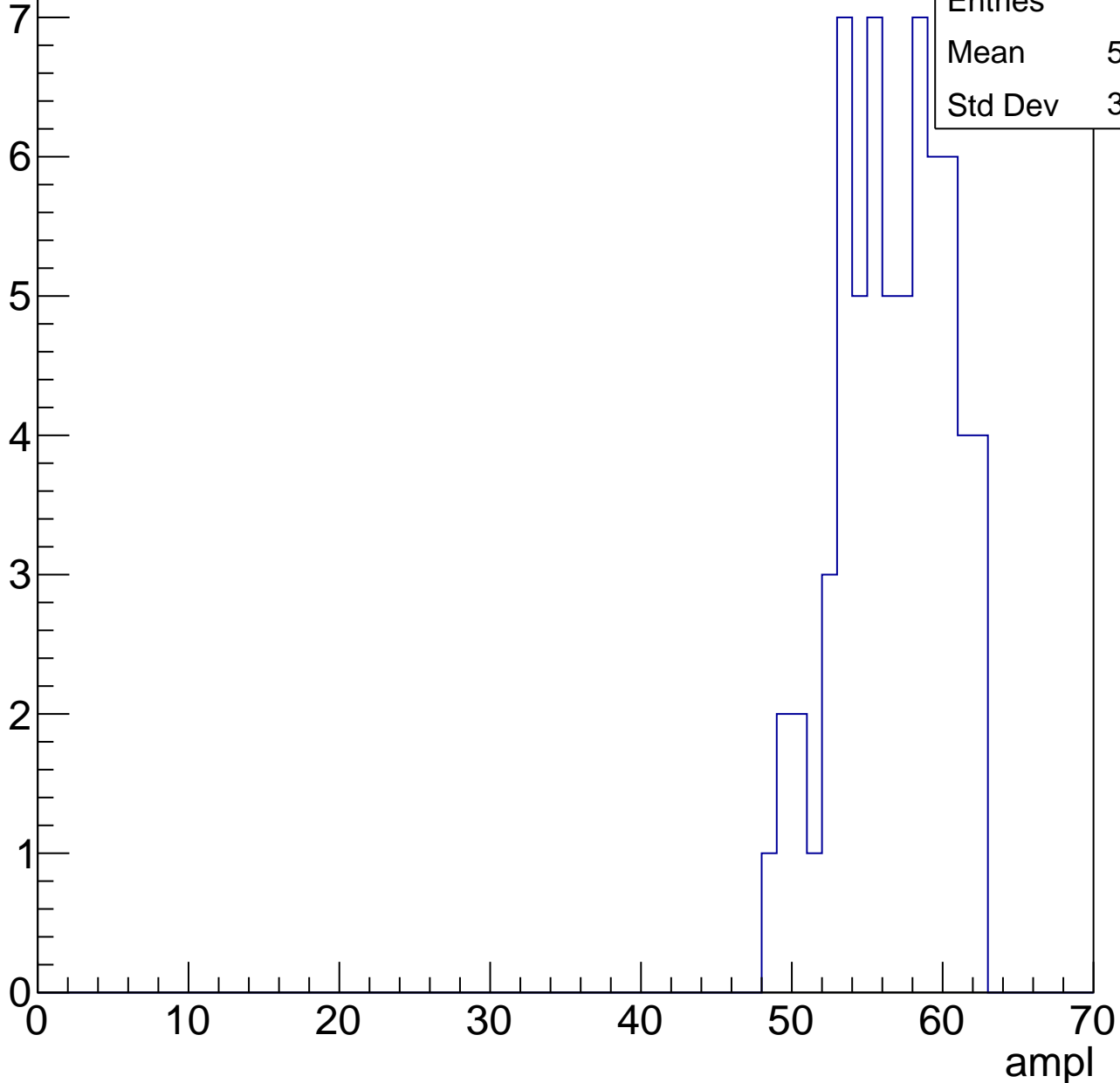


B1L101S, U26-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	56.25
Std Dev	3.552



B1L101S, U26-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	40
Mean	58.33
Std Dev	9.748

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

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B1L101S, U26-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

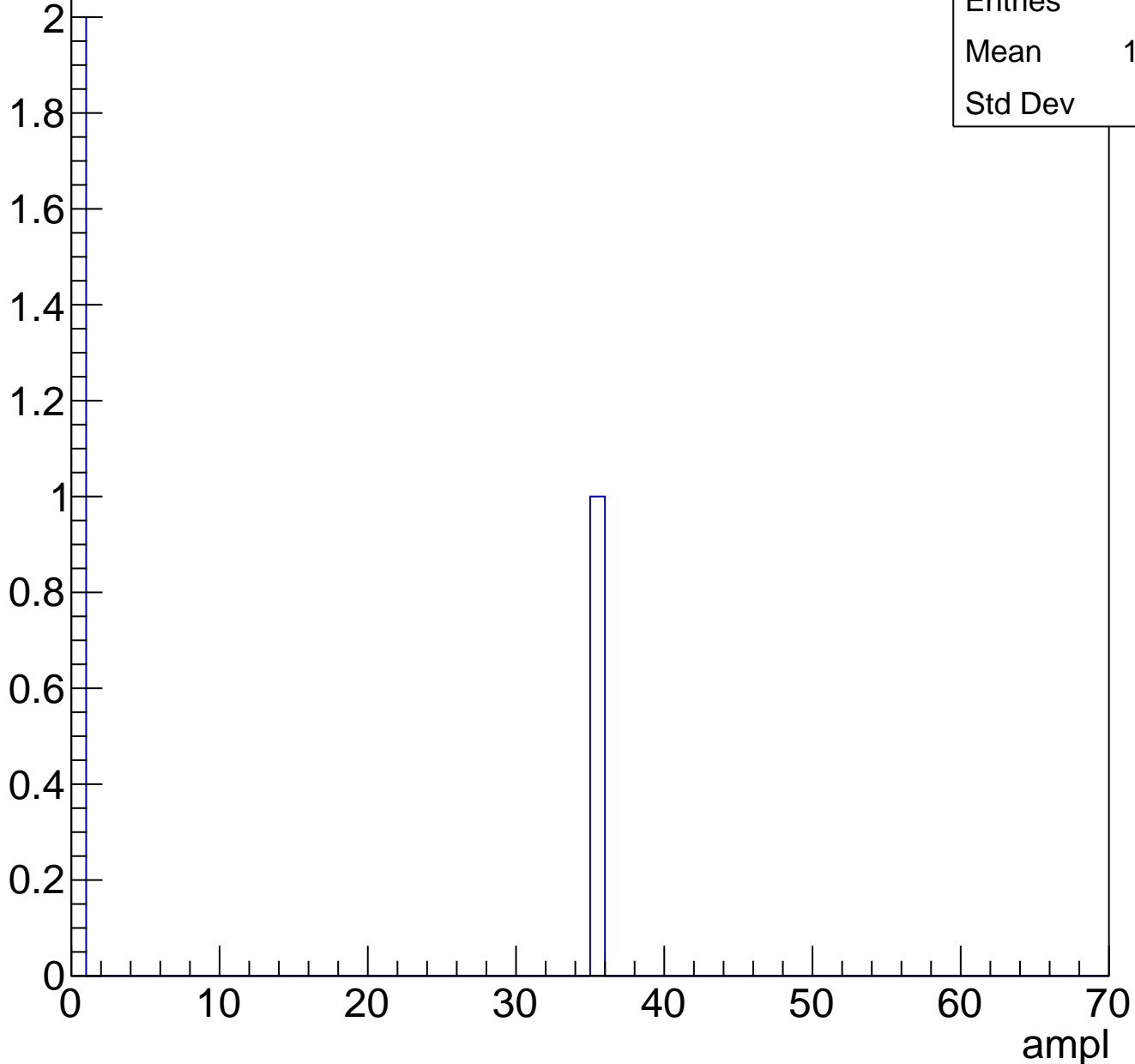
ampl

0 10 20 30 40 50 60 70

B1L101S, U26-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	11.67
Std Dev	16.5

B1L101S, U26-ch124, adc0

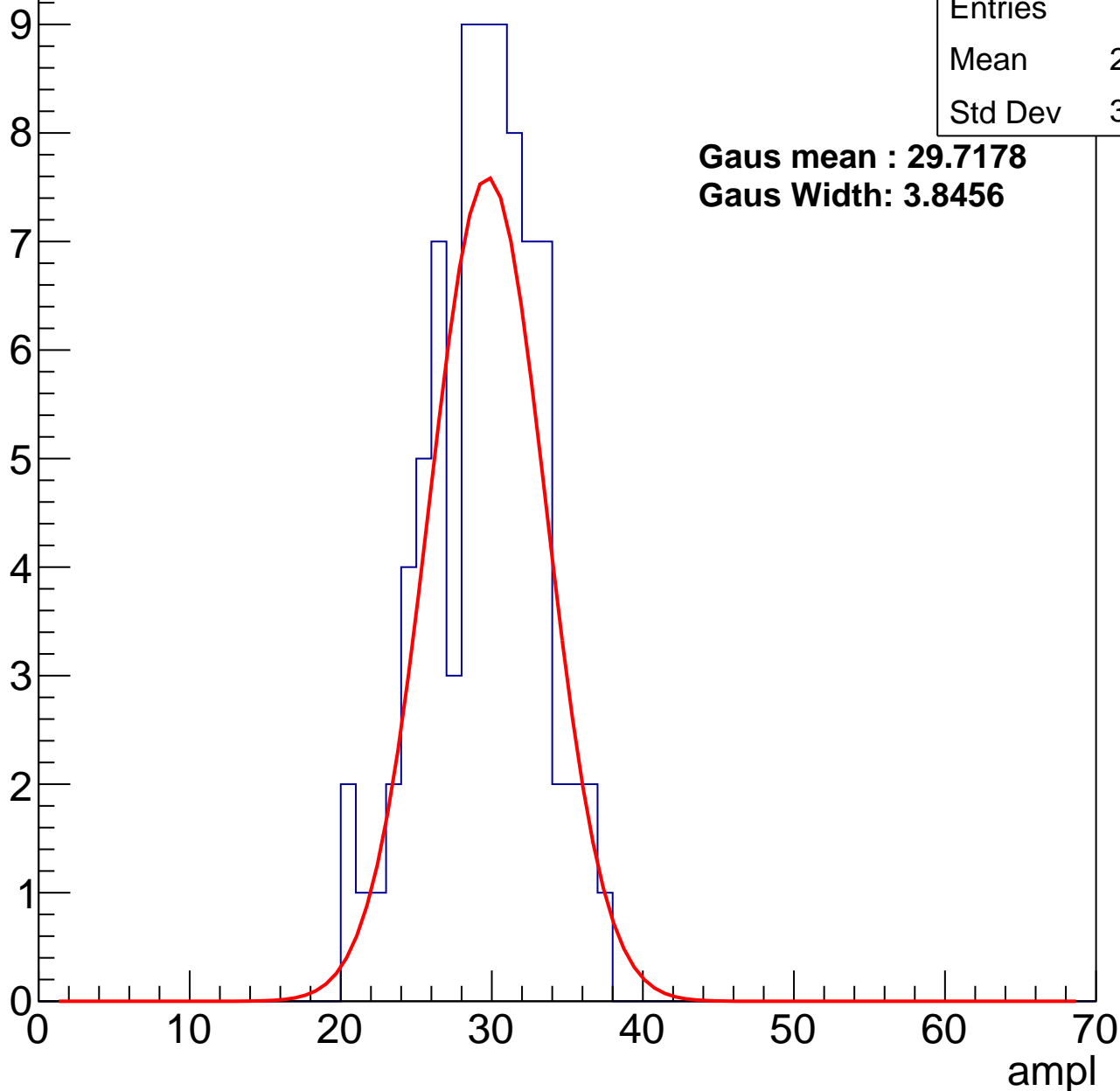
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.96
Std Dev	3.703

Gaus mean : 29.7178

Gaus Width: 3.8456



B1L101S, U26-ch124, adc1

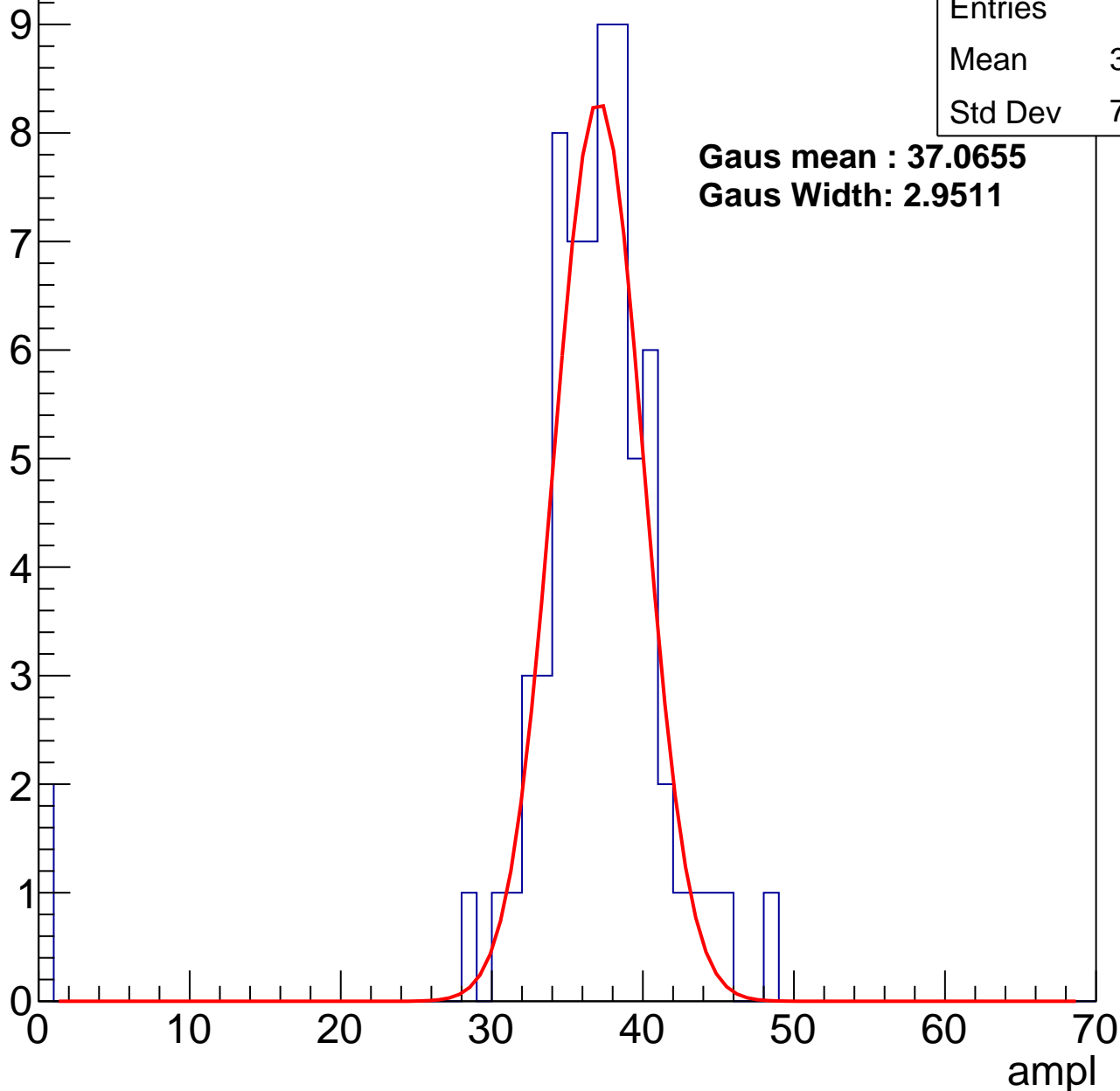
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.75
Std Dev	7.058

Gaus mean : 37.0655

Gaus Width: 2.9511



B1L101S, U26-ch124, adc2

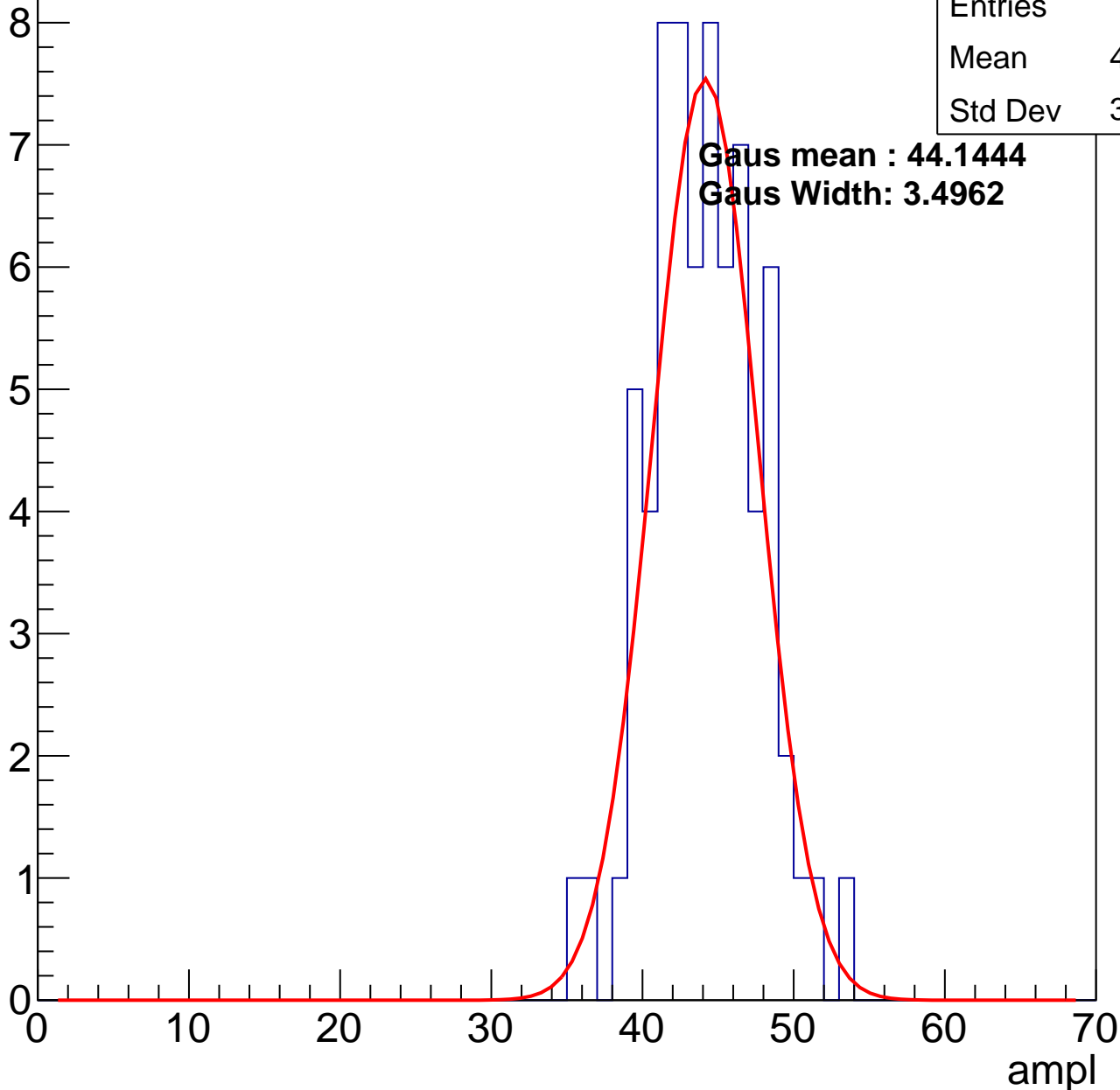
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.69
Std Dev	3.495

Gaus mean : 44.1444

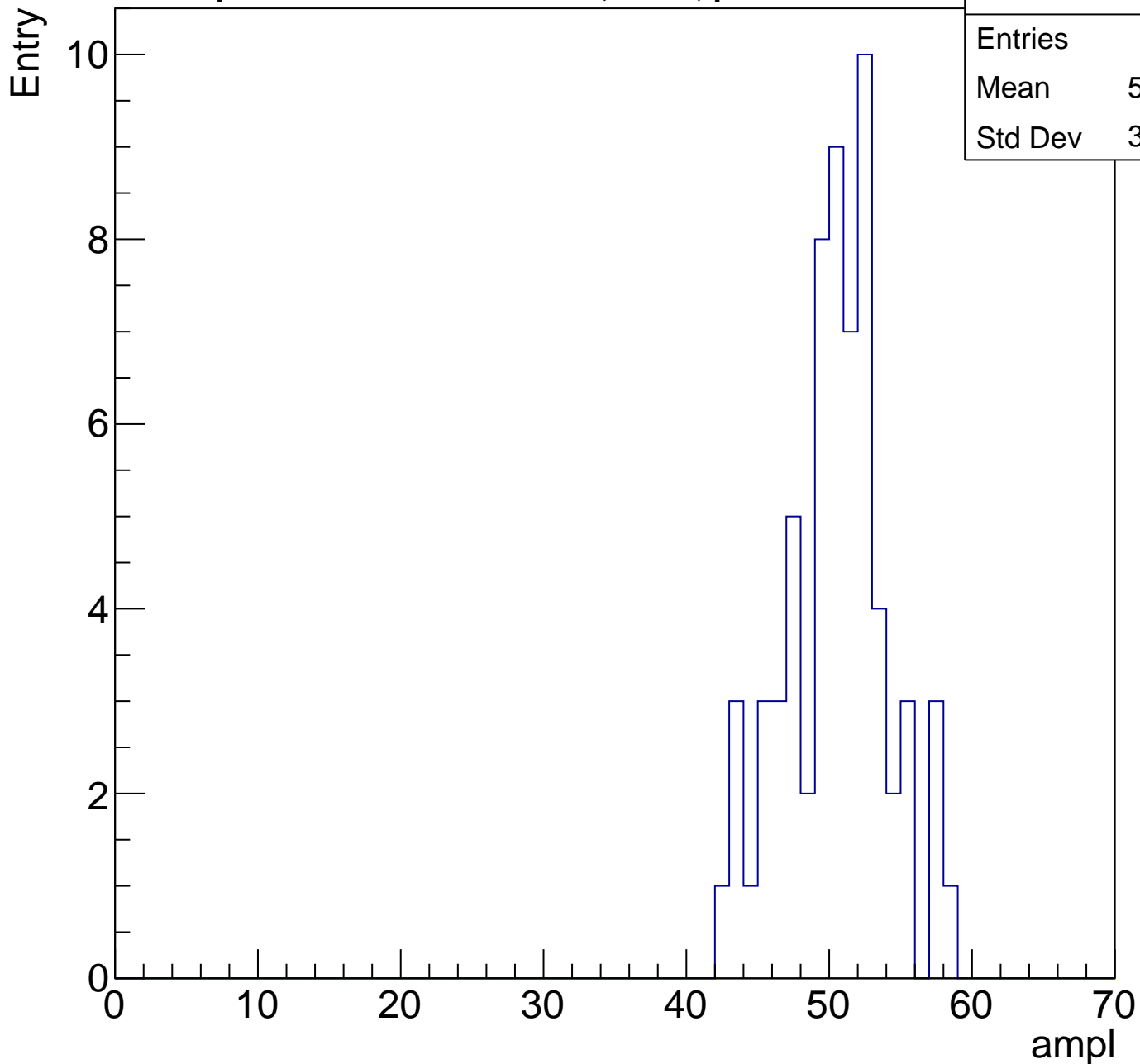
Gaus Width: 3.4962



B1L101S, U26-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	50.03
Std Dev	3.578

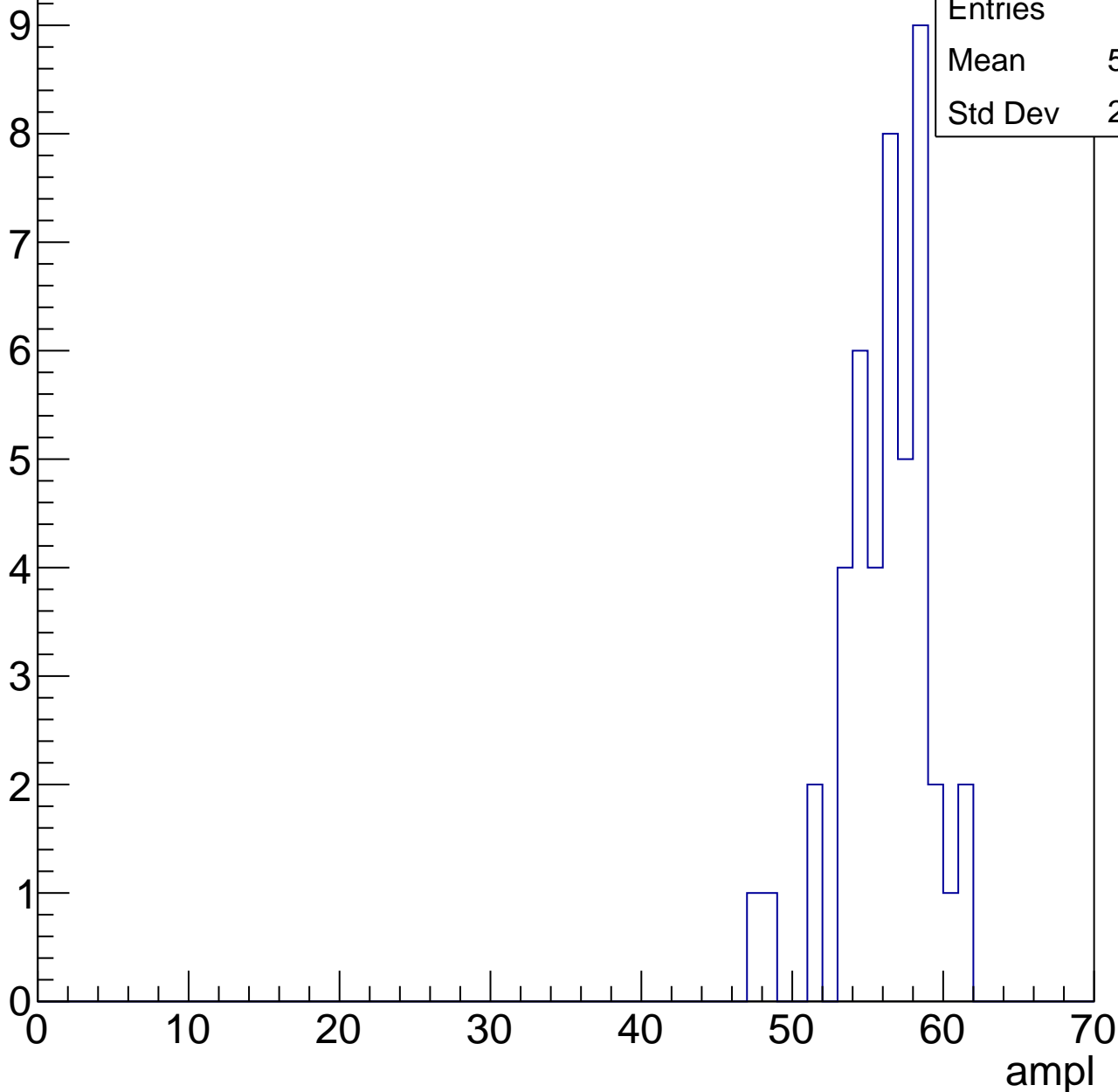


B1L101S, U26-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.73
Std Dev	2.909

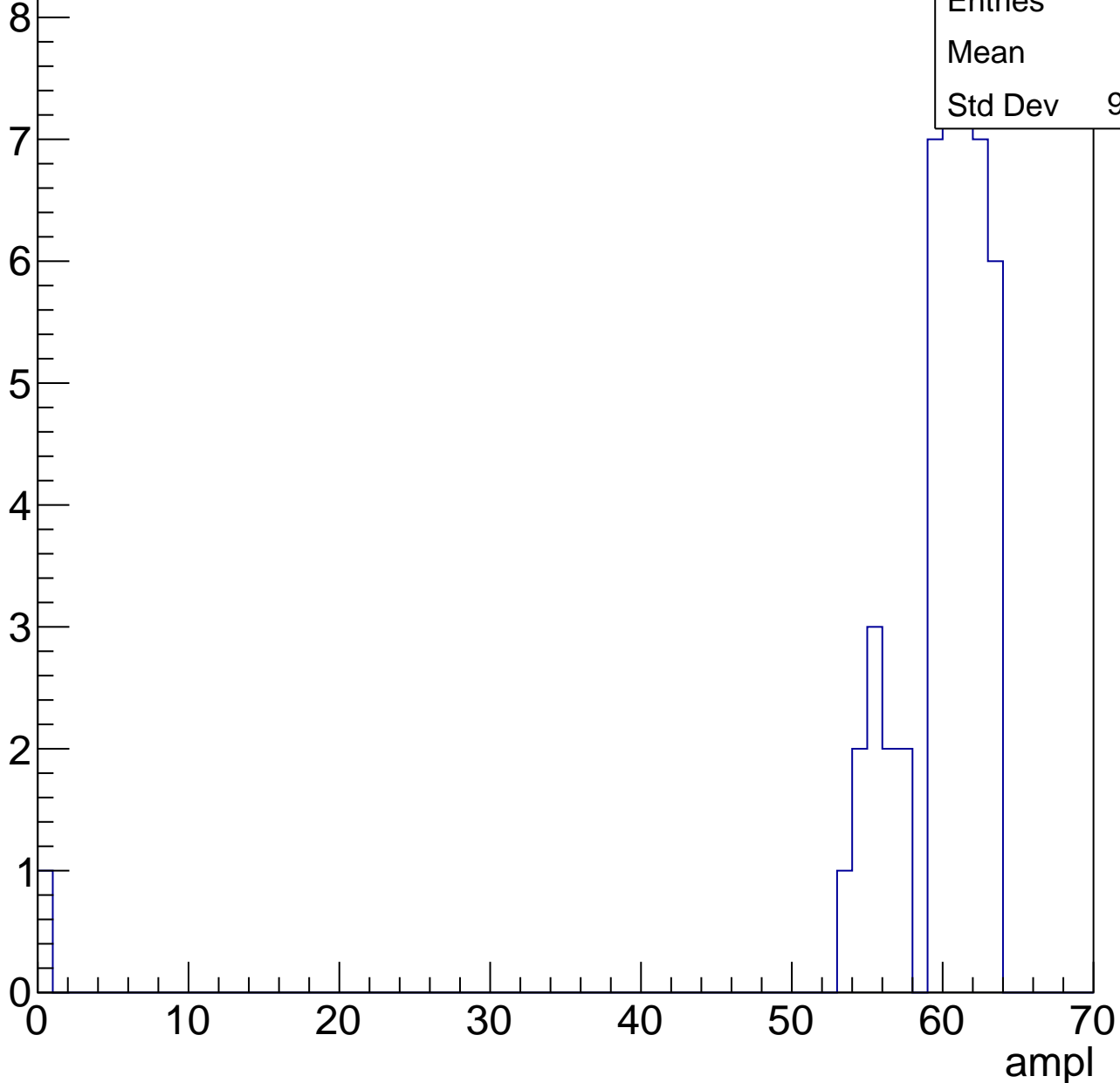


B1L101S, U26-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

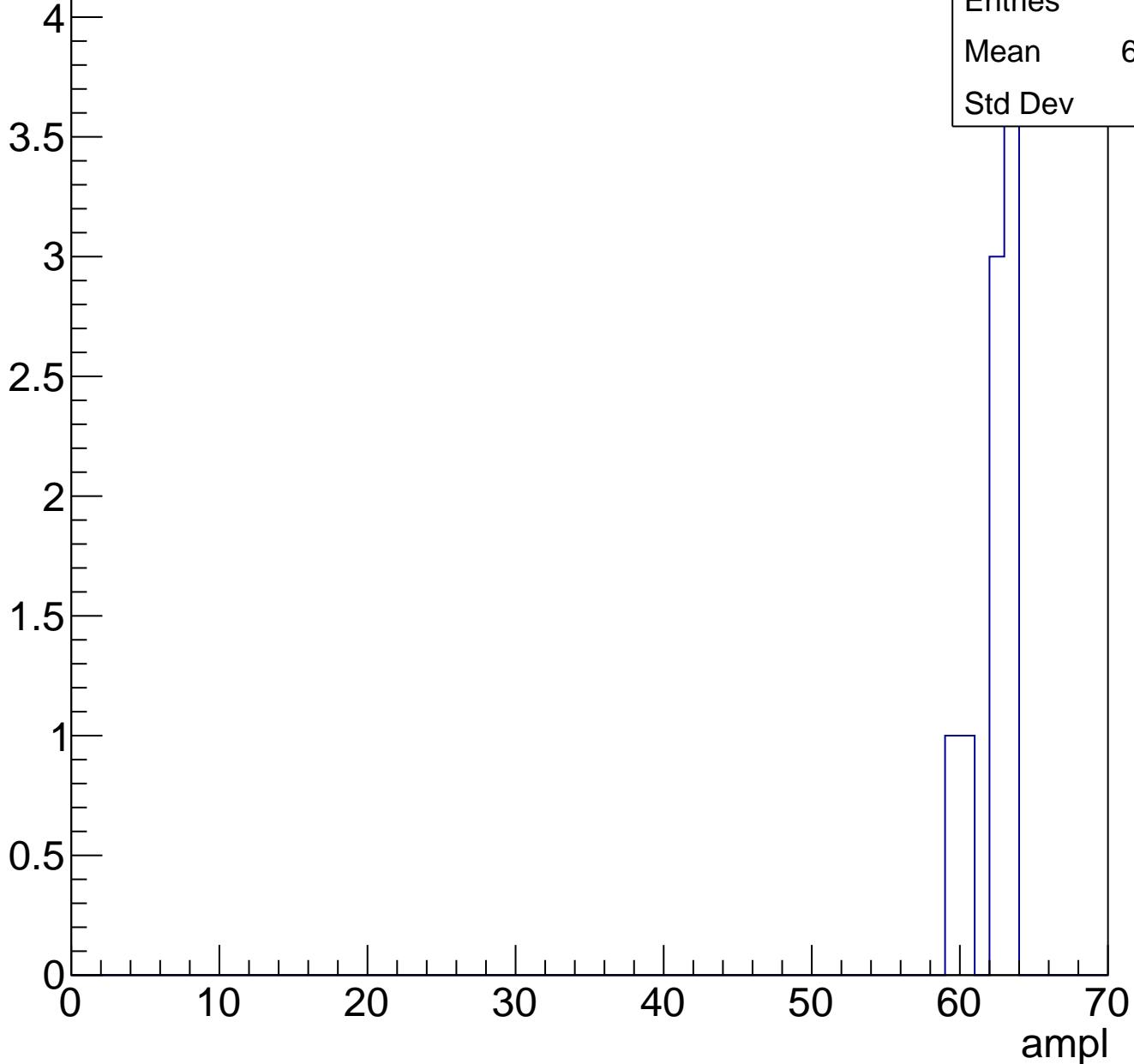
Entries	47
Mean	58.4
Std Dev	9.019



B1L101S, U26-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch125, adc0

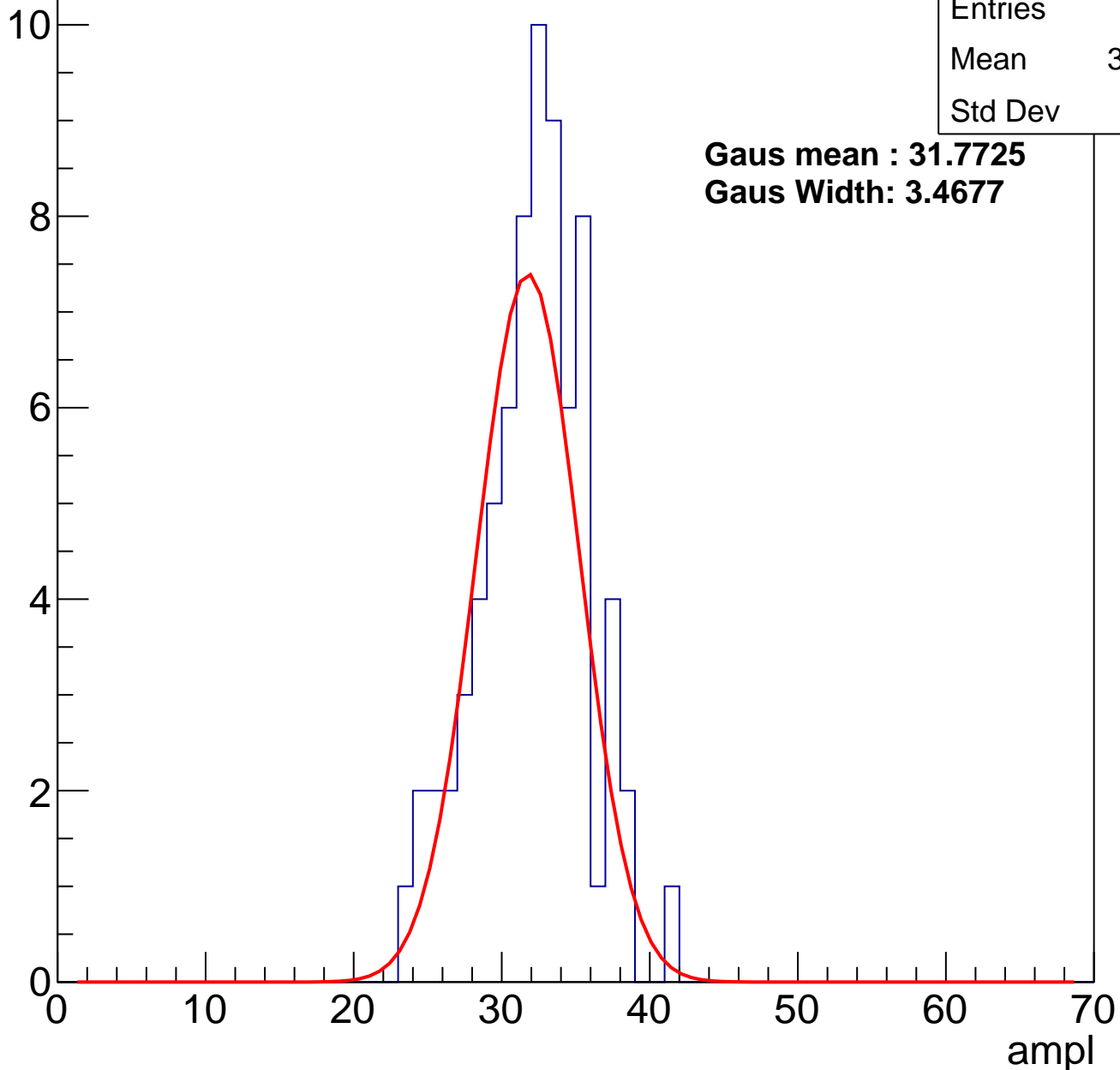
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	31.64
Std Dev	3.6

Gaus mean : 31.7725

Gaus Width: 3.4677

Entry



B1L101S, U26-ch125, adc1

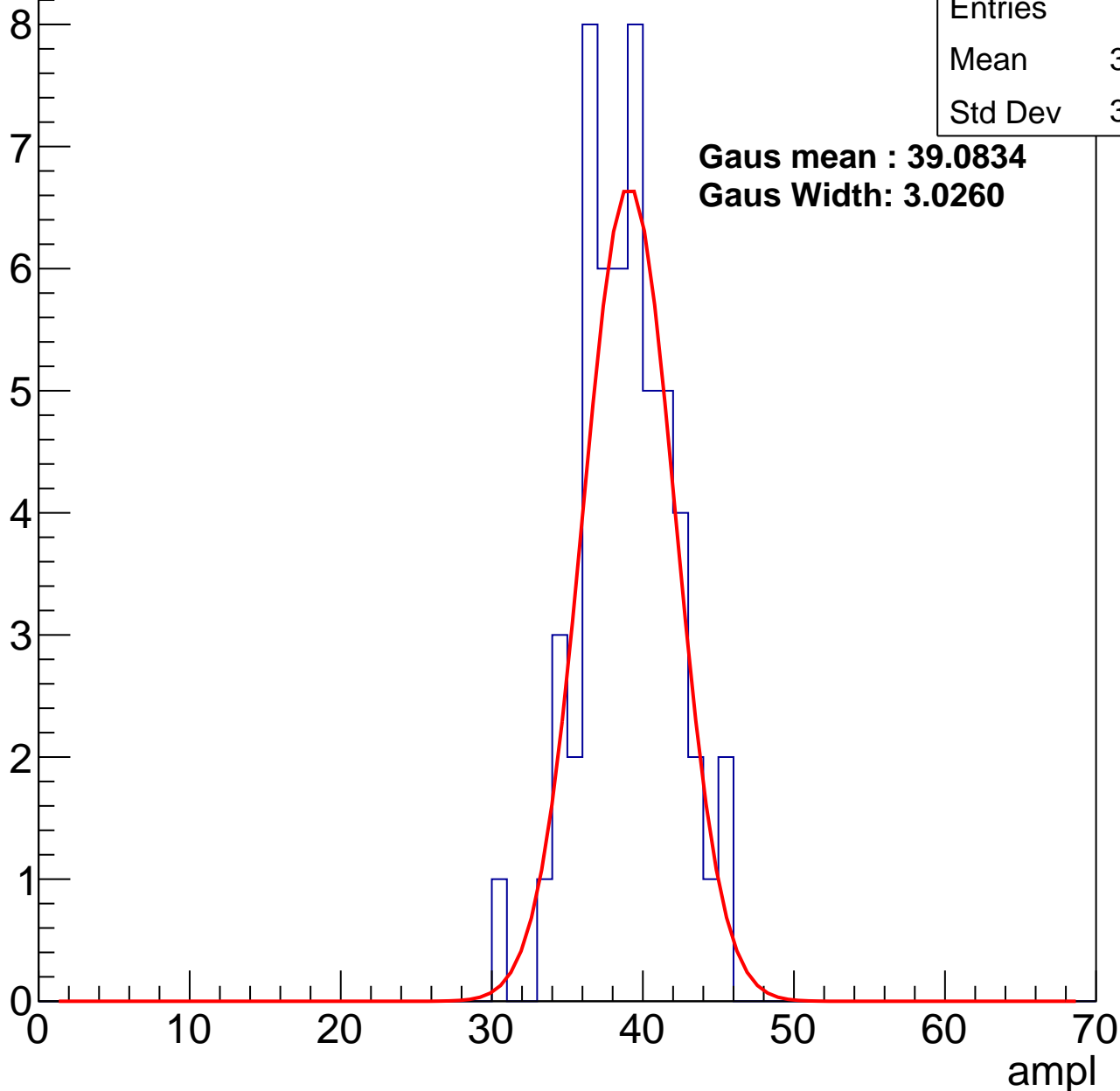
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	38.48
Std Dev	3.047

Gaus mean : 39.0834

Gaus Width: 3.0260



B1L101S, U26-ch125, adc2

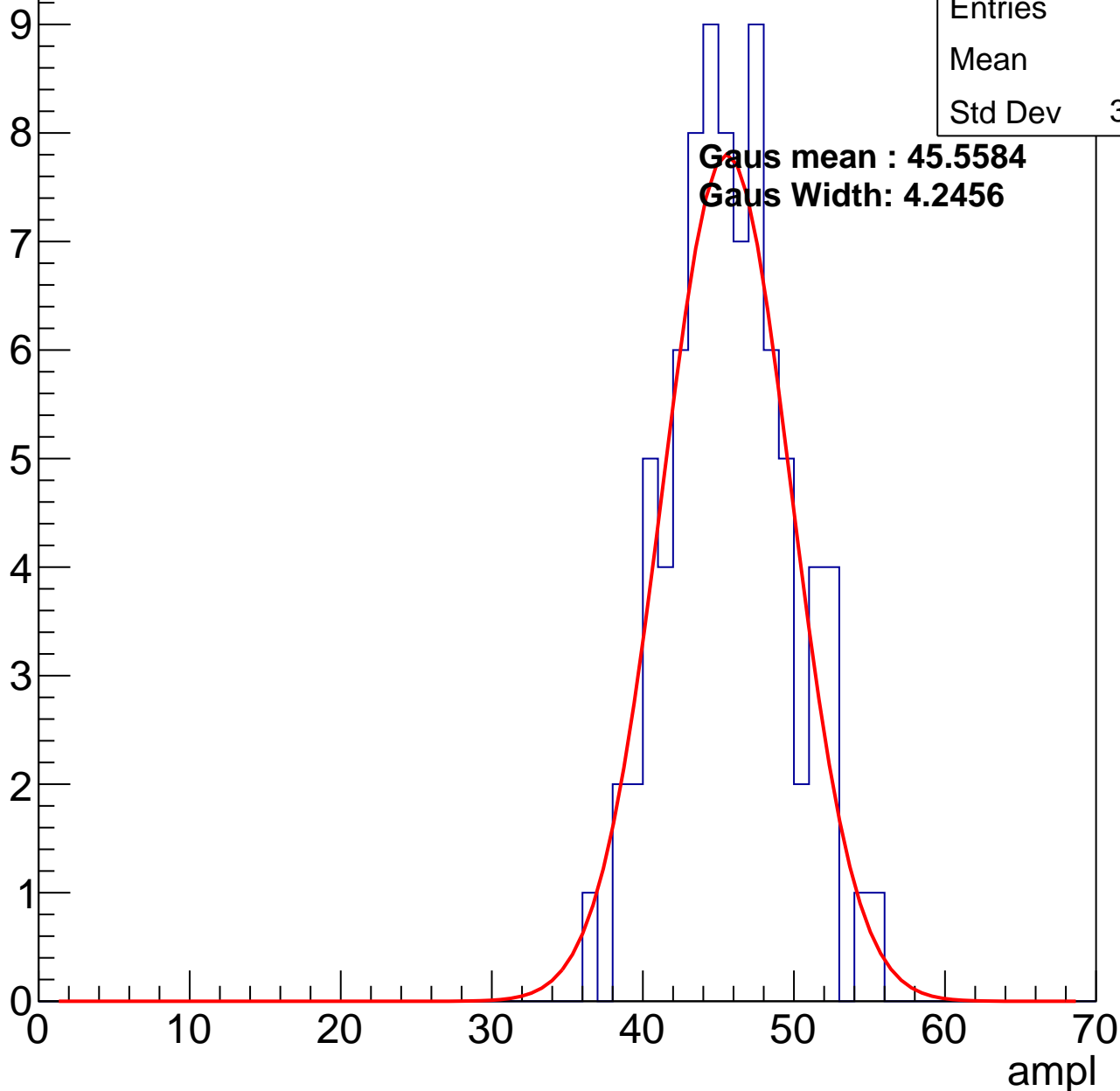
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	45.3
Std Dev	3.897

Gaus mean : 45.5584

Gaus Width: 4.2456

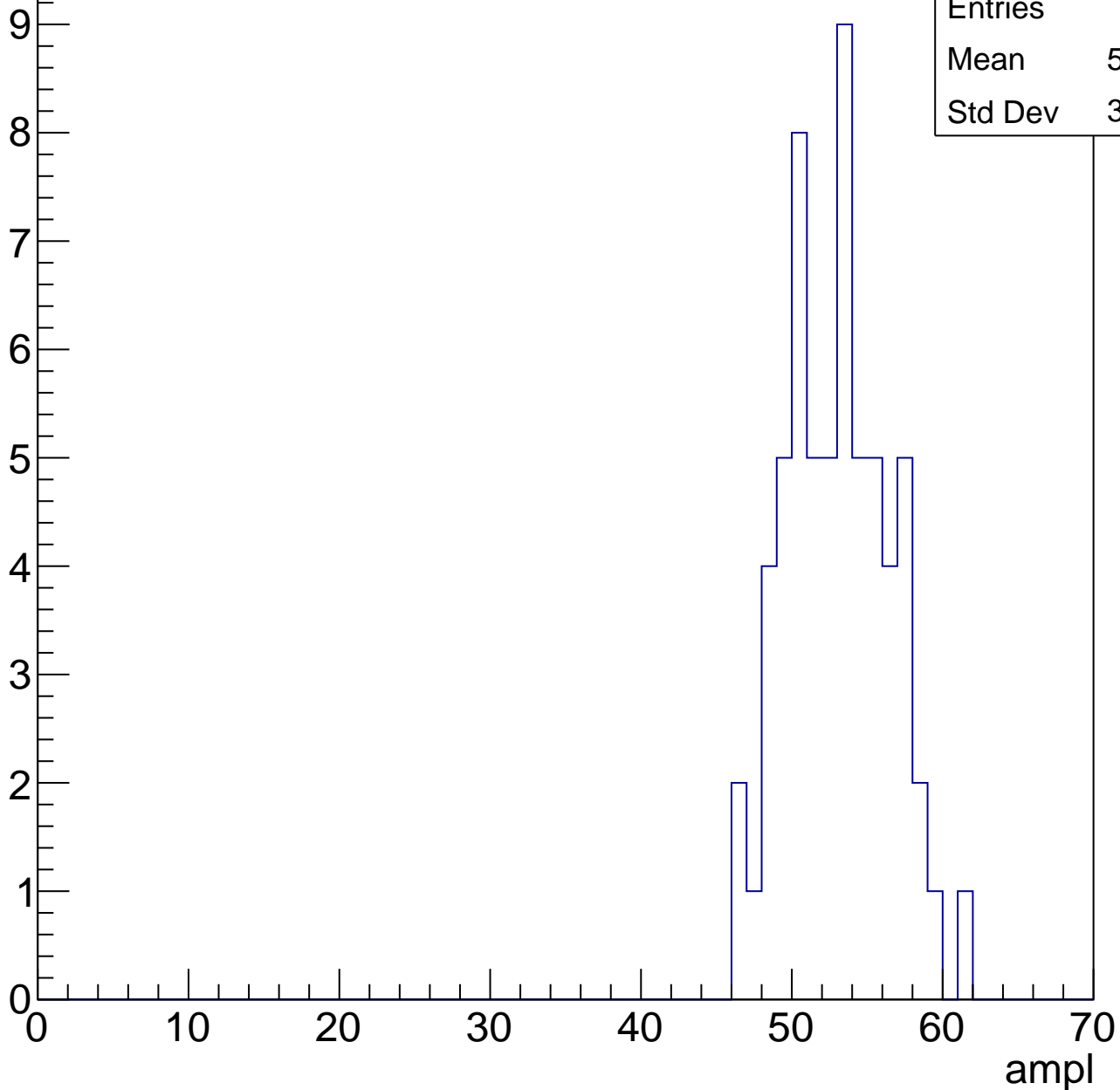


B1L101S, U26-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

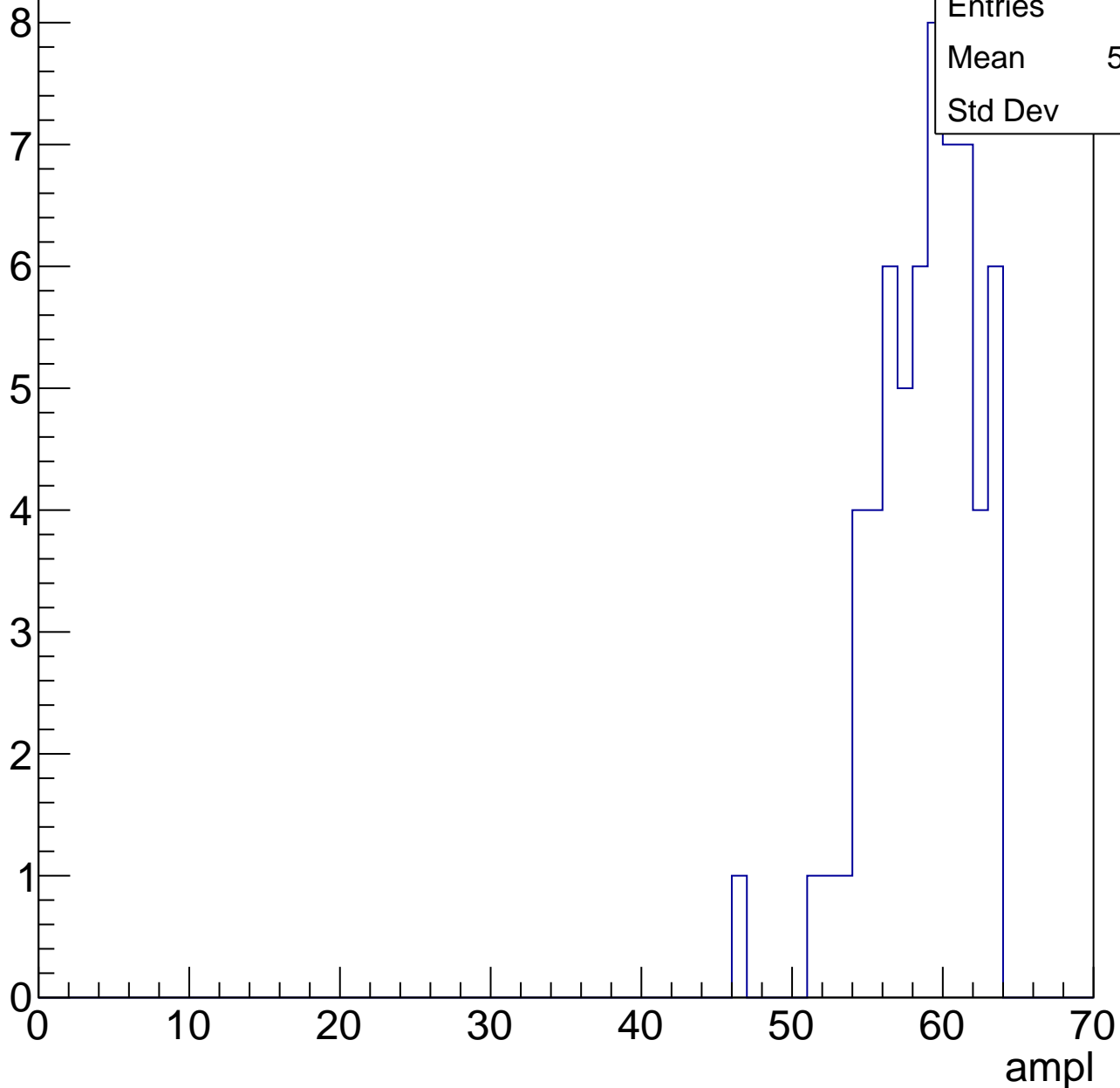
Entries	62
Mean	52.55
Std Dev	3.334



B1L101S, U26-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

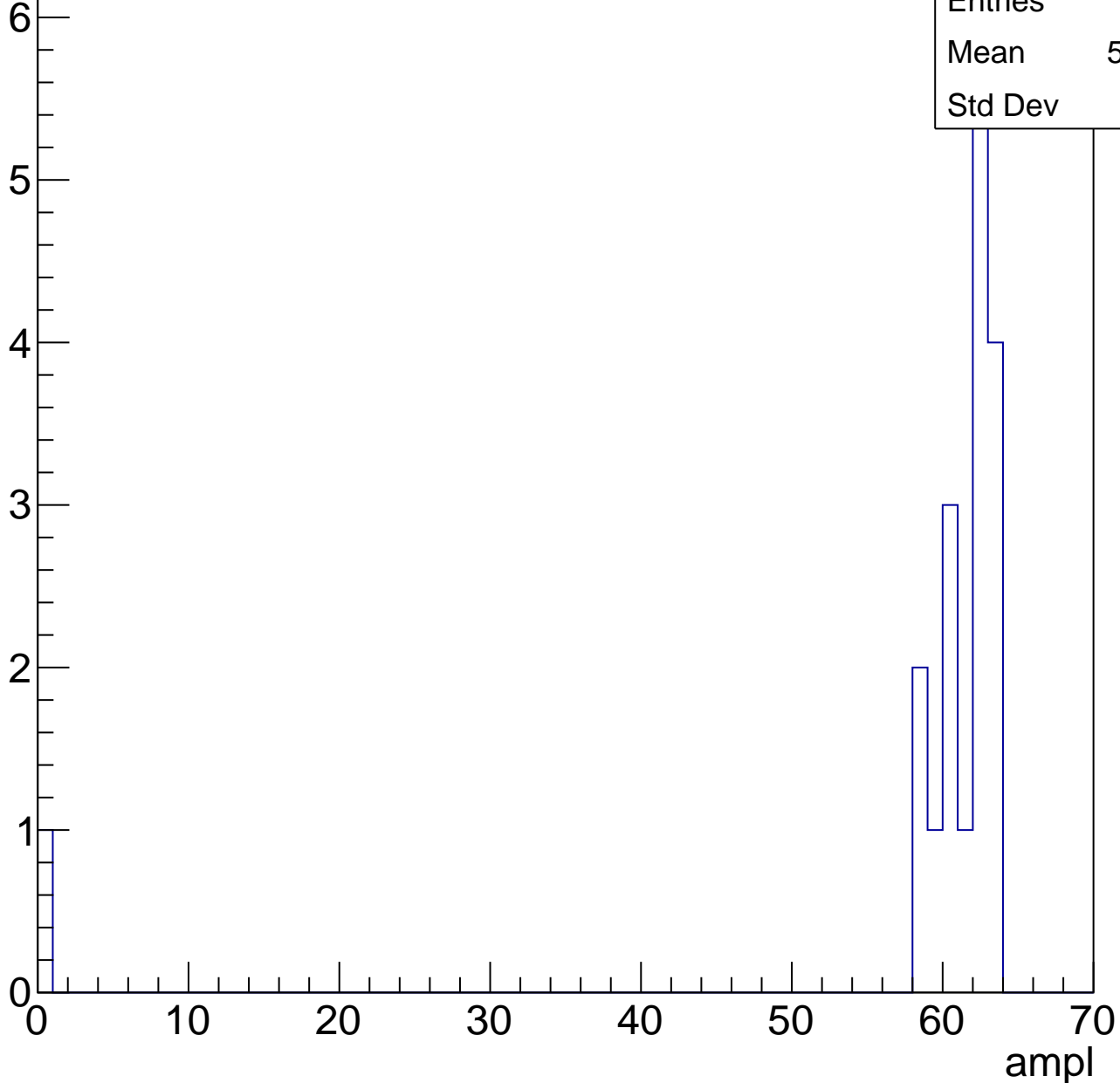


B1L101S, U26-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	57.78
Std Dev	14.1



B1L101S, U26-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U26-ch126, adc0

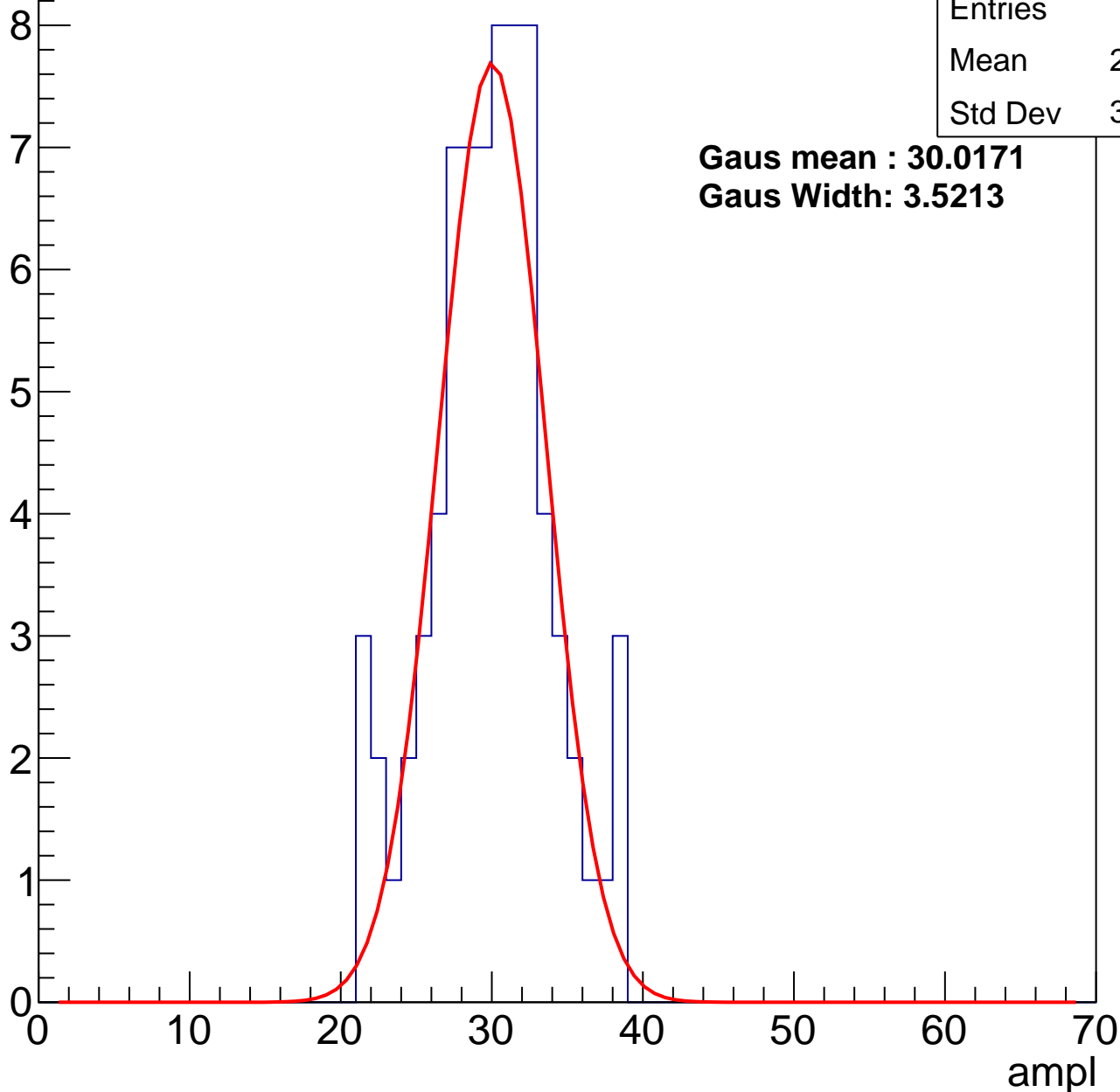
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.46
Std Dev	3.956

Gaus mean : 30.0171

Gaus Width: 3.5213



B1L101S, U26-ch126, adc1

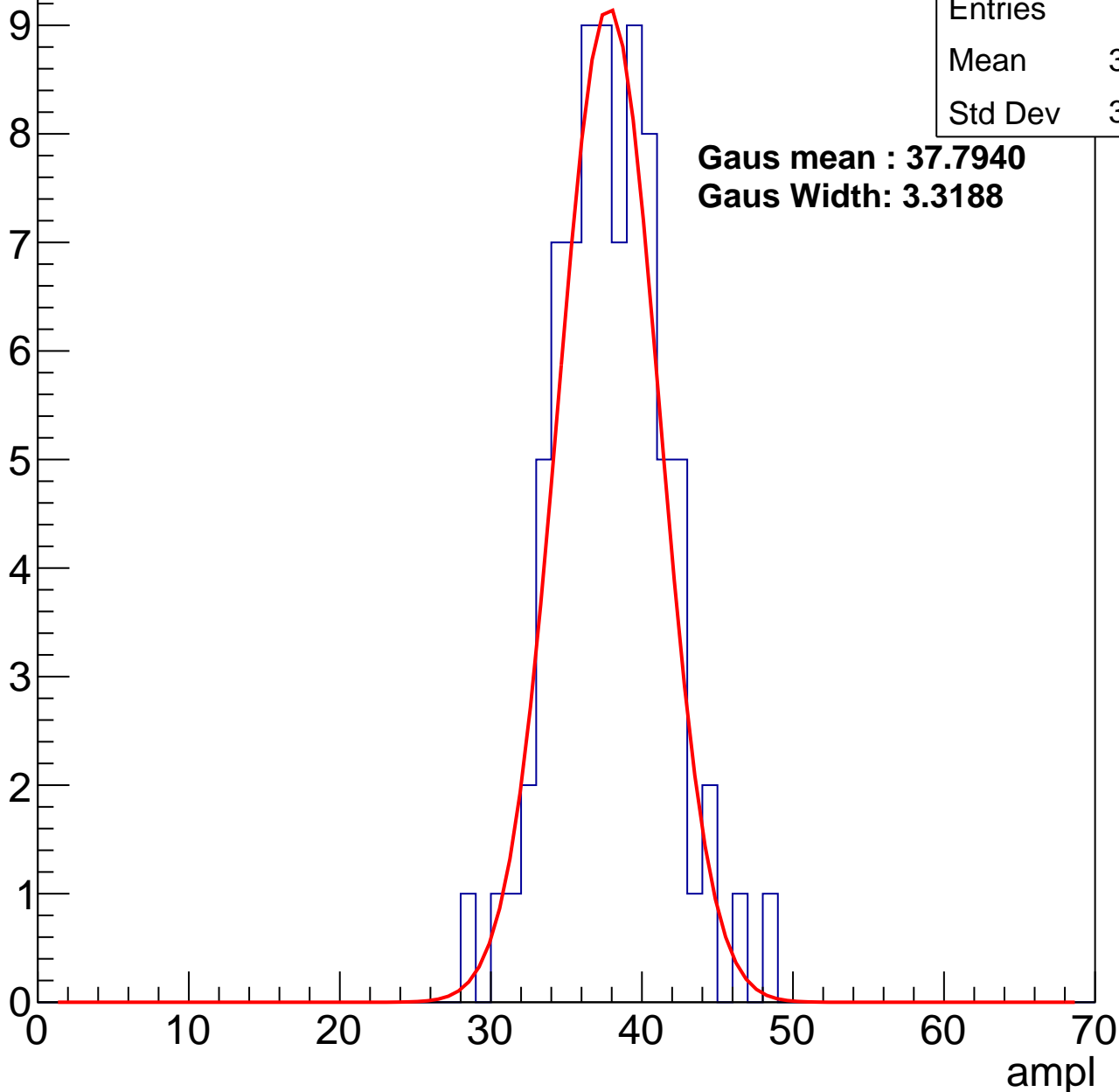
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.47
Std Dev	3.573

Gaus mean : 37.7940

Gaus Width: 3.3188



B1L101S, U26-ch126, adc2

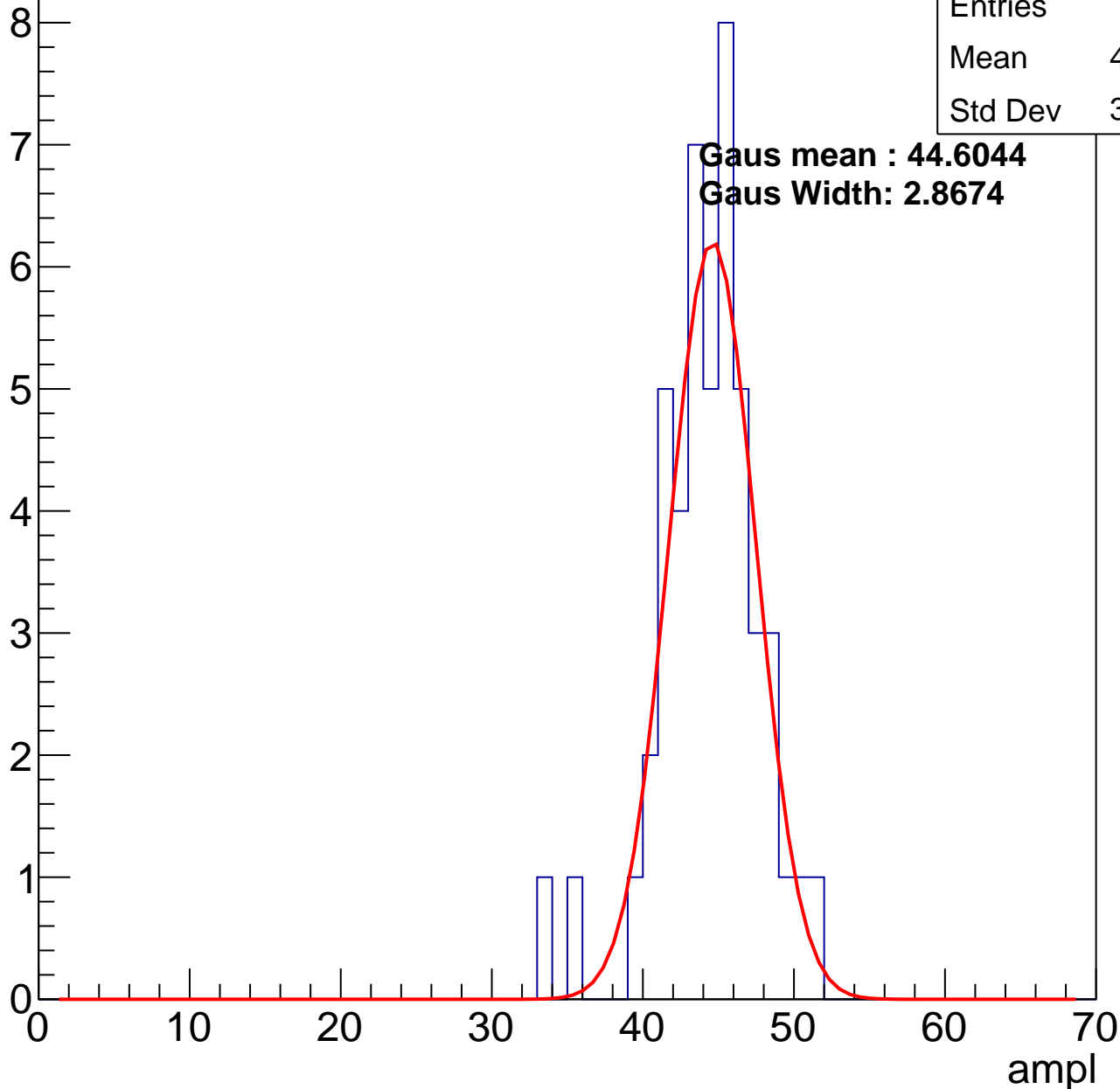
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	43.88
Std Dev	3.352

Gaus mean : 44.6044

Gaus Width: 2.8674

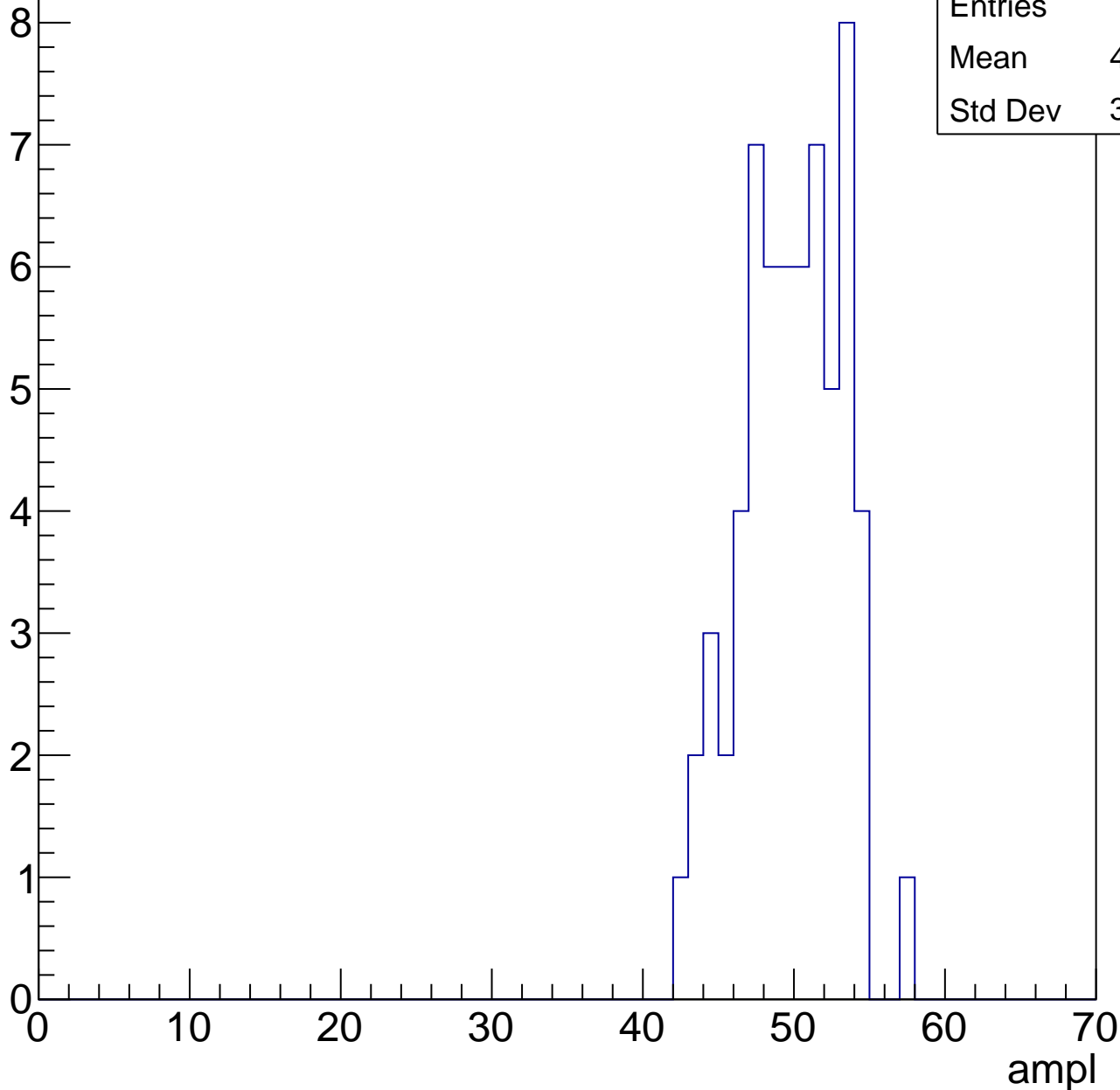


B1L101S, U26-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.34
Std Dev	3.267



B1L101S, U26-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

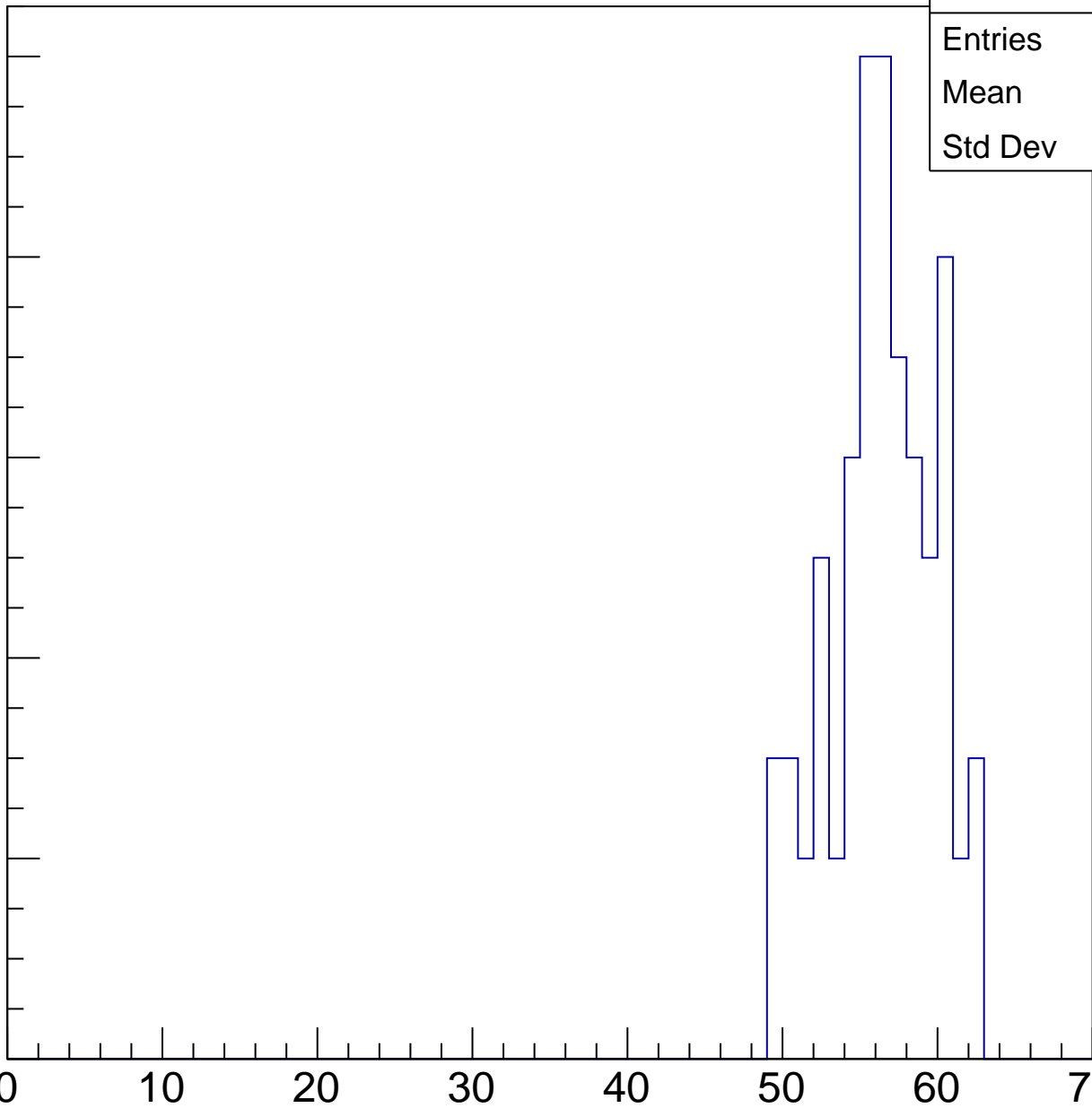
Entries	72
Mean	55.96
Std Dev	3.331

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

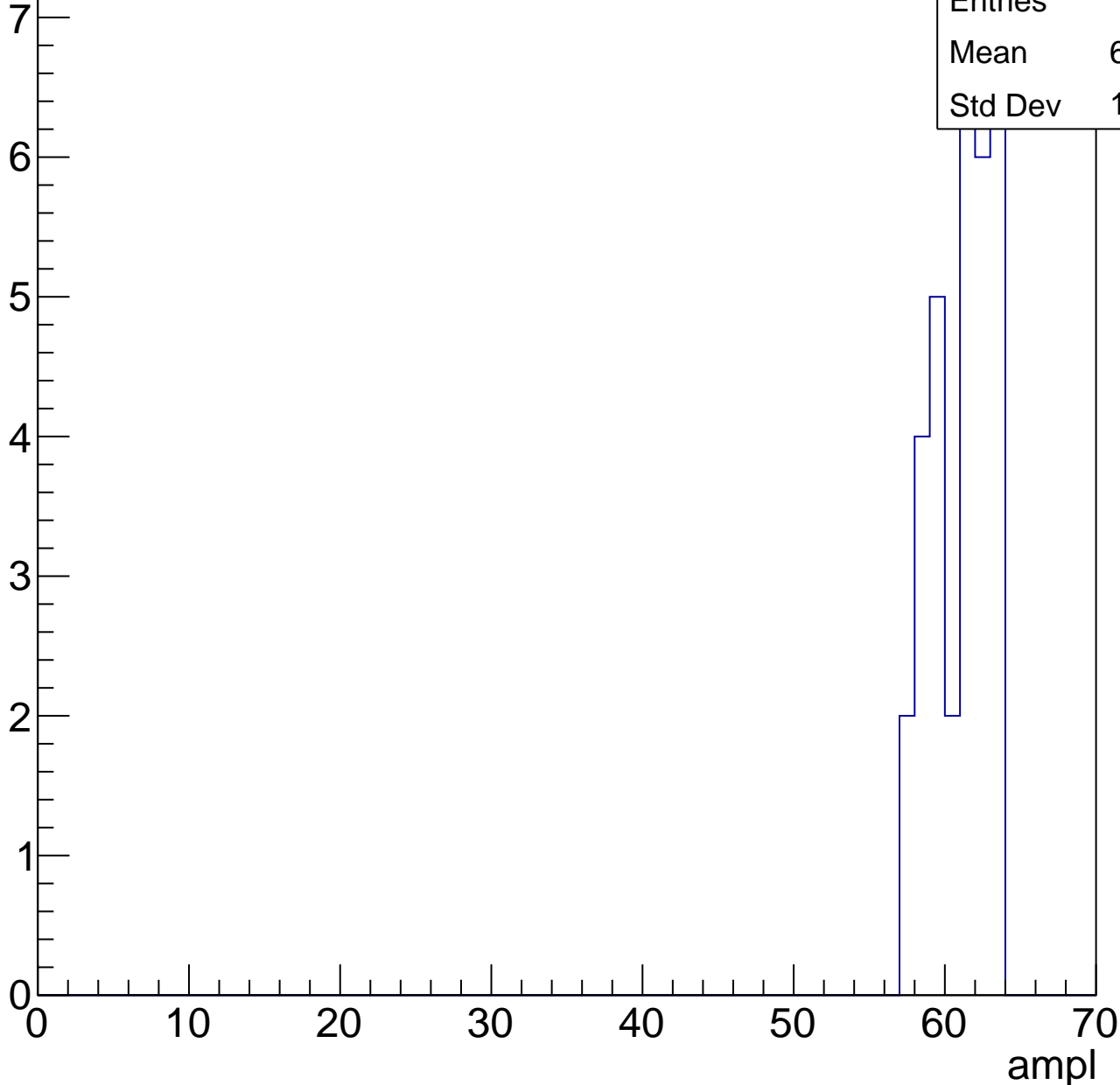


B1L101S, U26-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.64
Std Dev	1.904



B1L101S, U26-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	52
Std Dev	23.27

B1L101S, U26-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U26-ch127, adc0

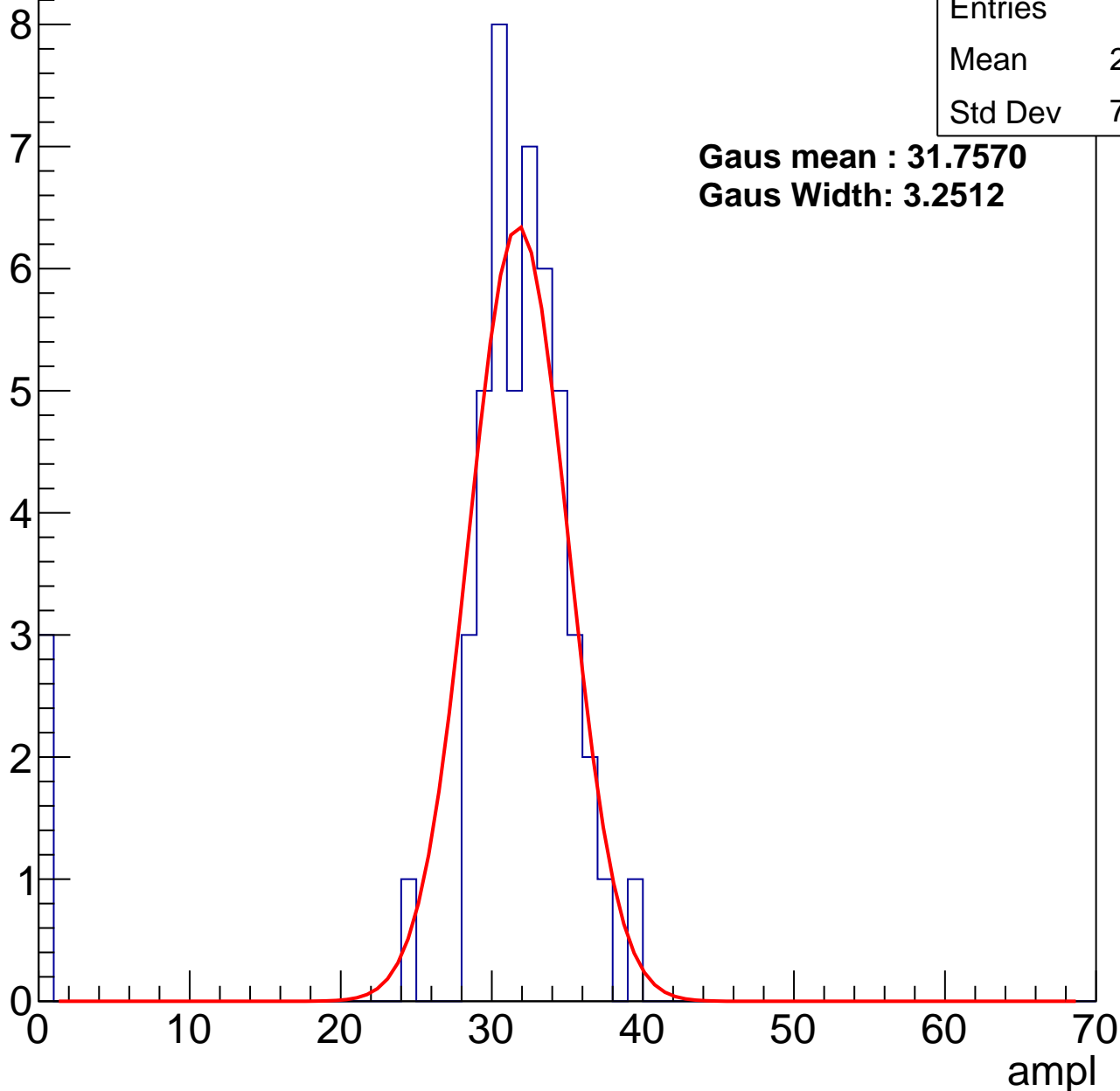
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	29.86
Std Dev	7.995

Gaus mean : 31.7570

Gaus Width: 3.2512



B1L101S, U26-ch127, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	38.55
Std Dev	3.747

Gaus mean : 39.2765

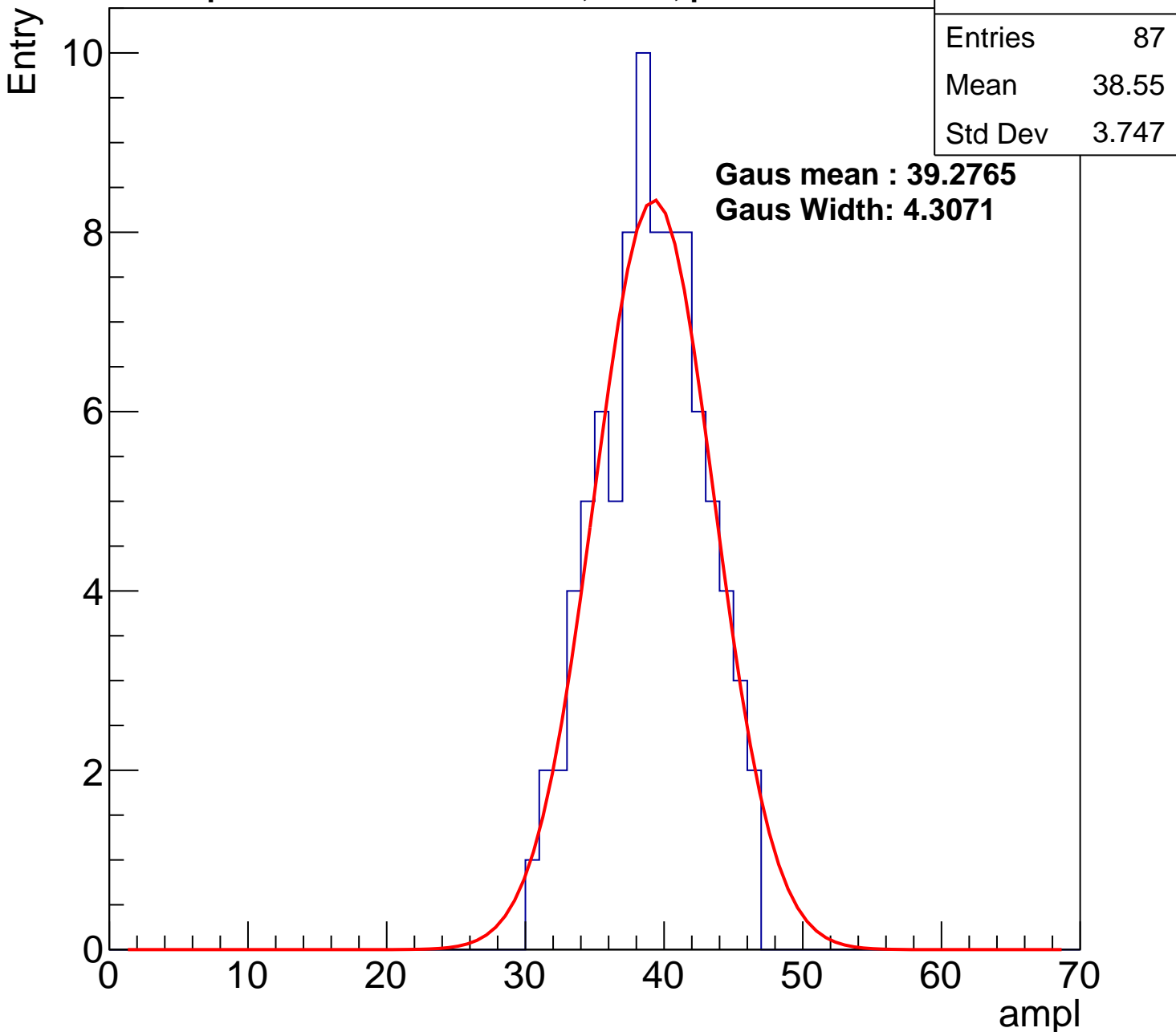
Gaus Width: 4.3071

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U26-ch127, adc2

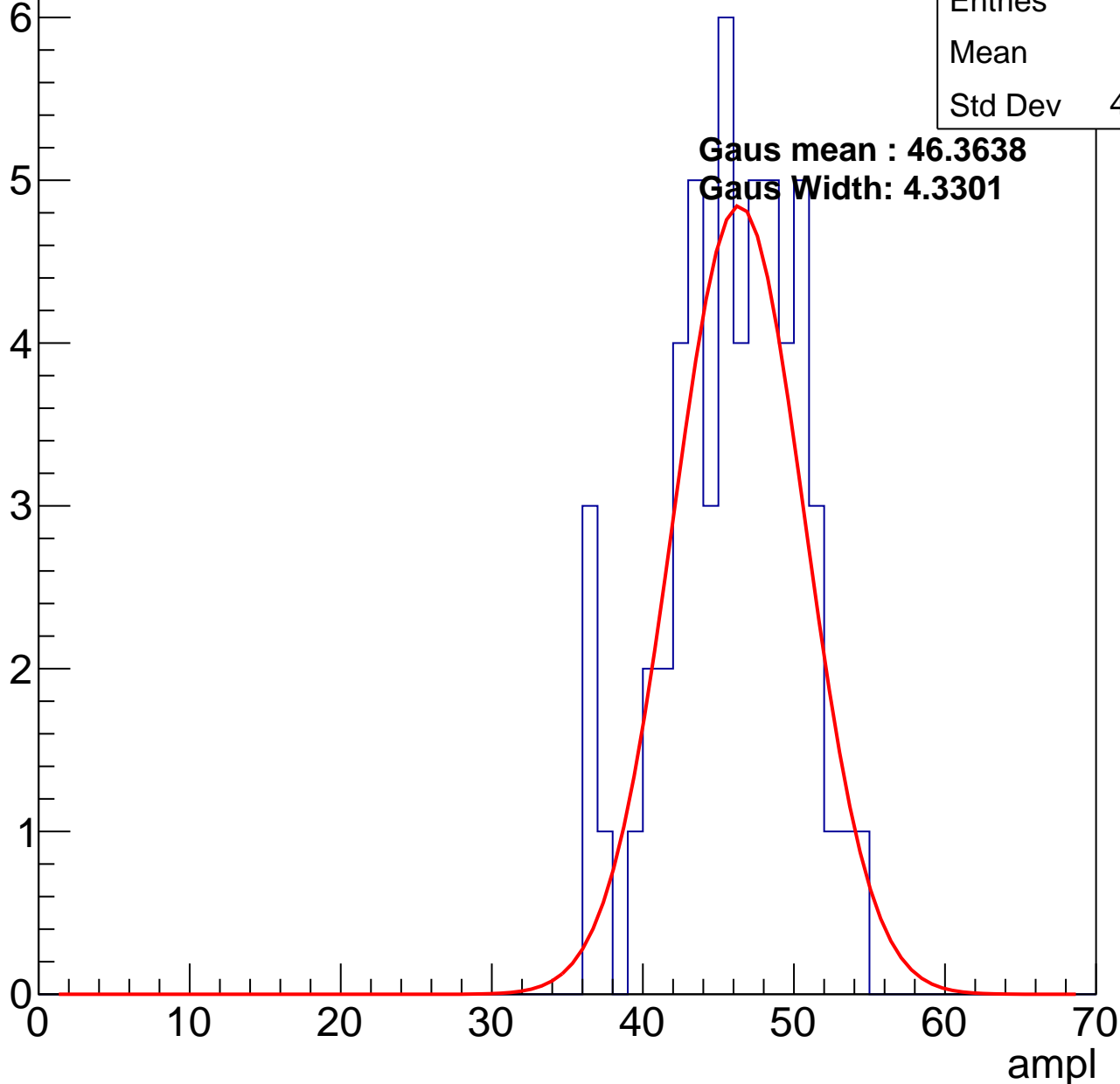
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	45.5
Std Dev	4.289

Gaus mean : 46.3638

Gaus Width: 4.3301

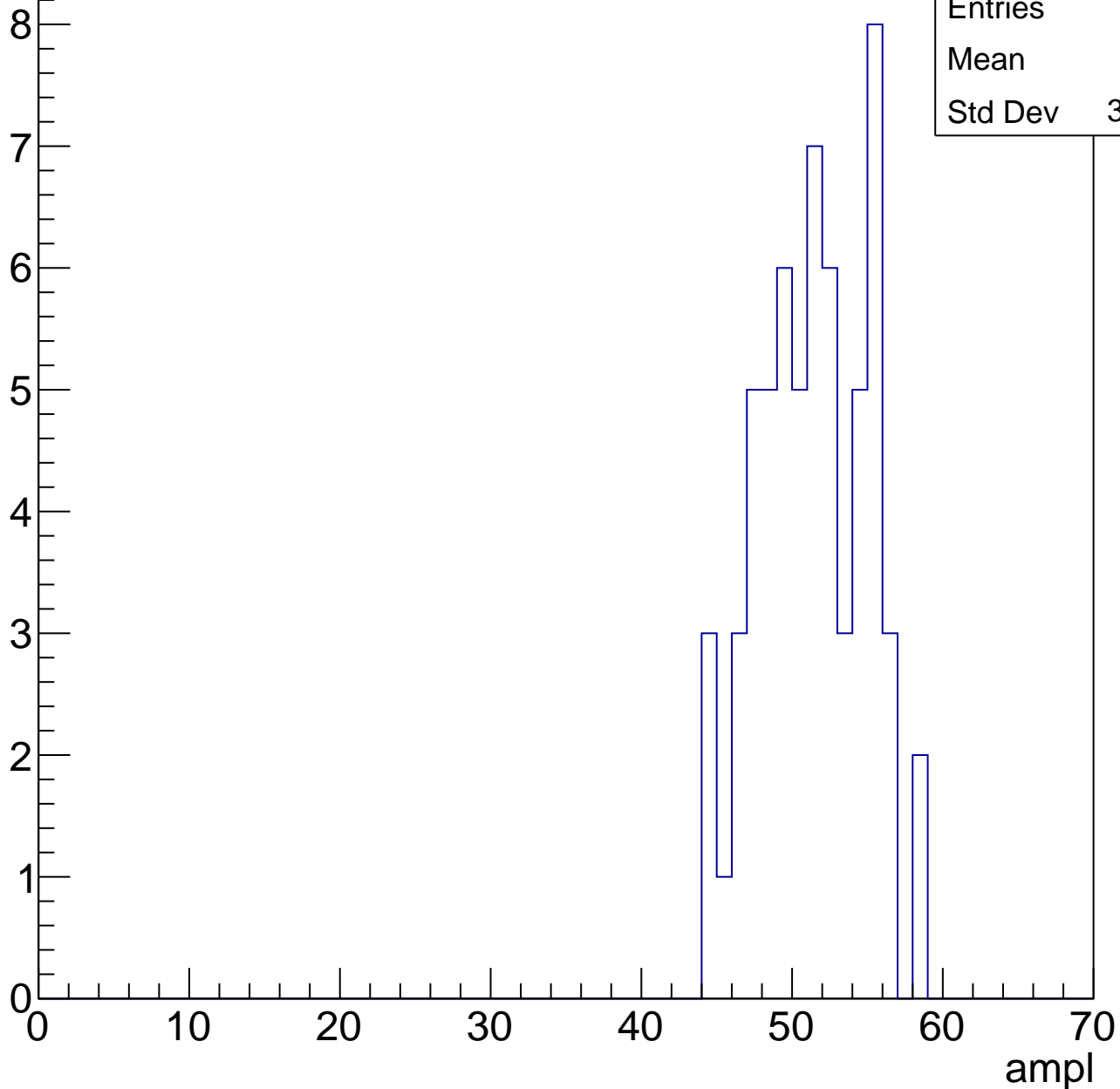


B1L101S, U26-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

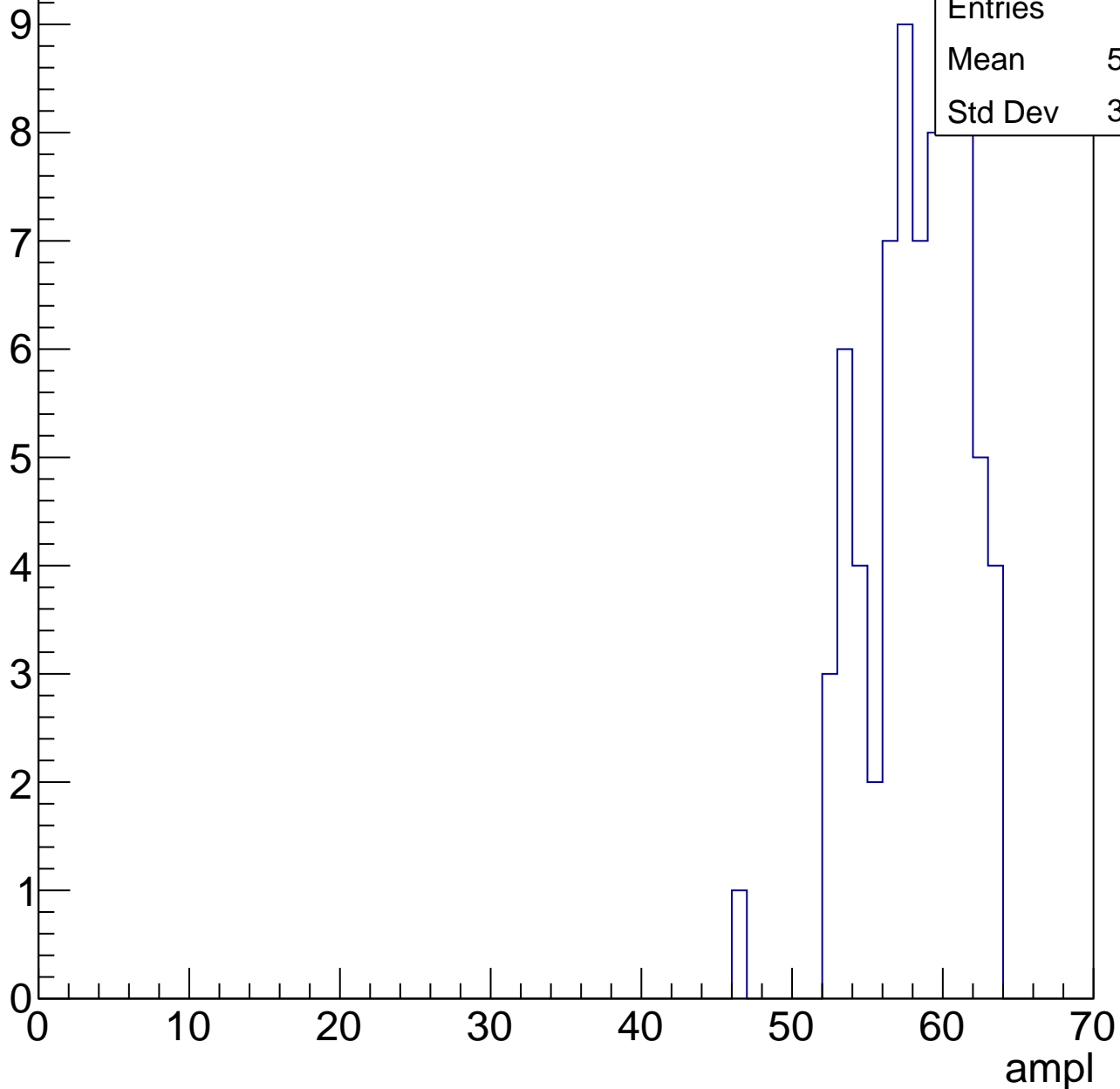
Entries	62
Mean	50.9
Std Dev	3.536



B1L101S, U26-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

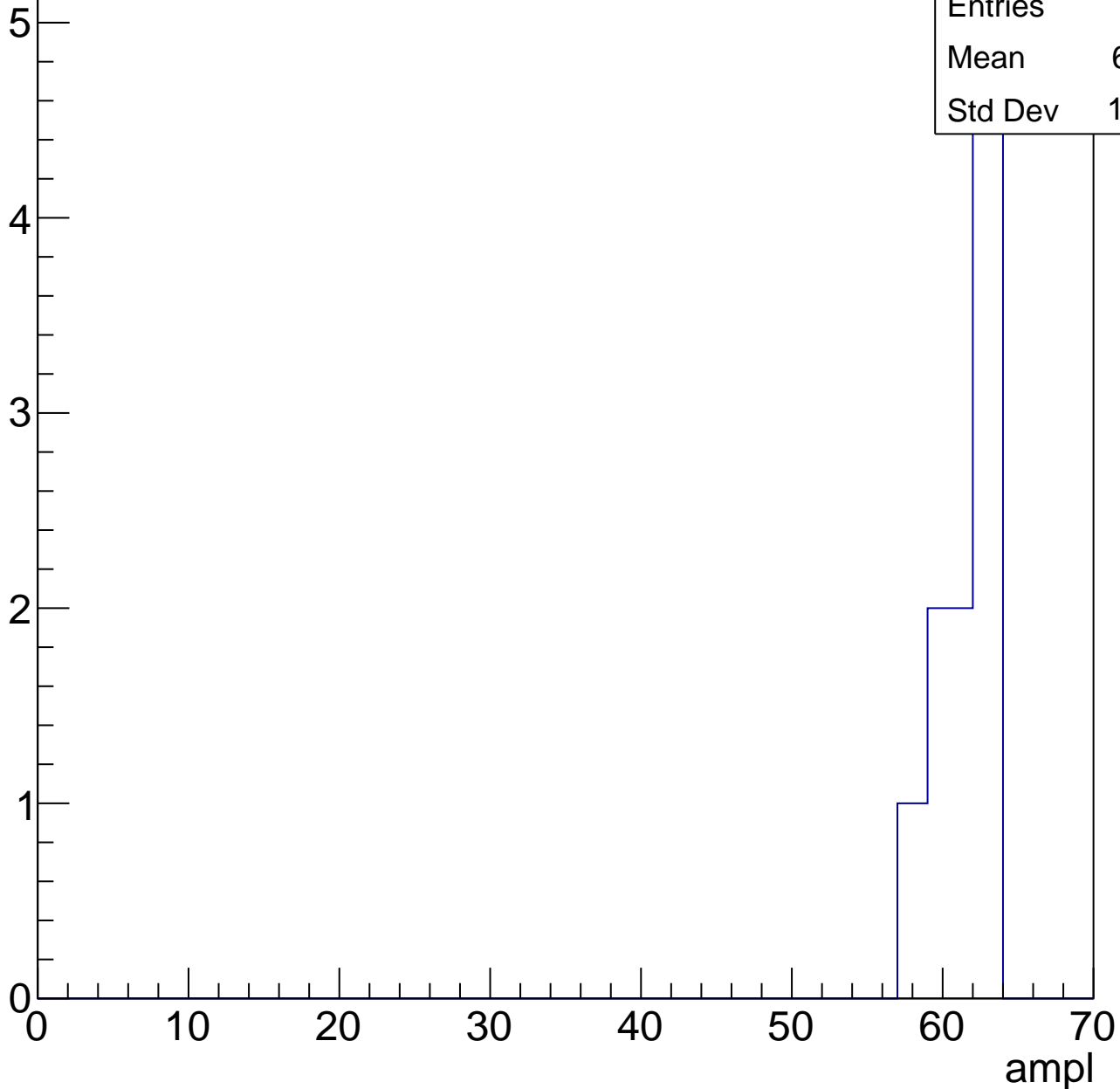


B1L101S, U26-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.11
Std Dev	1.822



B1L101S, U26-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

