

B0L001S, U15-ch0, adc0

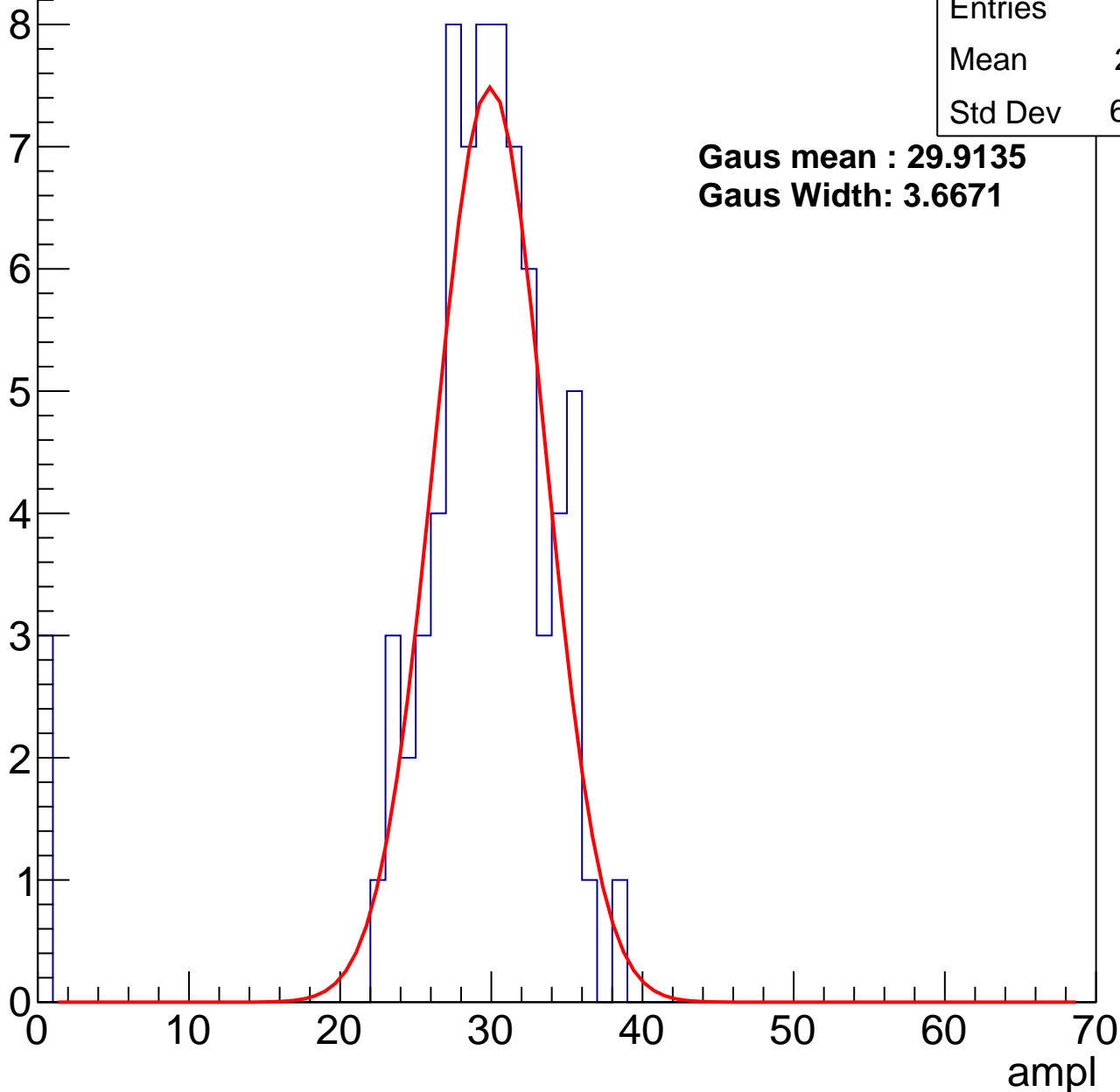
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	28.31
Std Dev	6.744

Gaus mean : 29.9135

Gaus Width: 3.6671



B0L001S, U15-ch0, adc1

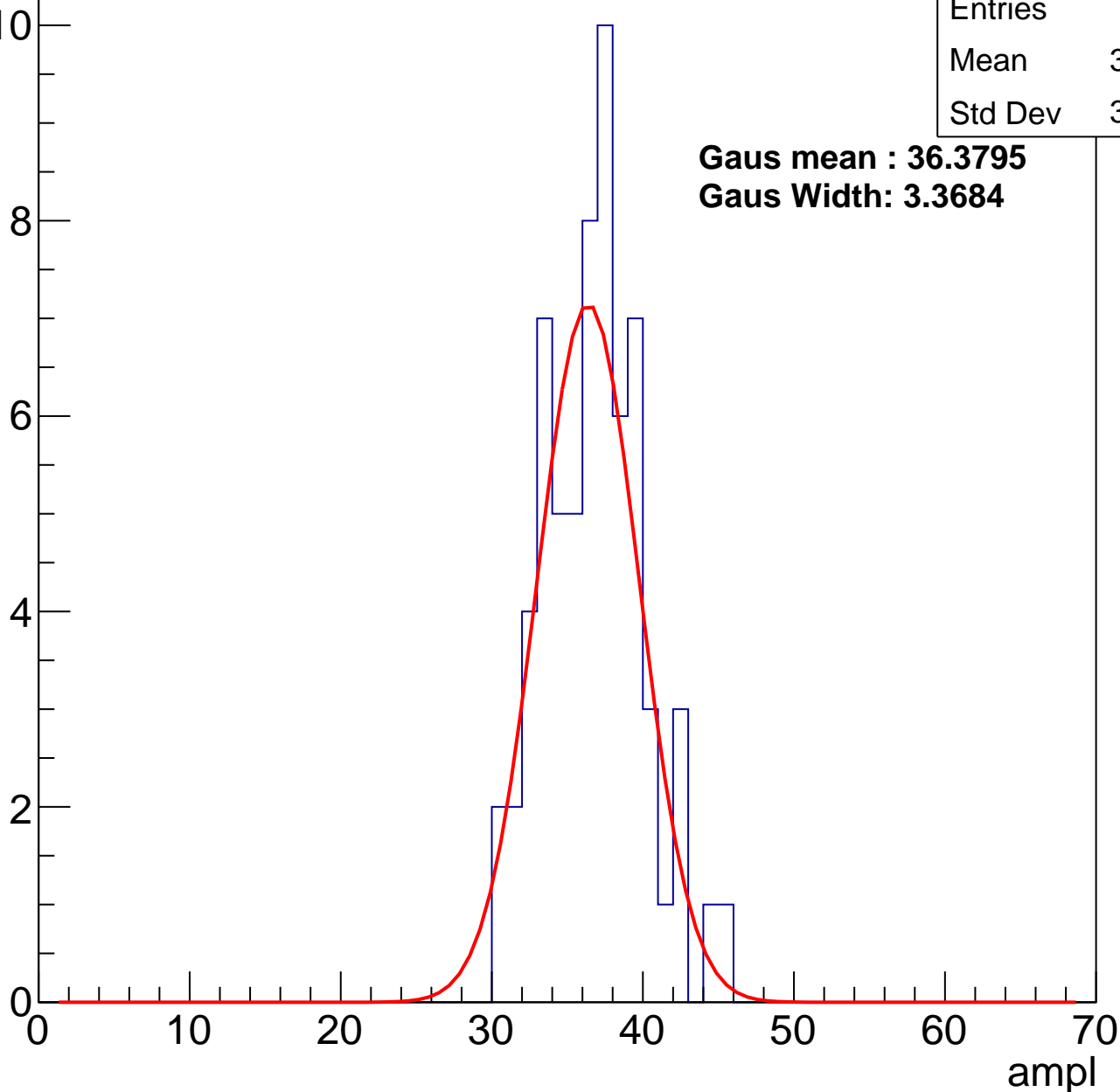
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.32
Std Dev	3.259

Gaus mean : 36.3795

Gaus Width: 3.3684



B0L001S, U15-ch0, adc2

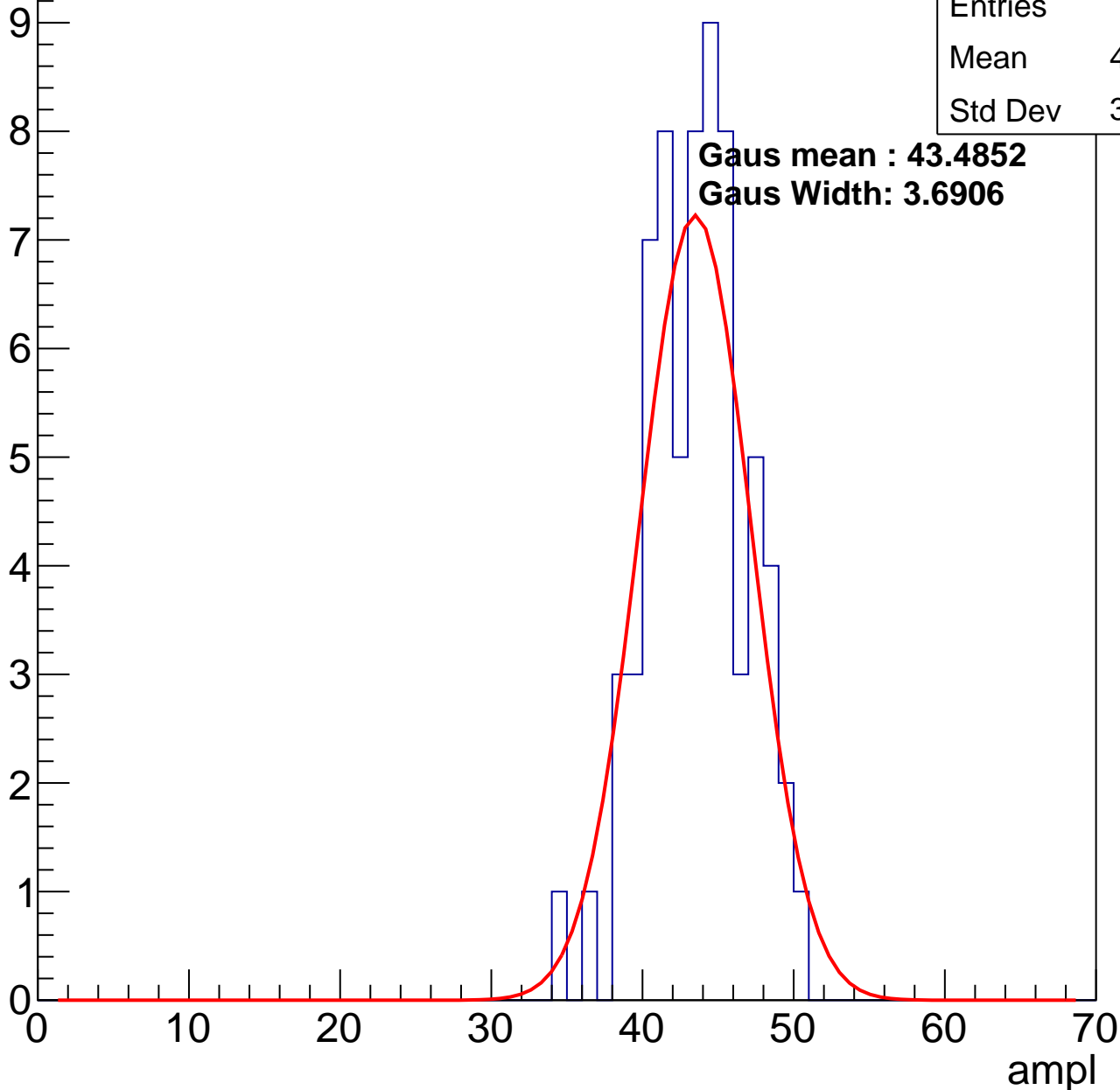
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.12
Std Dev	3.256

Gaus mean : 43.4852

Gaus Width: 3.6906

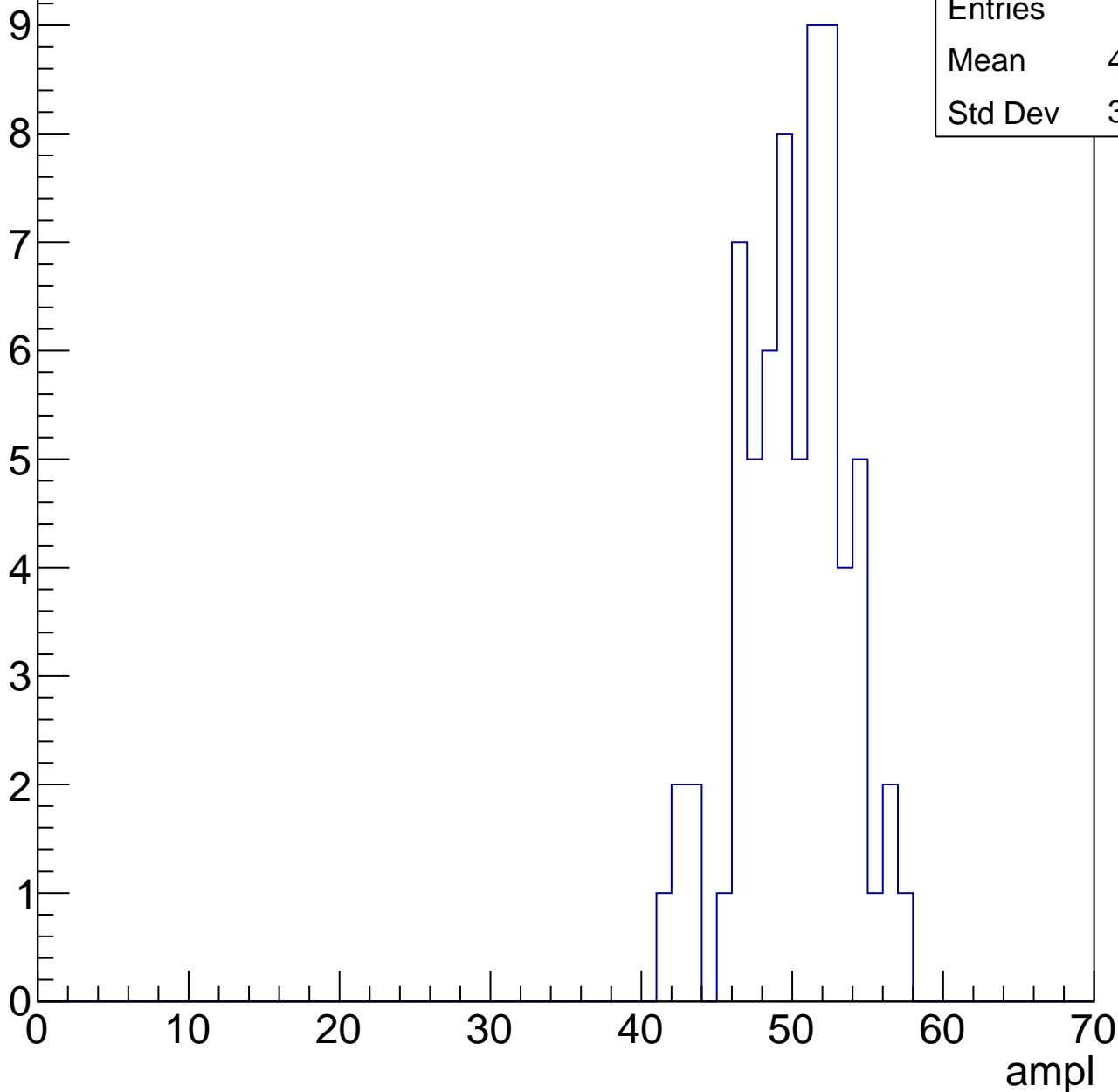


B0L001S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

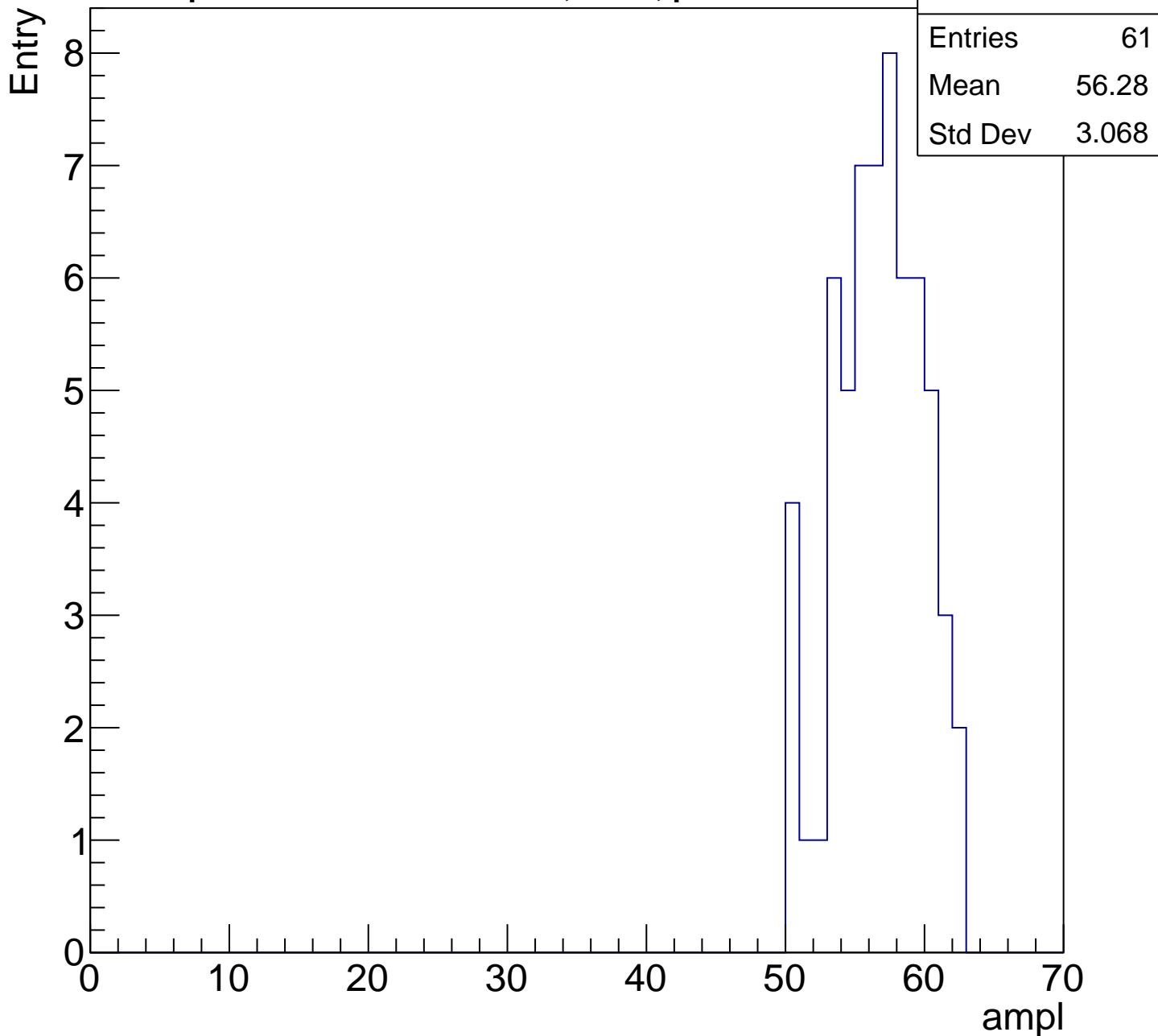
Entry

Entries	68
Mean	49.65
Std Dev	3.472



B0L001S, U15-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

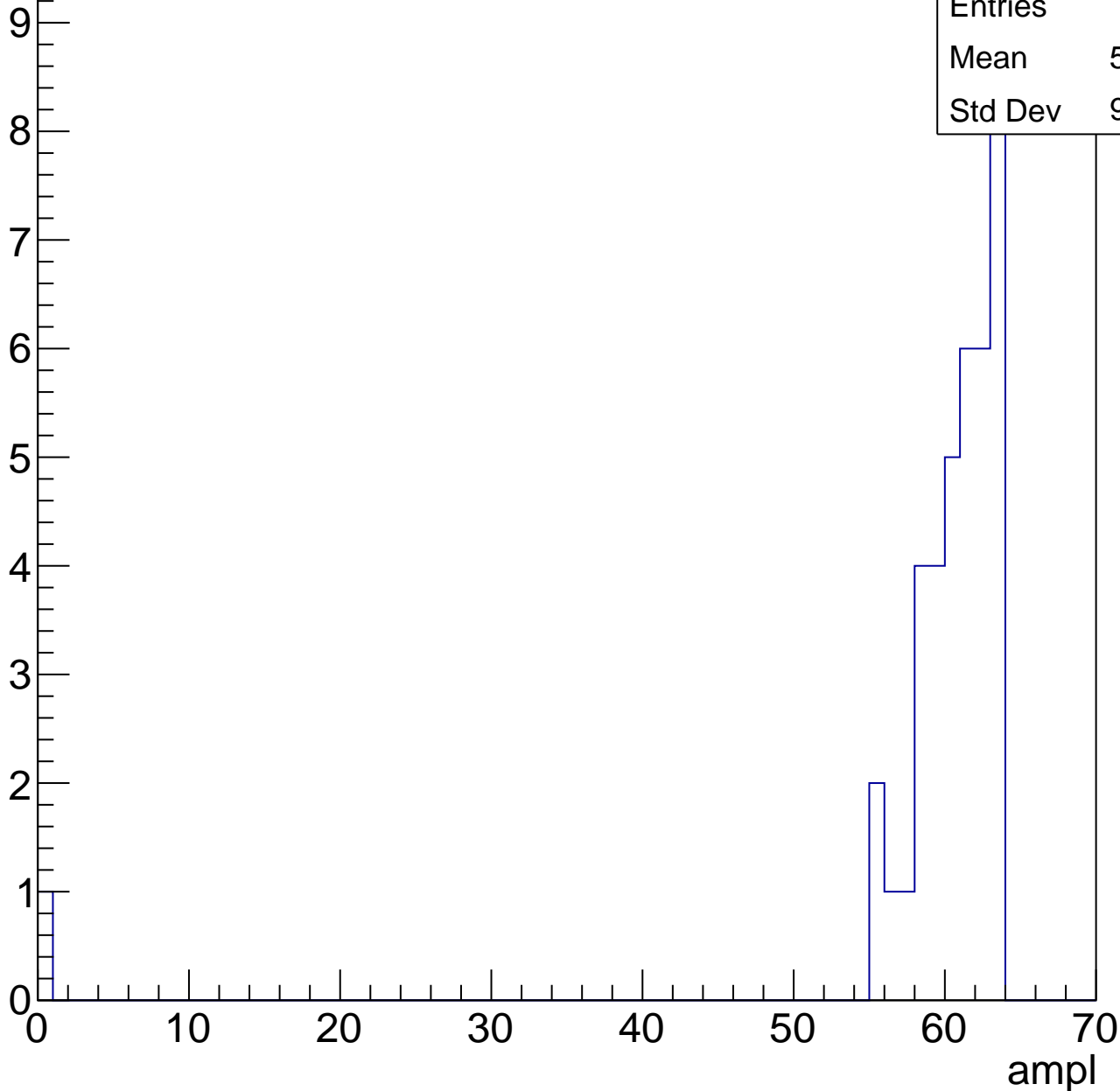


B0L001S, U15-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.87
Std Dev	9.814



B0L001S, U15-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

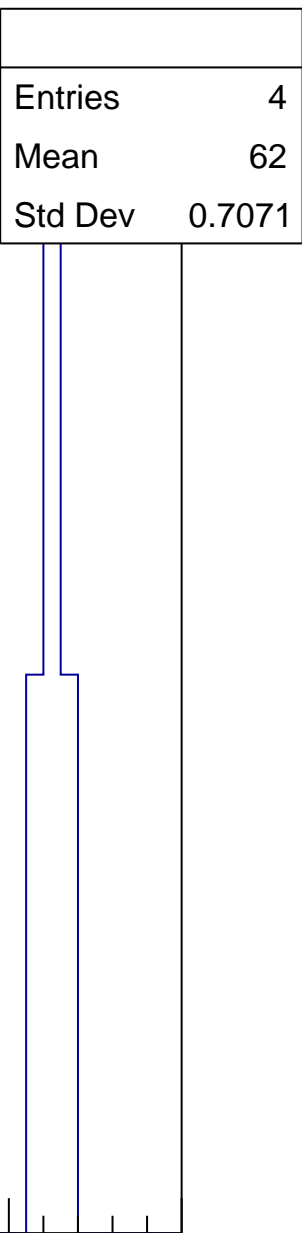
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	21
Std Dev	0

B0L001S, U15-ch1, adc0

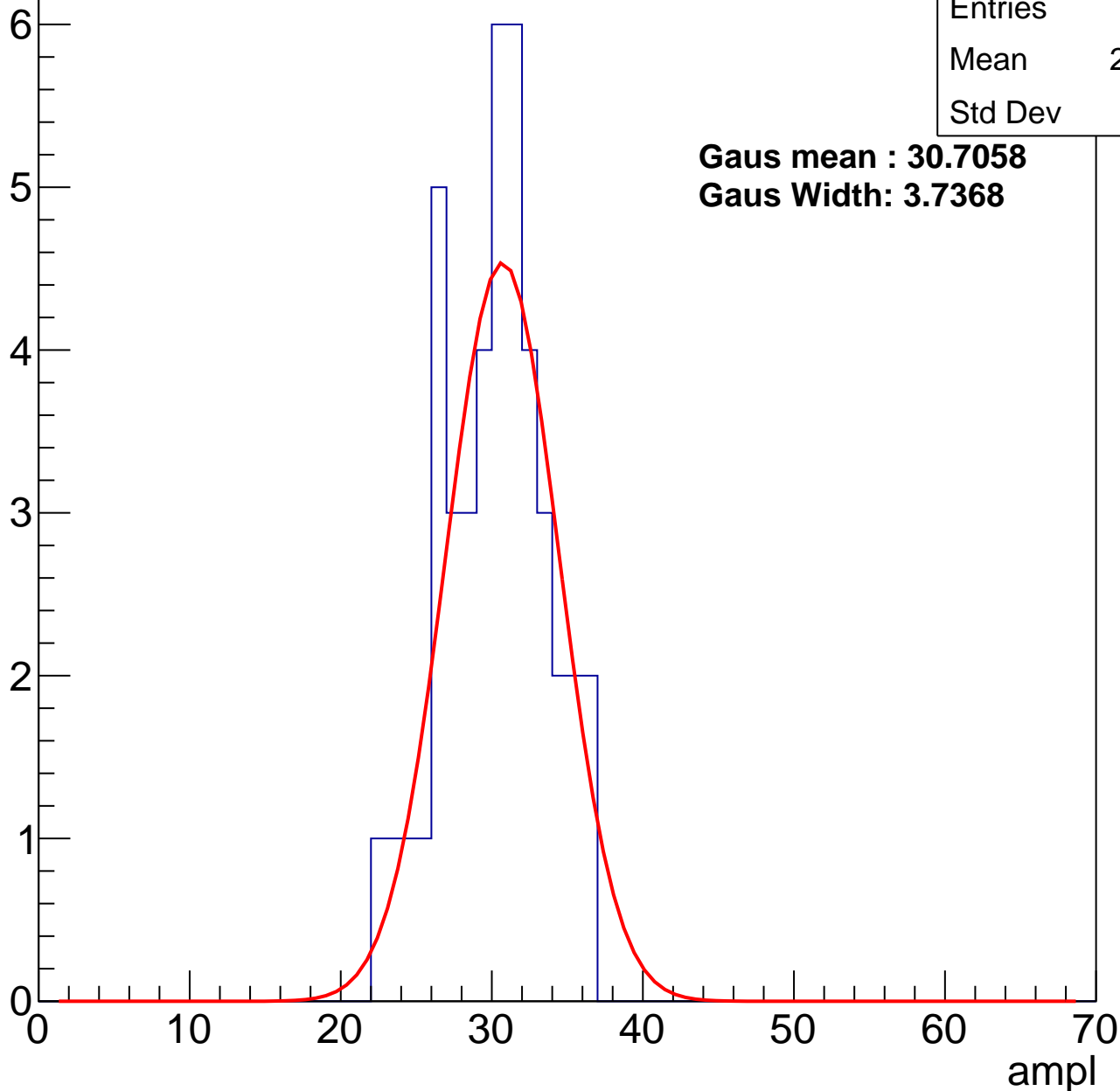
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	29.73
Std Dev	3.36

Gaus mean : 30.7058

Gaus Width: 3.7368



B0L001S, U15-ch1, adc1

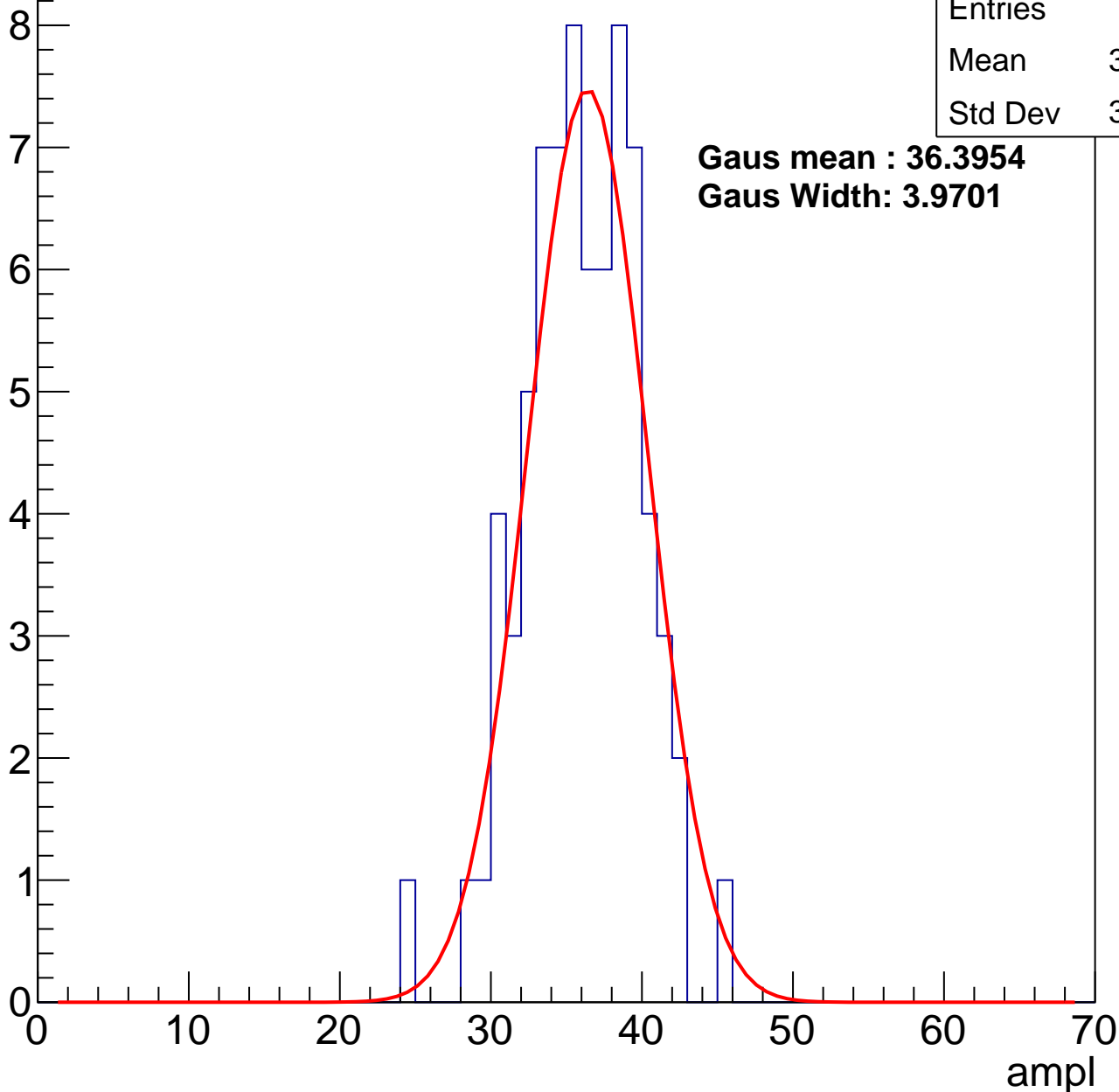
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	35.54
Std Dev	3.735

Gaus mean : 36.3954

Gaus Width: 3.9701



B0L001S, U15-ch1, adc2

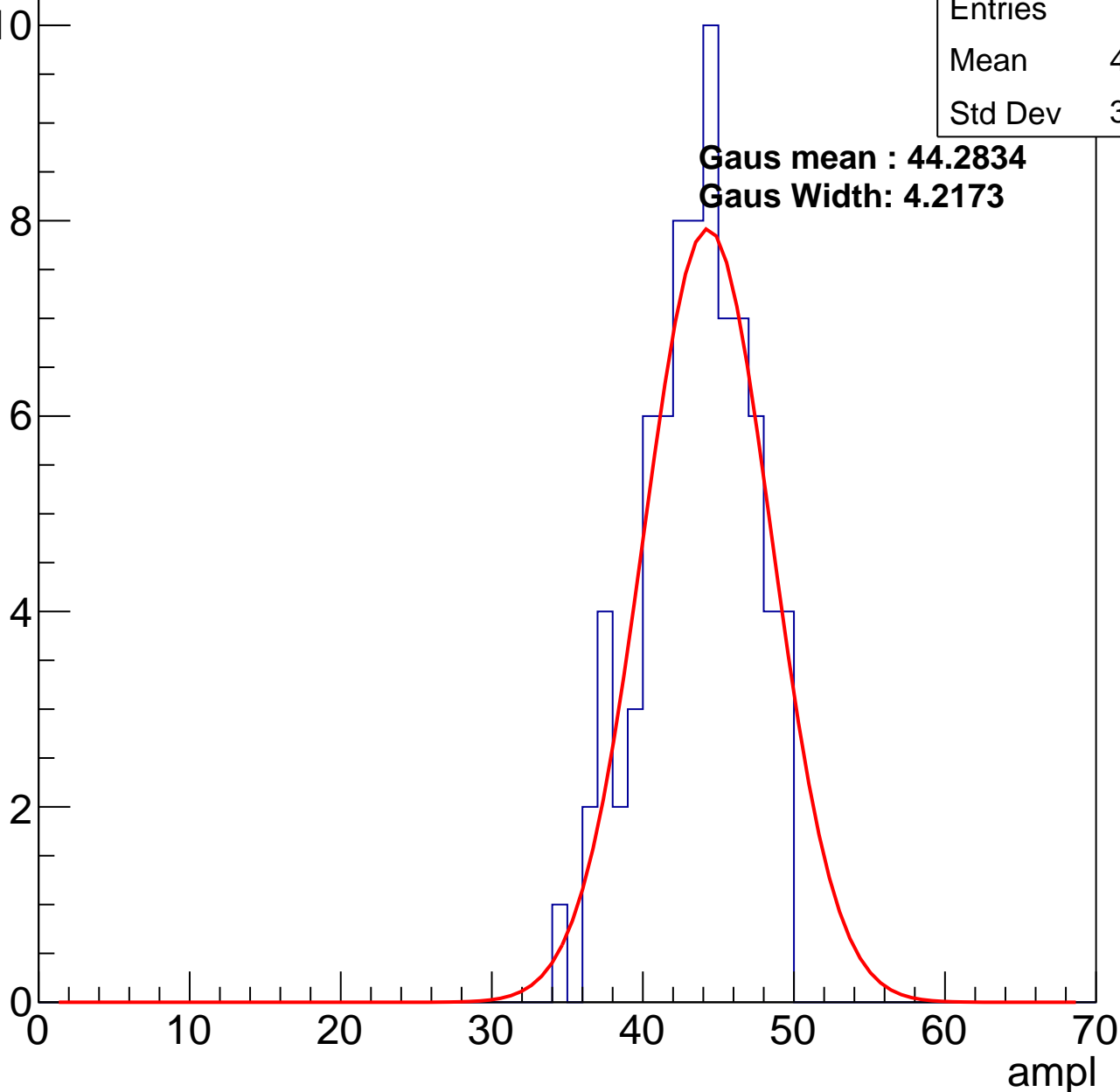
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	43.08
Std Dev	3.485

Gaus mean : 44.2834

Gaus Width: 4.2173

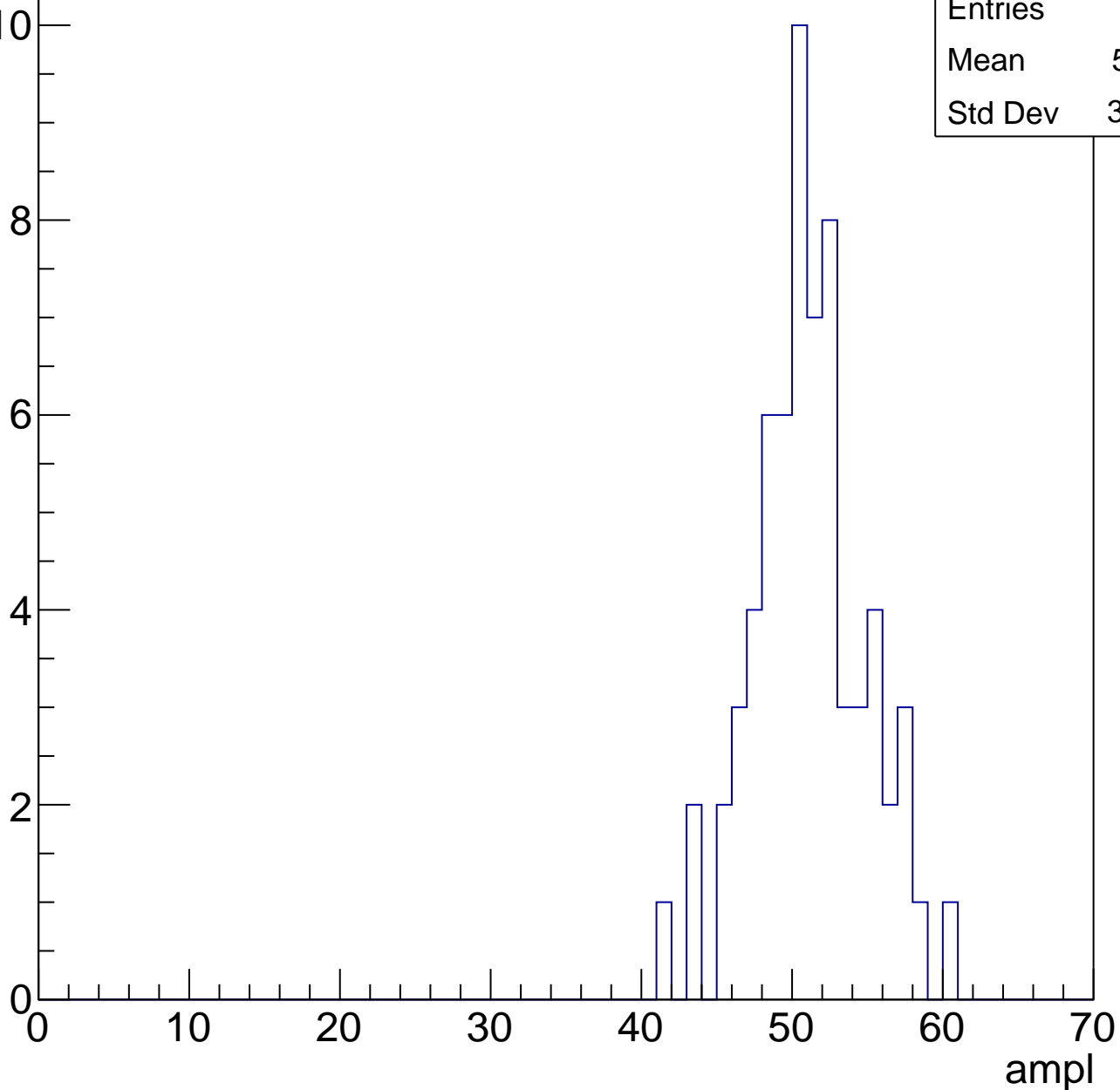


B0L001S, U15-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.61
Std Dev	3.725

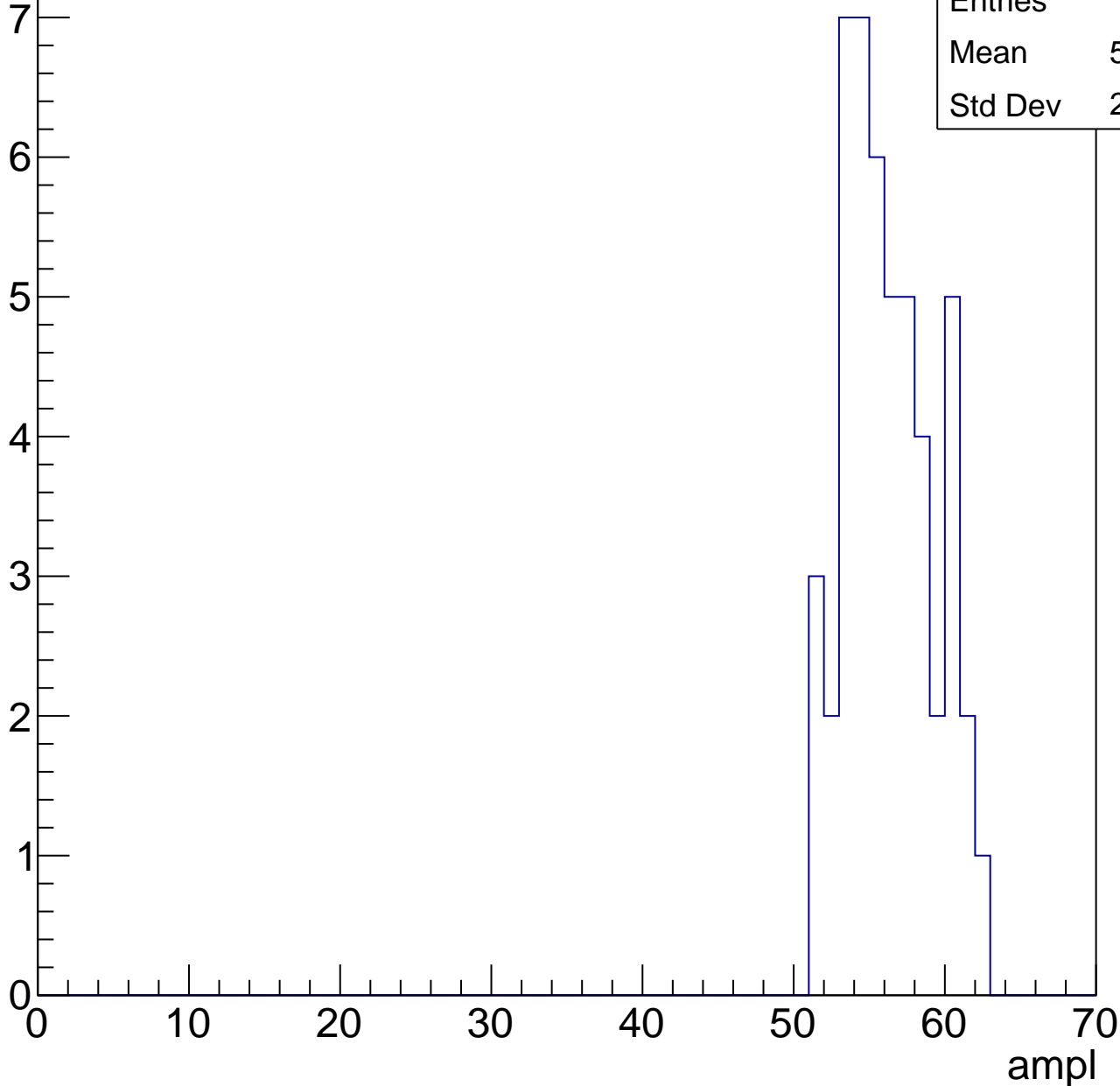


B0L001S, U15-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

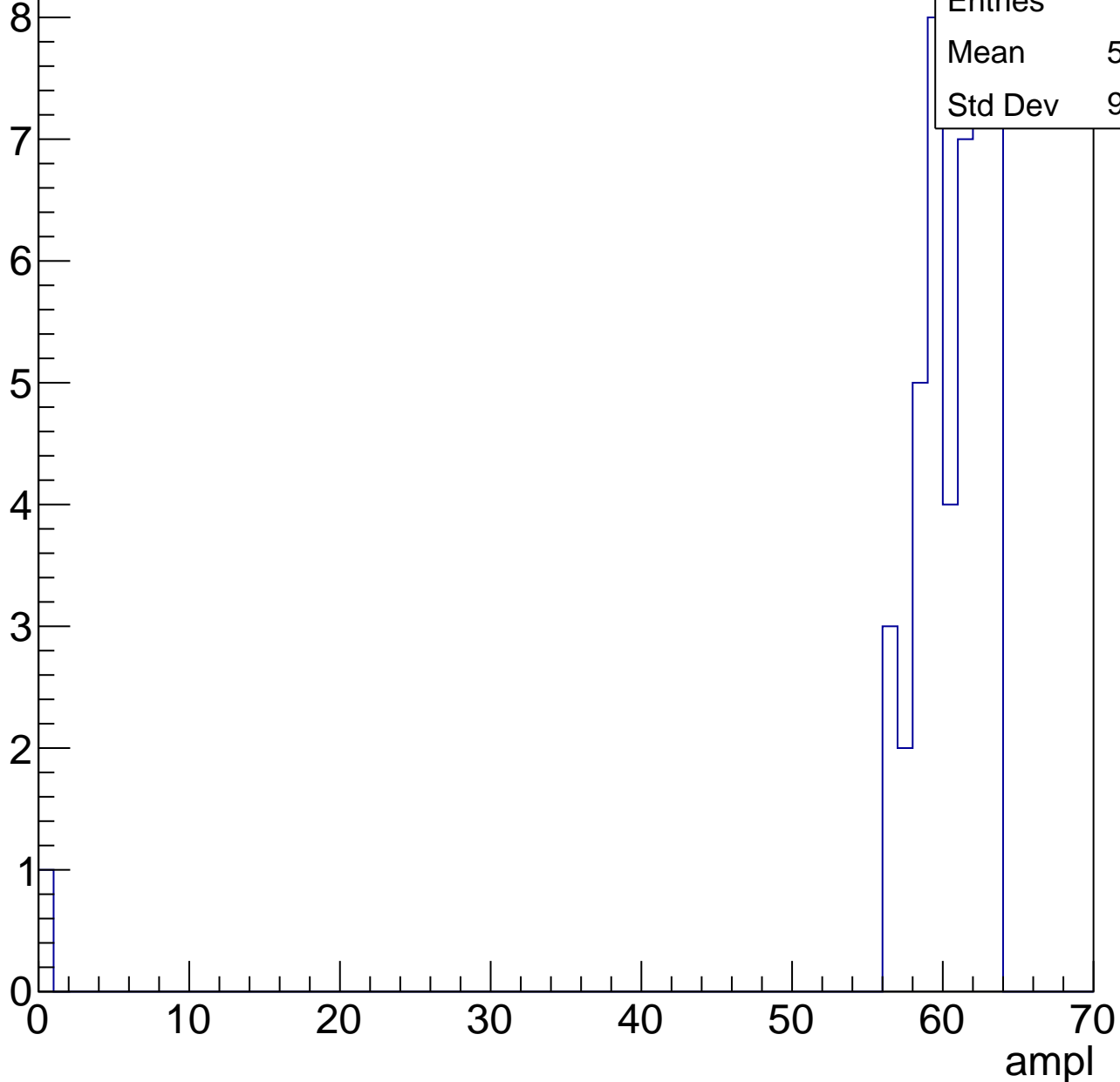
Entries	49
Mean	55.82
Std Dev	2.883



B0L001S, U15-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch2, adc0

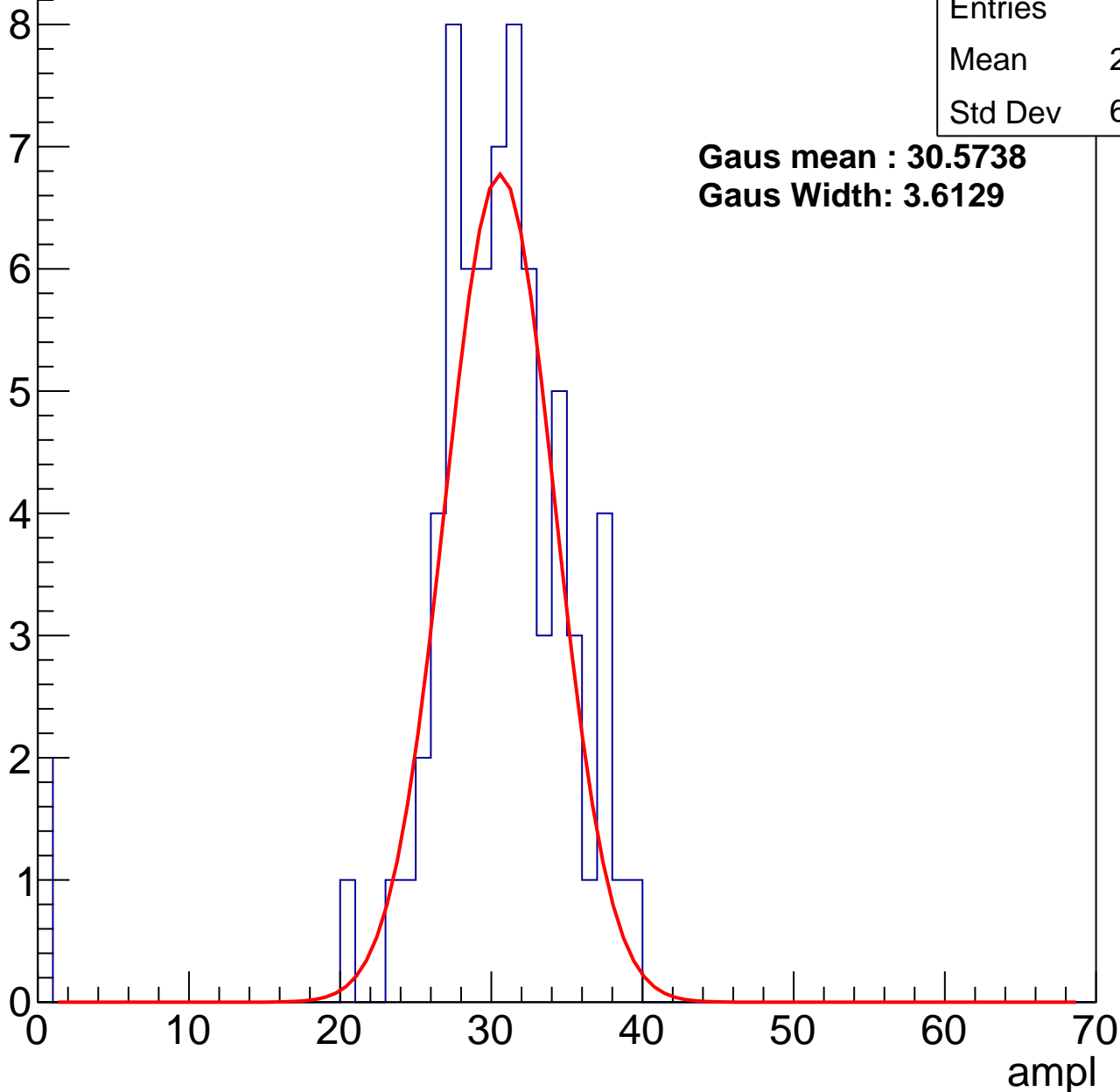
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.49
Std Dev	6.295

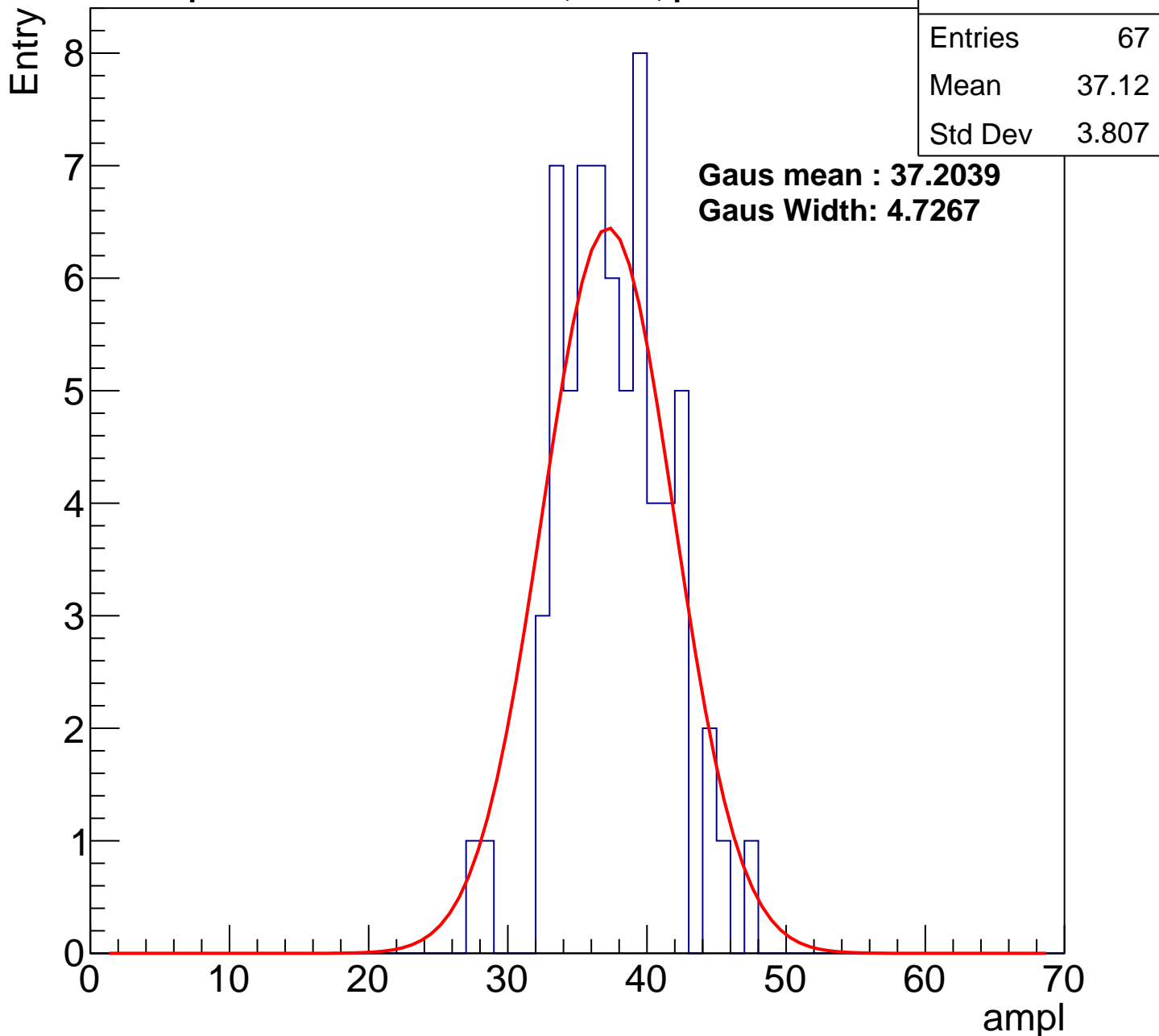
Gaus mean : 30.5738

Gaus Width: 3.6129



B0L001S, U15-ch2, adc1

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U15-ch2, adc2

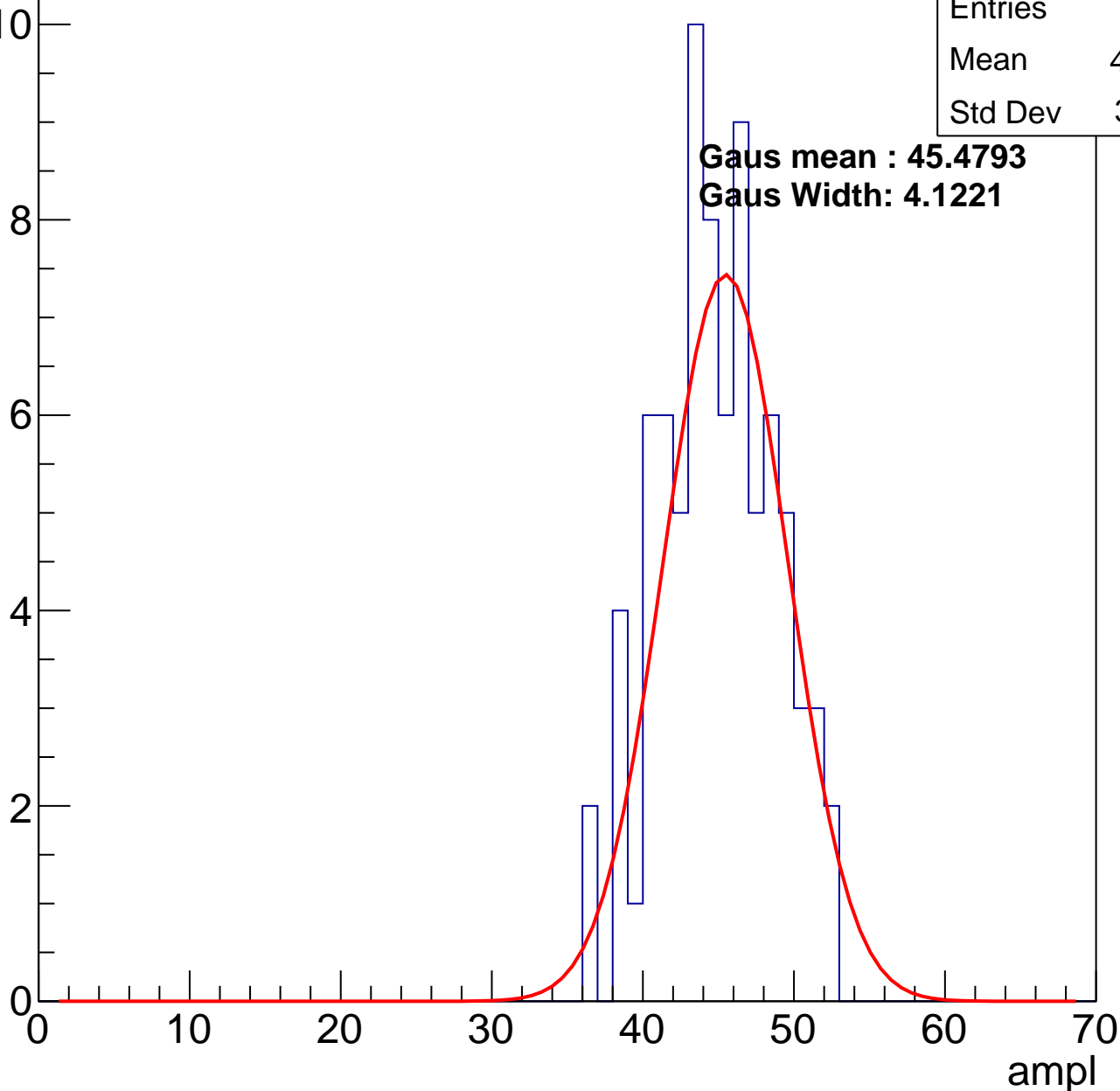
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	44.44
Std Dev	3.771

Gaus mean : 45.4793

Gaus Width: 4.1221

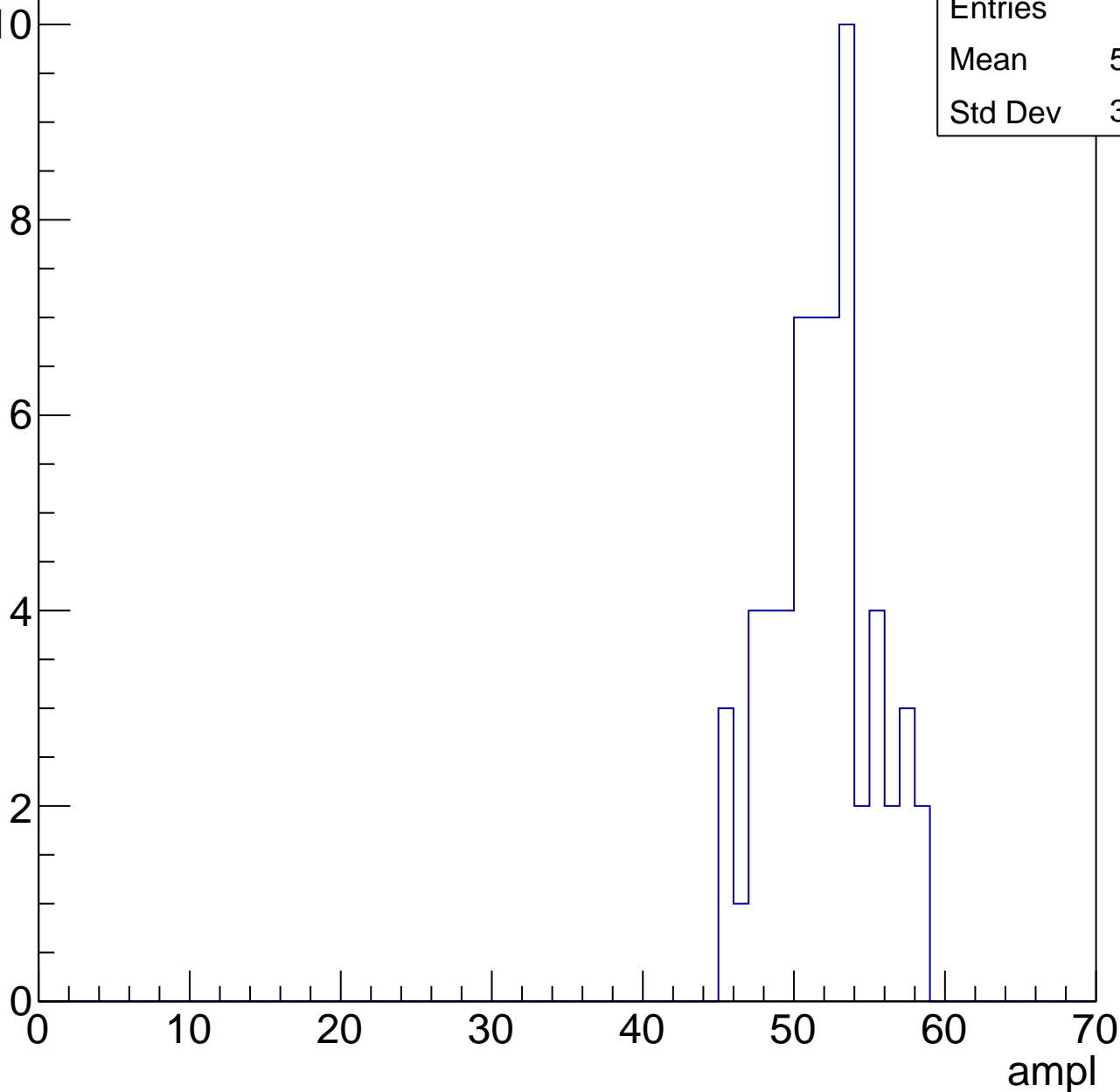


B0L001S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	51.42
Std Dev	3.242

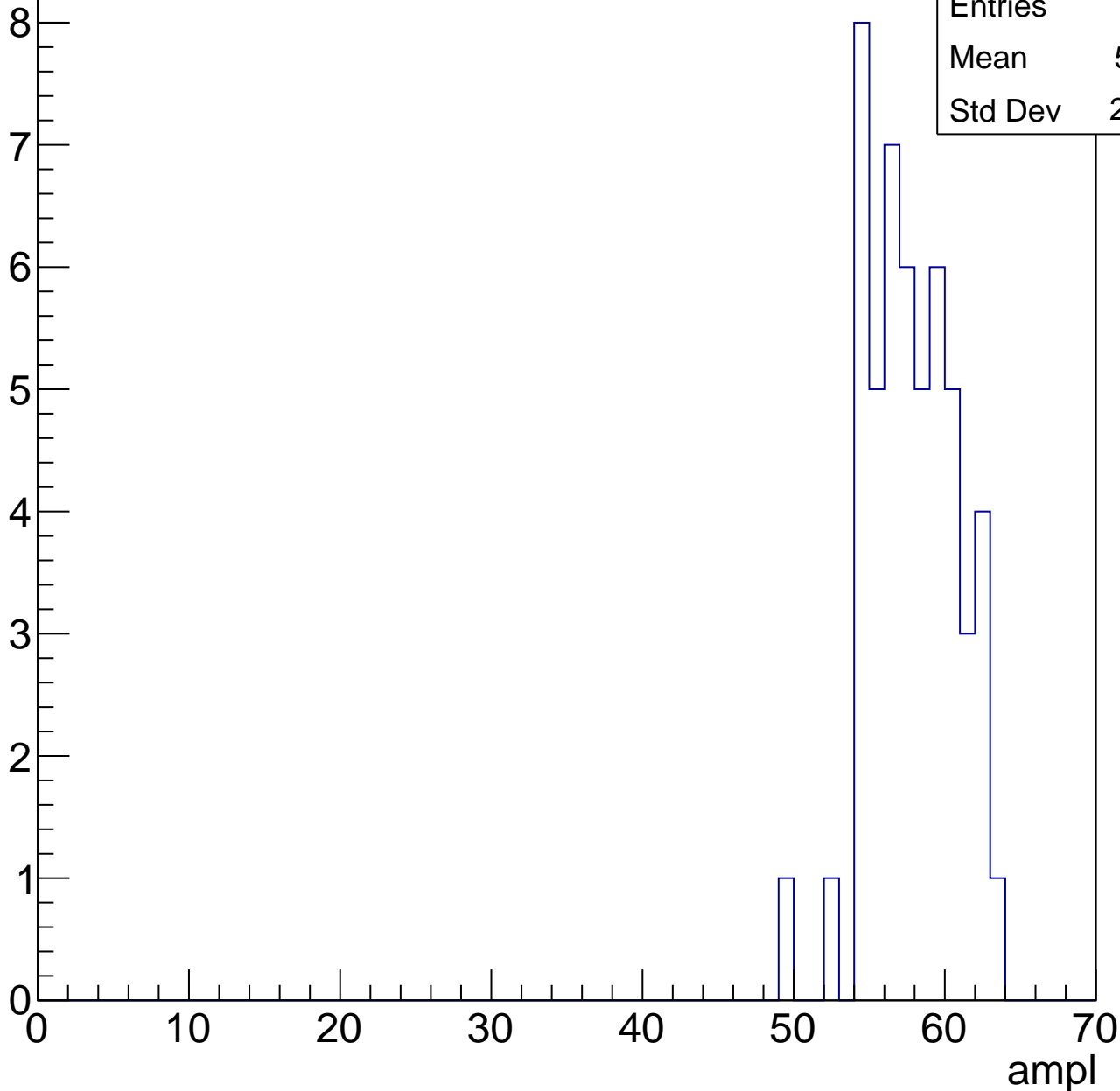


B0L001S, U15-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	57.31
Std Dev	2.912

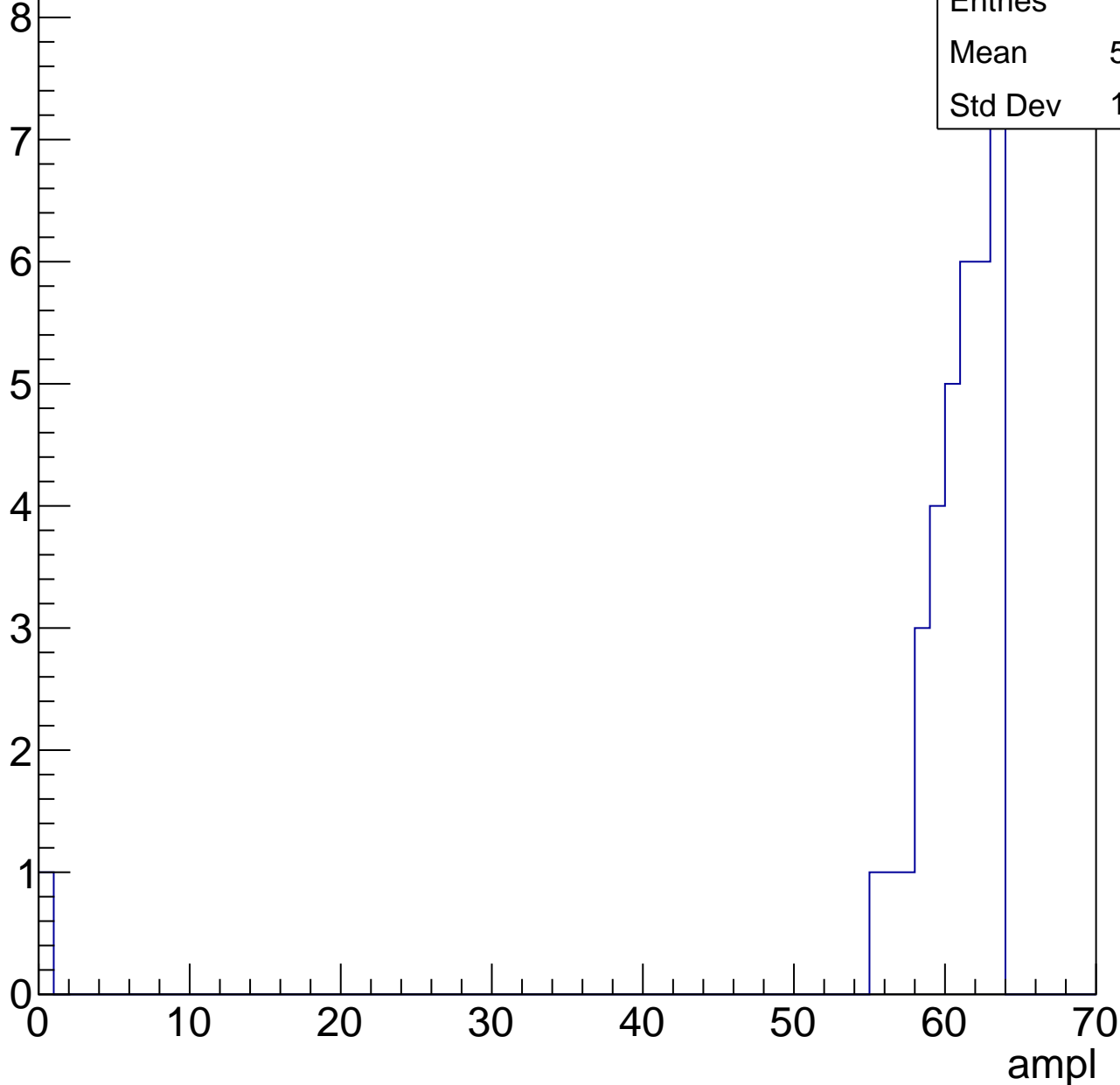


B0L001S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.89
Std Dev	10.17



B0L001S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch3, adc0

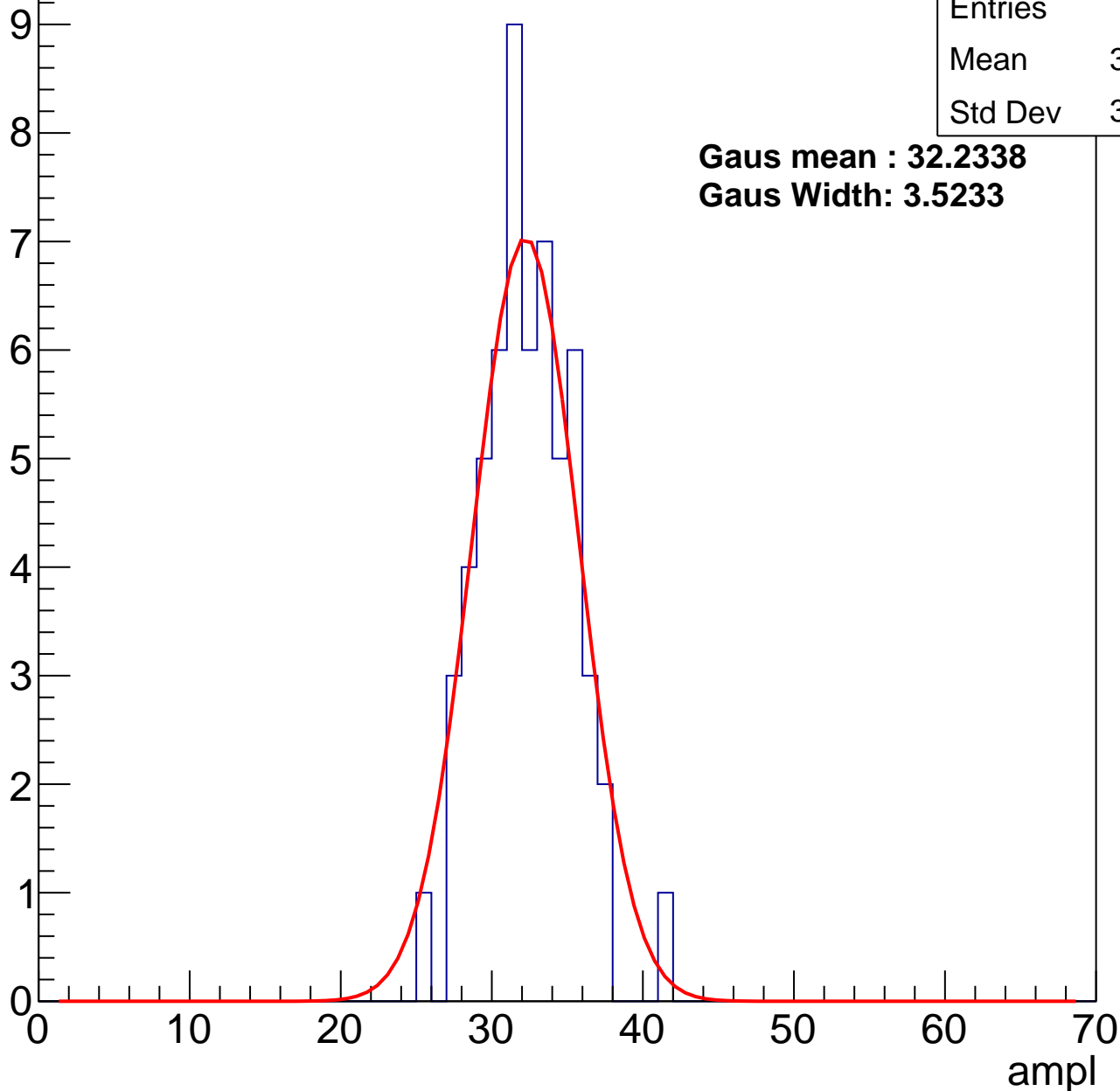
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.86
Std Dev	3.008

Gaus mean : 32.2338

Gaus Width: 3.5233



B0L001S, U15-ch3, adc1

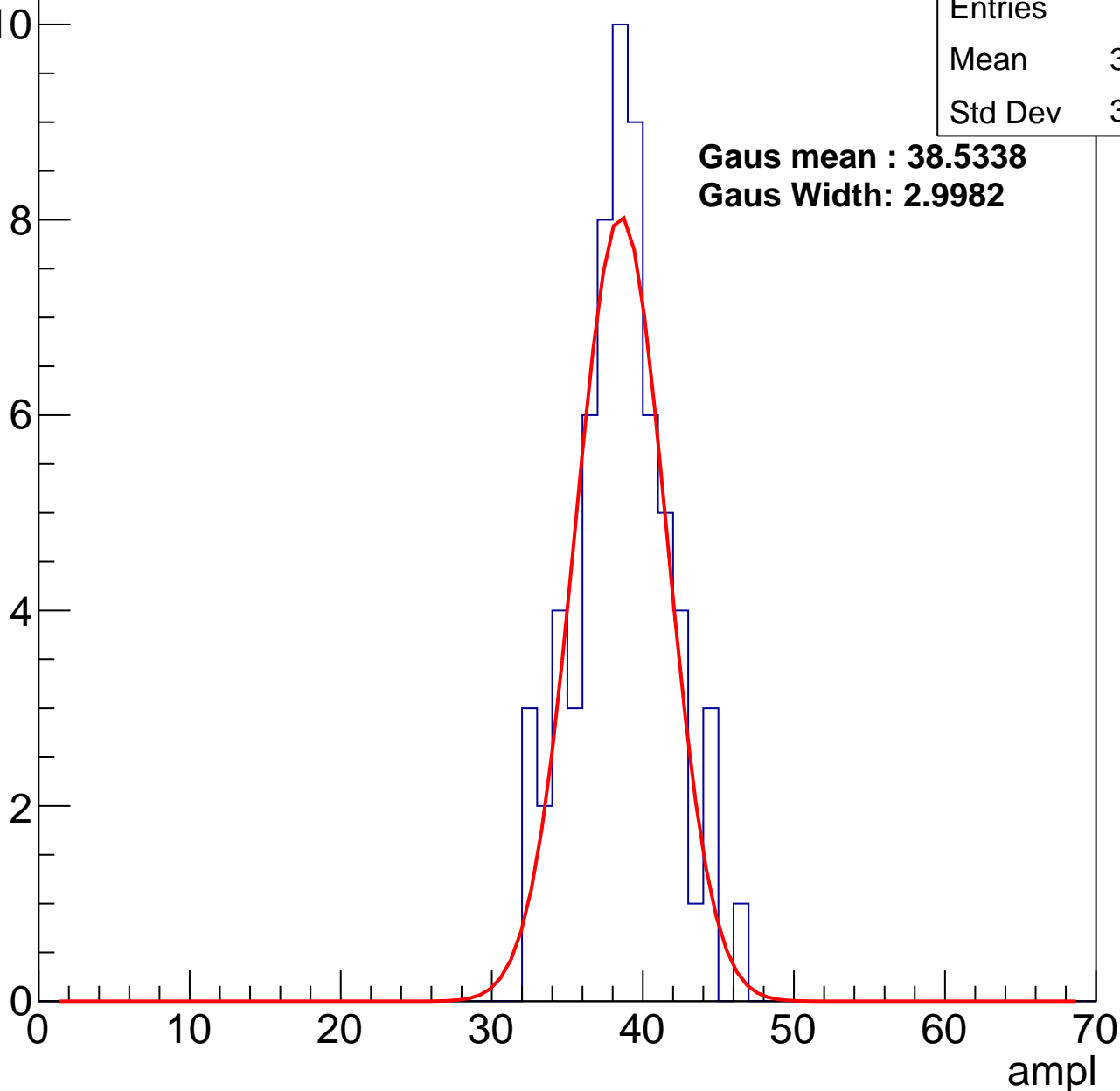
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.15
Std Dev	3.085

Gaus mean : 38.5338

Gaus Width: 2.9982



B0L001S, U15-ch3, adc2

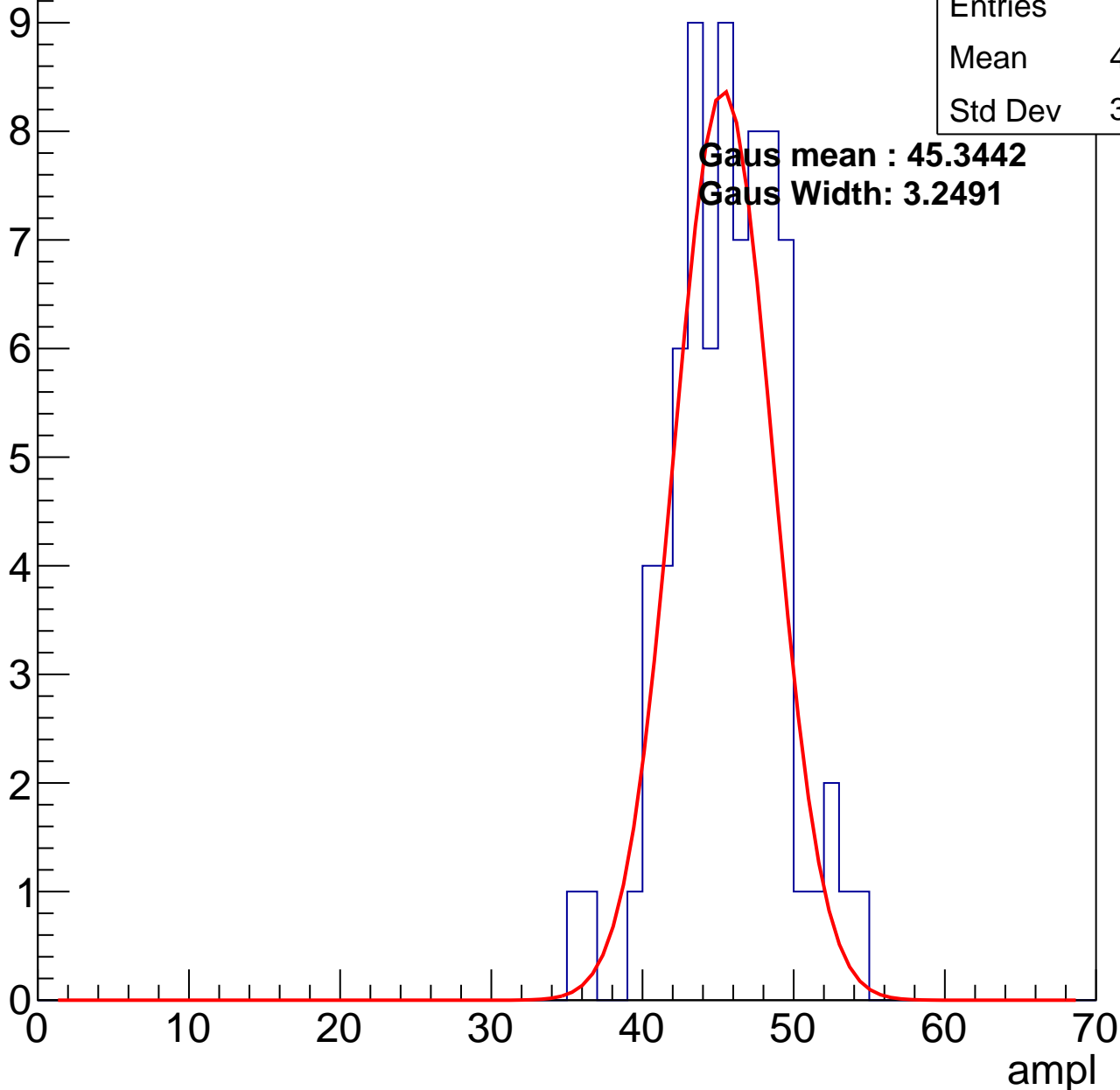
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	45.18
Std Dev	3.606

Gaus mean : 45.3442

Gaus Width: 3.2491

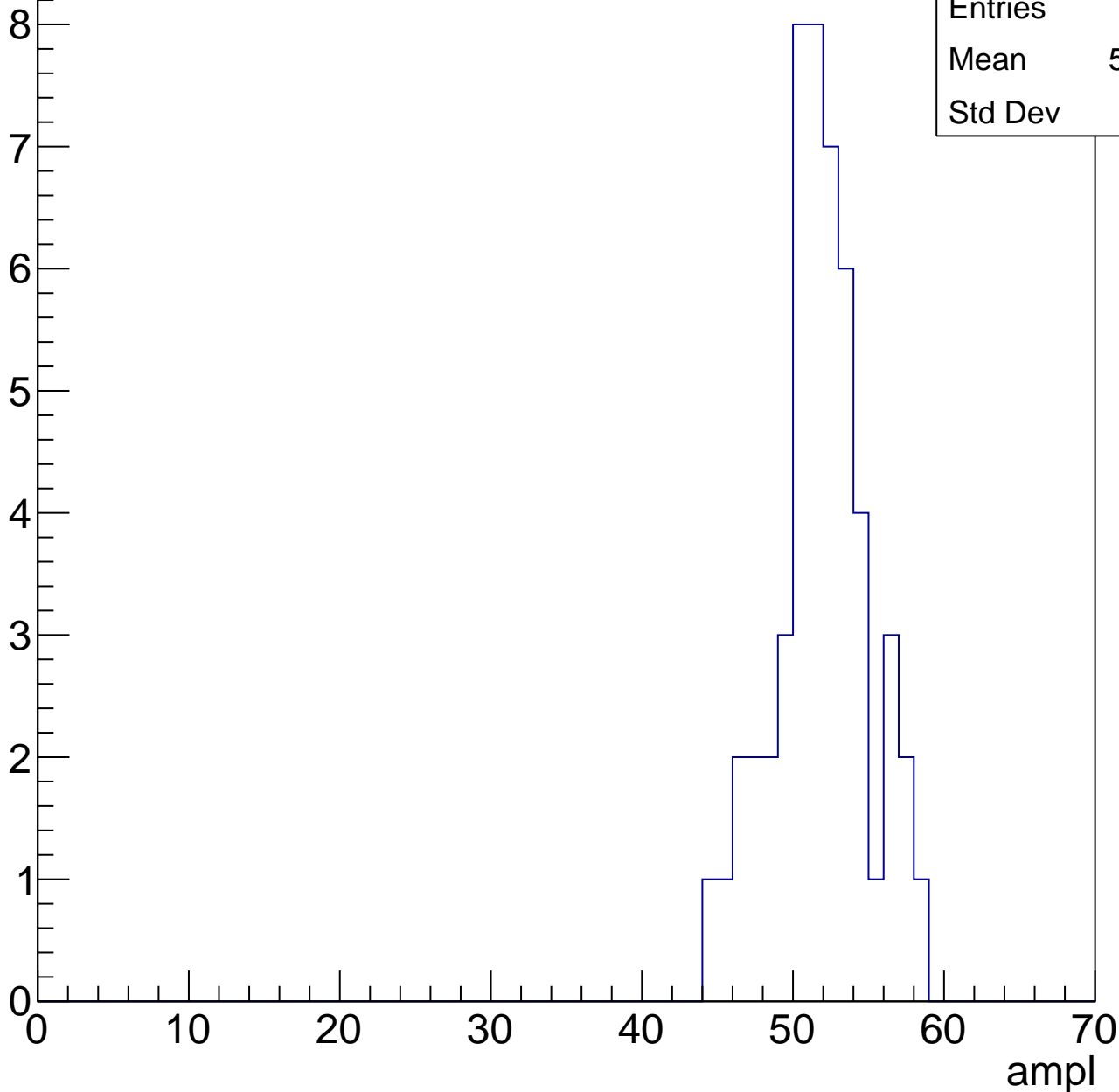


B0L001S, U15-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	51.35
Std Dev	3.06

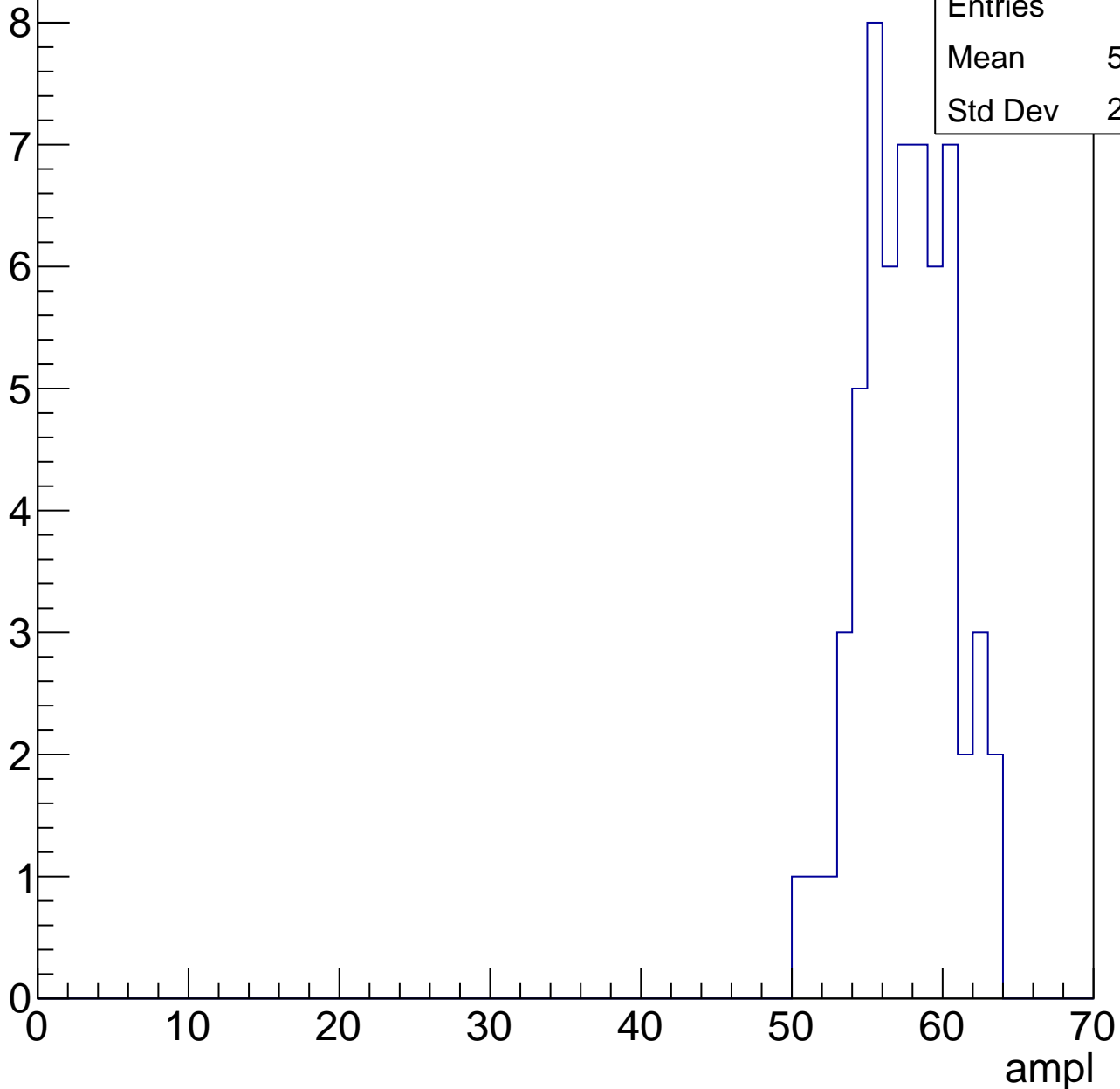


B0L001S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.14
Std Dev	2.948

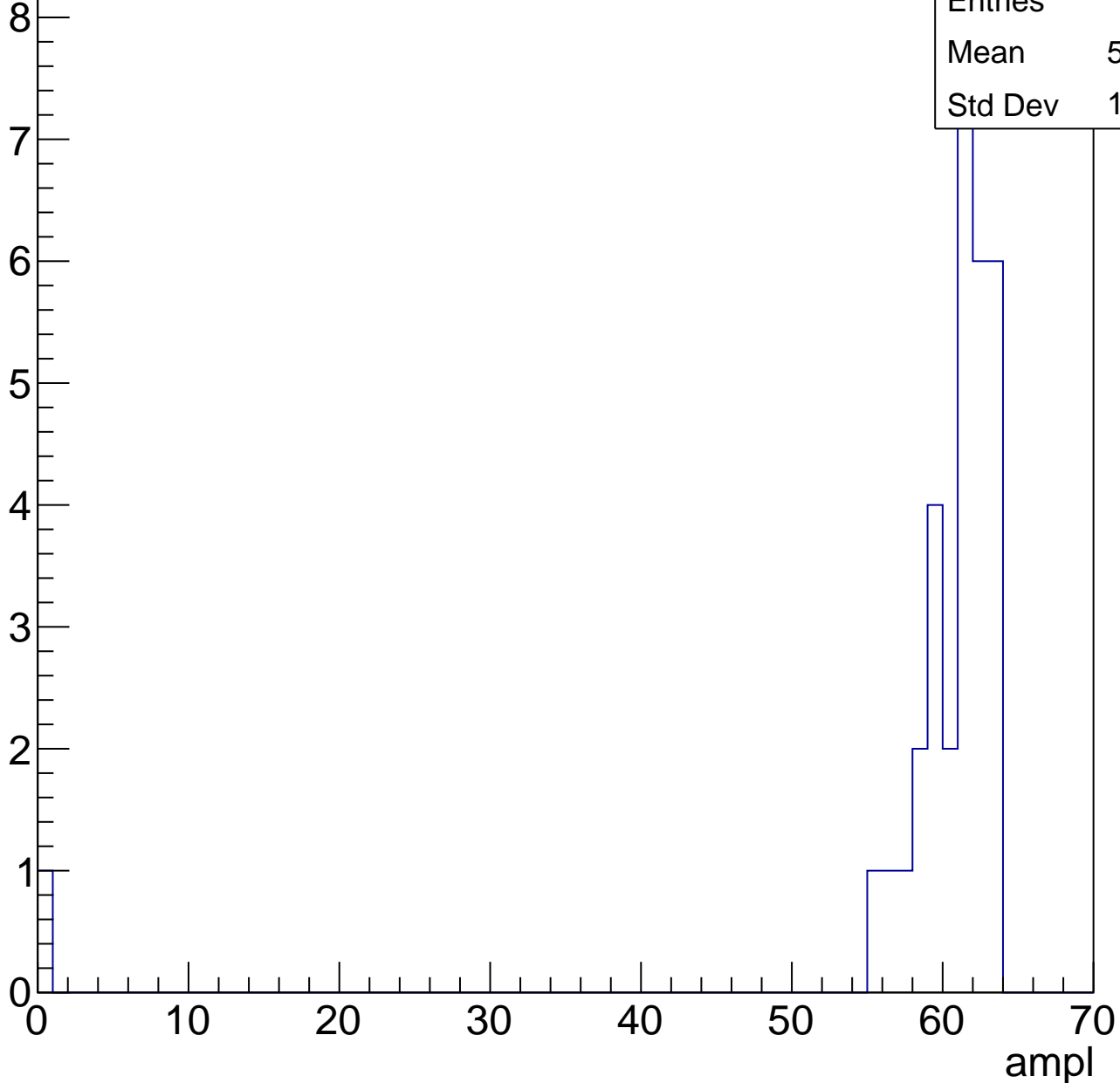


B0L001S, U15-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.69
Std Dev	10.74



B0L001S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

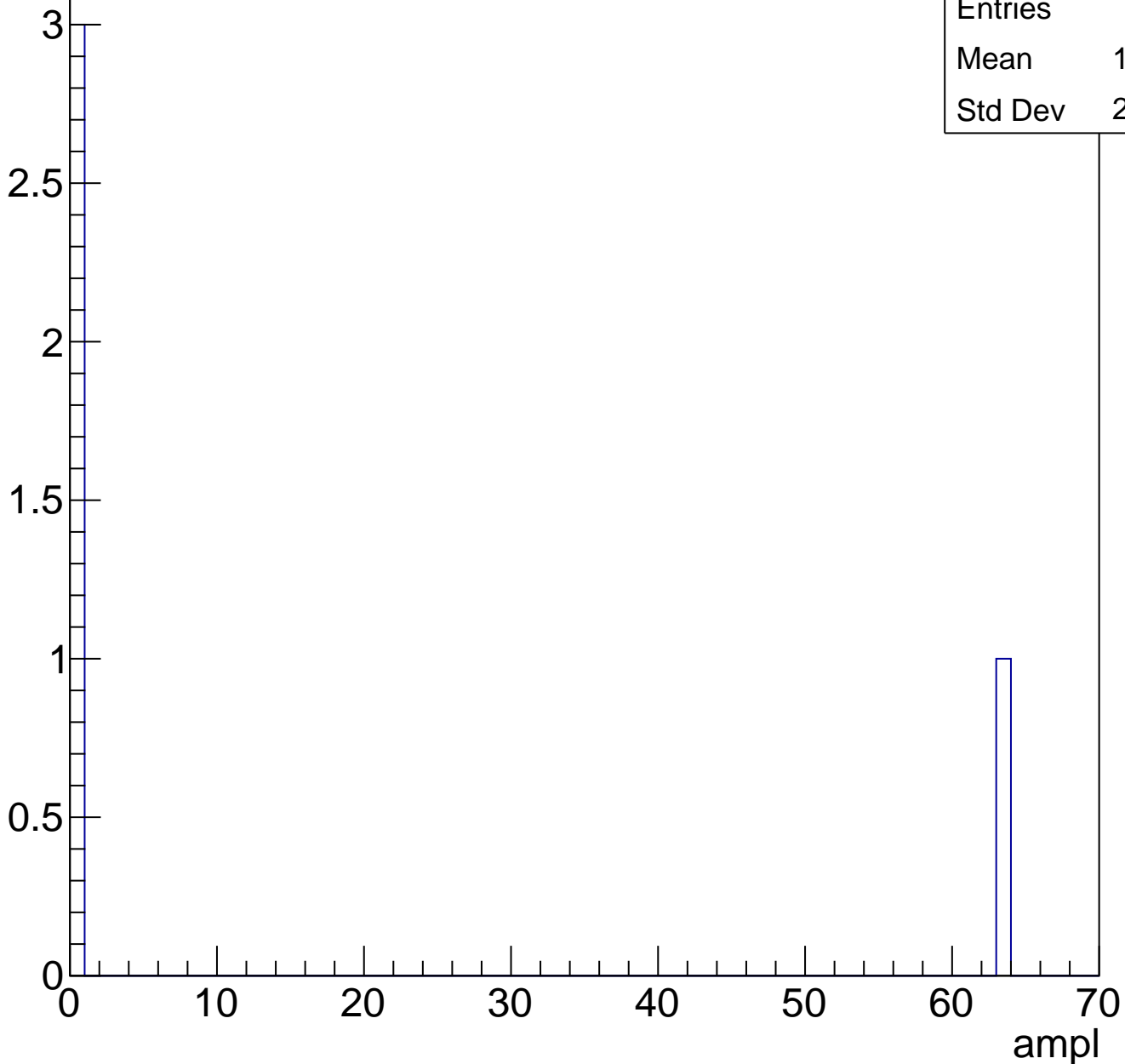
ampl

Entries	3
Mean	61.67
Std Dev	1.247

B0L001S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch4, adc0

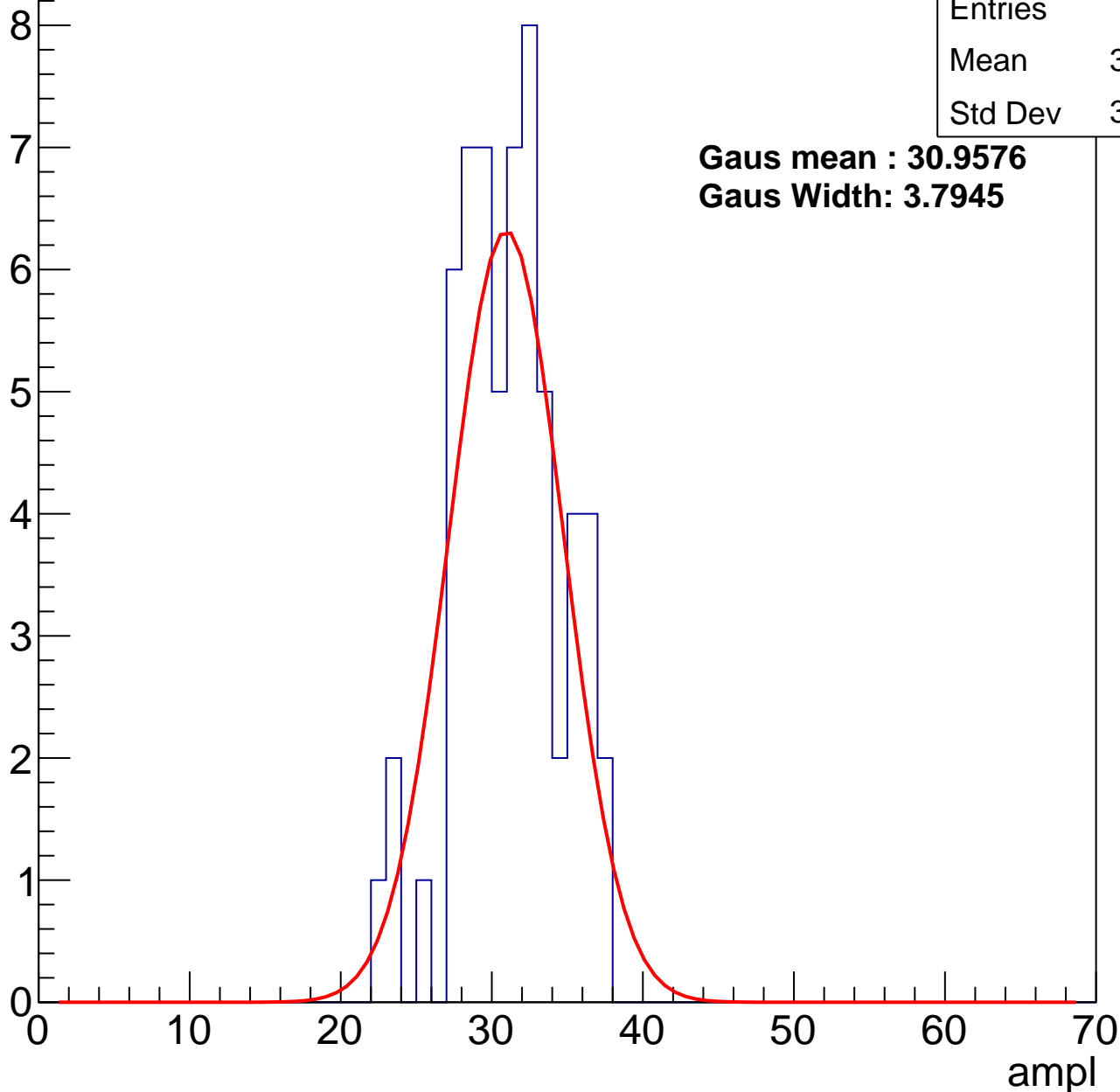
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.62
Std Dev	3.412

Gaus mean : 30.9576

Gaus Width: 3.7945



B0L001S, U15-ch4, adc1

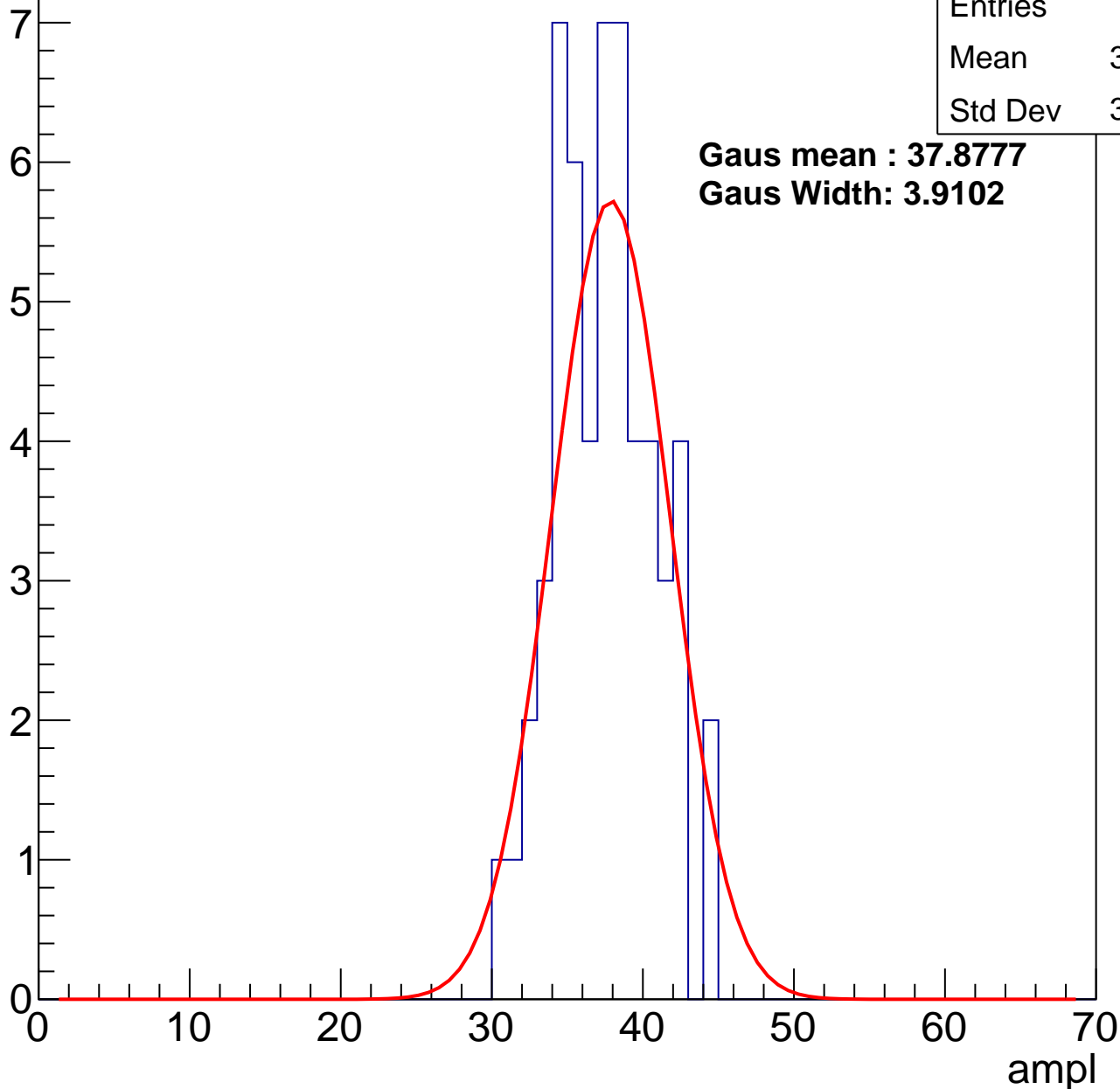
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.02
Std Dev	3.245

Gaus mean : 37.8777

Gaus Width: 3.9102



B0L001S, U15-ch4, adc2

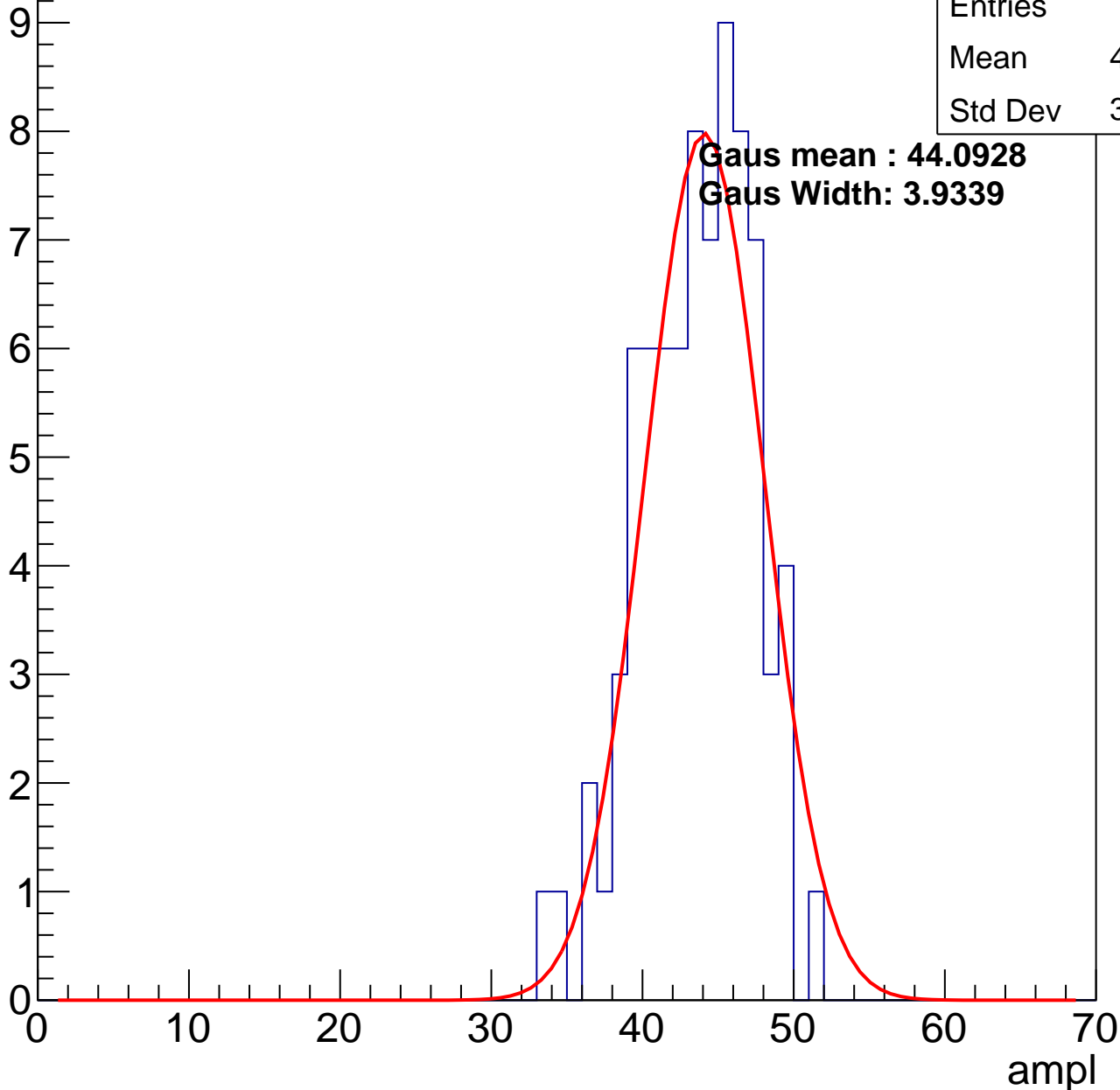
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	43.13
Std Dev	3.695

Gaus mean : 44.0928

Gaus Width: 3.9339

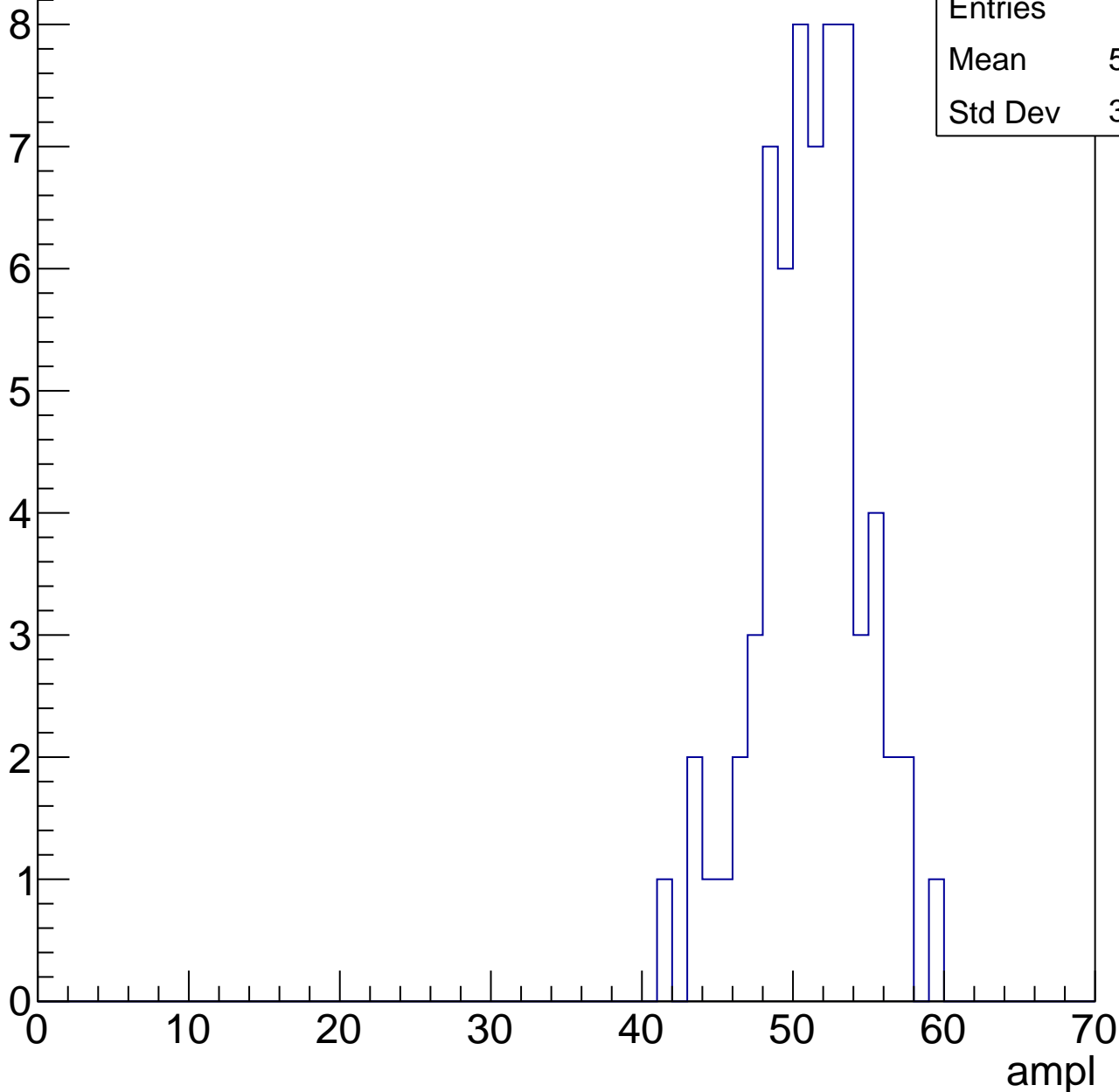


B0L001S, U15-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.65
Std Dev	3.514

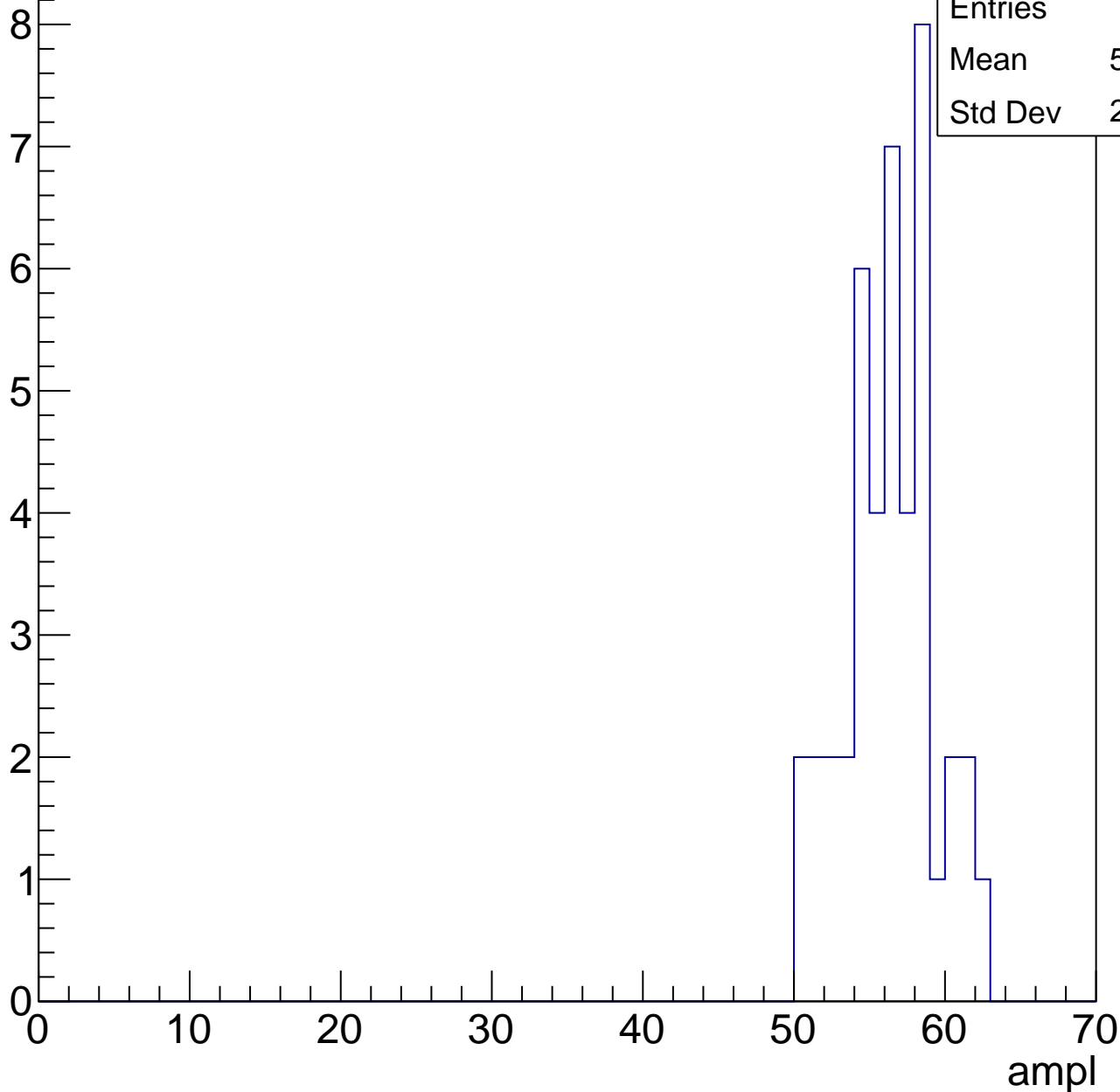


B0L001S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	55.88
Std Dev	2.903



B0L001S, U15-ch4, adc5

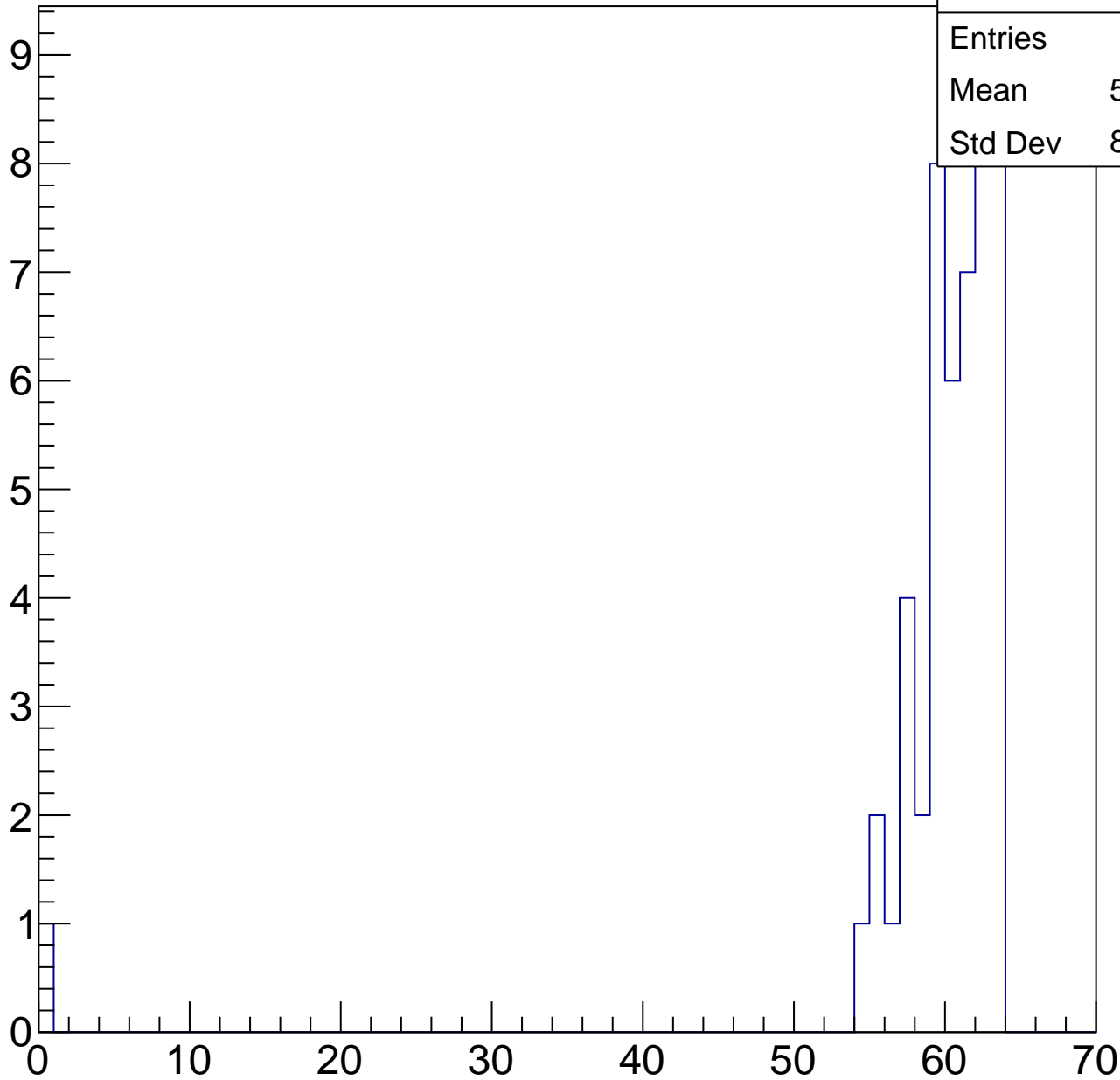
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.96
Std Dev	8.745

ampl



B0L001S, U15-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch5, adc0

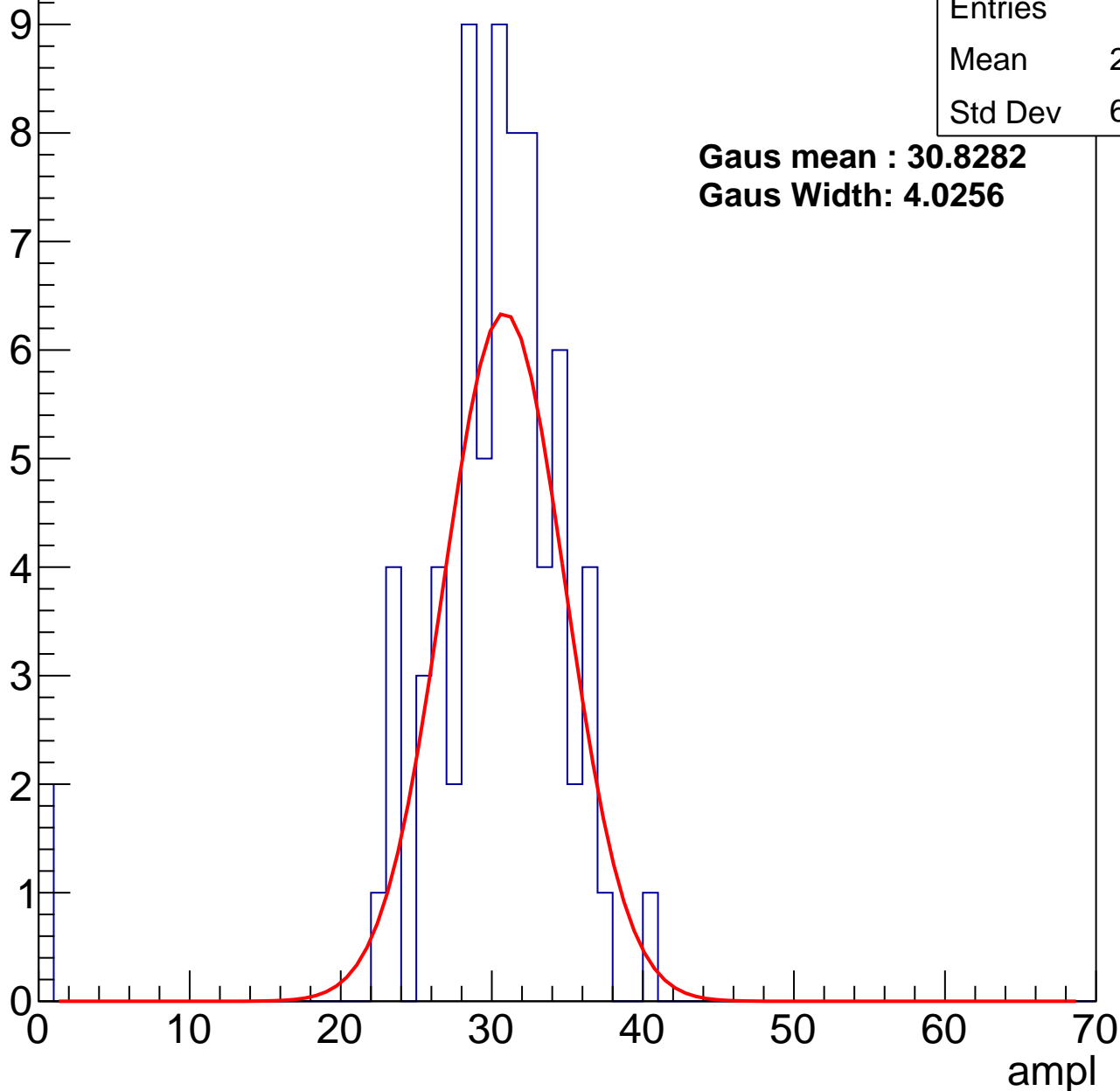
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.38
Std Dev	6.132

Gaus mean : 30.8282

Gaus Width: 4.0256



B0L001S, U15-ch5, adc1

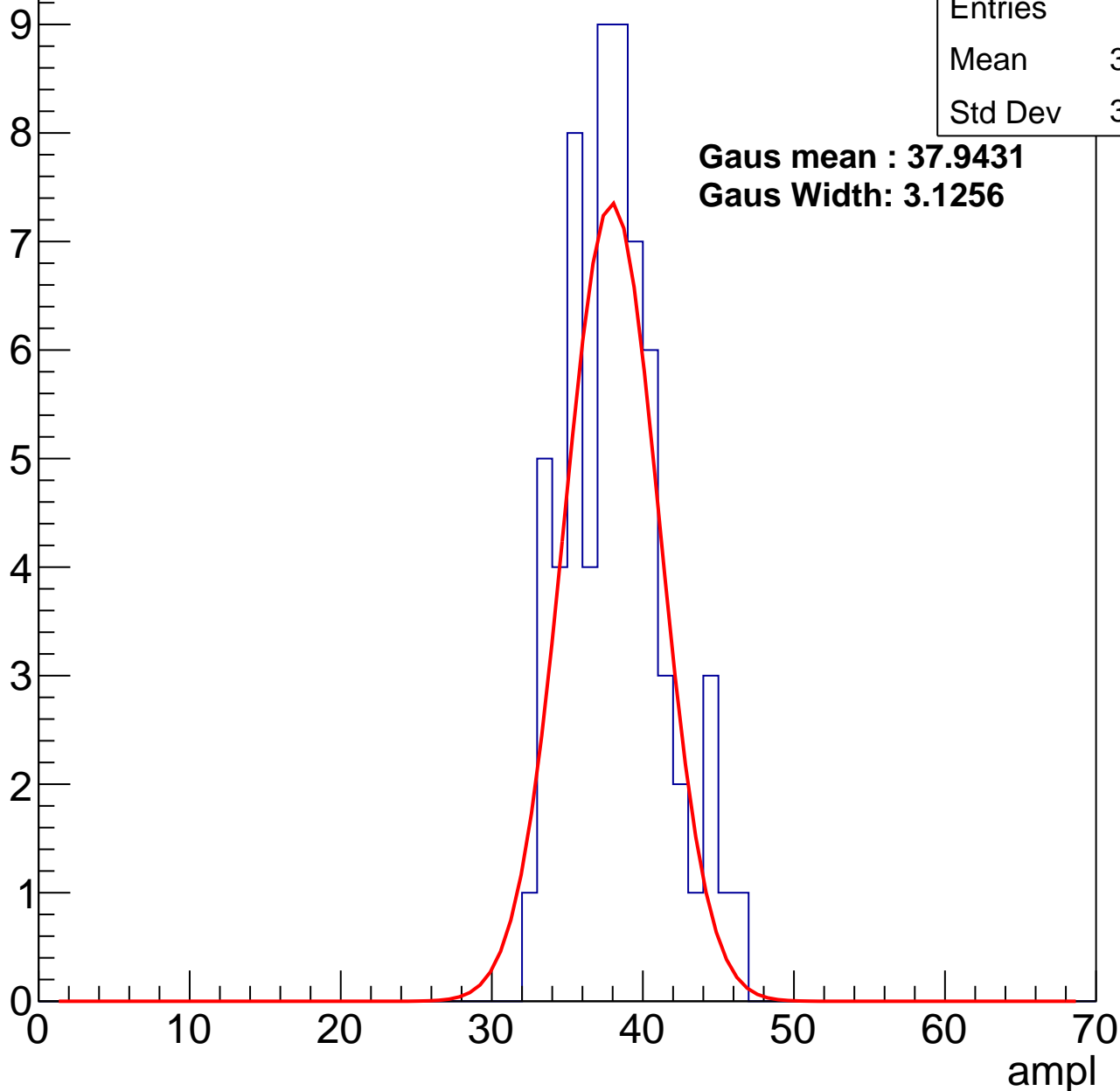
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.78
Std Dev	3.199

Gaus mean : 37.9431

Gaus Width: 3.1256



B0L001S, U15-ch5, adc2

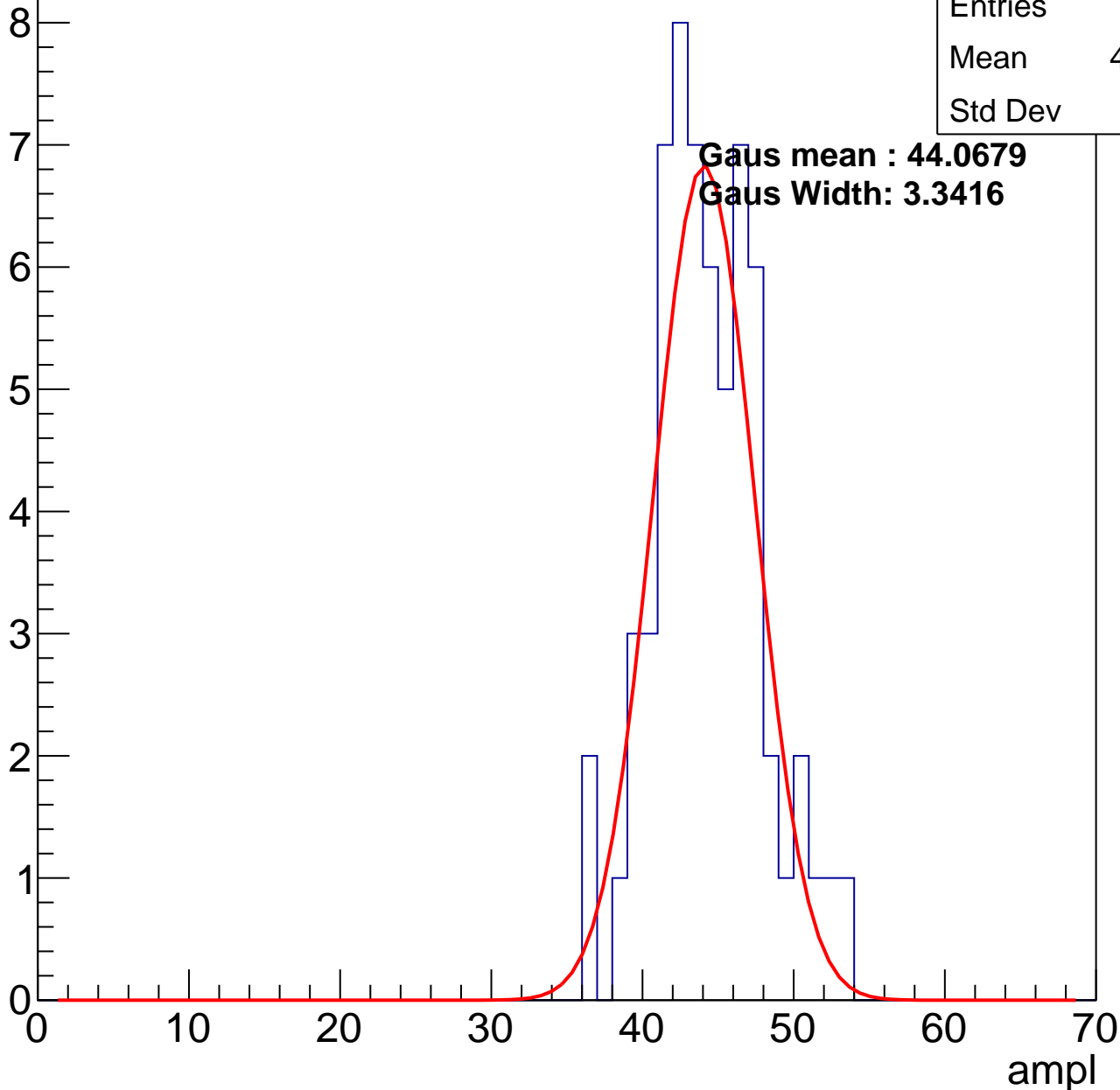
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.89
Std Dev	3.56

Gaus mean : 44.0679

Gaus Width: 3.3416

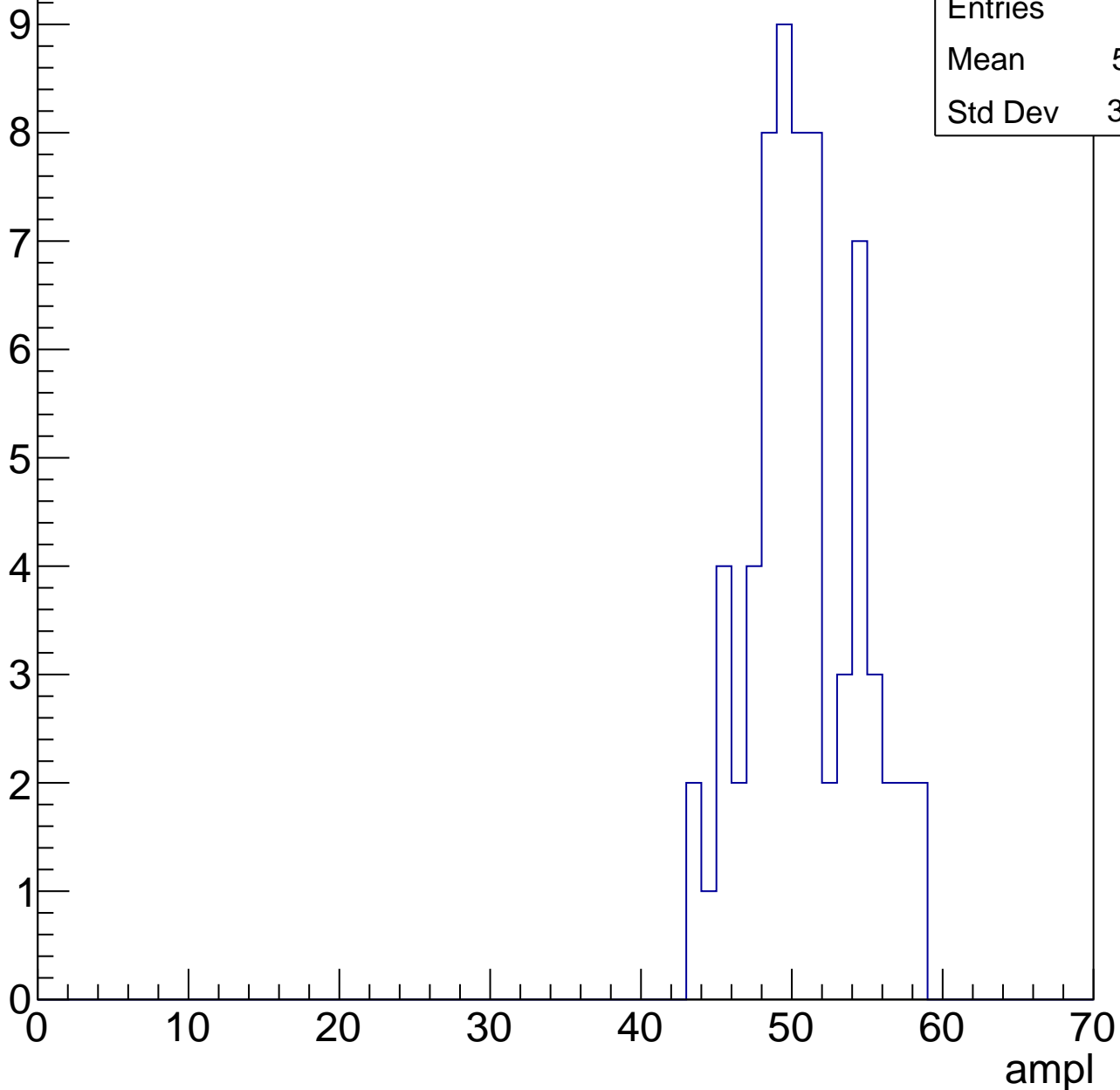


B0L001S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.31
Std Dev	3.592

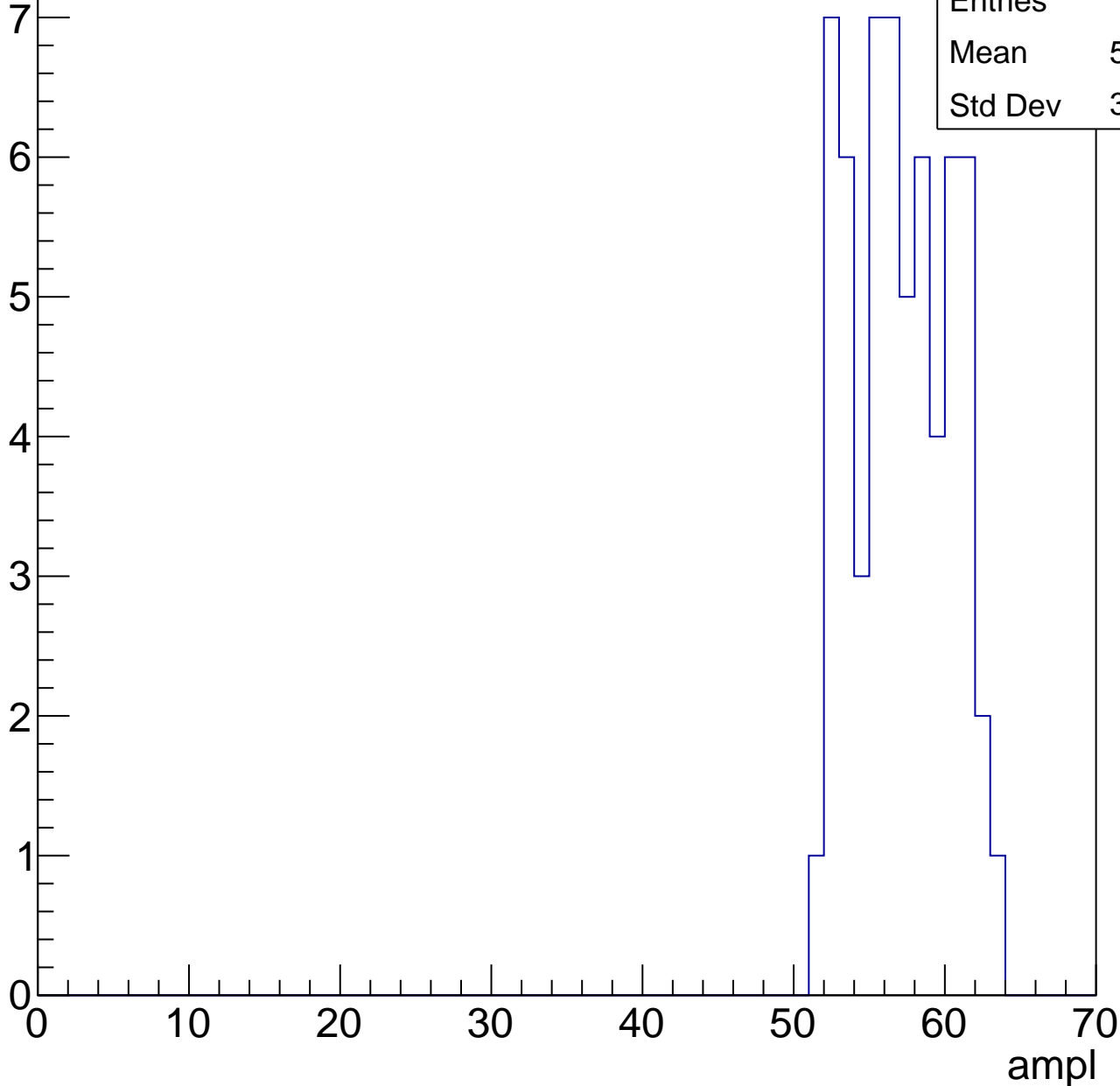


B0L001S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.62
Std Dev	3.184

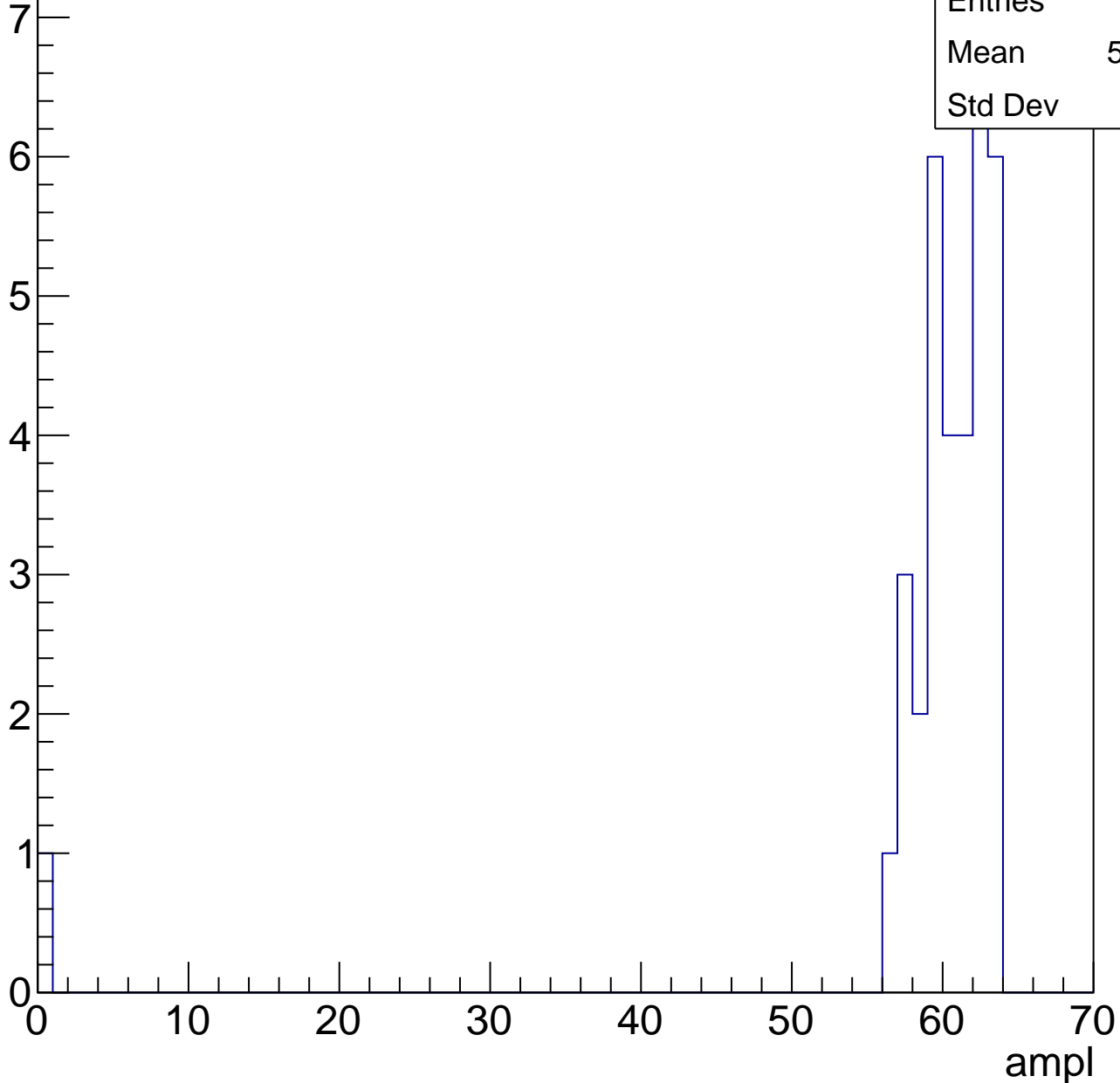


B0L001S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.62
Std Dev	10.4



B0L001S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch6, adc0

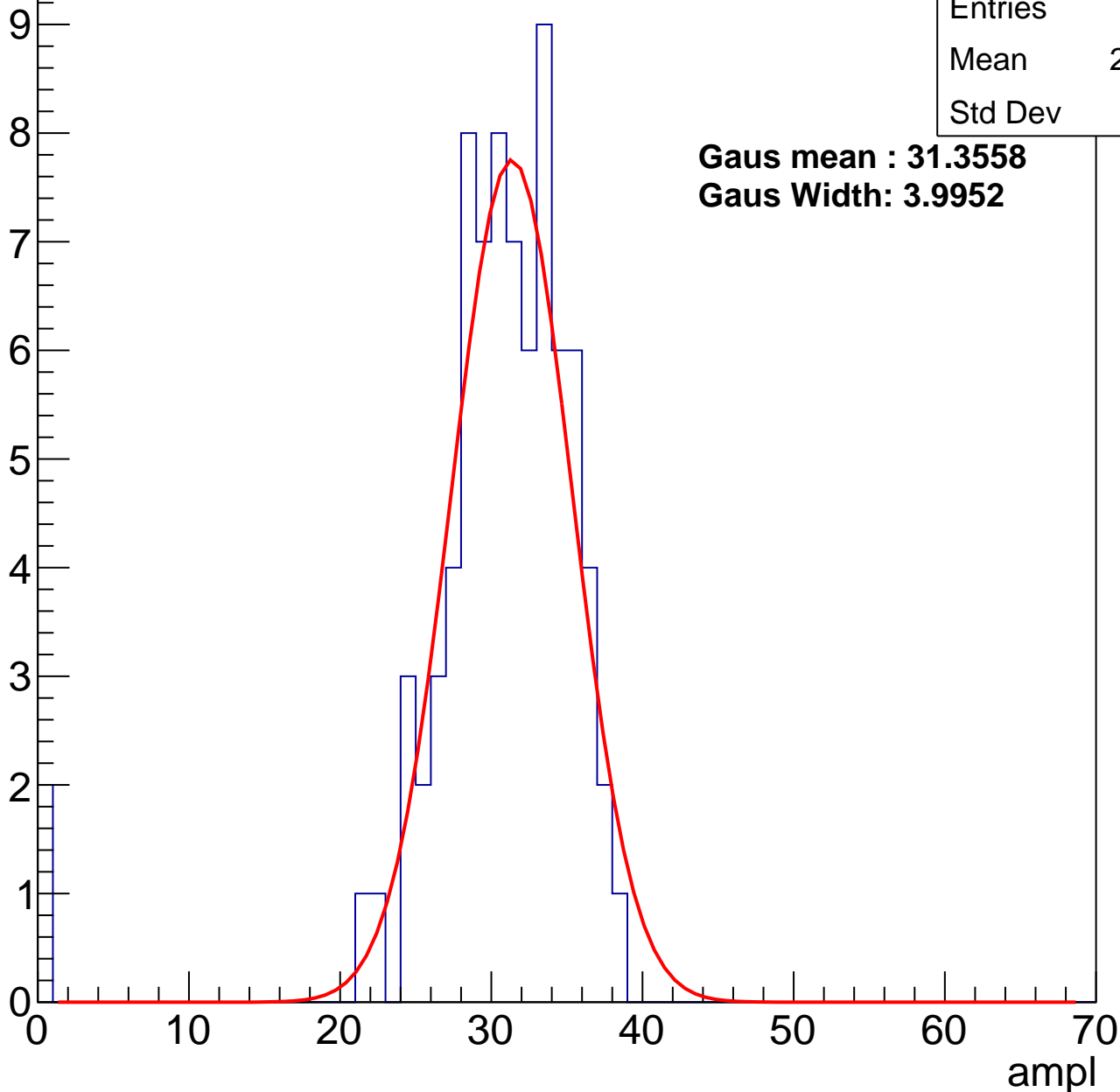
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	29.93
Std Dev	6.01

Gaus mean : 31.3558

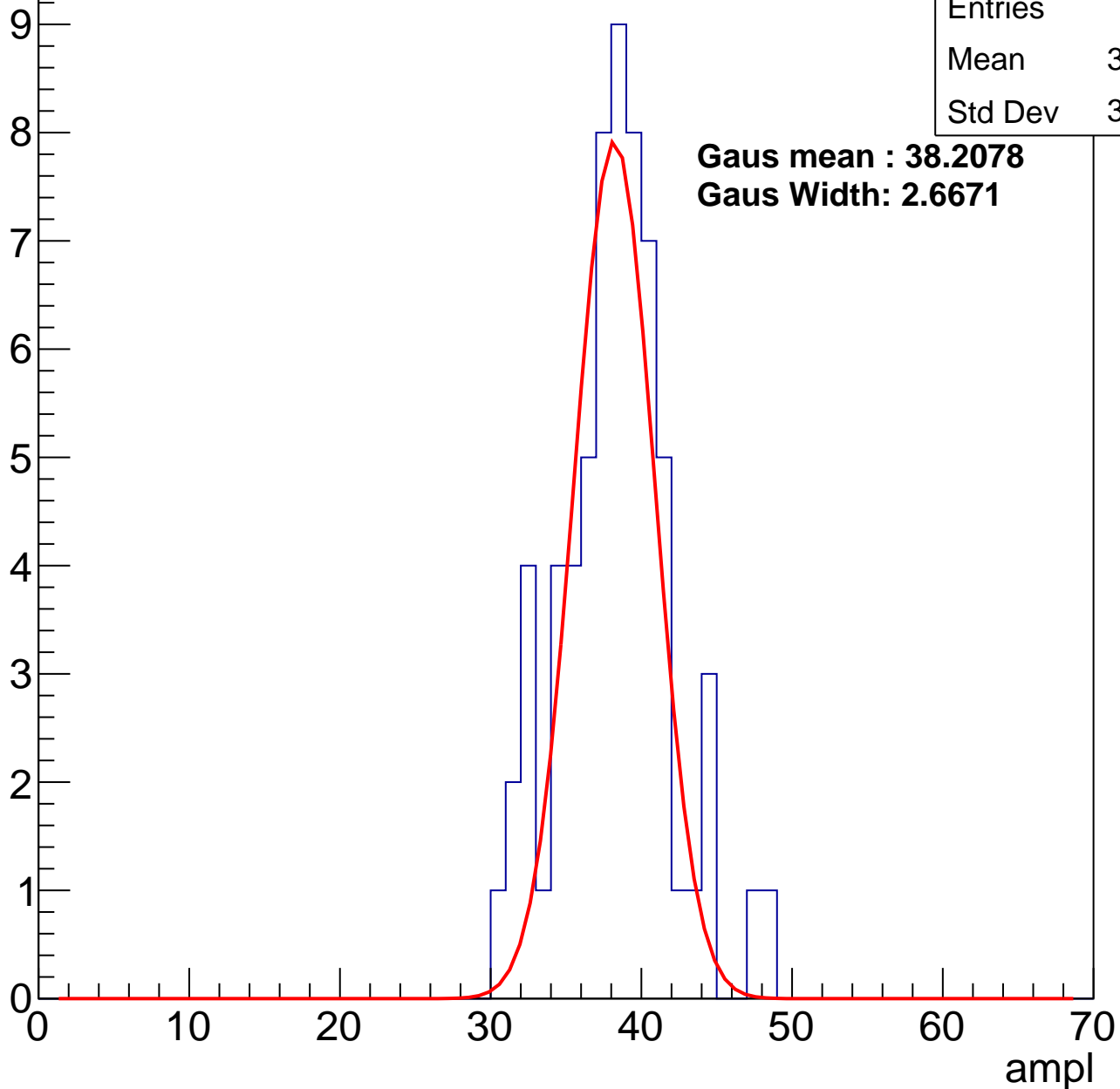
Gaus Width: 3.9952



B0L001S, U15-ch6, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch6, adc2

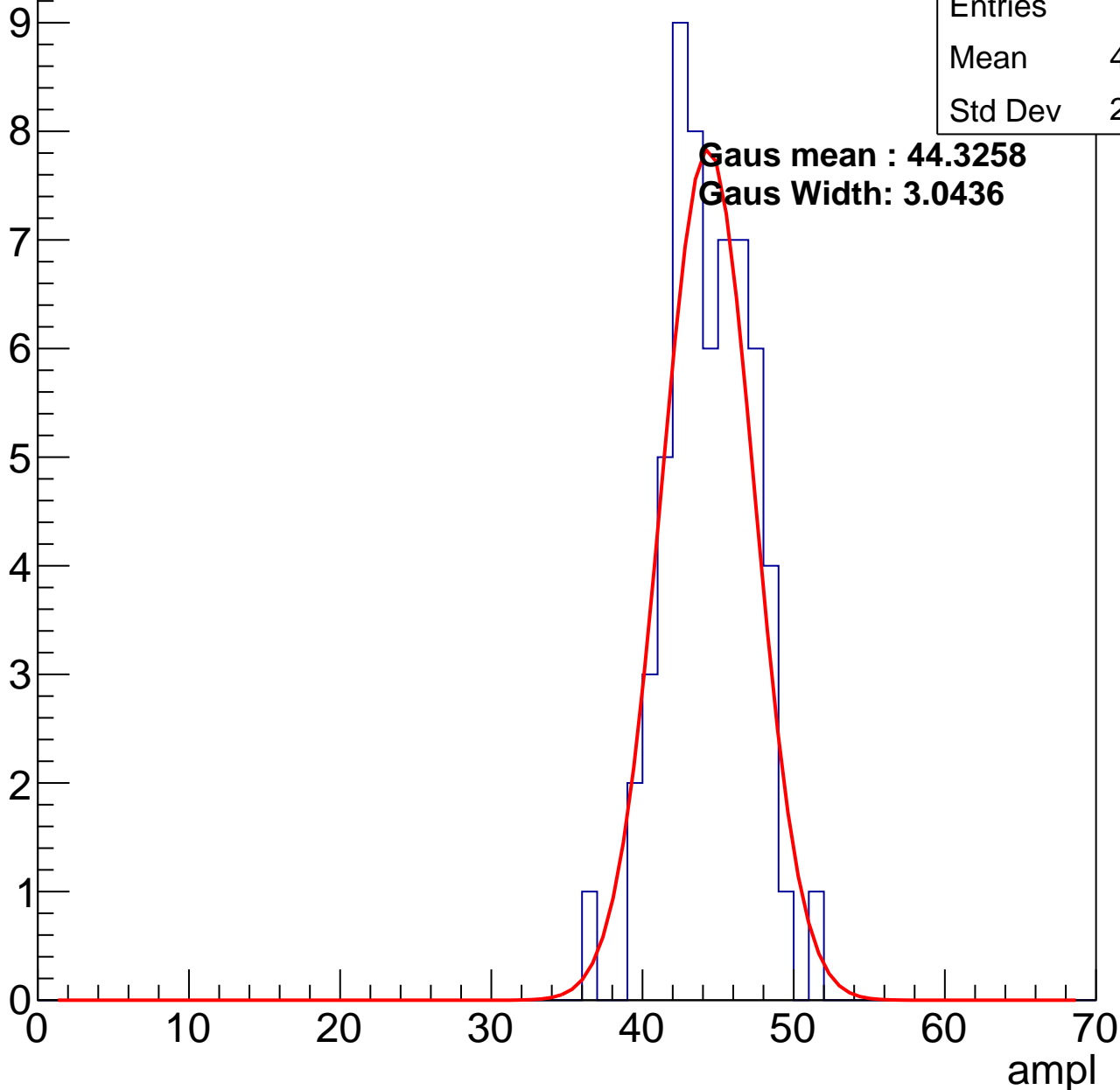
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.93
Std Dev	2.822

Gaus mean : 44.3258

Gaus Width: 3.0436

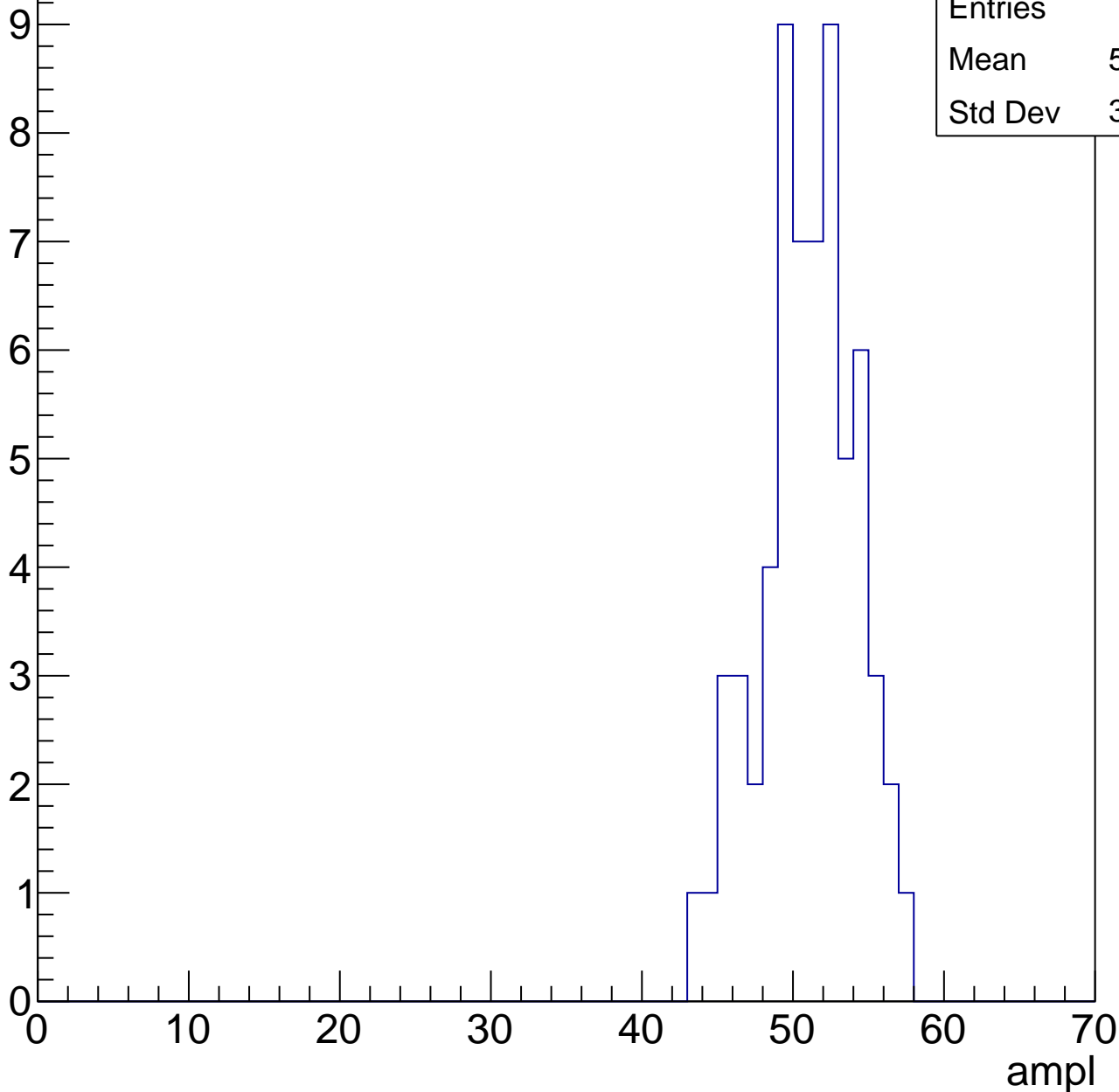


B0L001S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.56
Std Dev	3.116

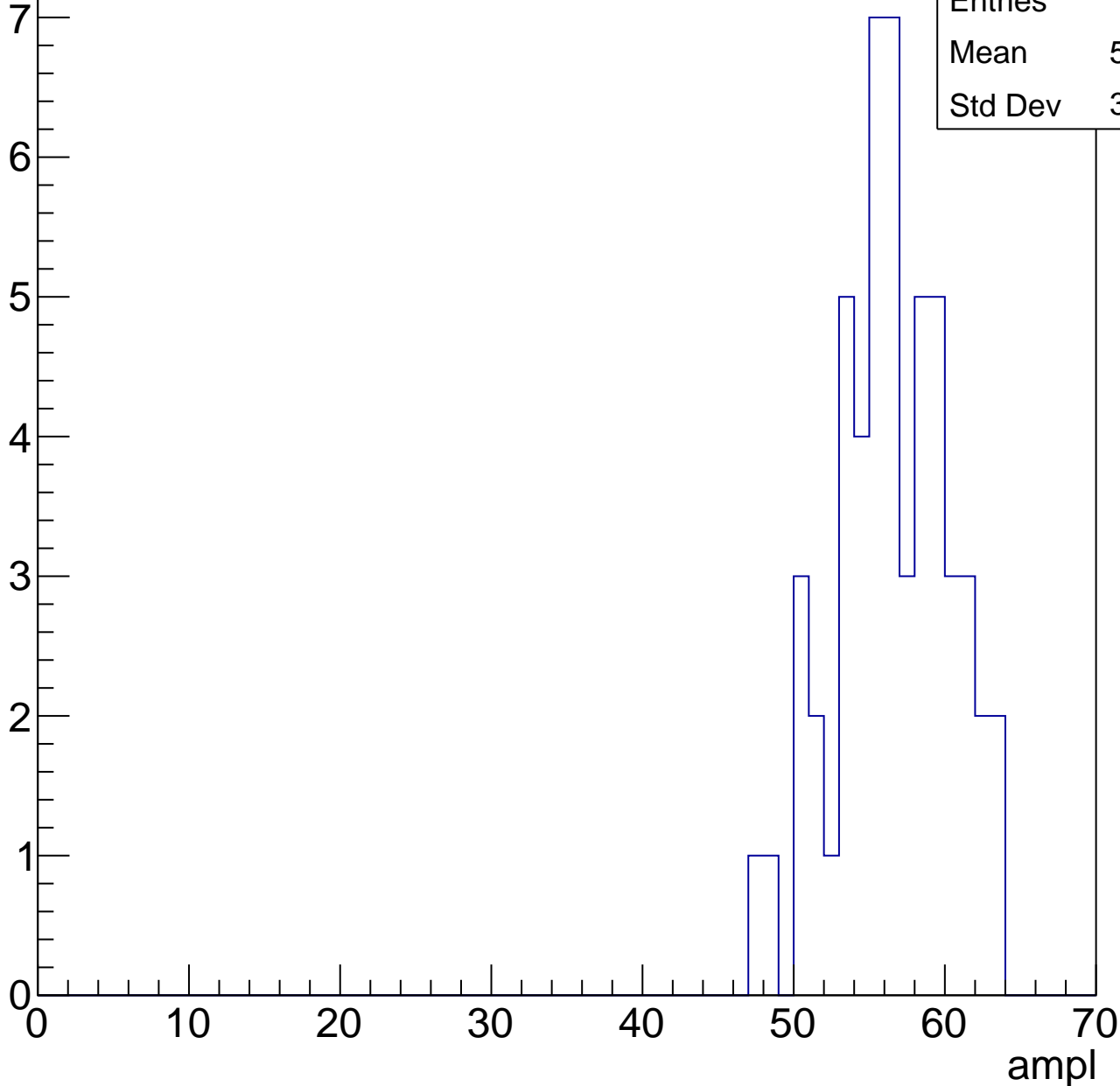


B0L001S, U15-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.04
Std Dev	3.722

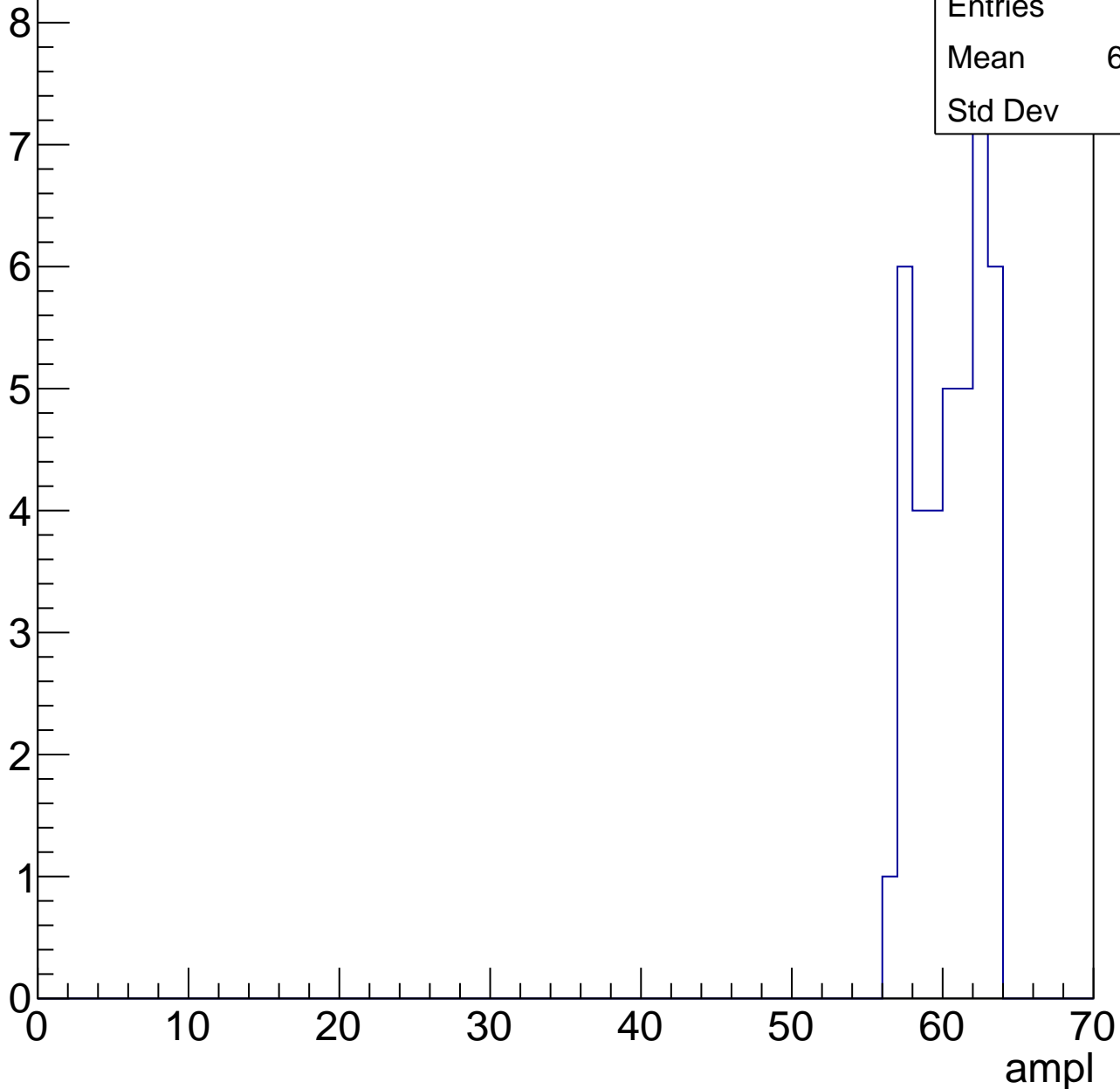


B0L001S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	60.13
Std Dev	2.15



B0L001S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

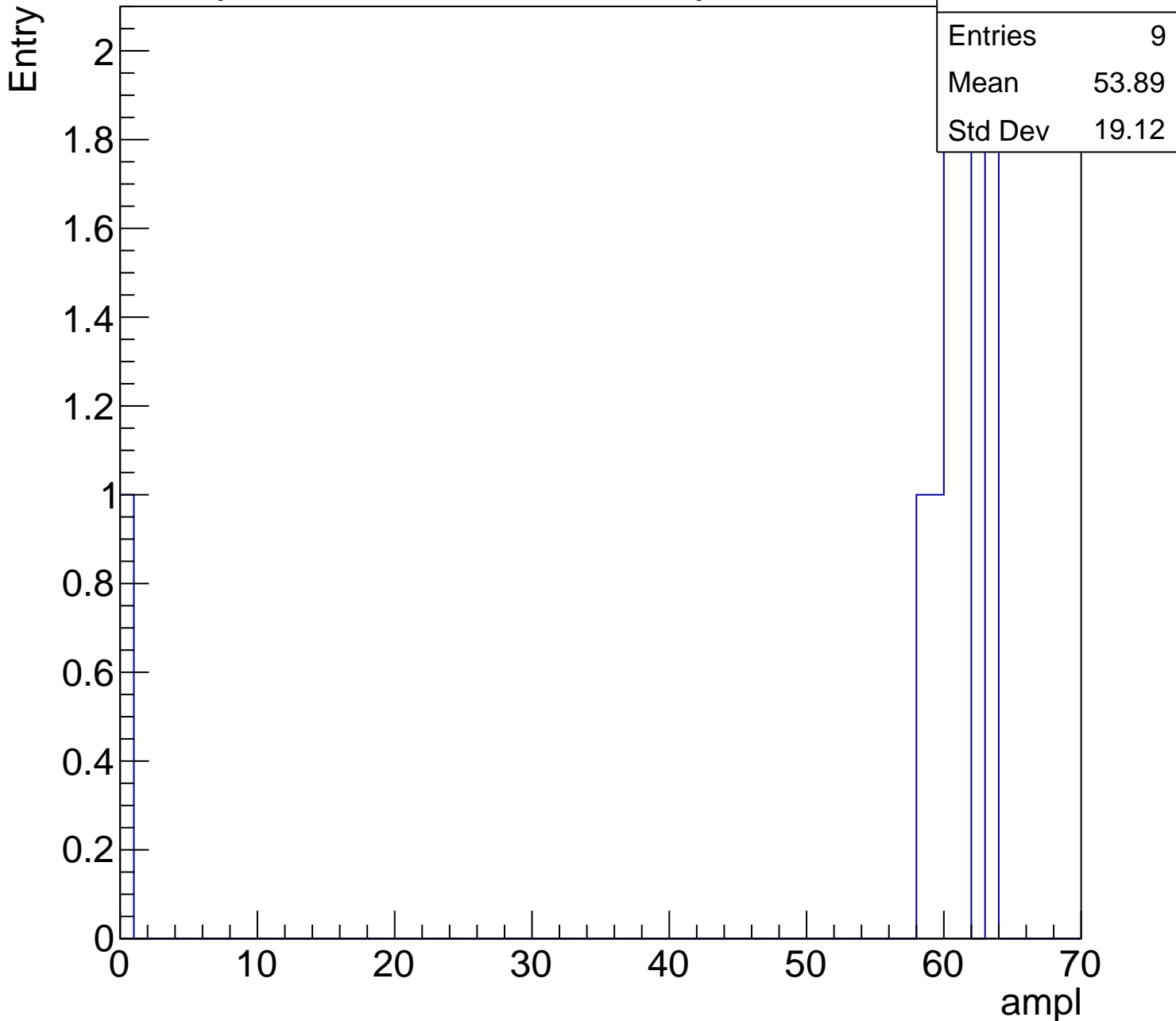
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.89
Std Dev	19.12

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch7, adc0

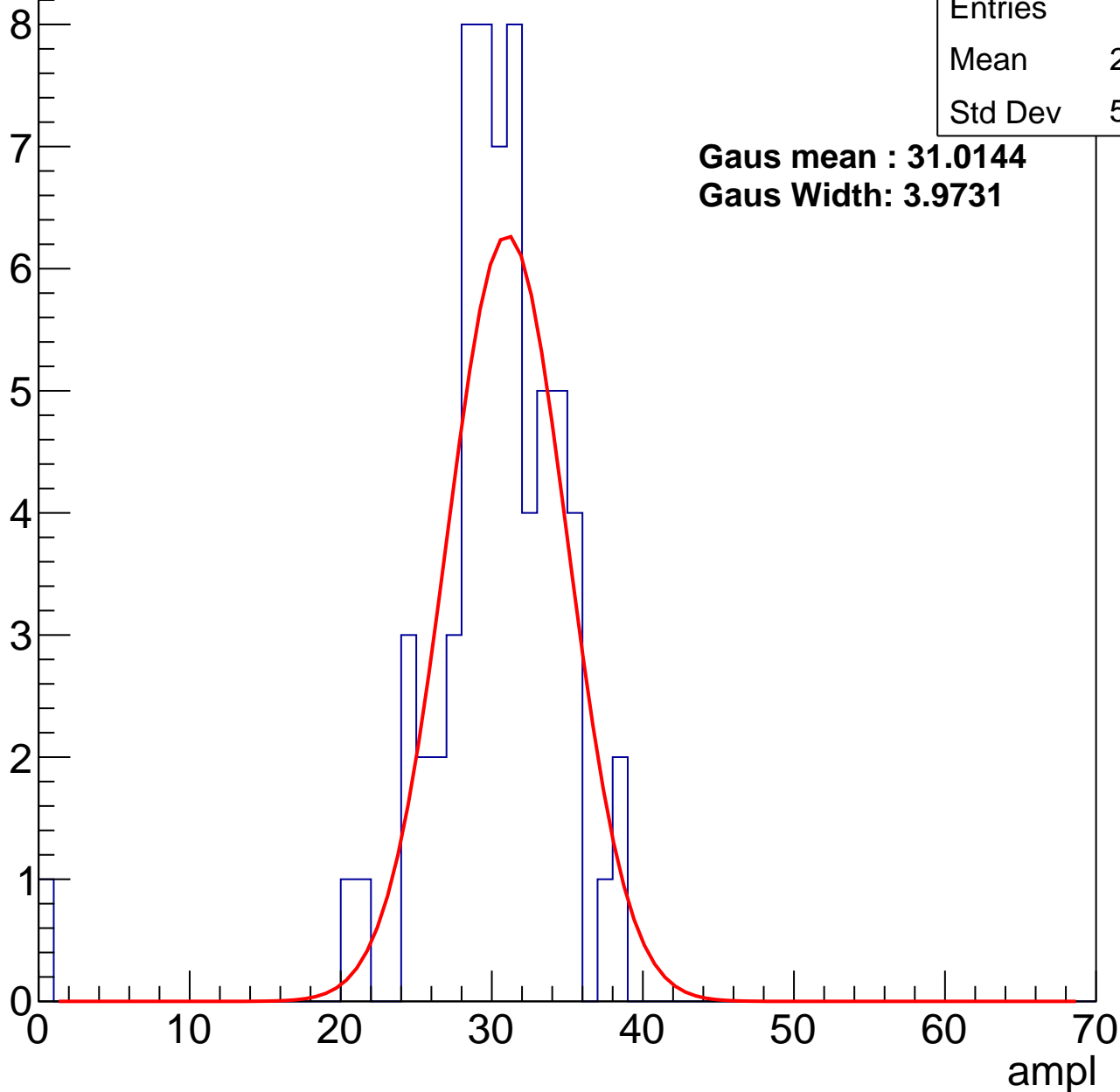
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.63
Std Dev	5.196

Gaus mean : 31.0144

Gaus Width: 3.9731



B0L001S, U15-ch7, adc1

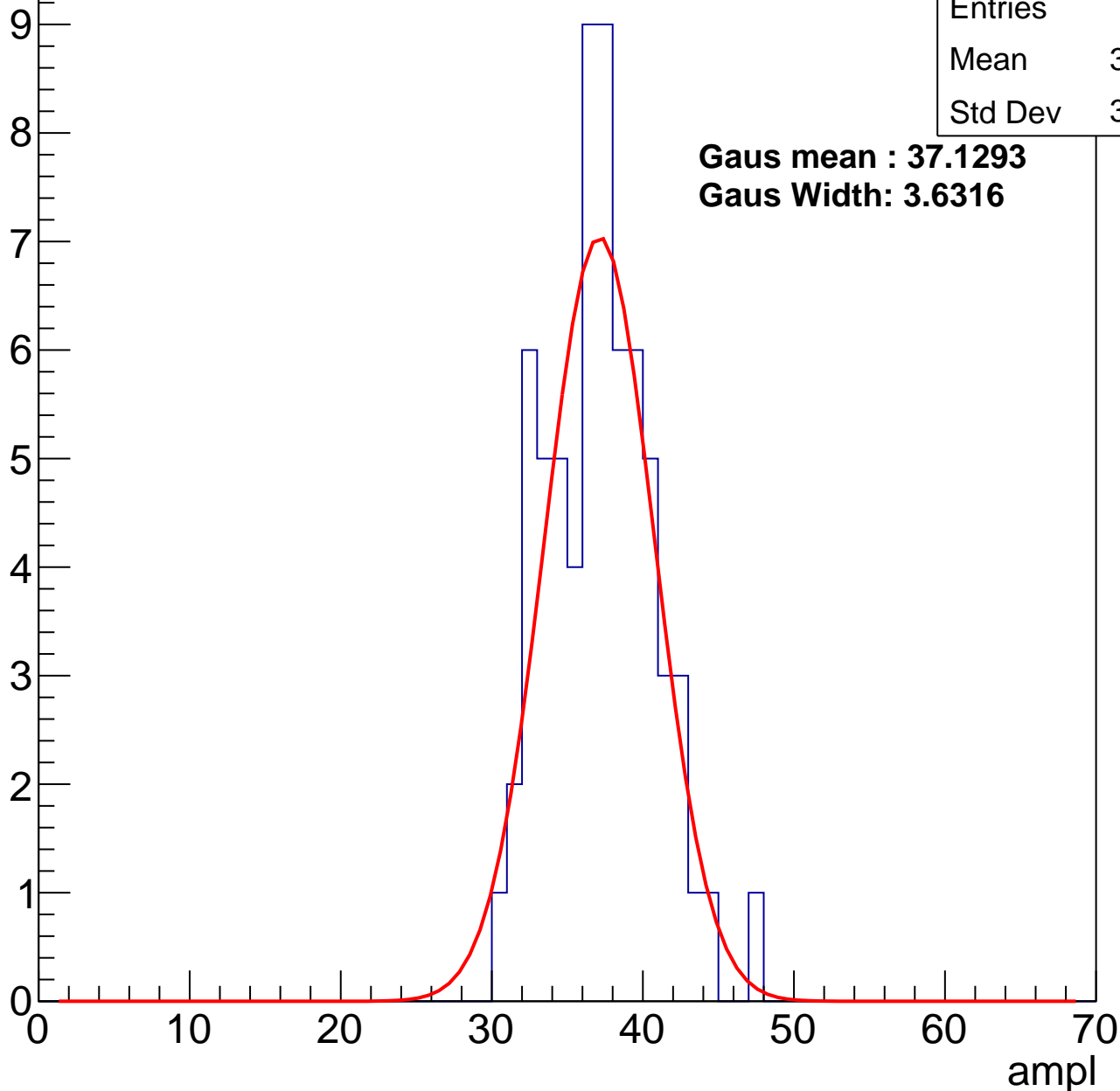
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.73
Std Dev	3.454

Gaus mean : 37.1293

Gaus Width: 3.6316



B0L001S, U15-ch7, adc2

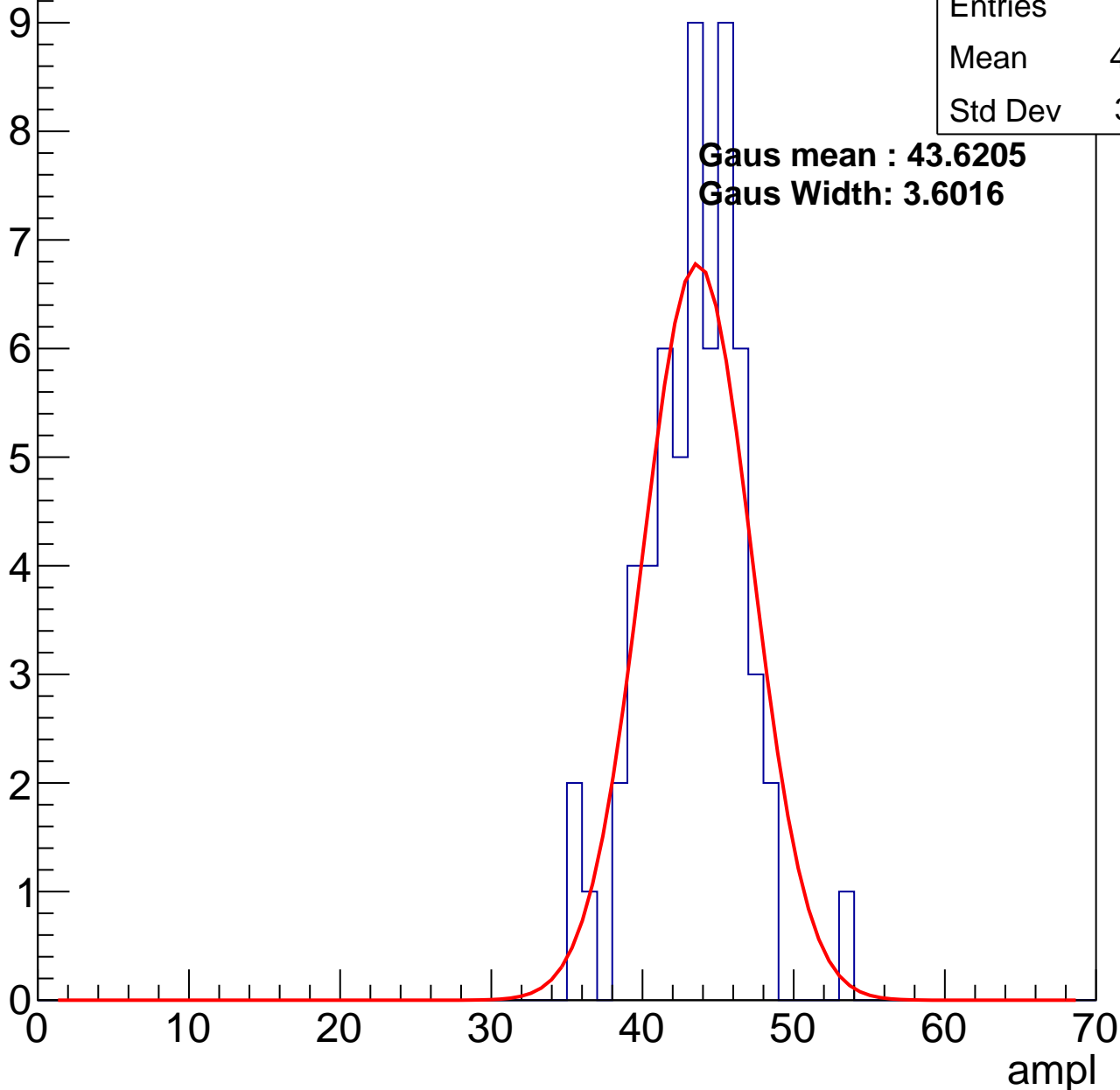
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	42.93
Std Dev	3.291

Gaus mean : 43.6205

Gaus Width: 3.6016



B0L001S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

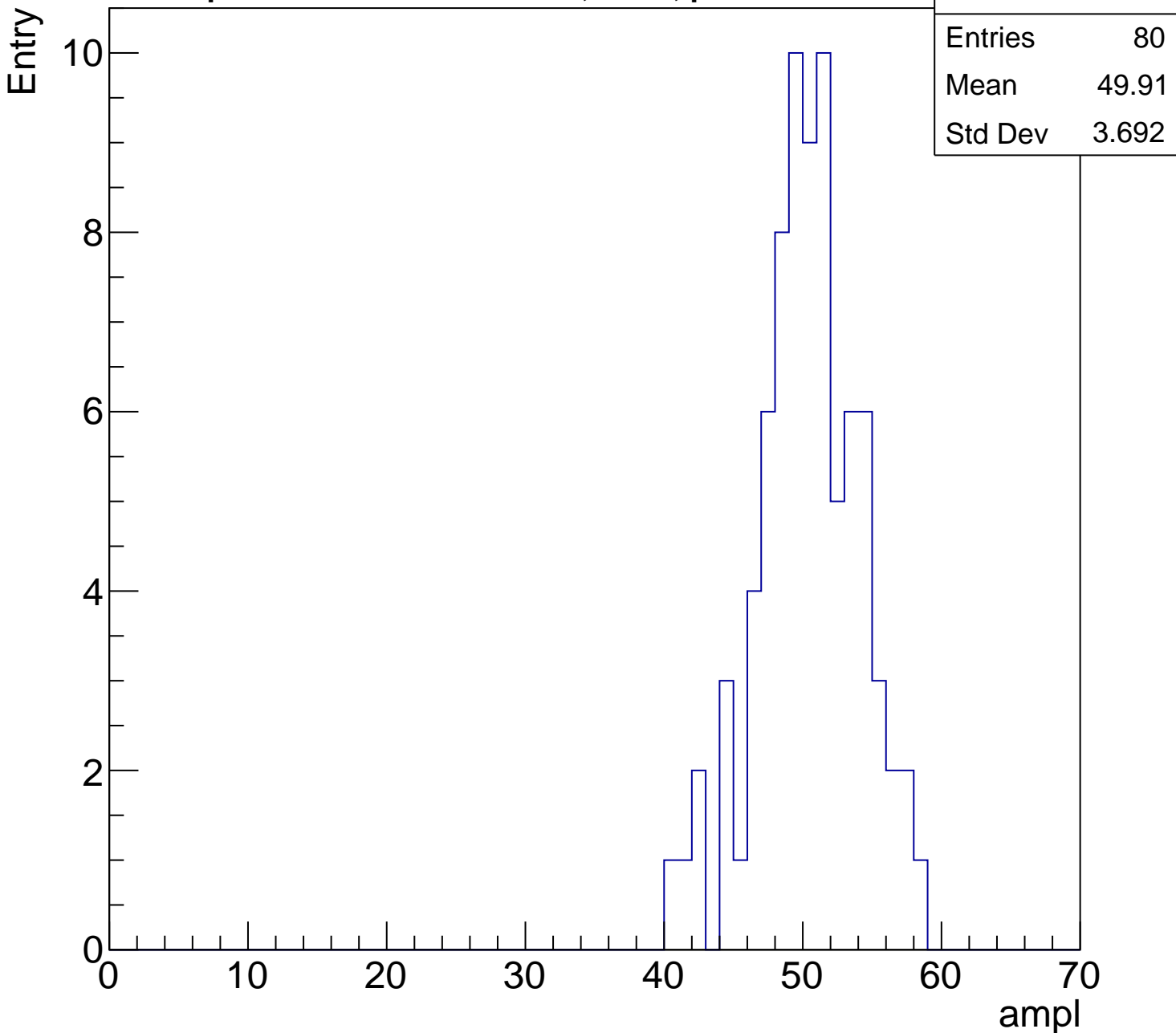
Entries	80
Mean	49.91
Std Dev	3.692

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

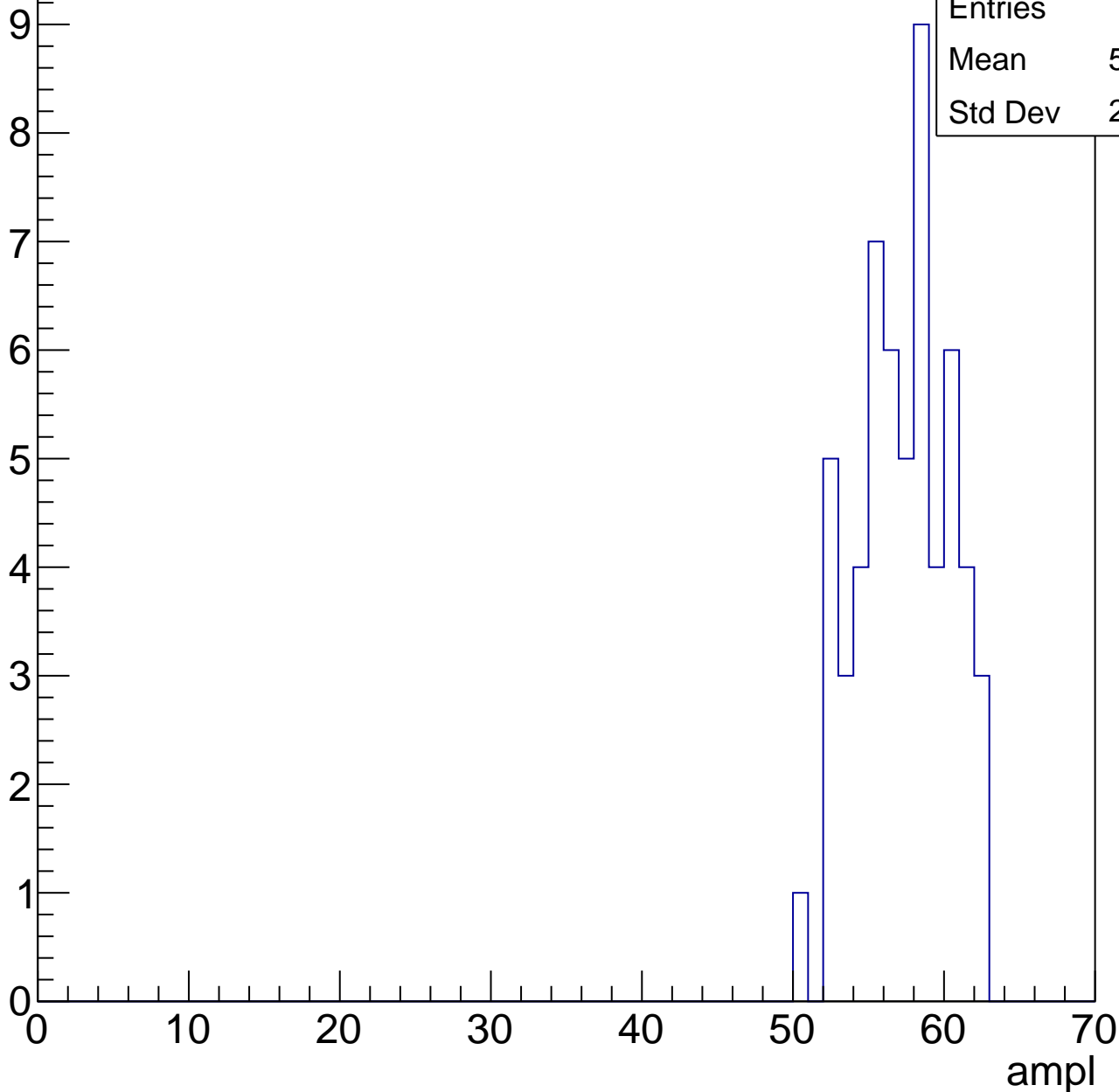


B0L001S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.82
Std Dev	2.986

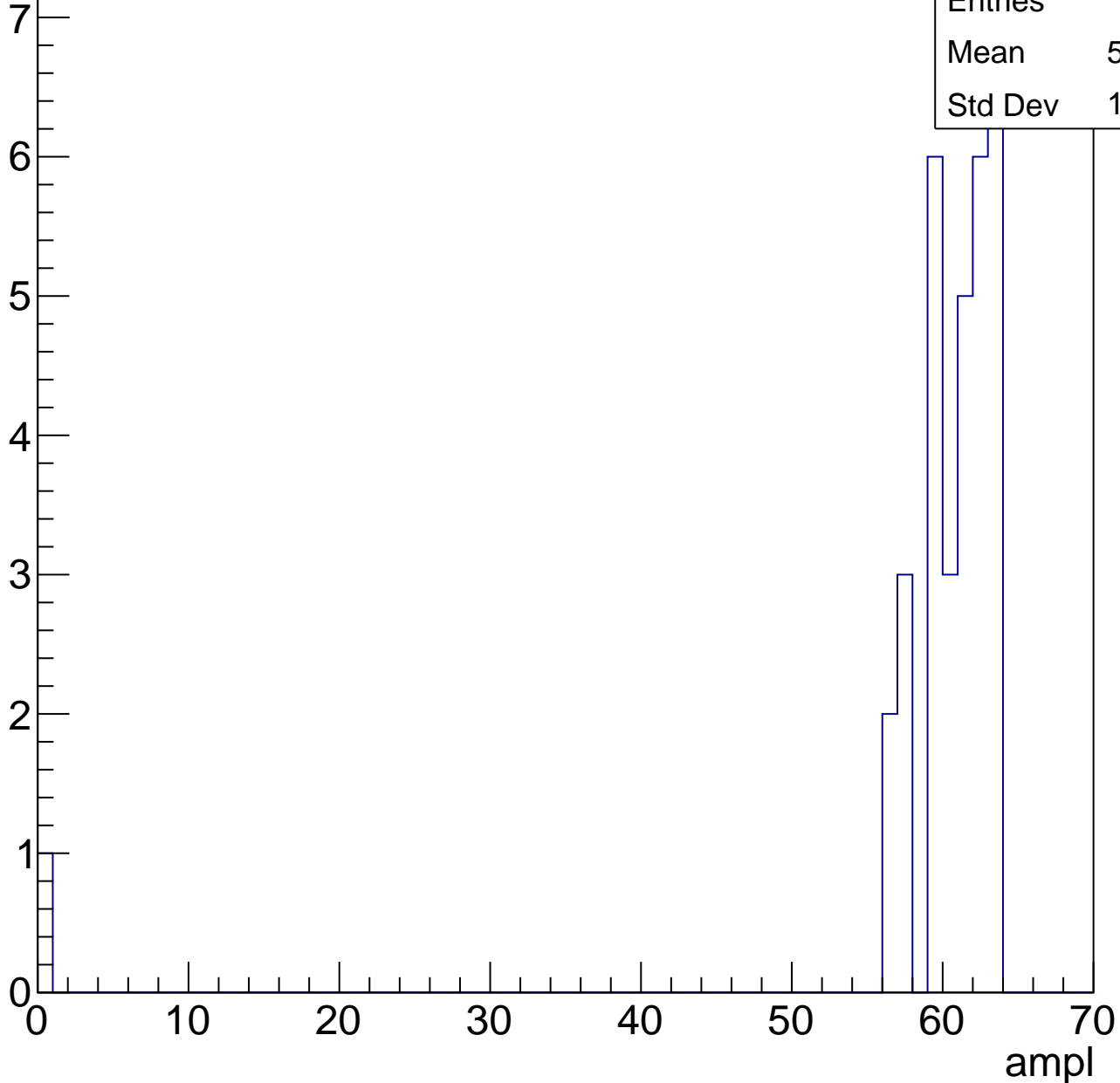


B0L001S, U15-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.64
Std Dev	10.58



B0L001S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch8, adc0

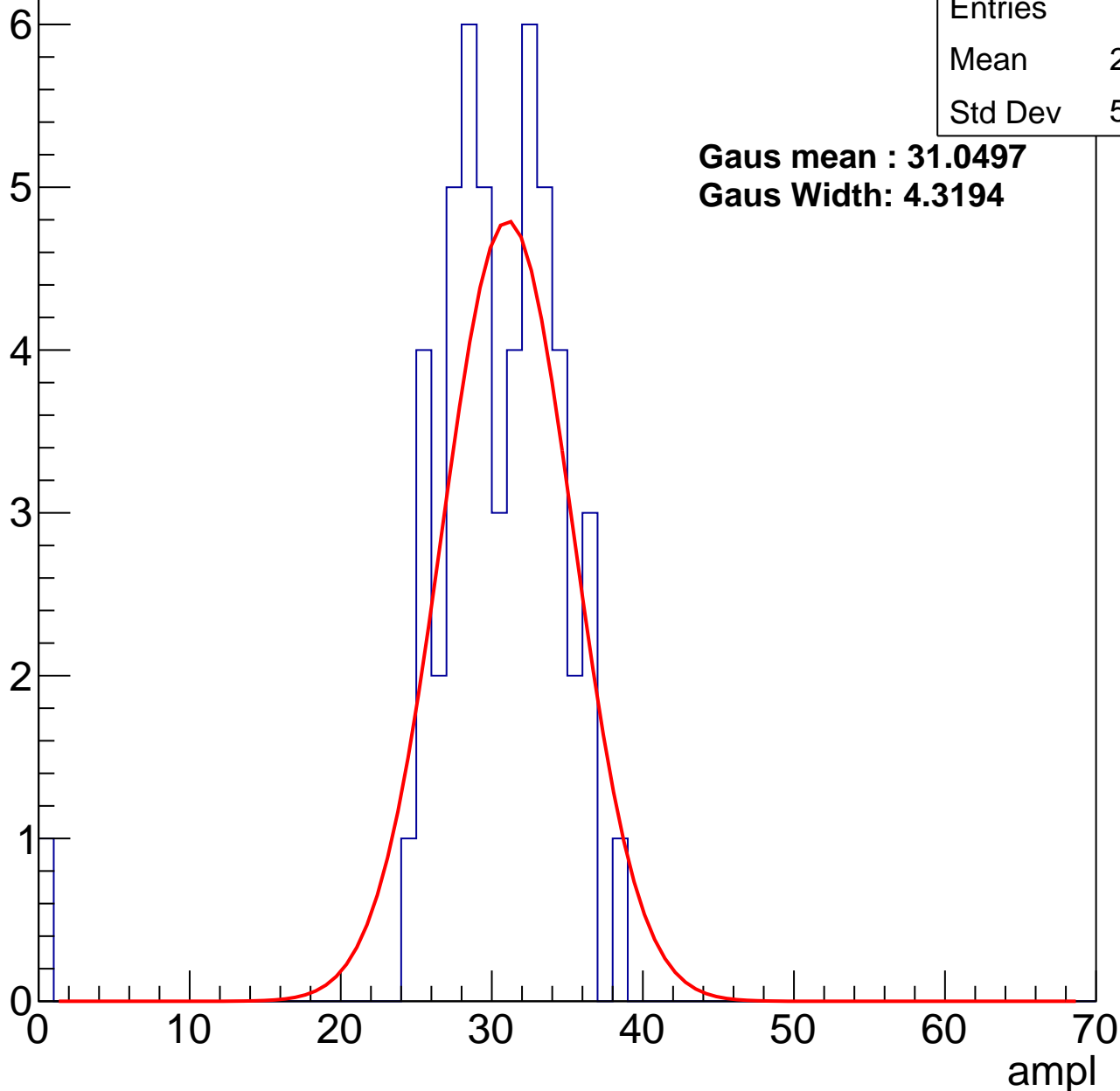
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.75
Std Dev	5.367

Gaus mean : 31.0497

Gaus Width: 4.3194



B0L001S, U15-ch8, adc1

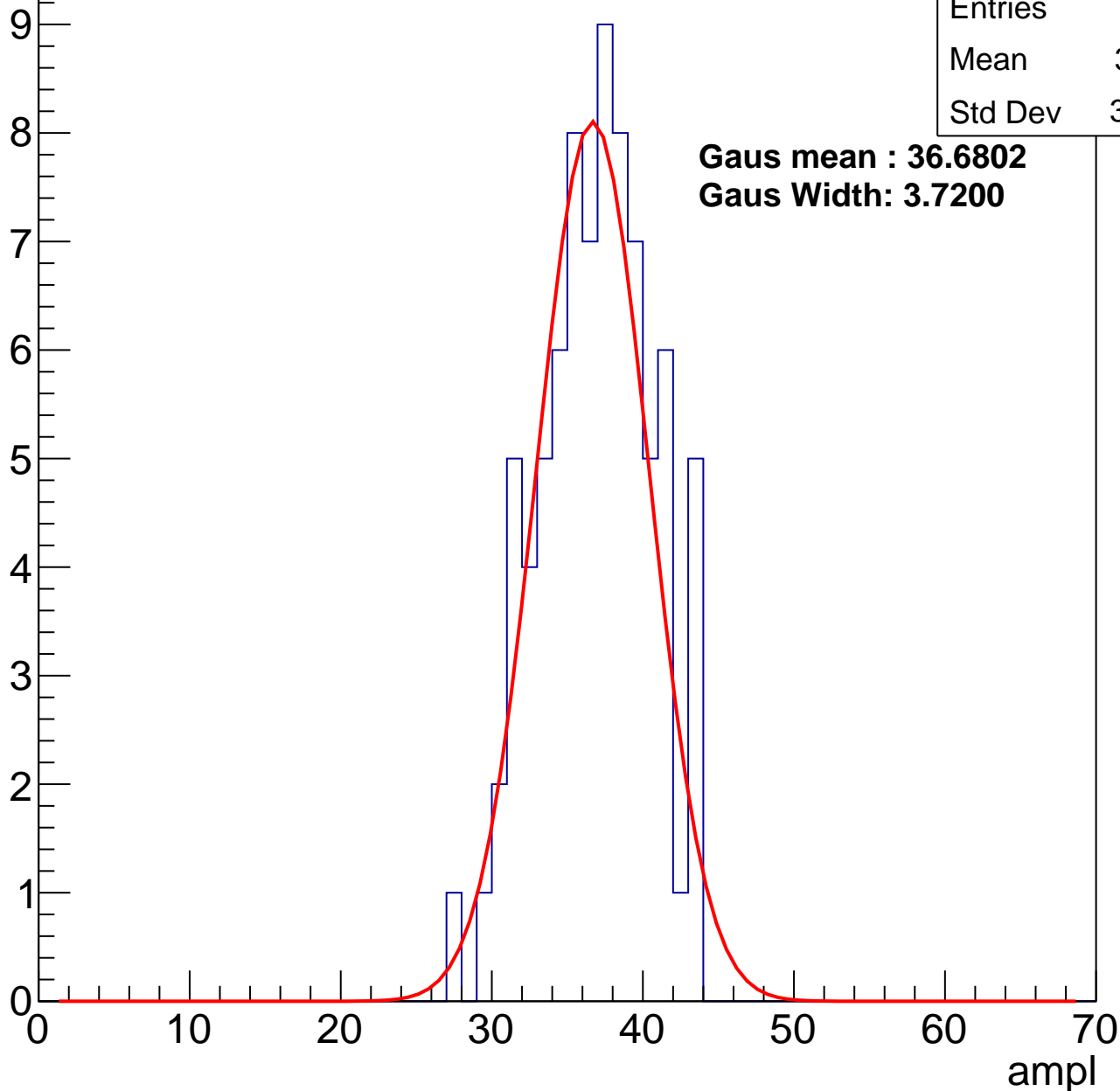
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	36.41
Std Dev	3.656

Gaus mean : 36.6802

Gaus Width: 3.7200



B0L001S, U15-ch8, adc2

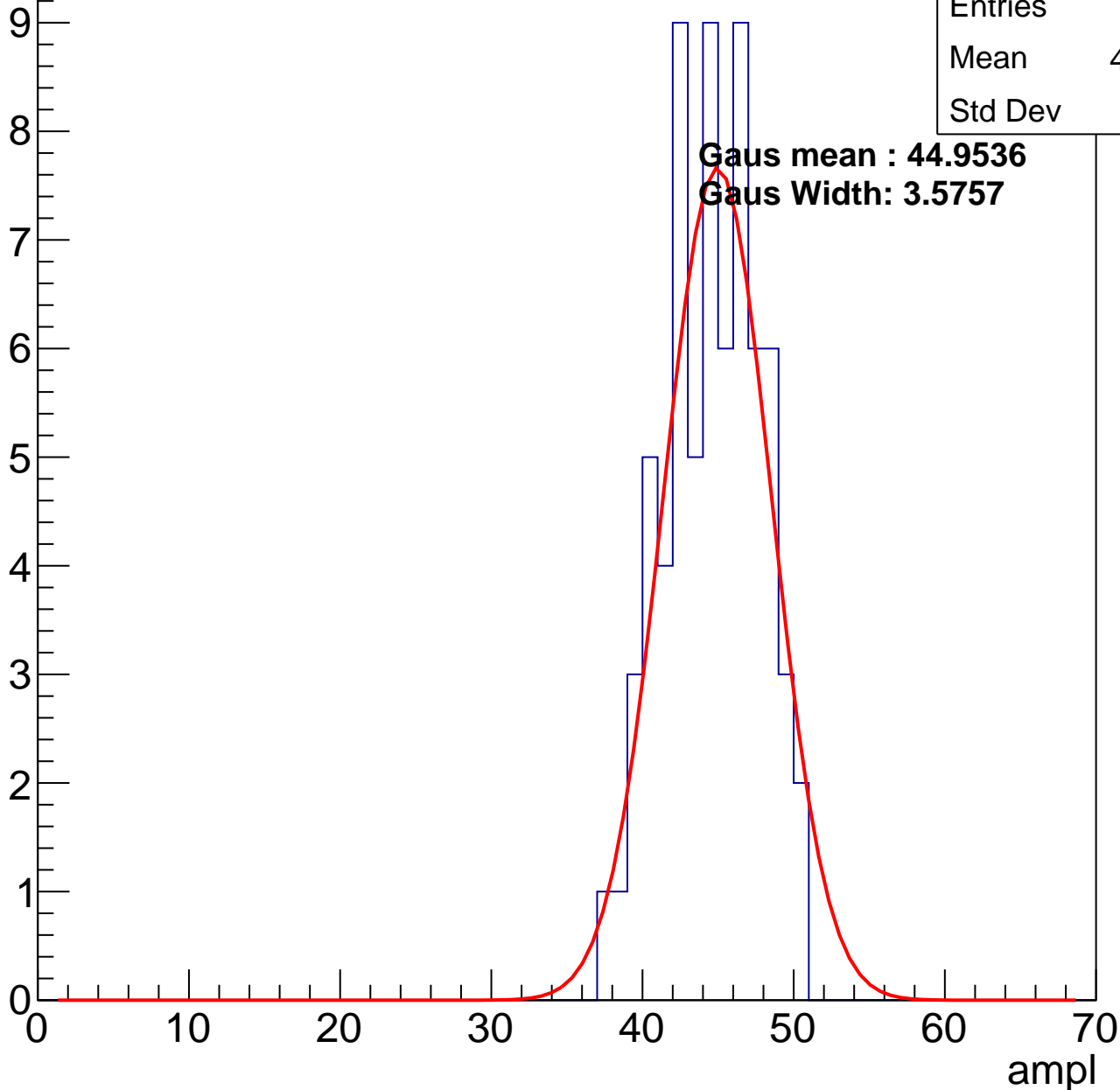
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.14
Std Dev	3.08

Gaus mean : 44.9536

Gaus Width: 3.5757

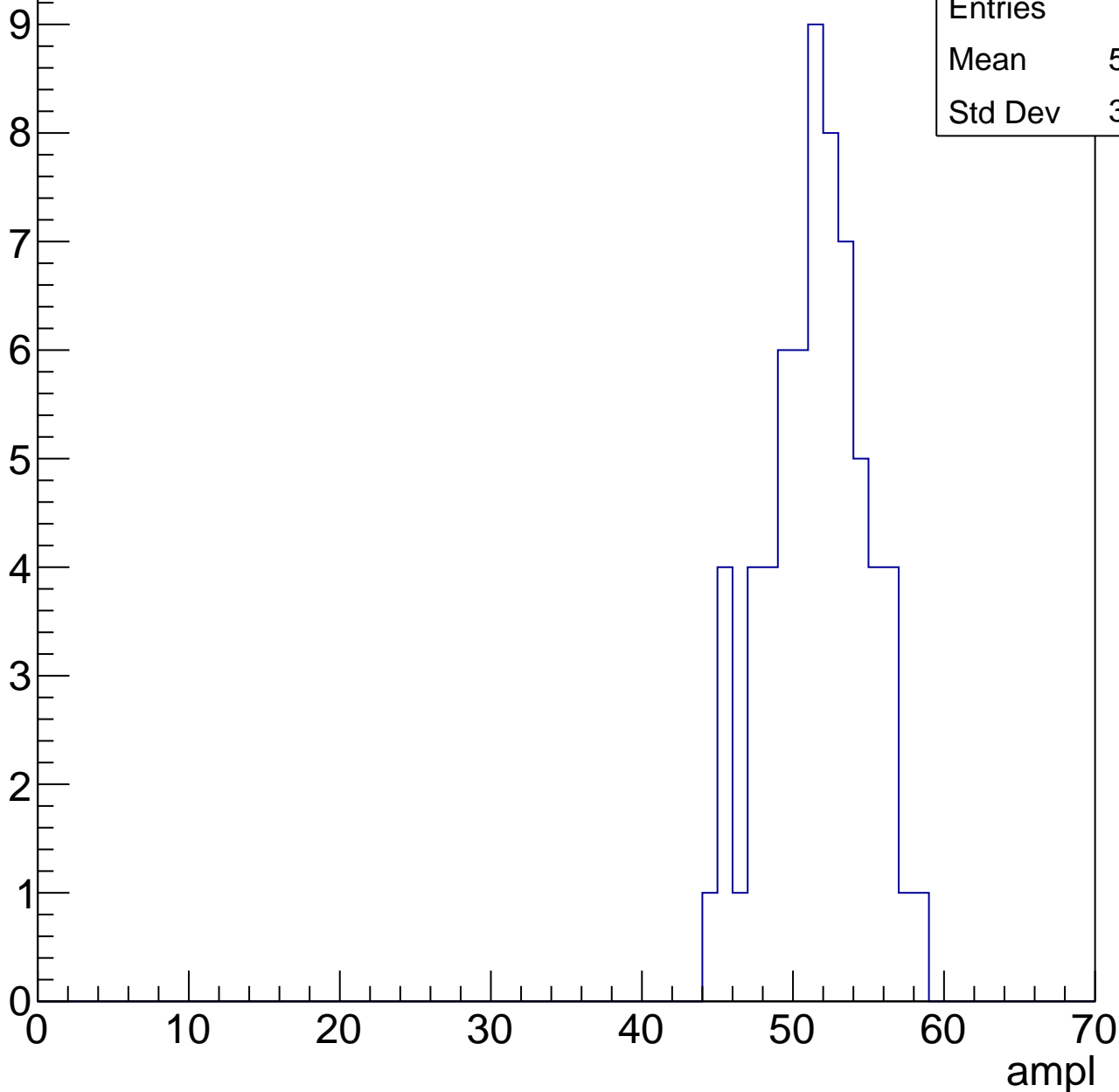


B0L001S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.06
Std Dev	3.229

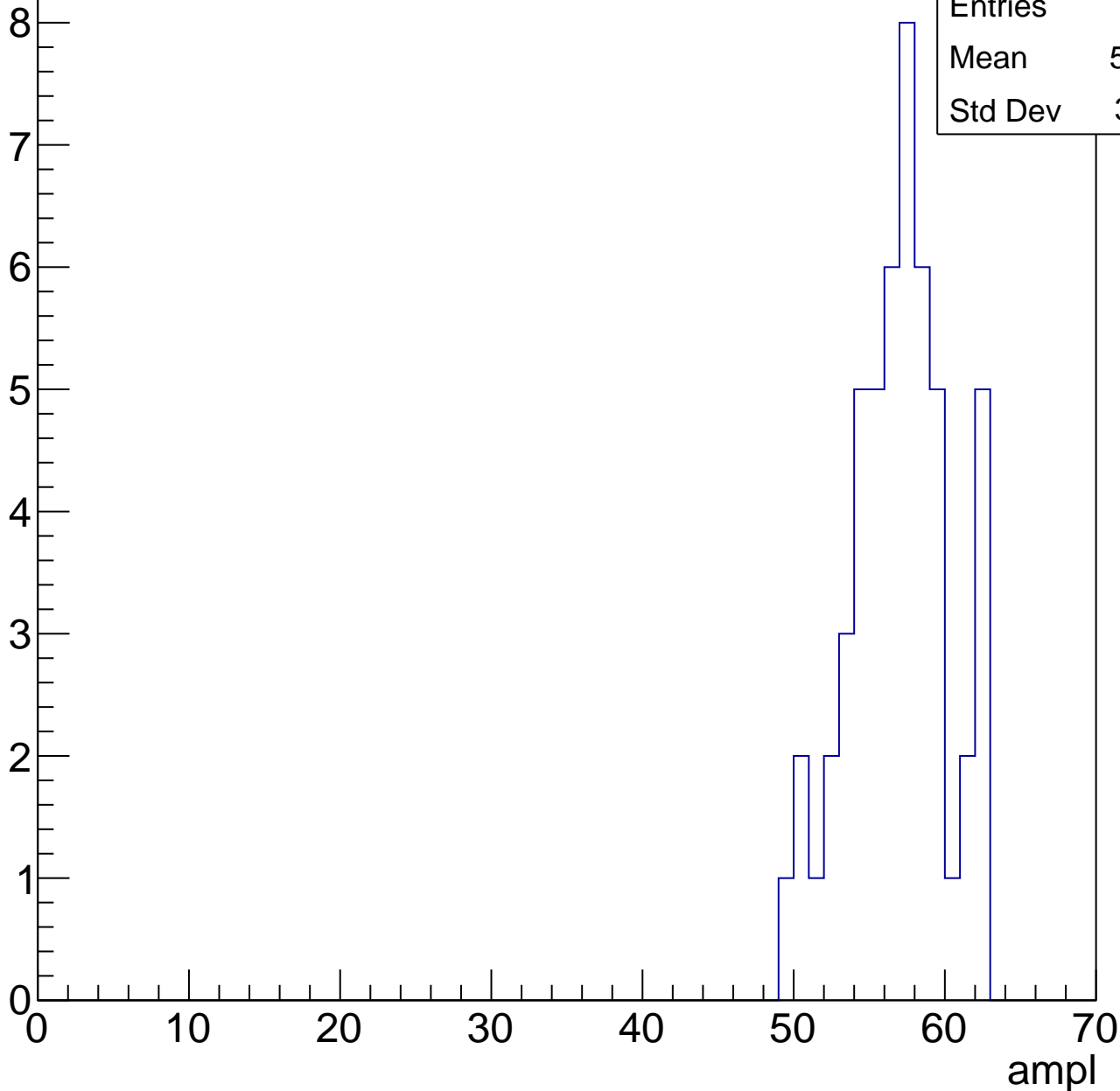


B0L001S, U15-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.44
Std Dev	3.231

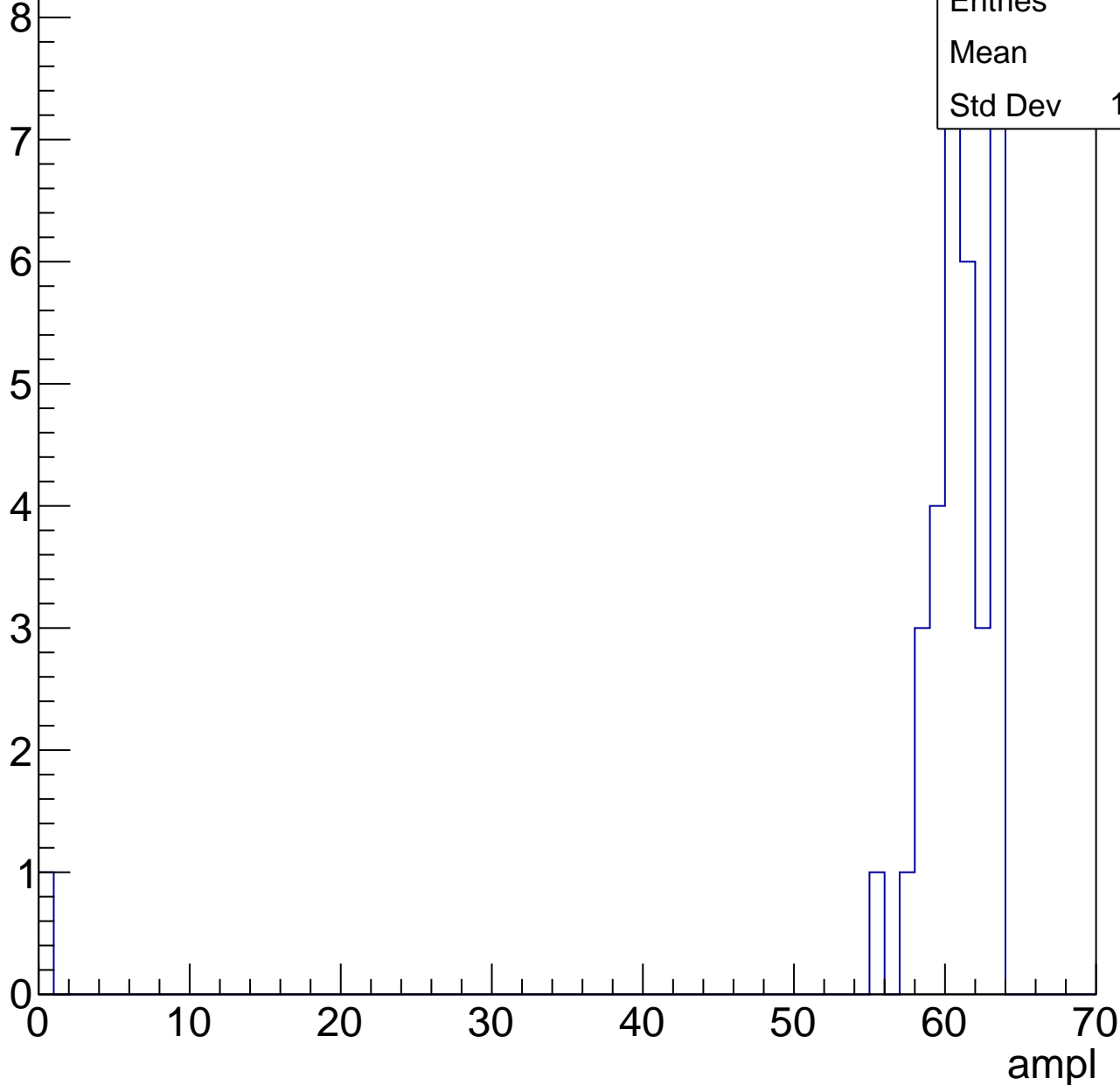


B0L001S, U15-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.8
Std Dev	10.27



B0L001S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.38
Std Dev	1.317

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch9, adc0

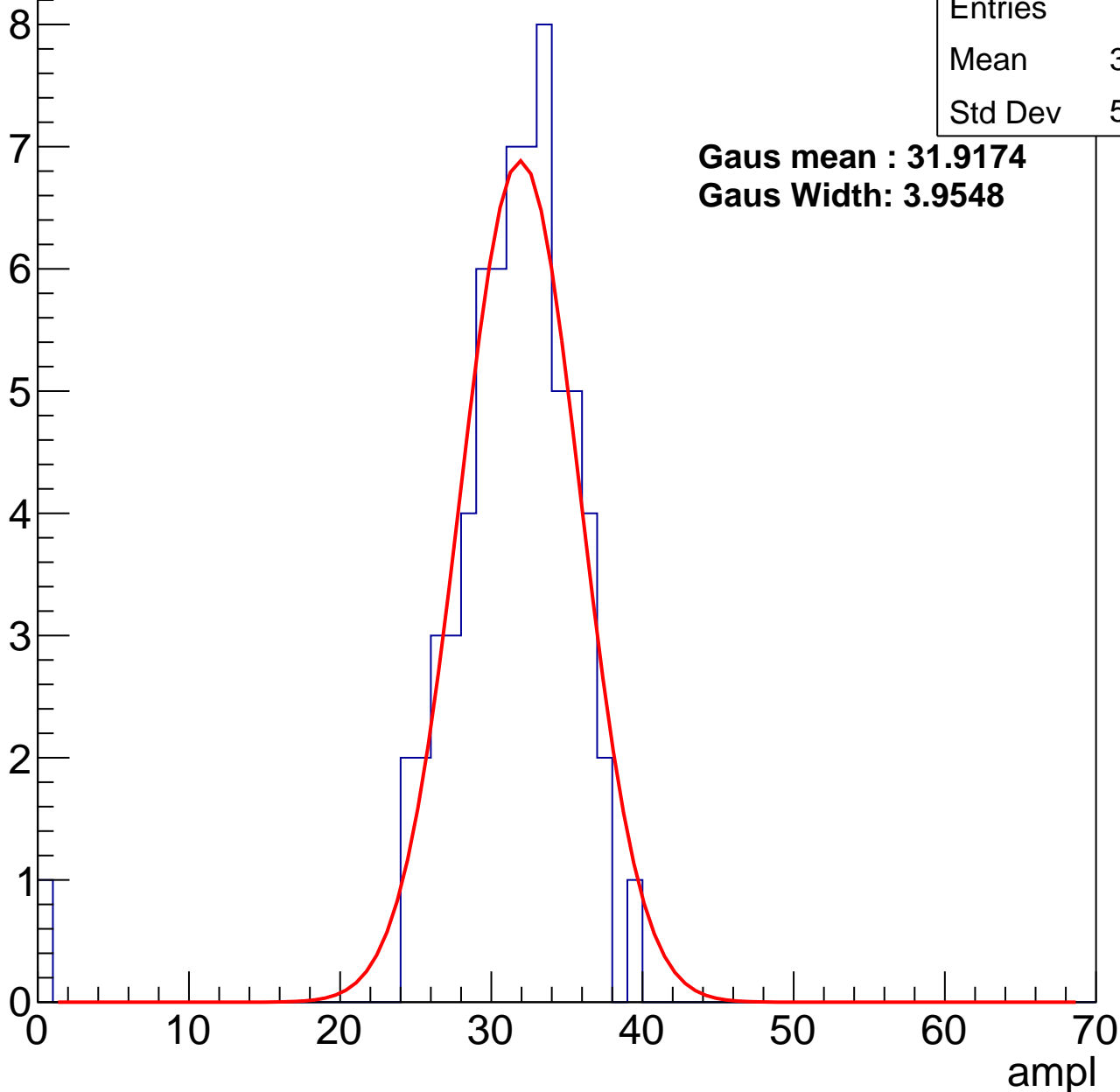
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.76
Std Dev	5.099

Gaus mean : 31.9174

Gaus Width: 3.9548



B0L001S, U15-ch9, adc1

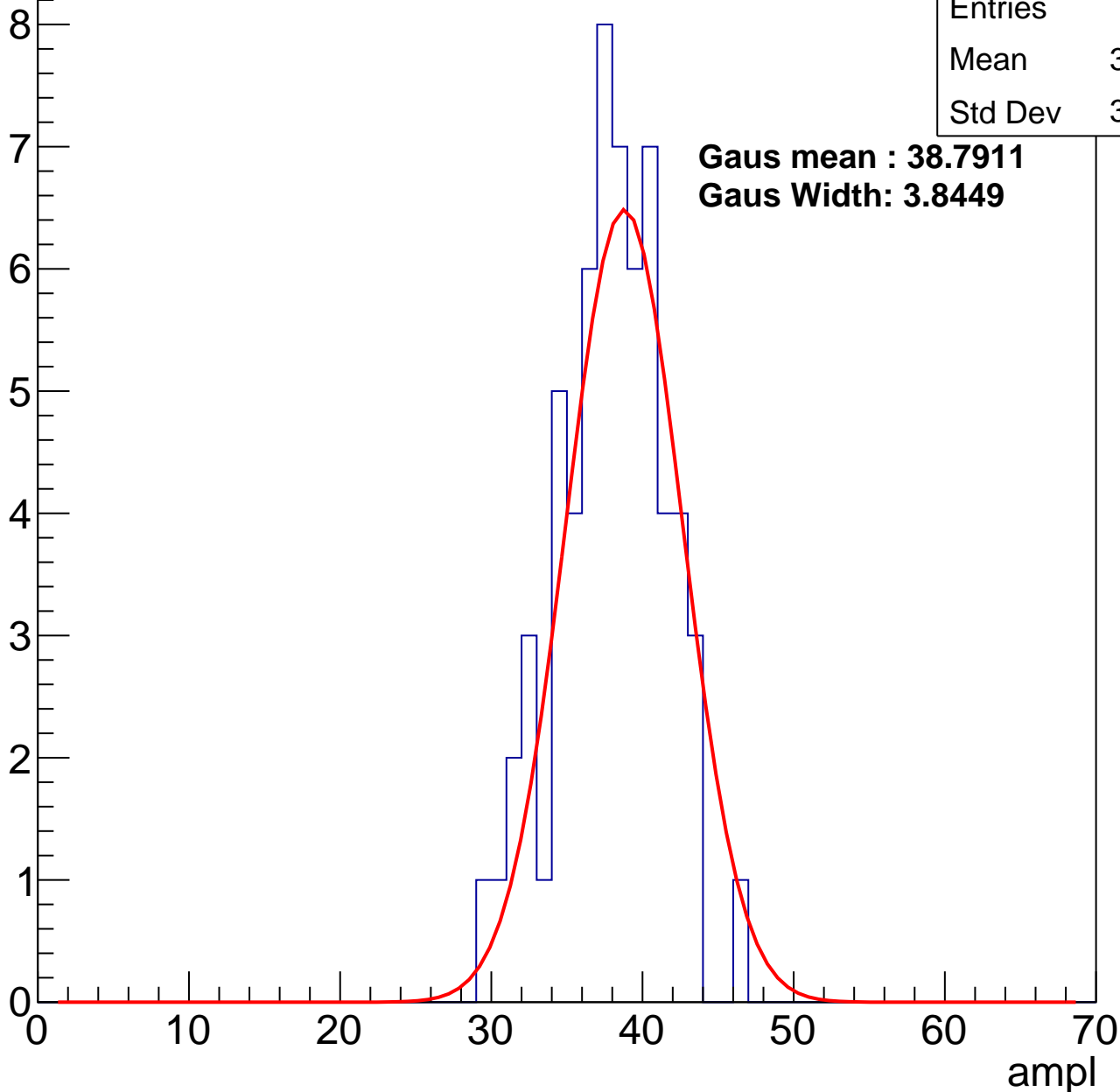
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.44
Std Dev	3.513

Gaus mean : 38.7911

Gaus Width: 3.8449



B0L001S, U15-ch9, adc2

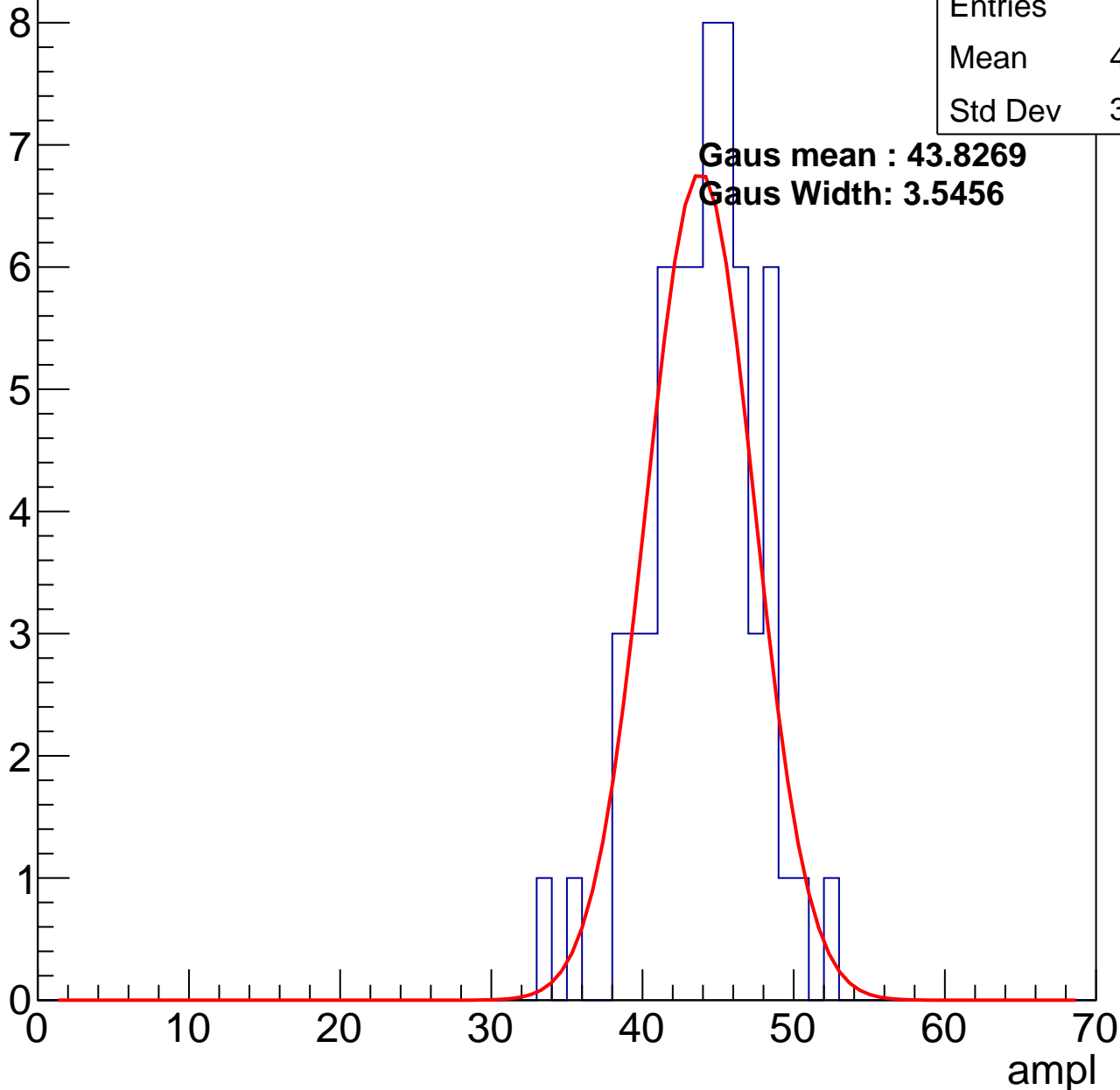
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.54
Std Dev	3.527

Gaus mean : 43.8269

Gaus Width: 3.5456

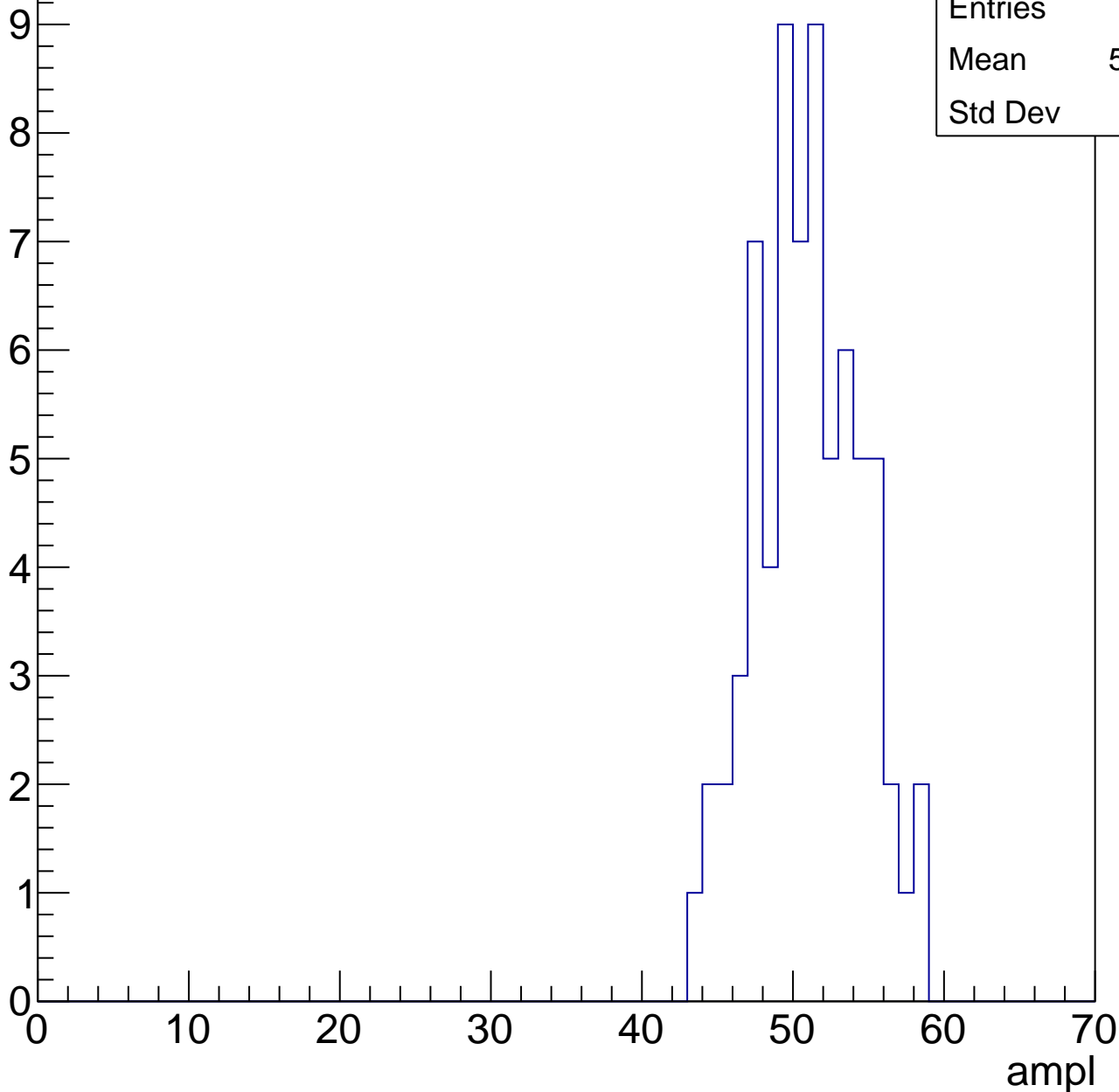


B0L001S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.54
Std Dev	3.43

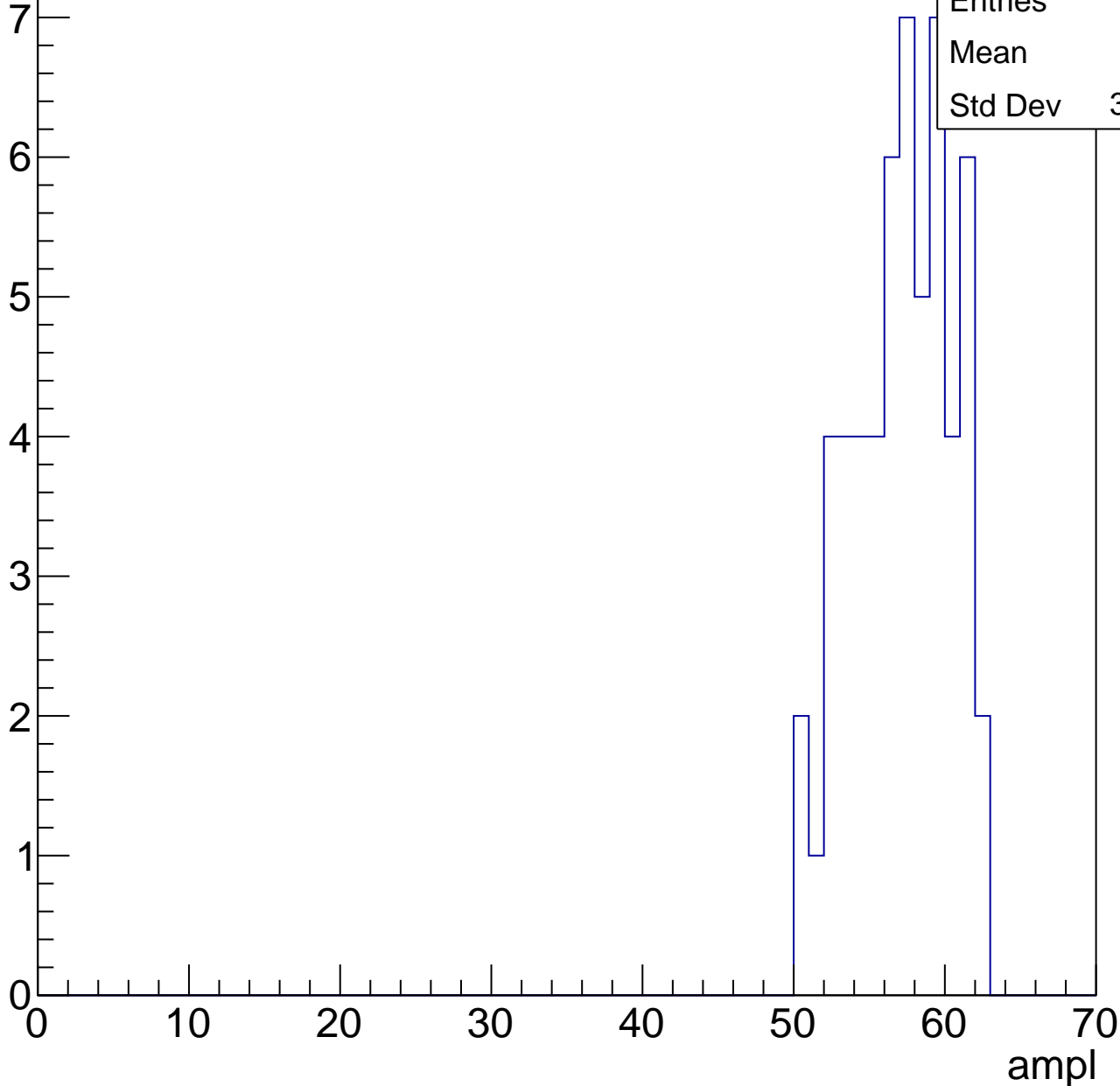


B0L001S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.7
Std Dev	3.179

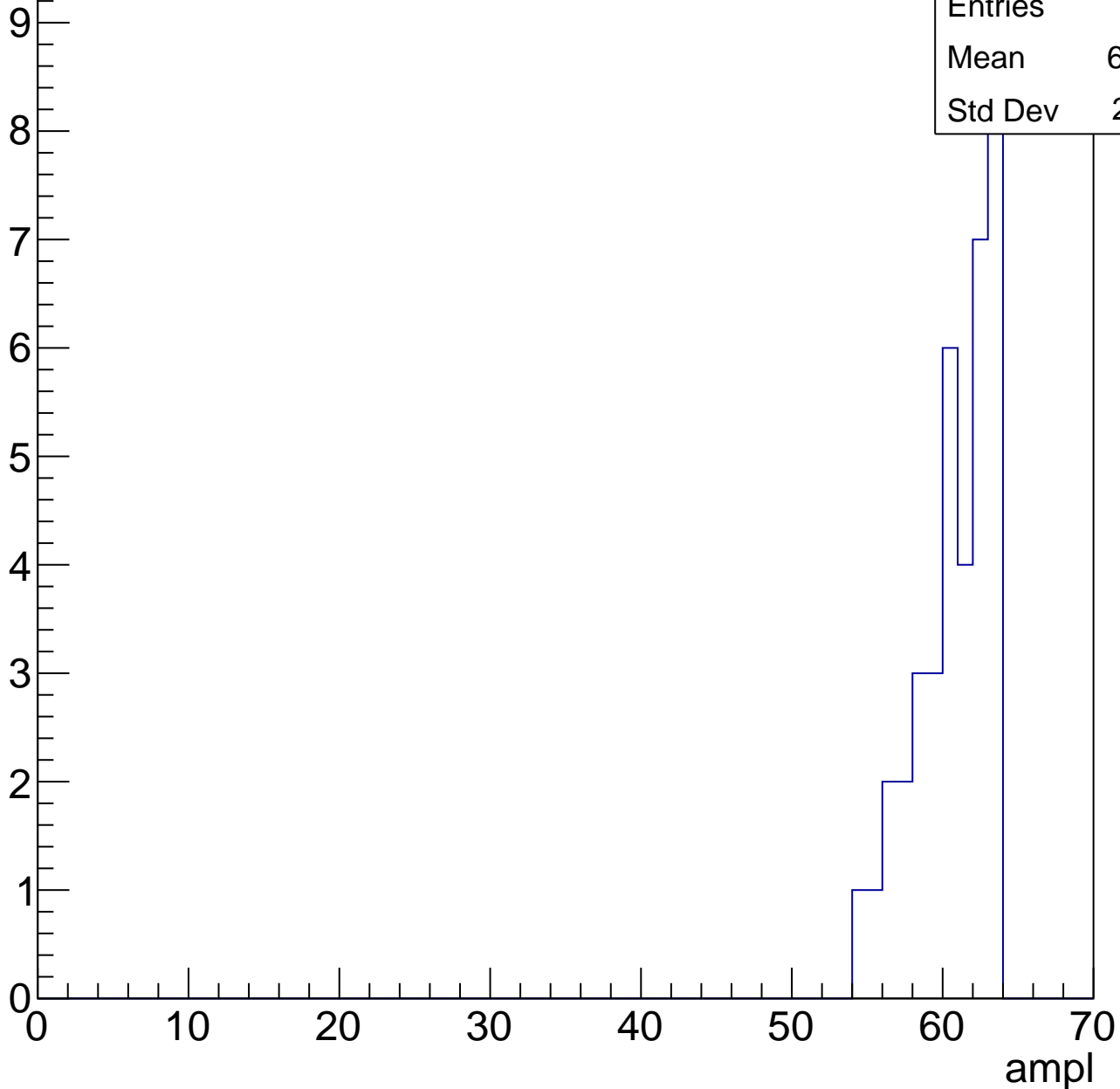


B0L001S, U15-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	60.29
Std Dev	2.491



B0L001S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch10, adc0

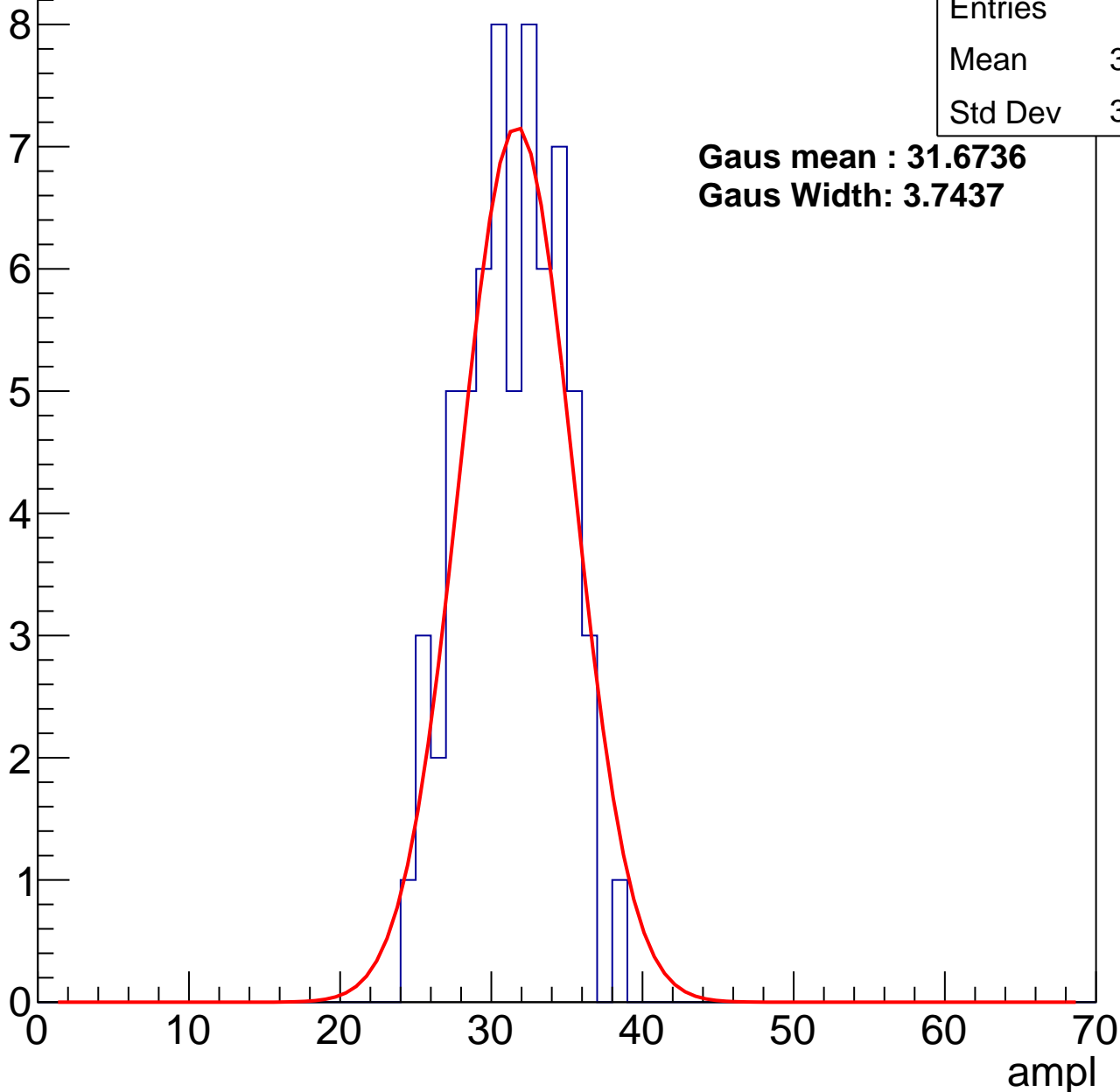
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.89
Std Dev	3.192

Gaus mean : 31.6736

Gaus Width: 3.7437



B0L001S, U15-ch10, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	37.83
Std Dev	3.684

Gaus mean : 38.7838

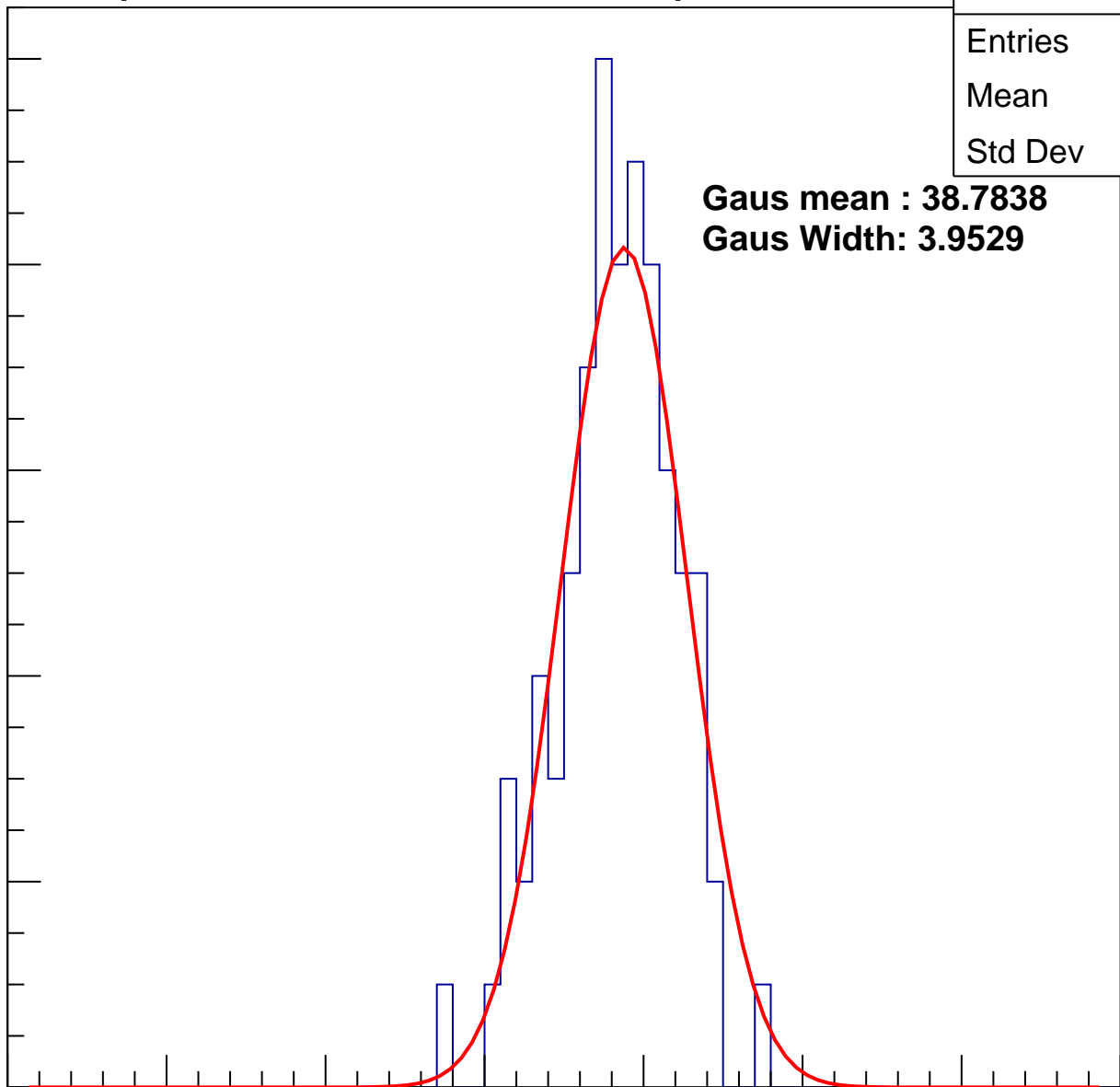
Gaus Width: 3.9529

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch10, adc2

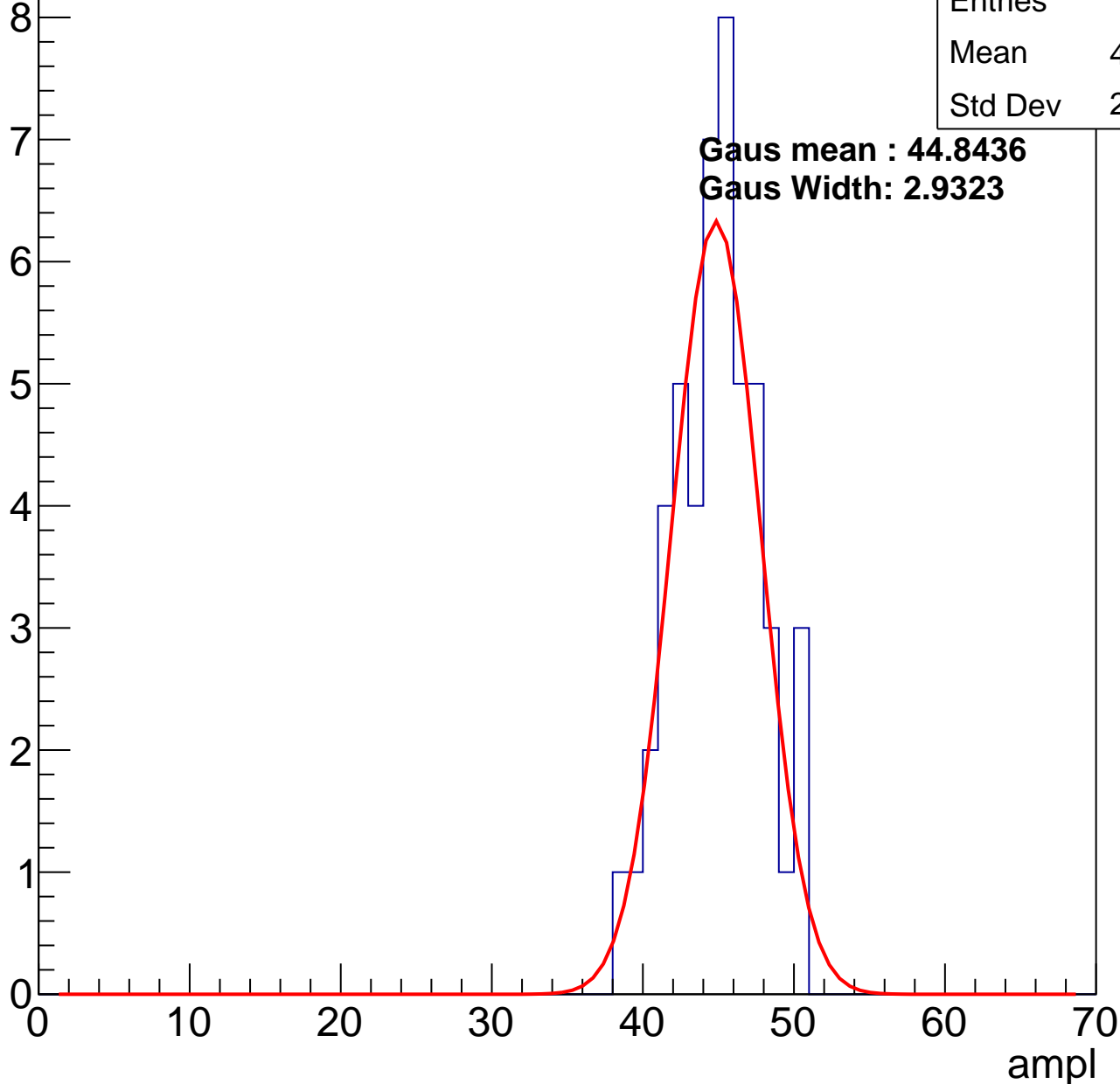
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	44.47
Std Dev	2.844

Gaus mean : 44.8436

Gaus Width: 2.9323

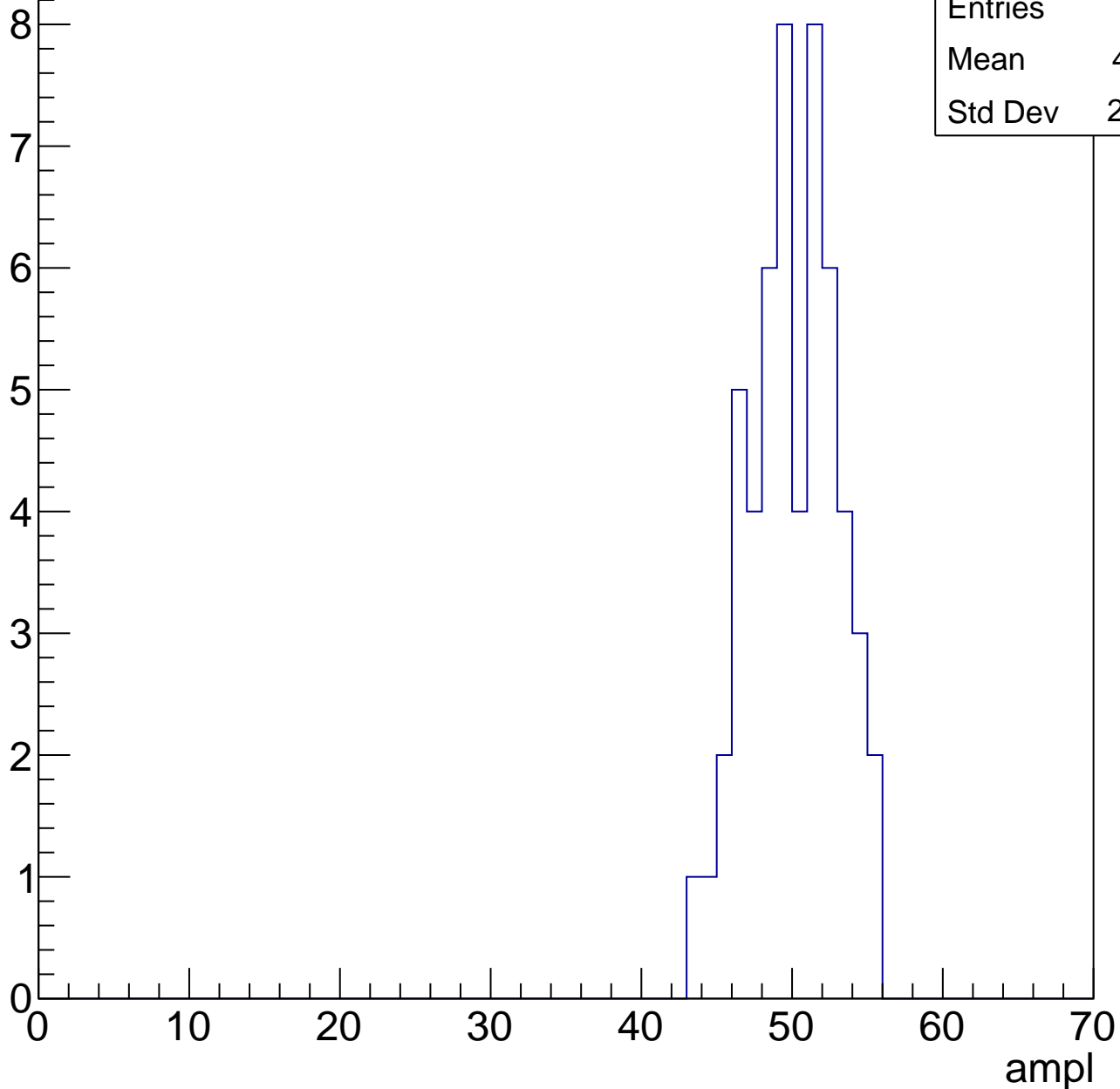


B0L001S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

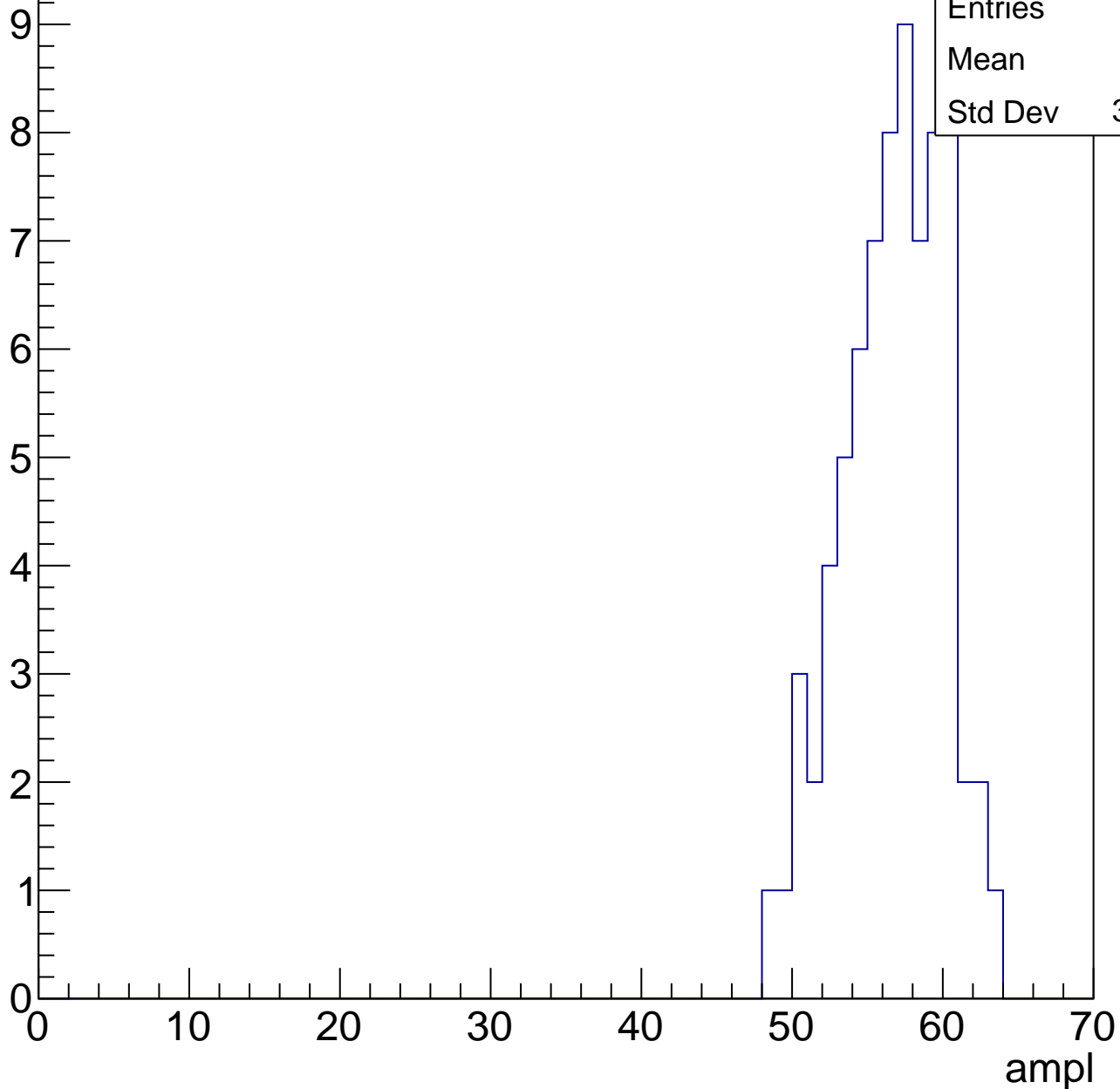
Entries	54
Mean	49.61
Std Dev	2.857



B0L001S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

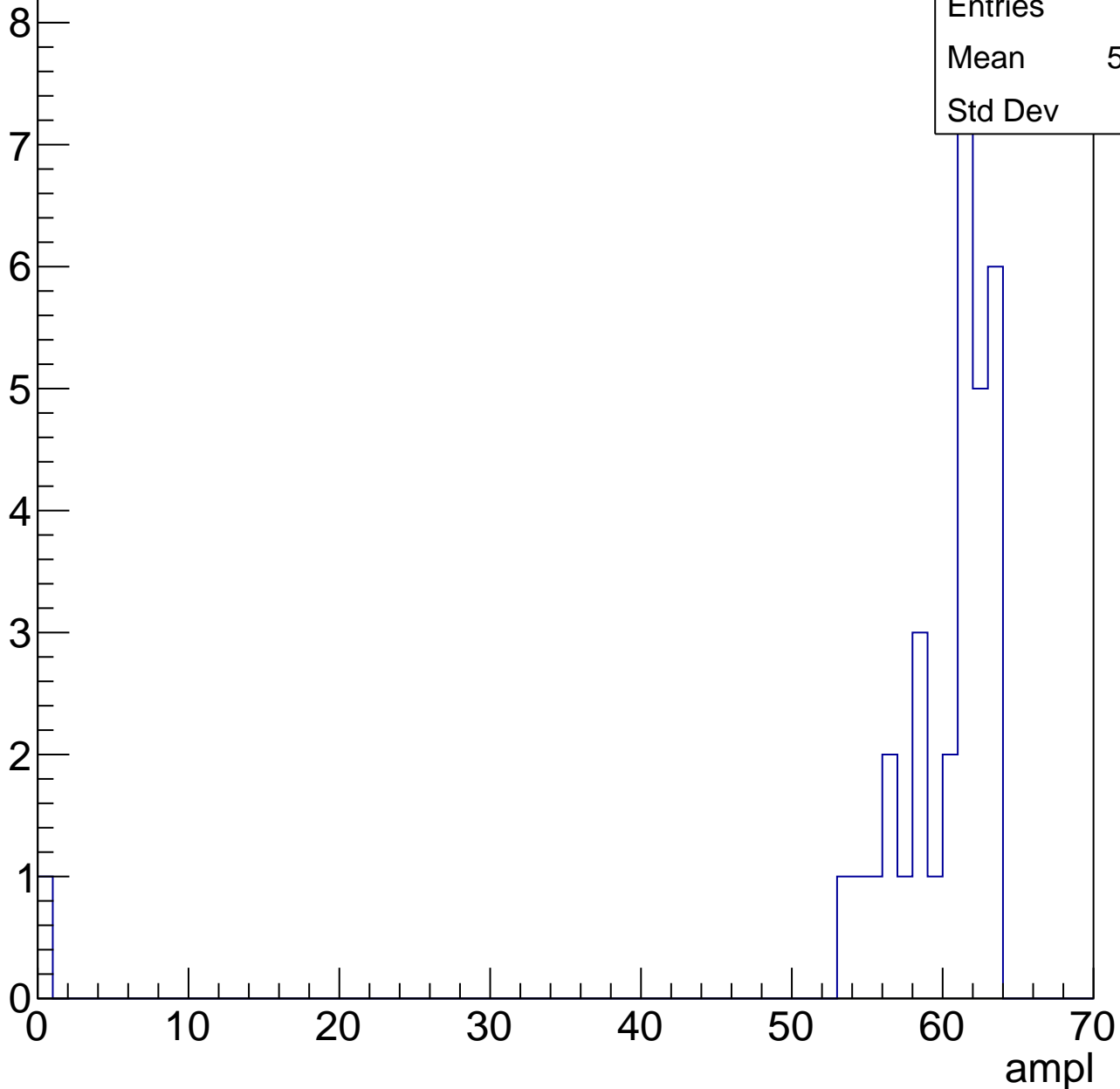


B0L001S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

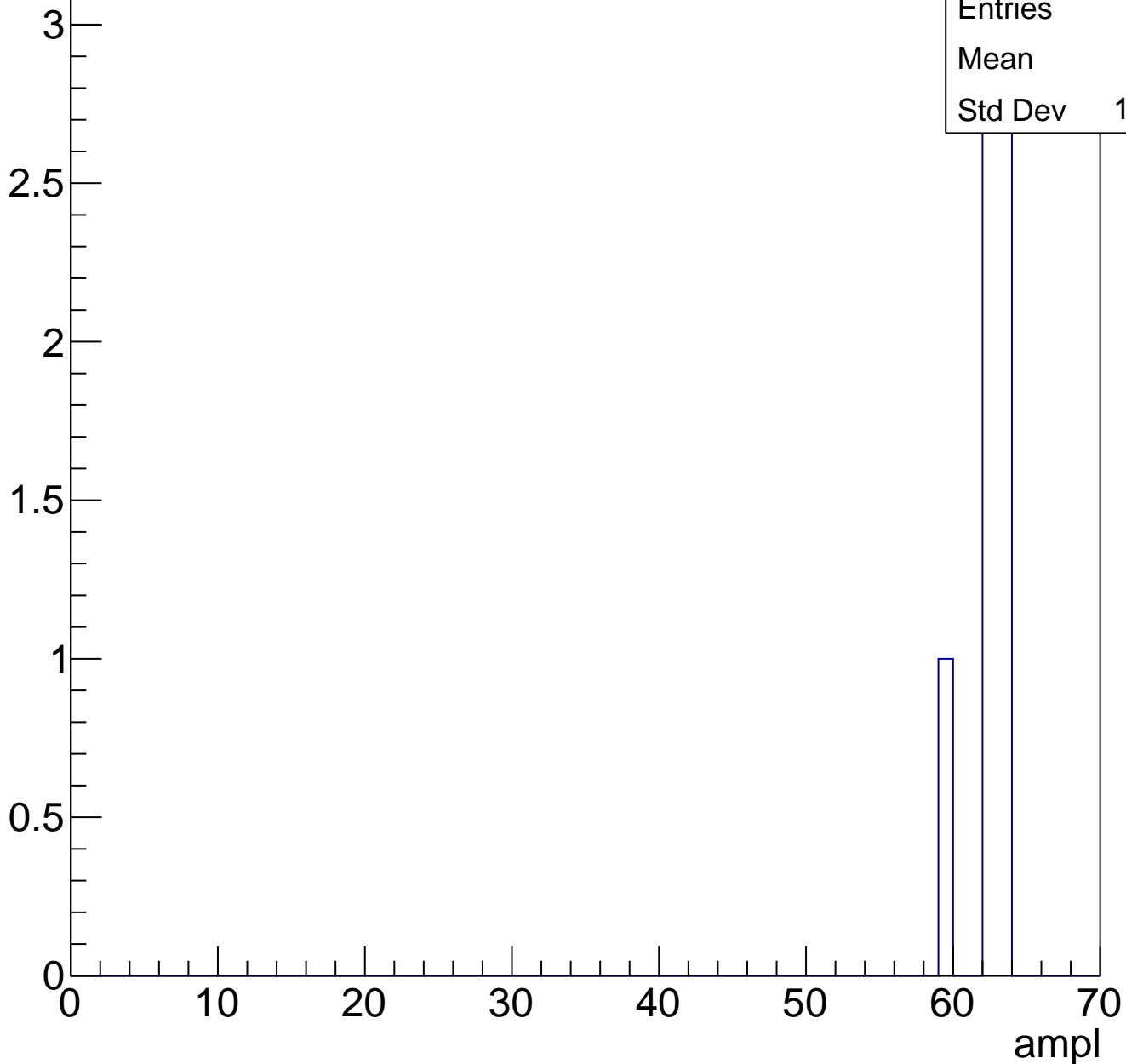
Entries	32
Mean	58.12
Std Dev	10.8



B0L001S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch11, adc0

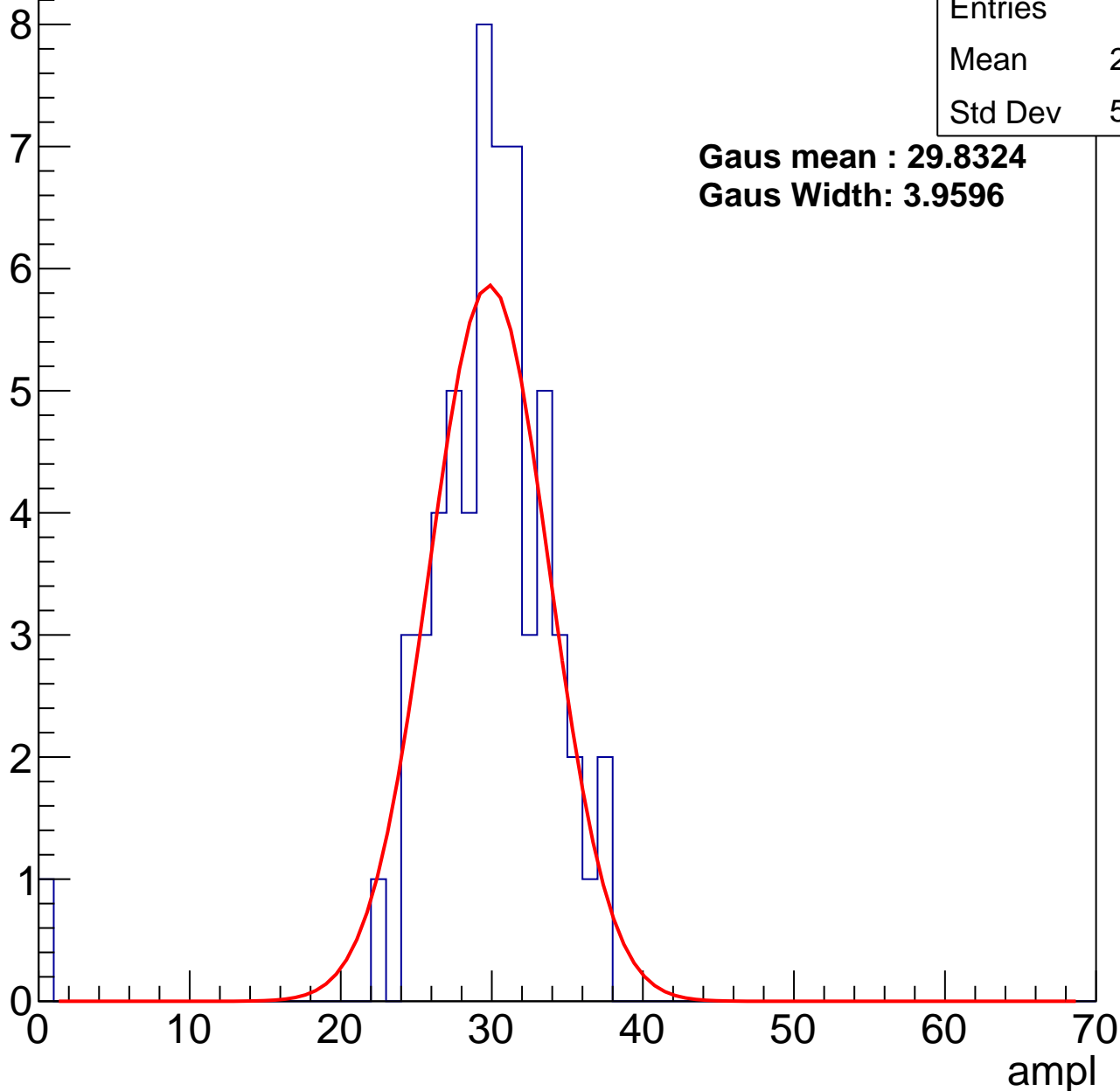
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	29.19
Std Dev	5.104

Gaus mean : 29.8324

Gaus Width: 3.9596



B0L001S, U15-ch11, adc1

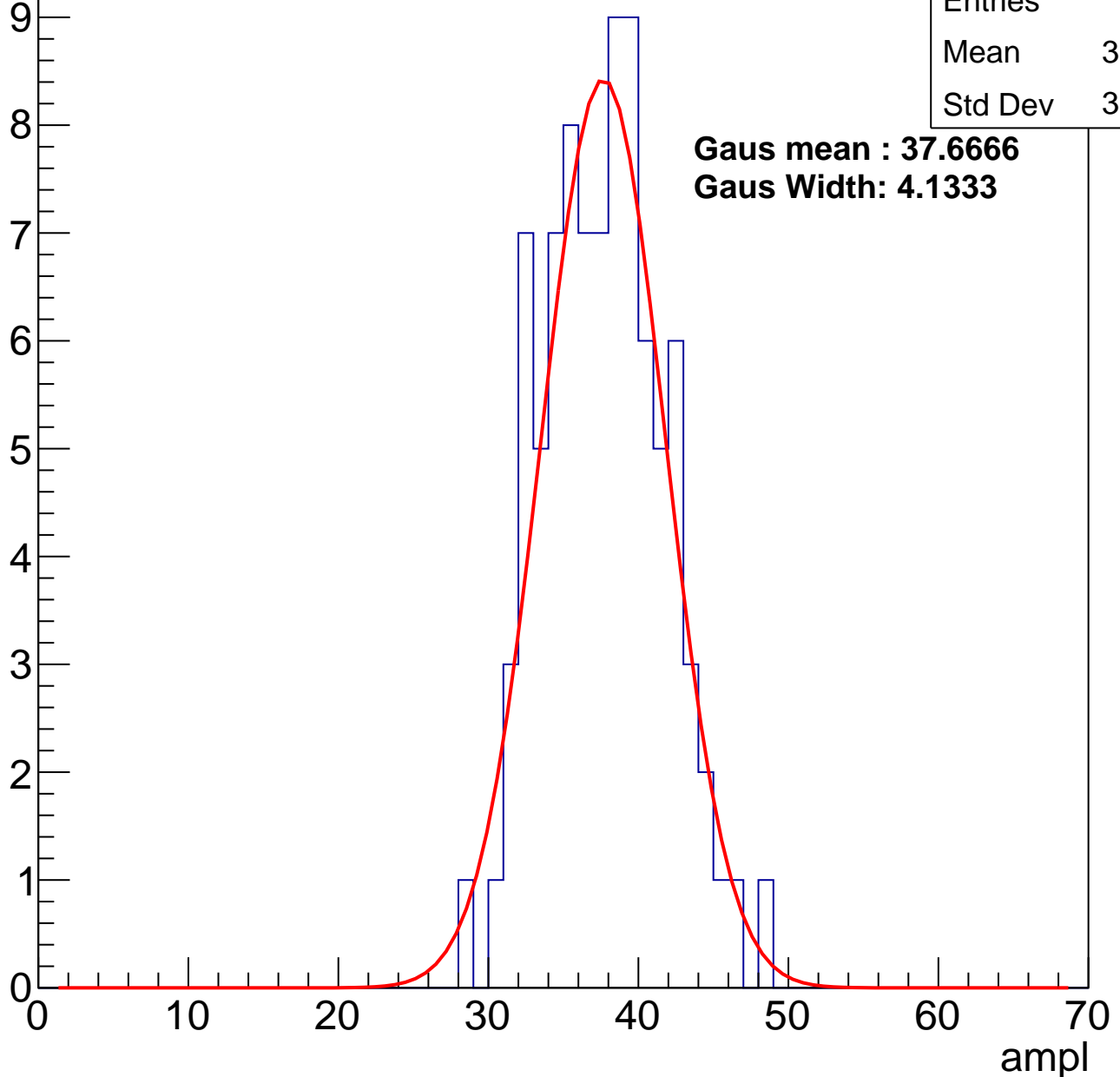
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	89
Mean	37.25
Std Dev	3.953

Gaus mean : 37.6666

Gaus Width: 4.1333



B0L001S, U15-ch11, adc2

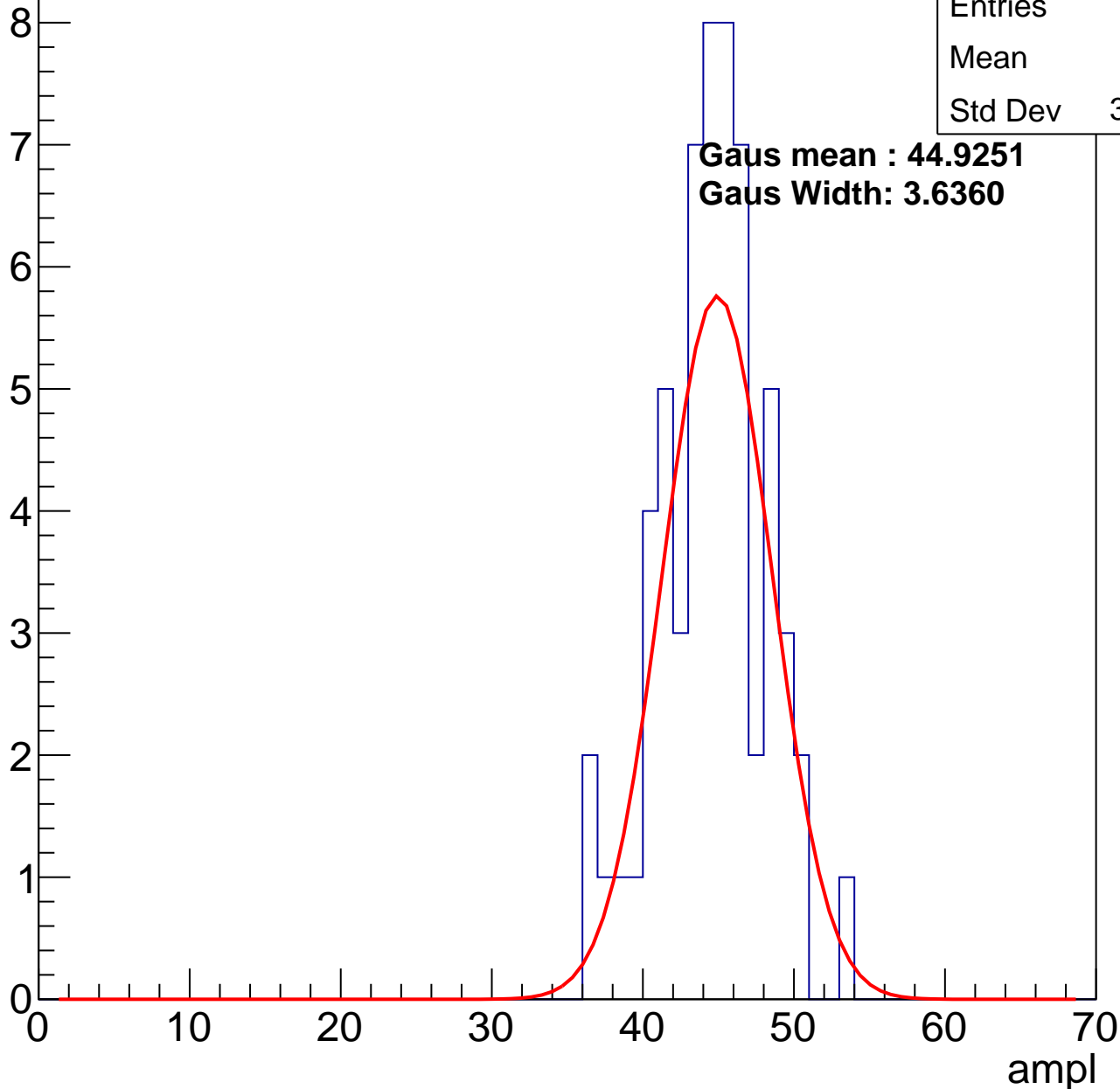
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.1
Std Dev	3.482

Gaus mean : 44.9251

Gaus Width: 3.6360

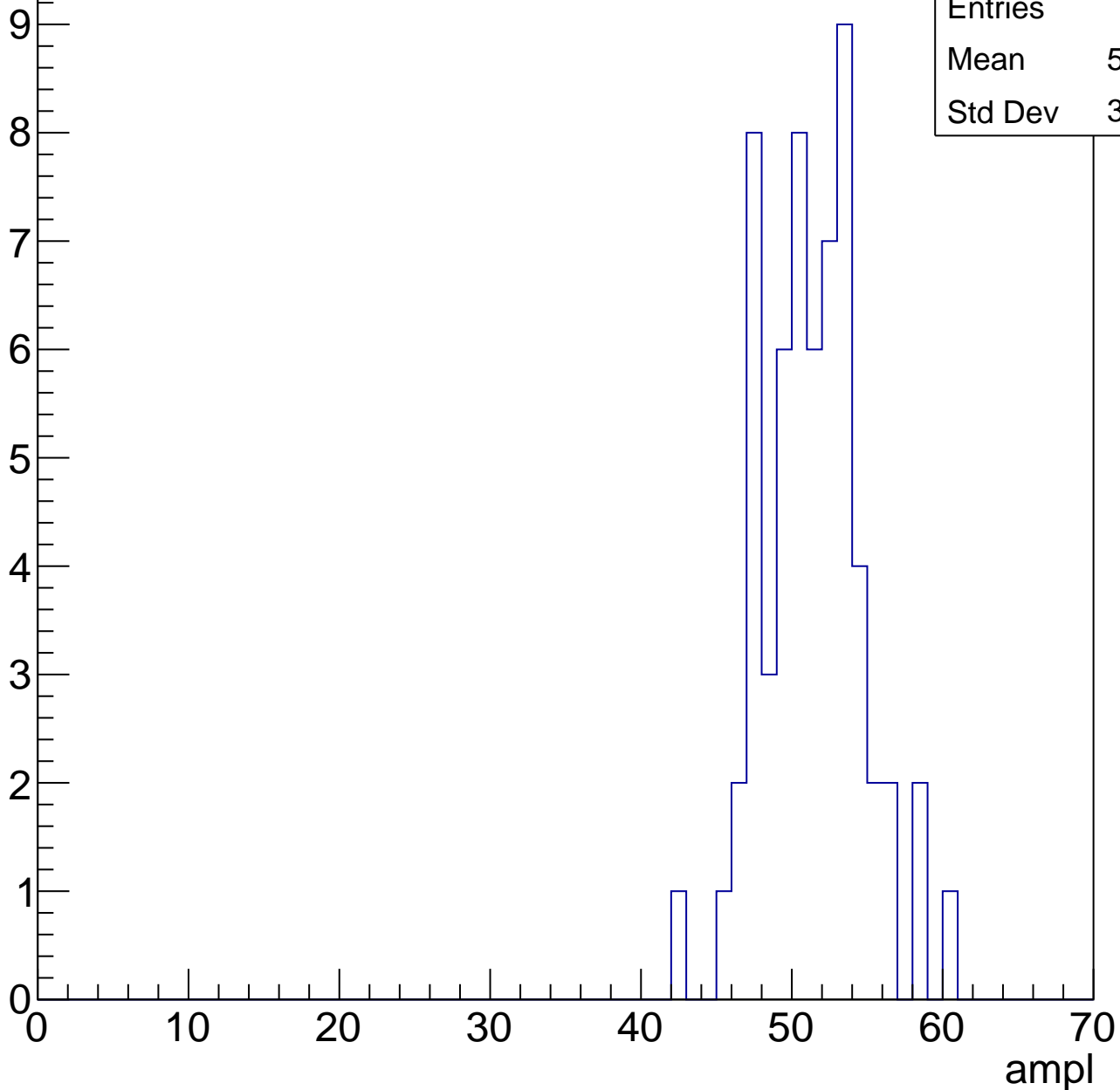


B0L001S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.87
Std Dev	3.343

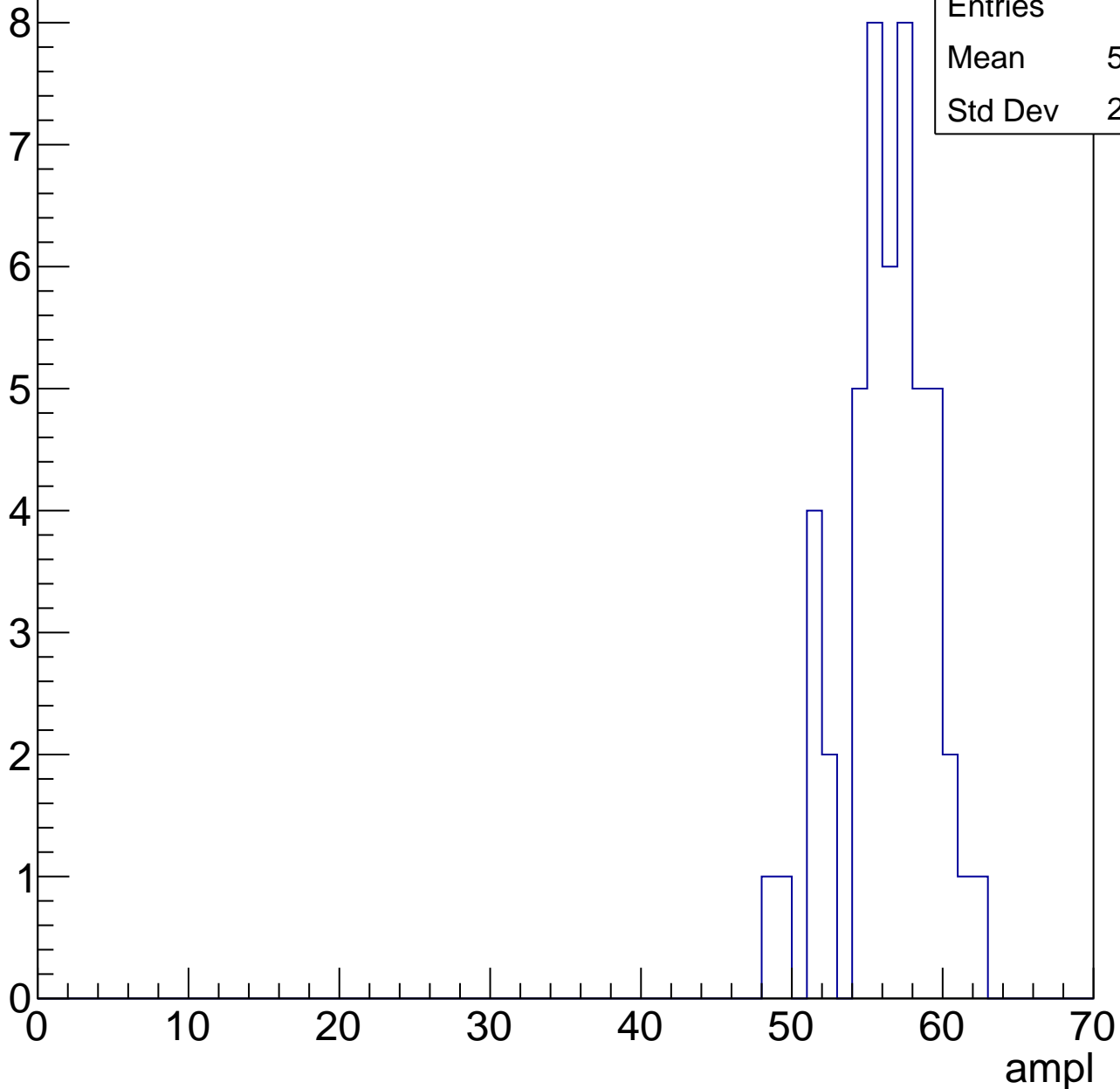


B0L001S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	55.82
Std Dev	2.988

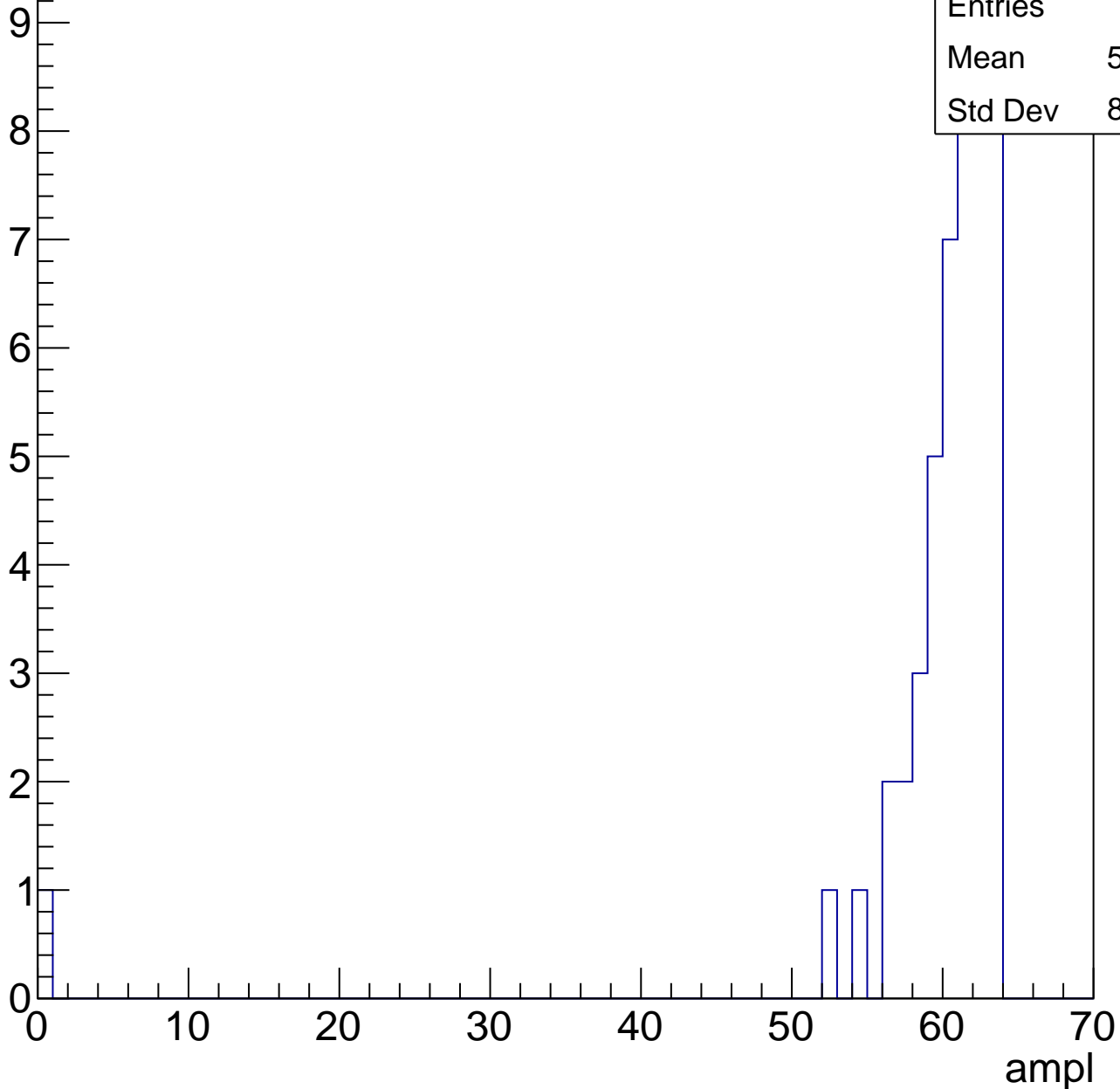


B0L001S, U15-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	59.08
Std Dev	8.866



B0L001S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch12, adc0

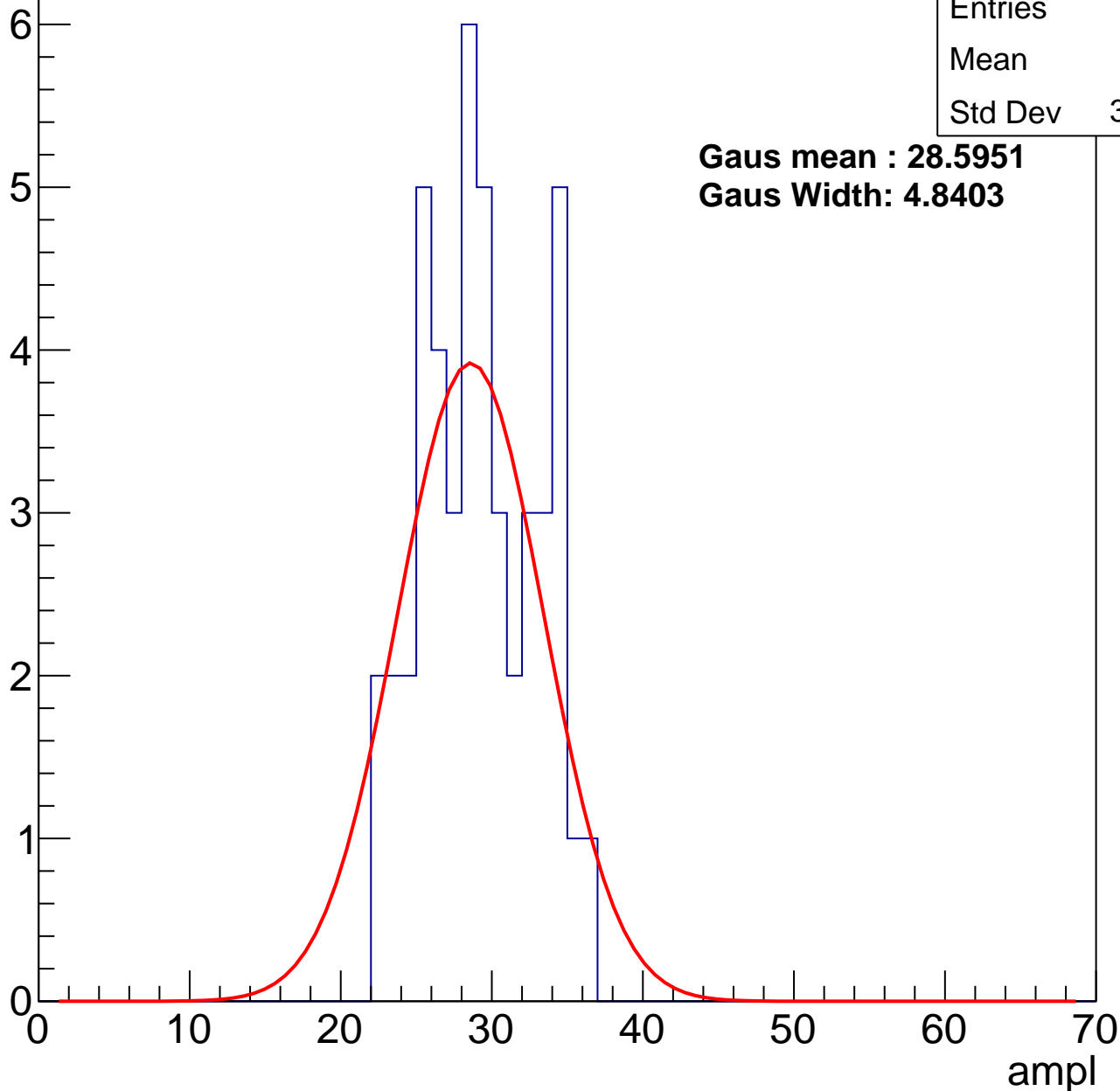
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	28.7
Std Dev	3.707

Gaus mean : 28.5951

Gaus Width: 4.8403



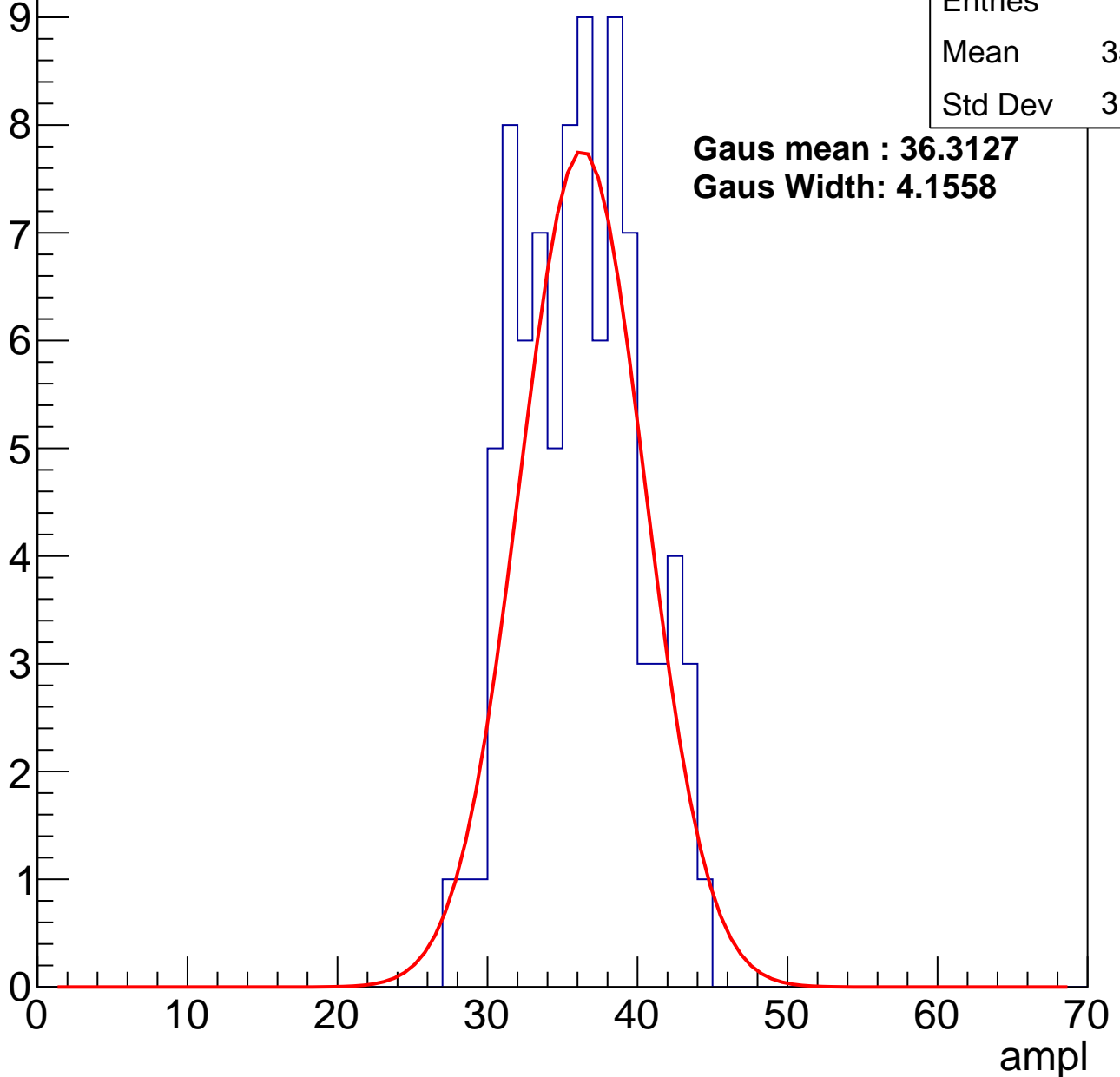
B0L001S, U15-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	35.63
Std Dev	3.913

Gaus mean : 36.3127
Gaus Width: 4.1558



B0L001S, U15-ch12, adc2

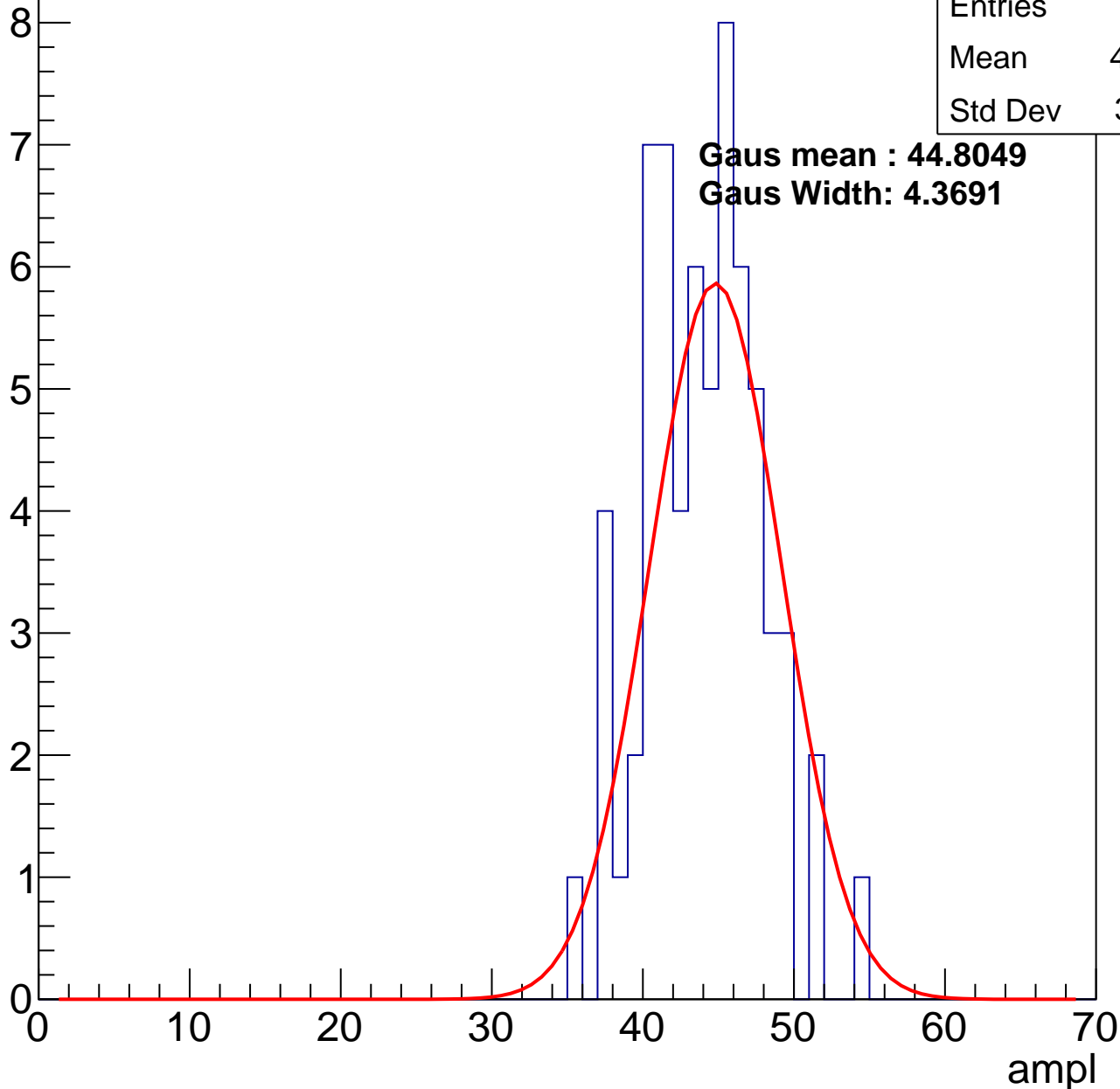
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.54
Std Dev	3.811

Gaus mean : 44.8049

Gaus Width: 4.3691

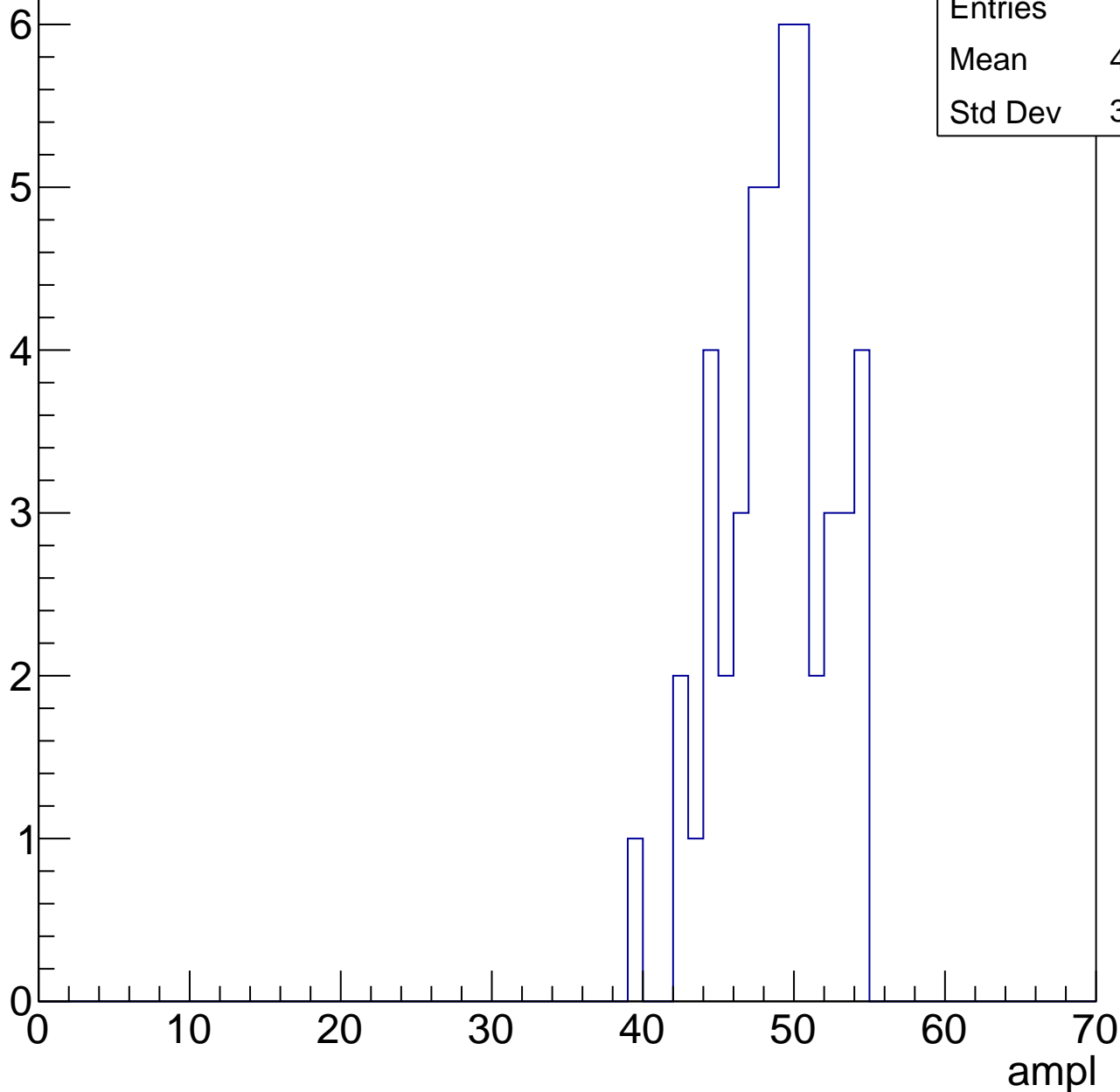


B0L001S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	48.34
Std Dev	3.533

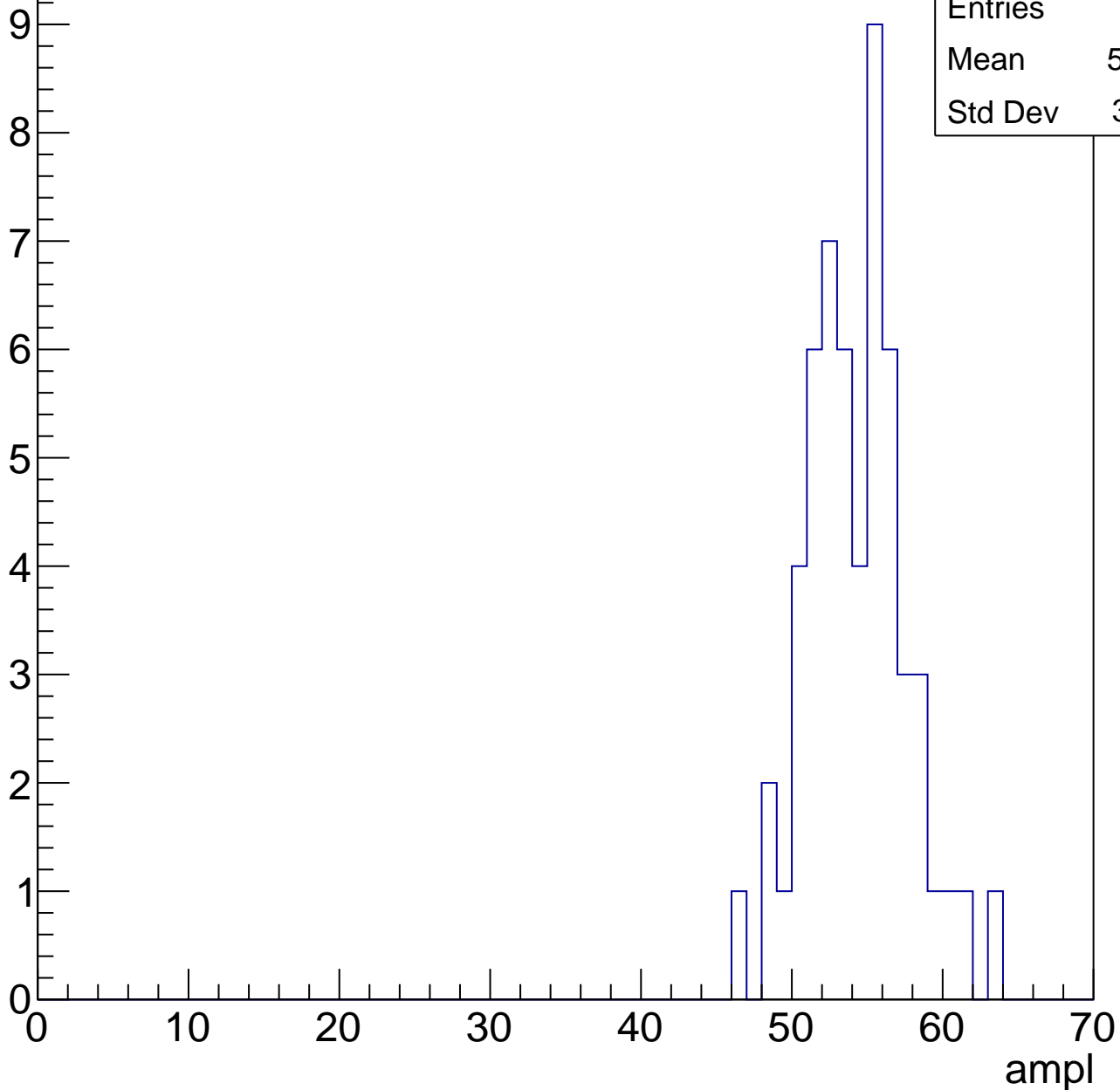


B0L001S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	53.82
Std Dev	3.301



B0L001S, U15-ch12, adc5

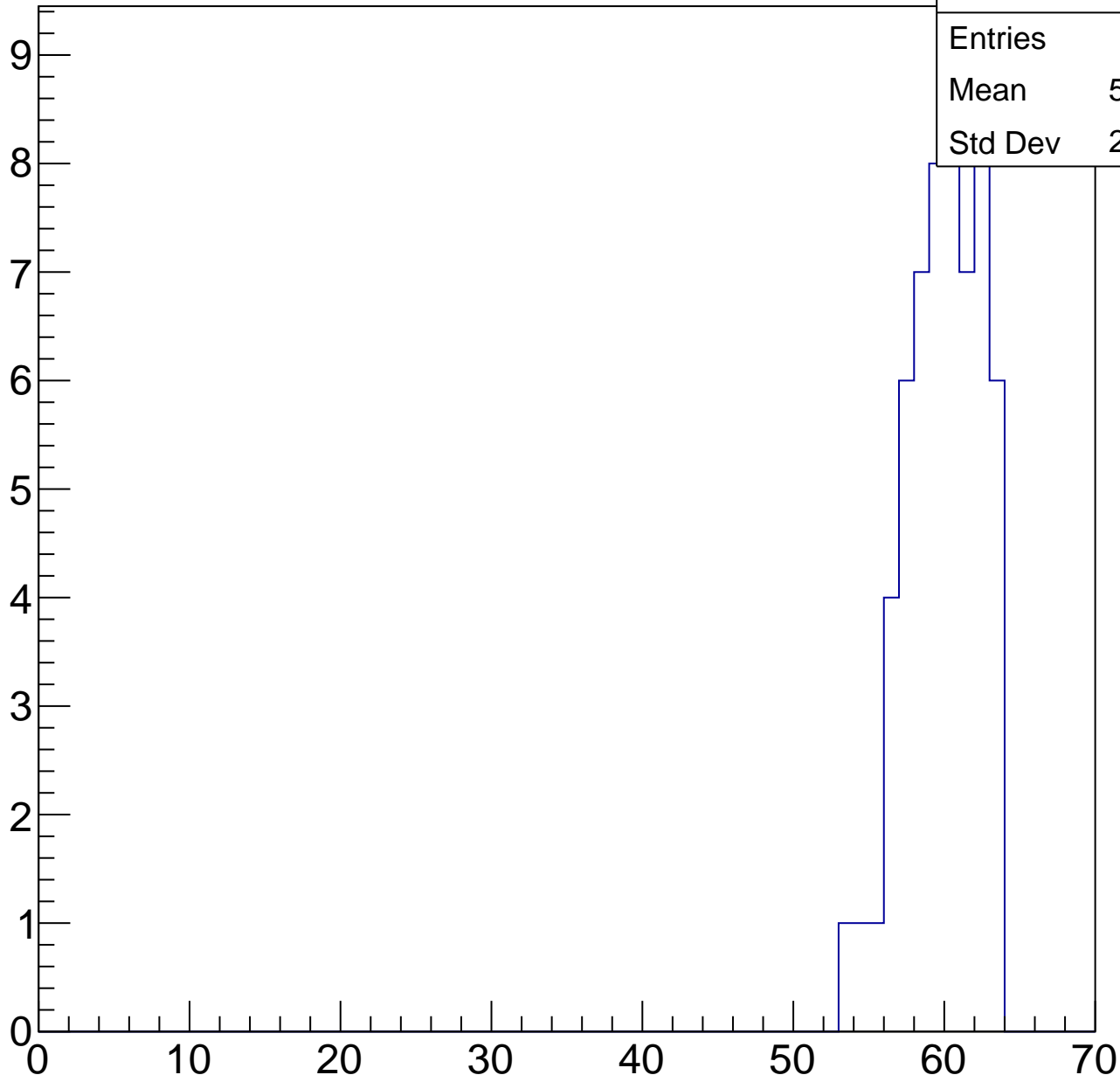
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	59.43
Std Dev	2.415

ampl



B0L001S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

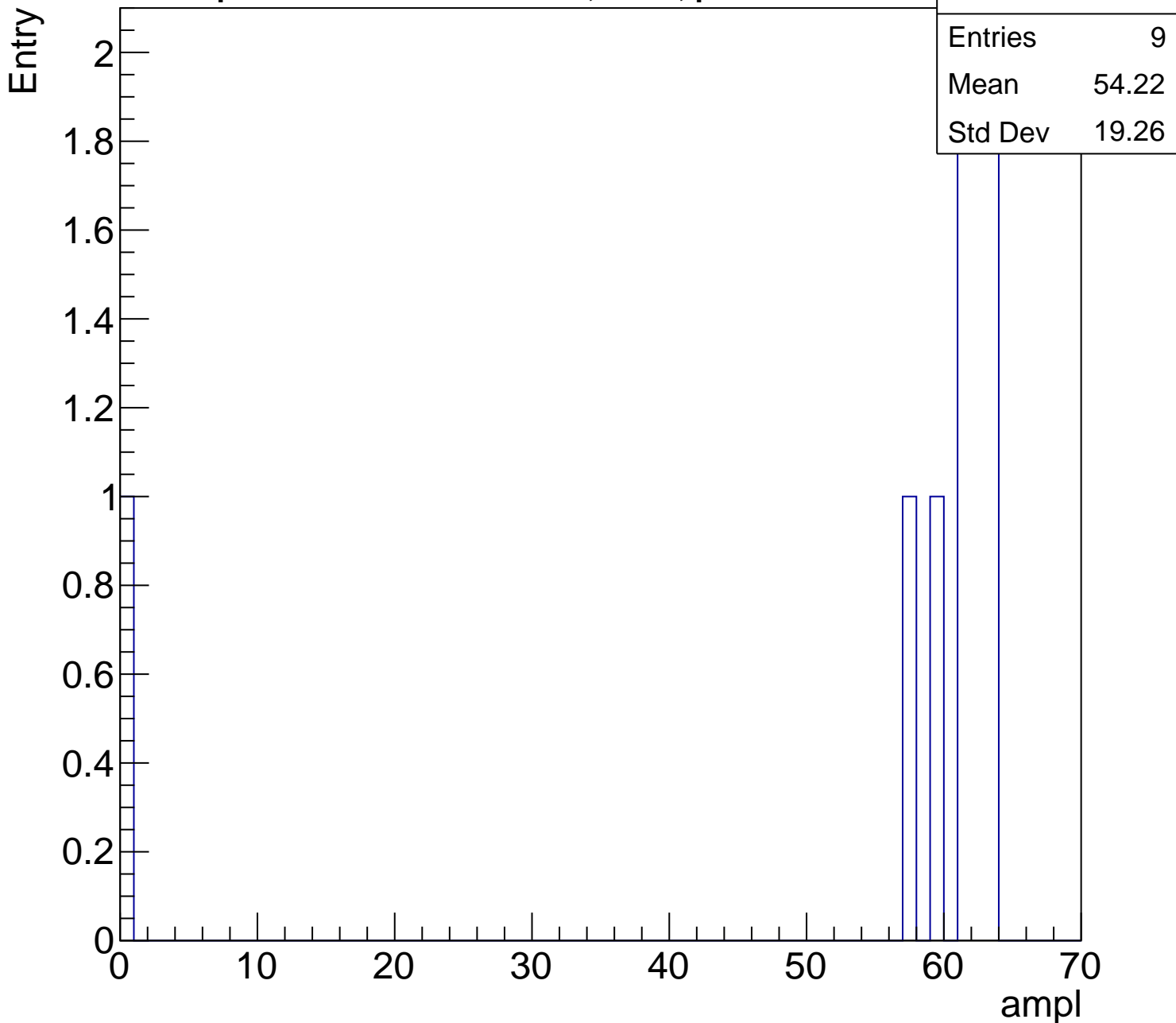
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.22
Std Dev	19.26

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U15-ch13, adc0

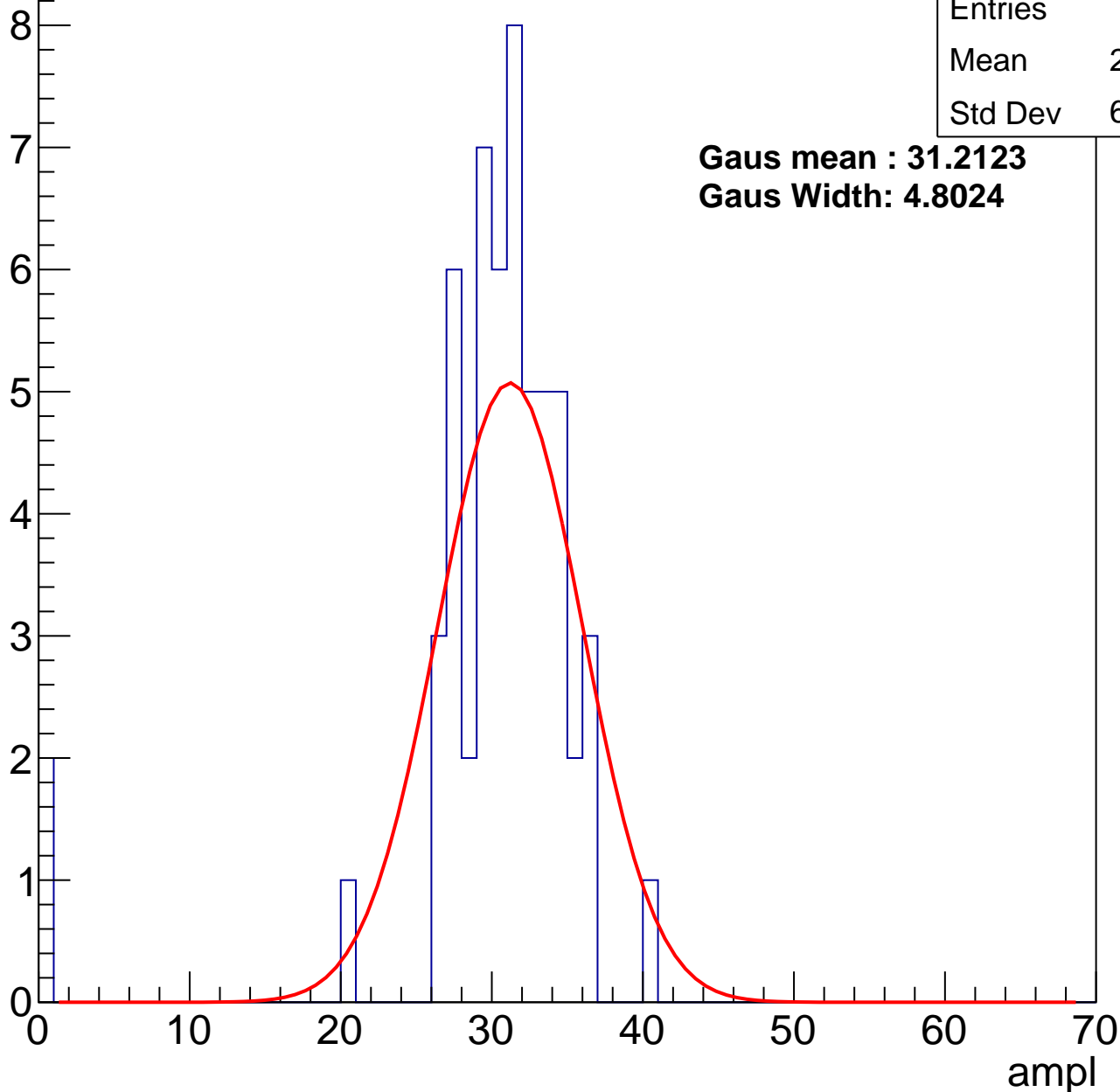
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.64
Std Dev	6.575

Gaus mean : 31.2123

Gaus Width: 4.8024



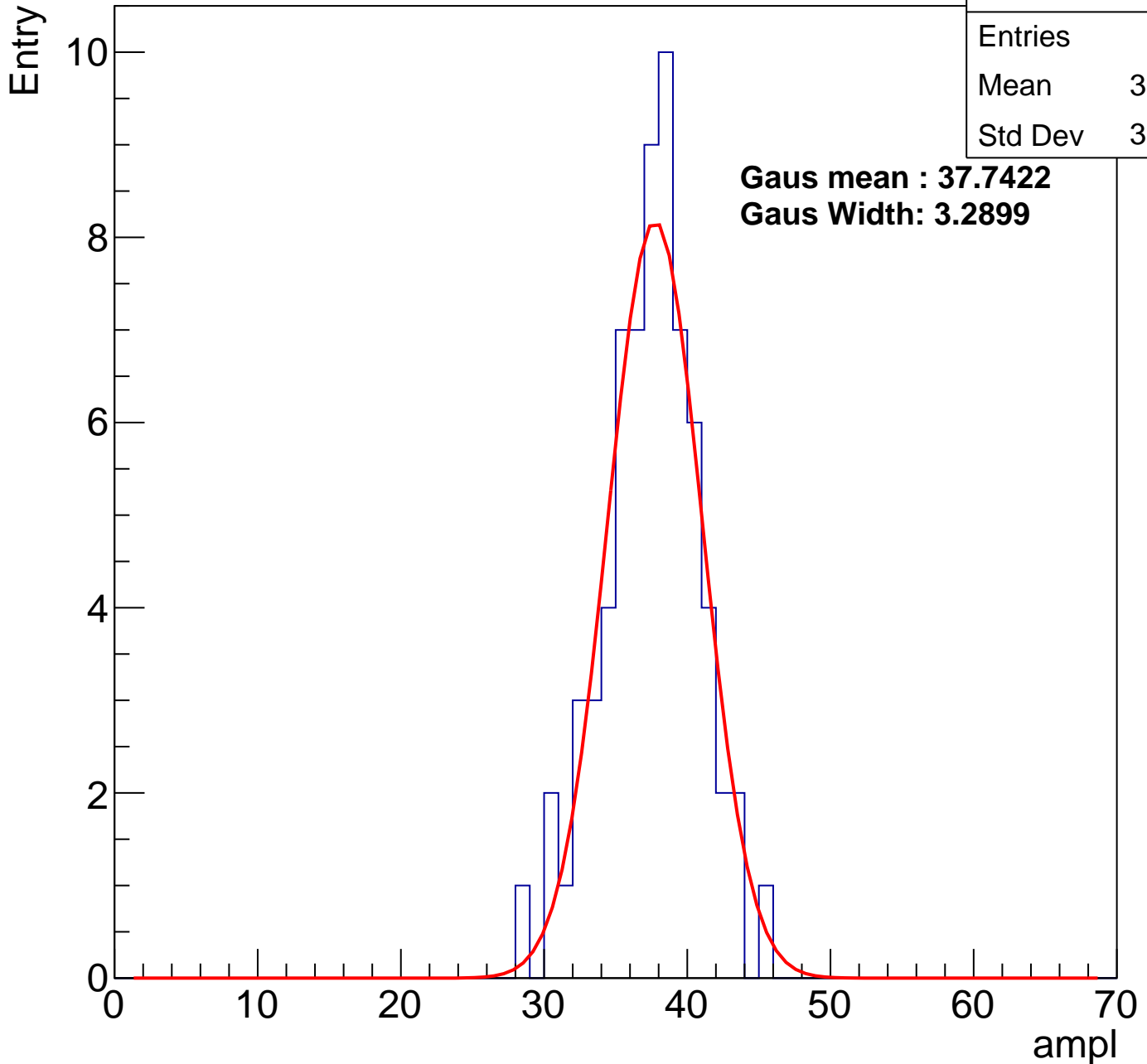
B0L001S, U15-ch13, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	36.99
Std Dev	3.299

Gaus mean : 37.7422

Gaus Width: 3.2899



B0L001S, U15-ch13, adc2

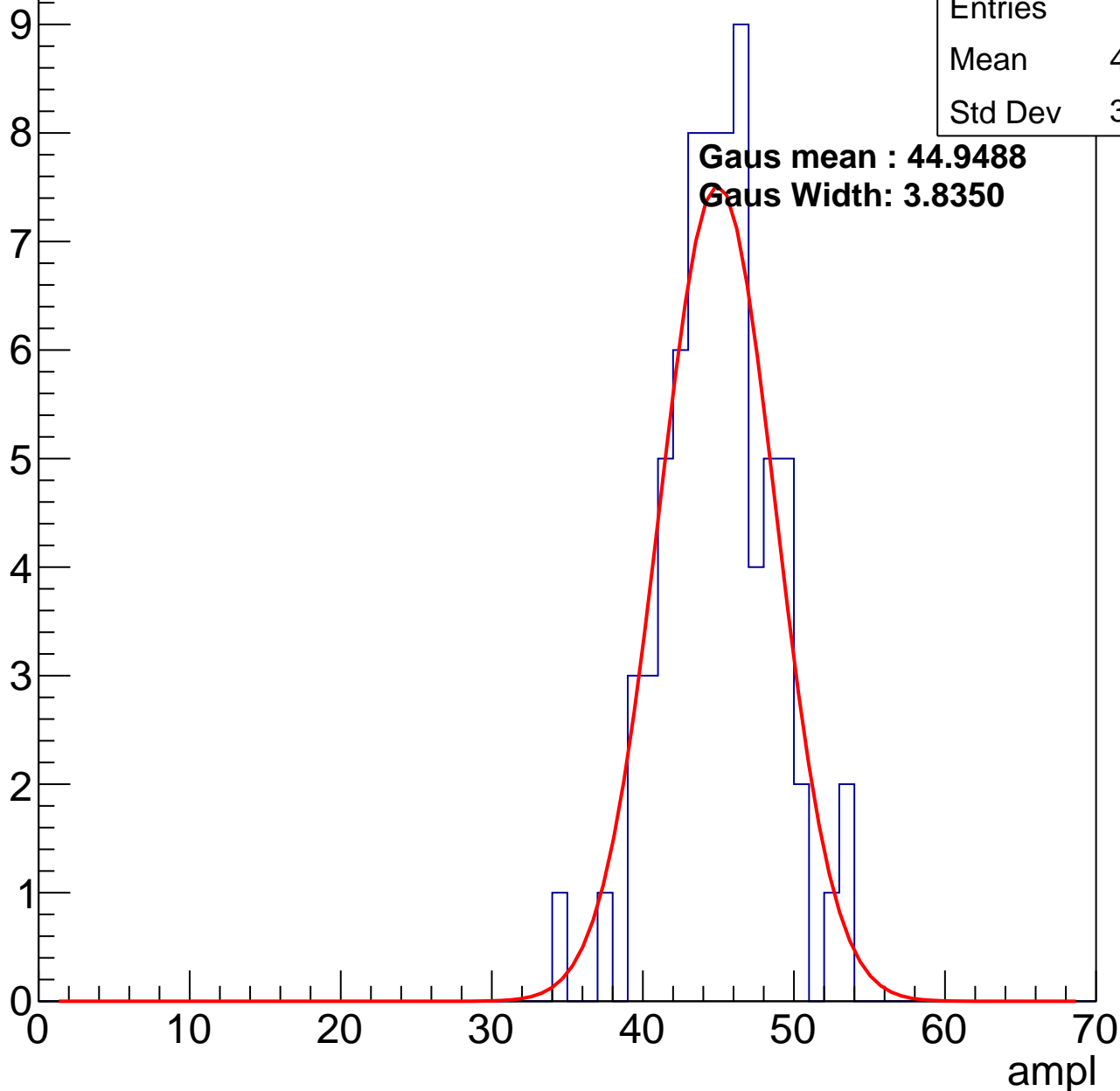
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.59
Std Dev	3.574

Gaus mean : 44.9488

Gaus Width: 3.8350

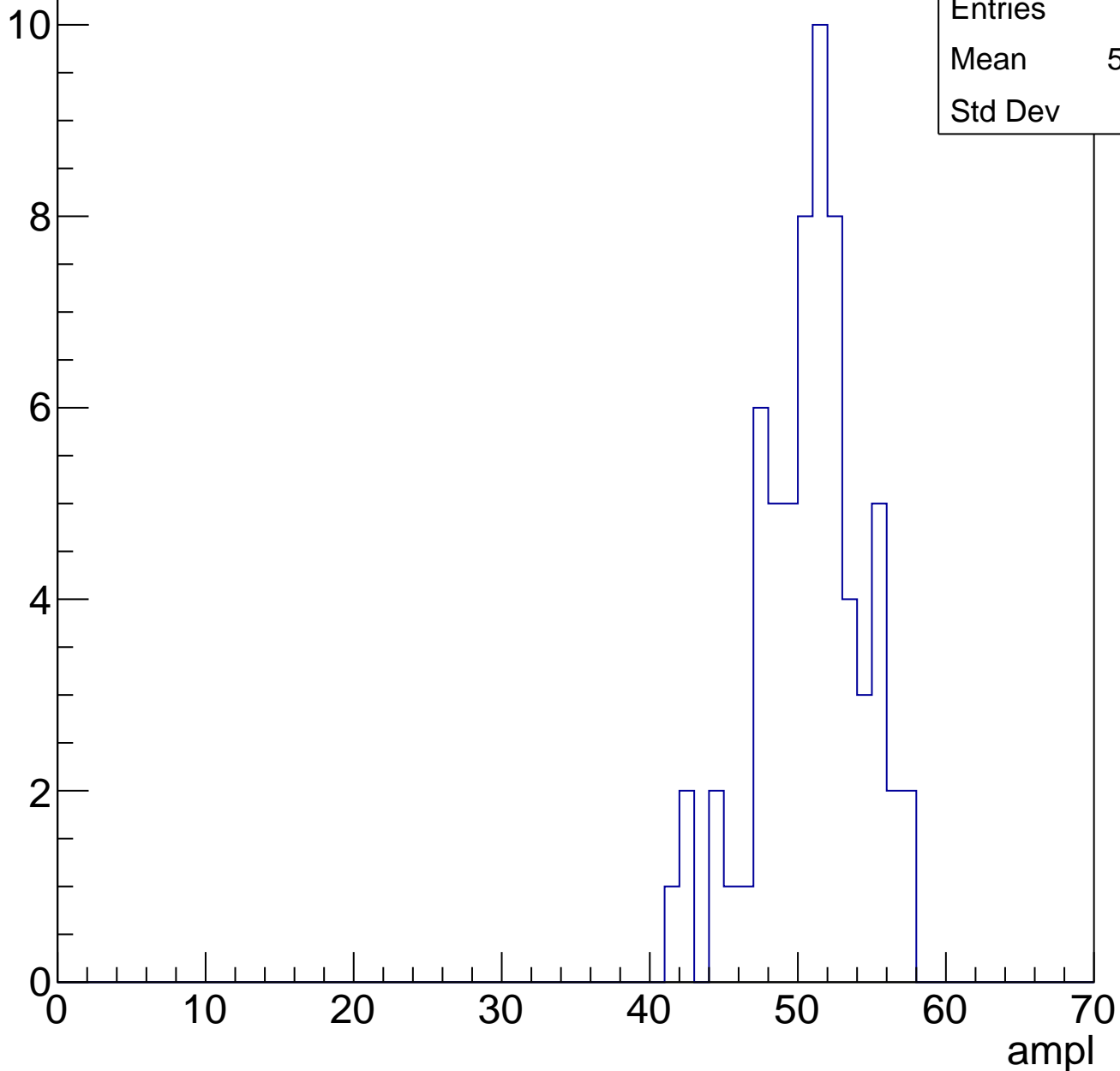


B0L001S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	50.34
Std Dev	3.54

Entry

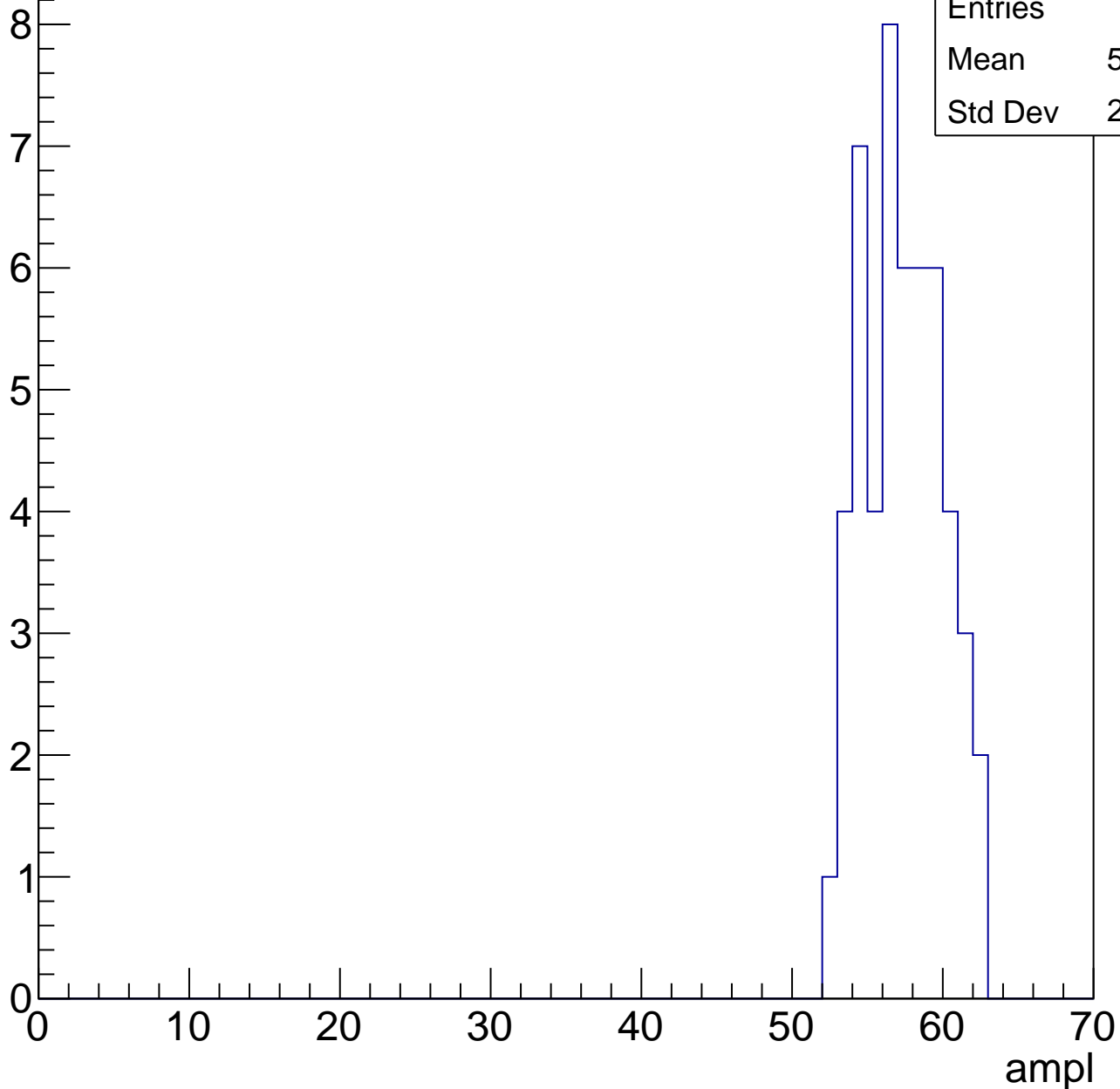


B0L001S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.88
Std Dev	2.579

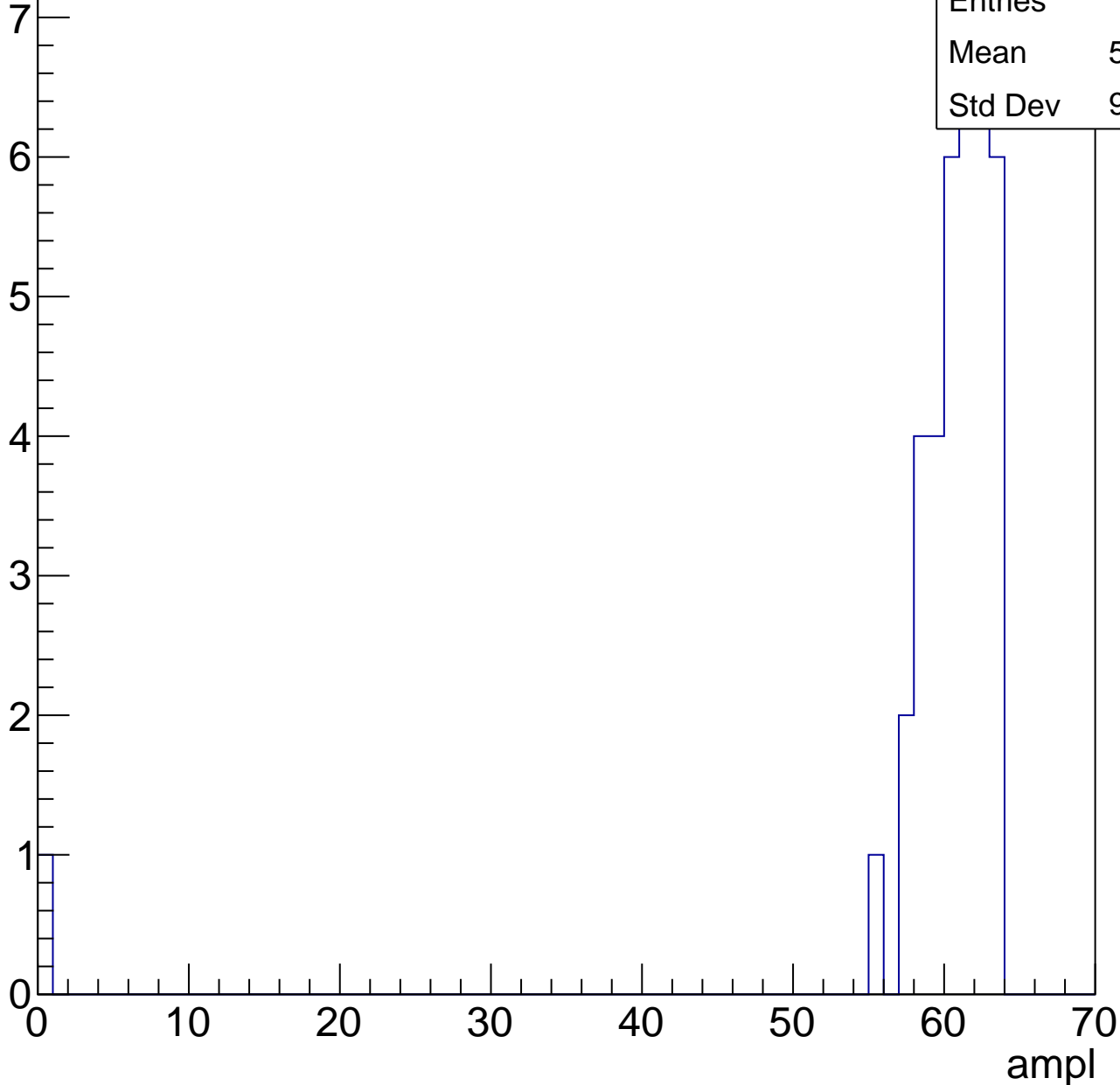


B0L001S, U15-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

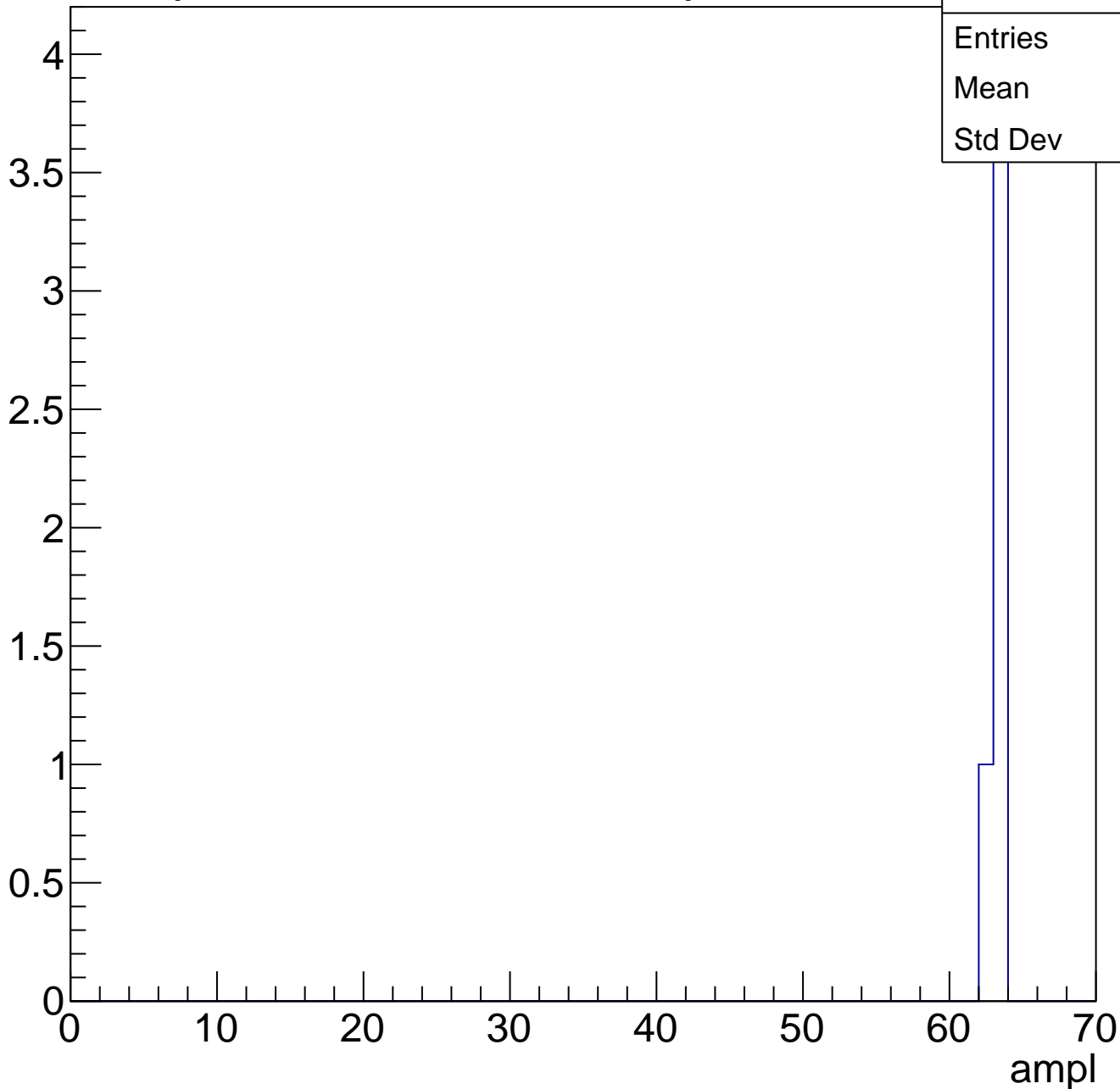
Entries	38
Mean	58.84
Std Dev	9.869



B0L001S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch14, adc0

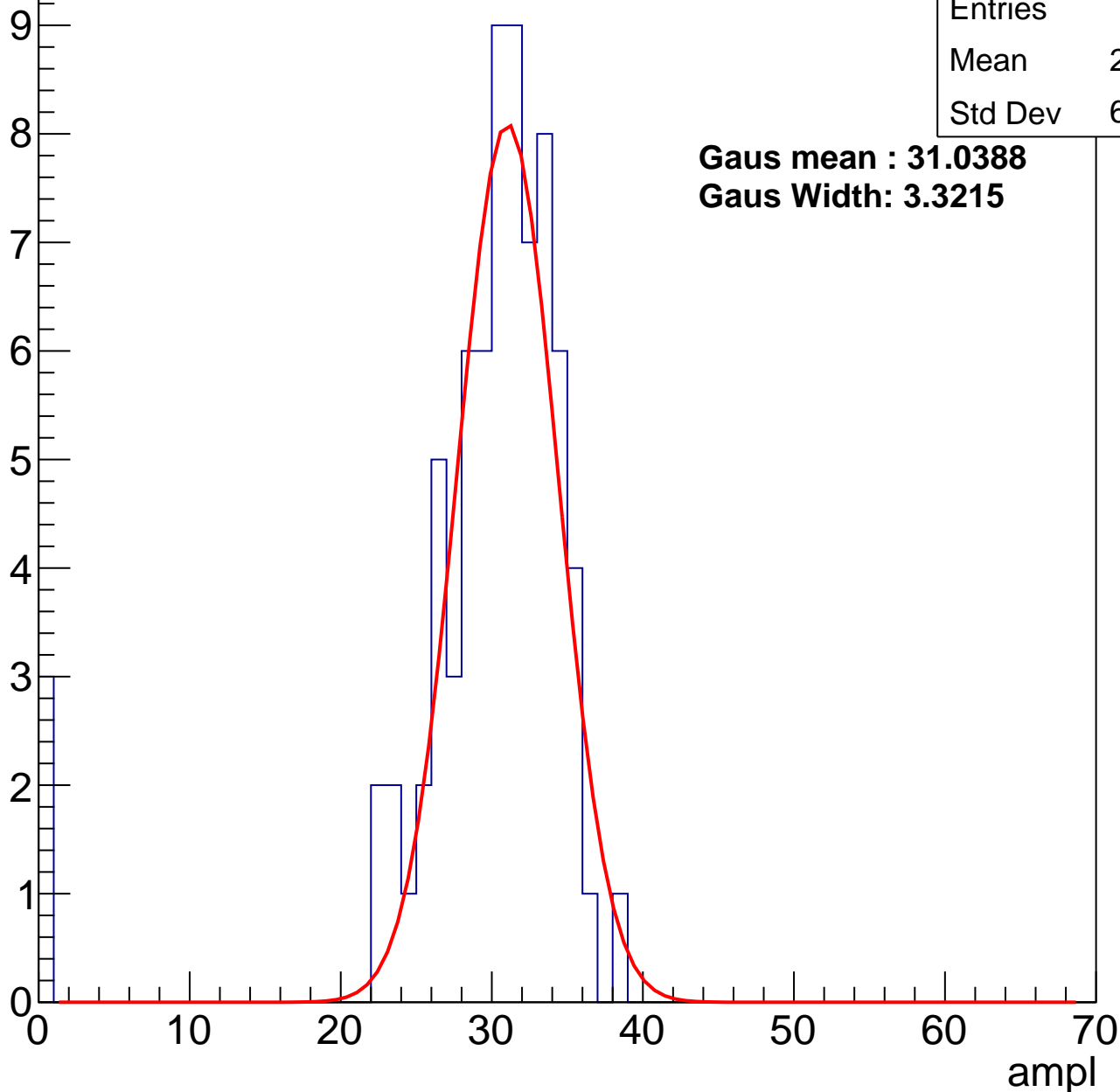
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	28.96
Std Dev	6.805

Gaus mean : 31.0388

Gaus Width: 3.3215



B0L001S, U15-ch14, adc1

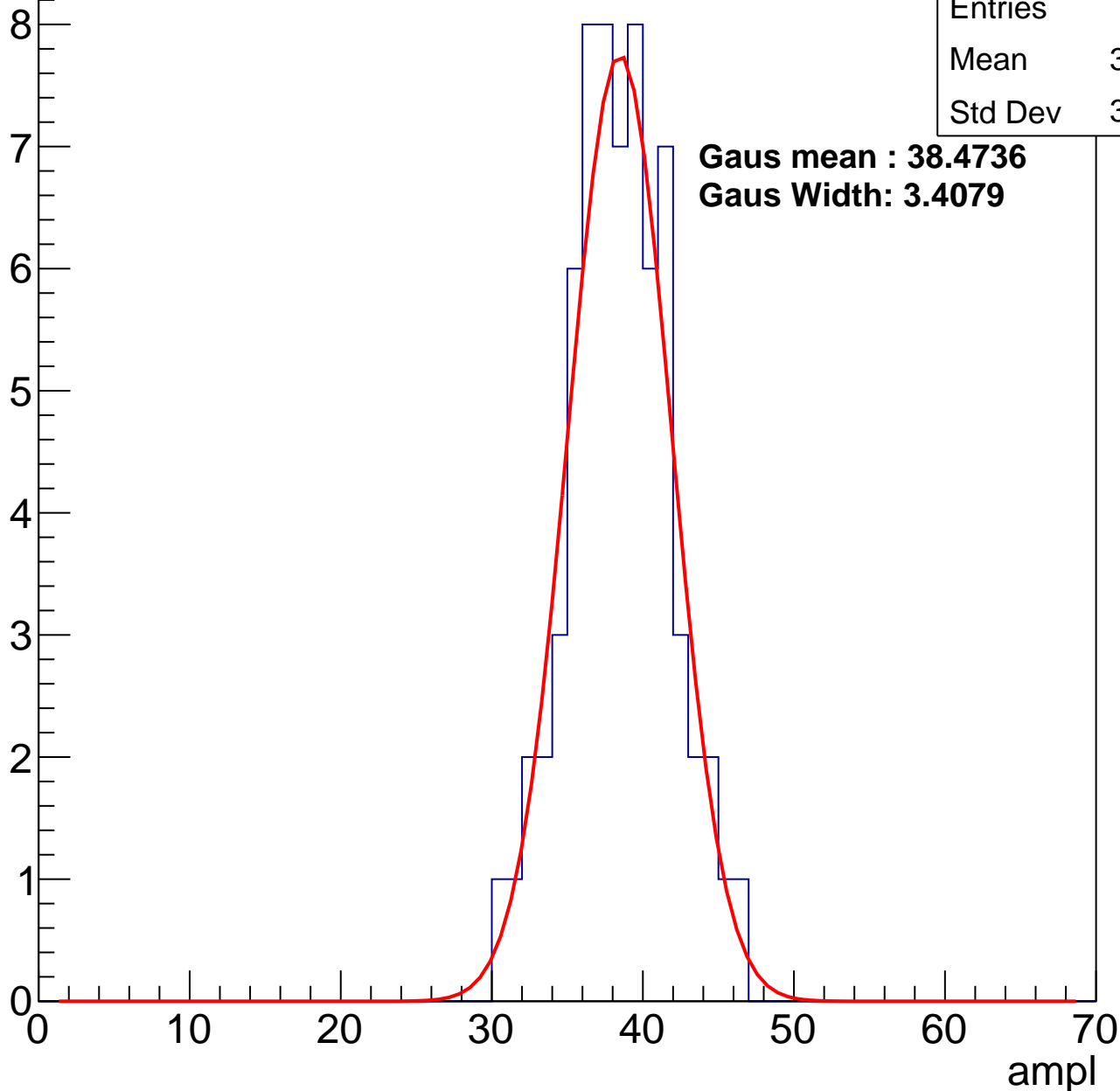
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.99
Std Dev	3.332

Gaus mean : 38.4736

Gaus Width: 3.4079



B0L001S, U15-ch14, adc2

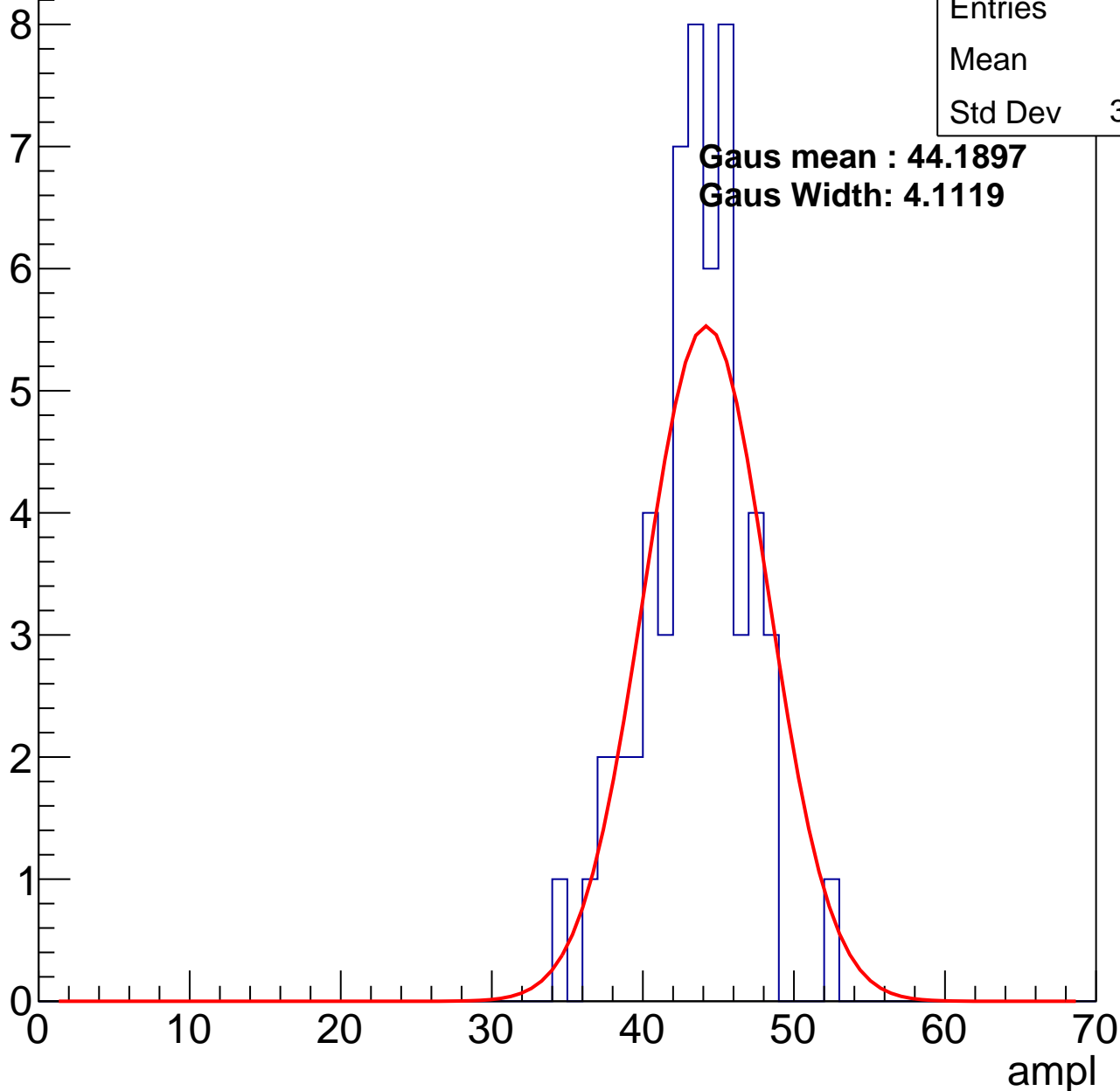
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43
Std Dev	3.368

Gaus mean : 44.1897

Gaus Width: 4.1119



B0L001S, U15-ch14, adc3

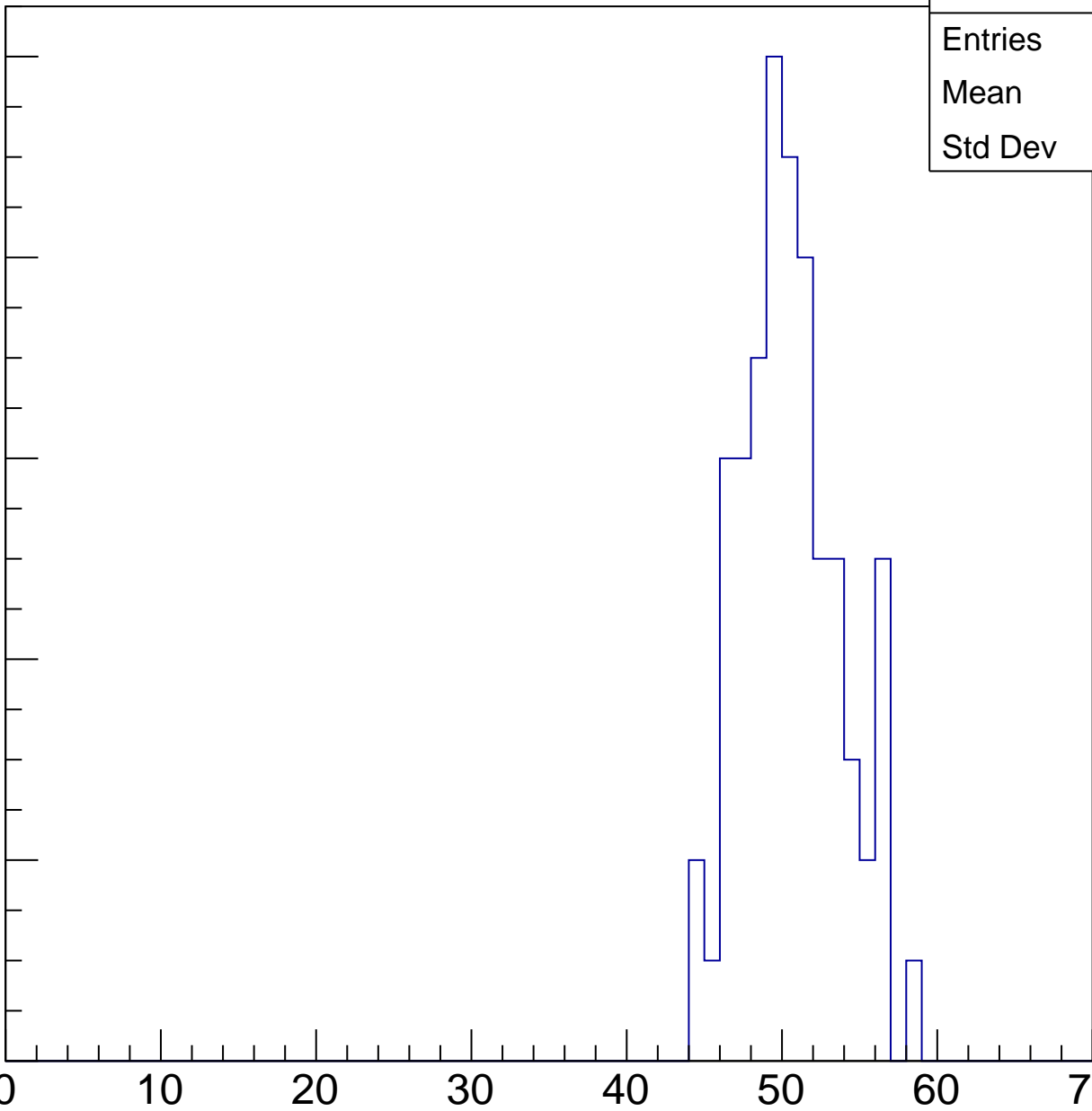
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	50.14
Std Dev	3.159

Entry

10
8
6
4
2
0

ampl

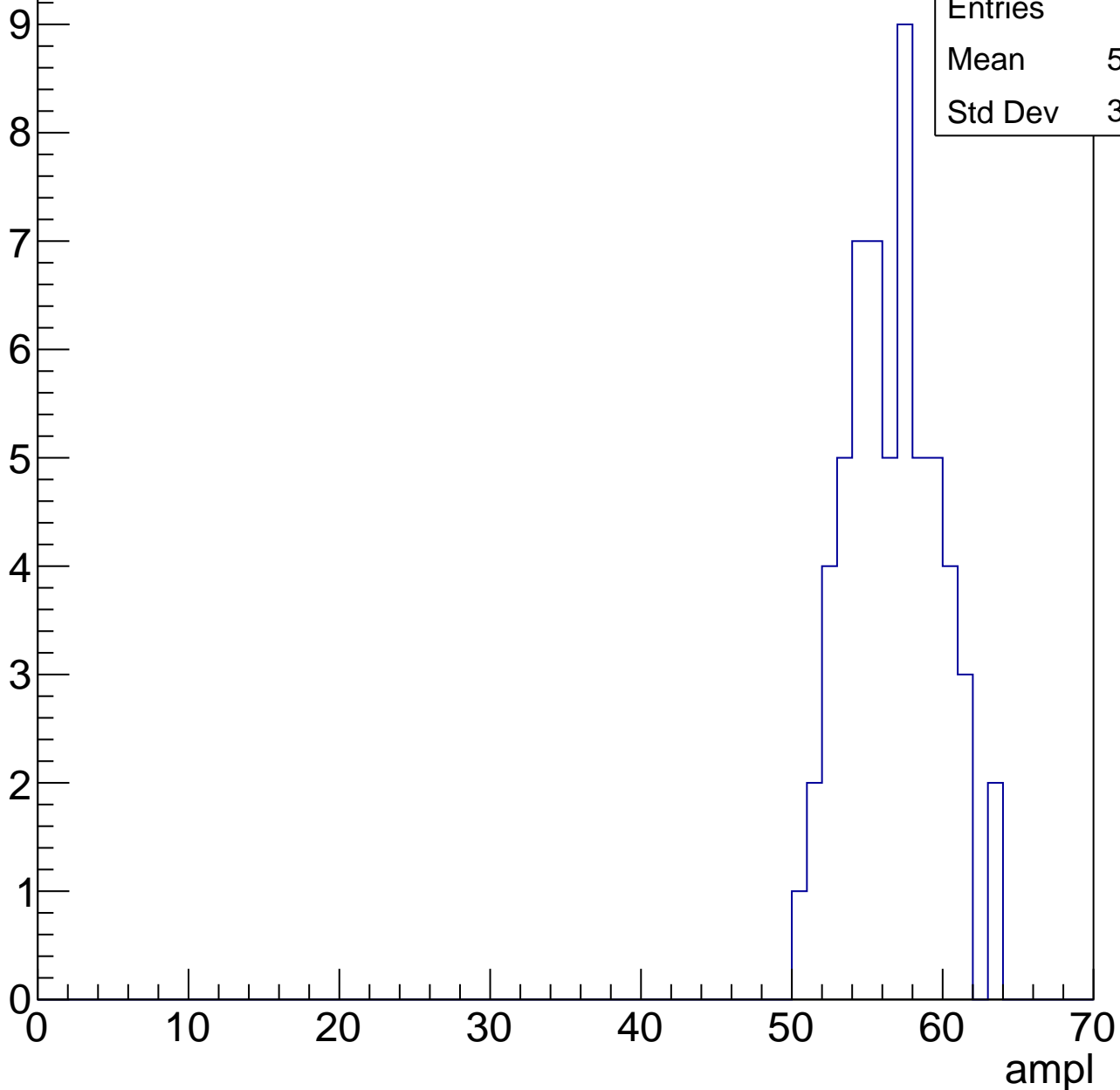


B0L001S, U15-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.19
Std Dev	3.022

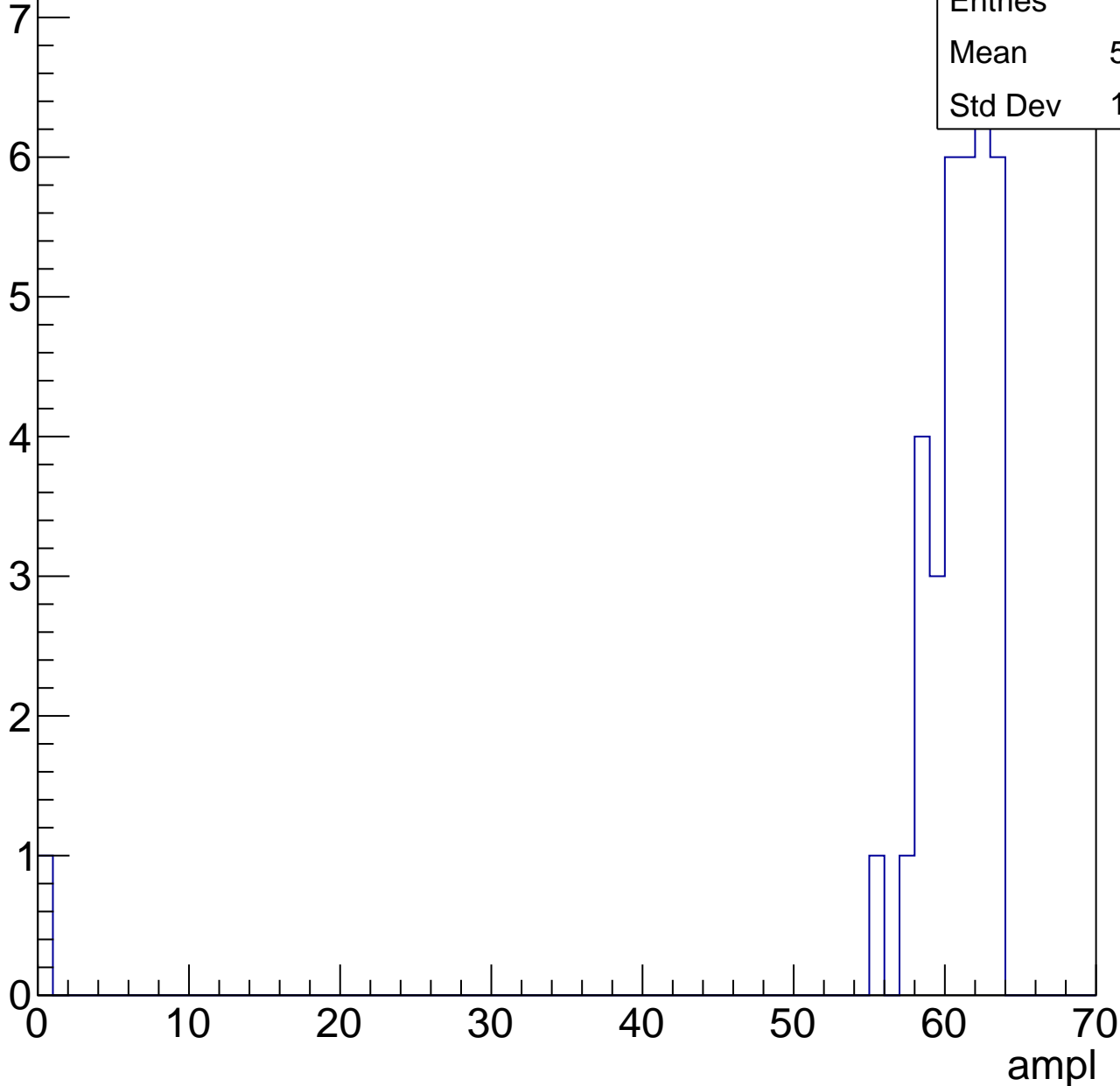


B0L001S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

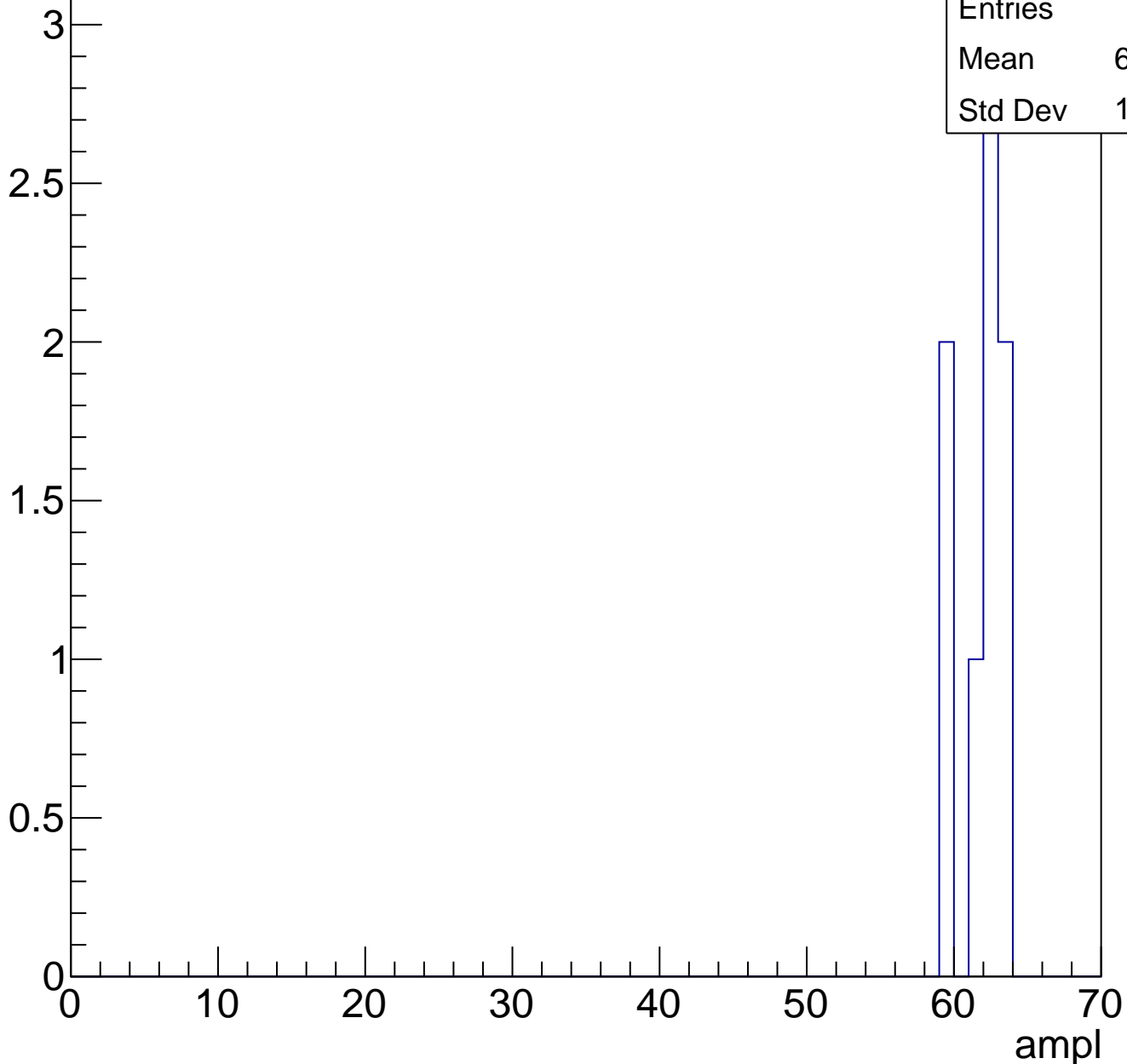
Entries	35
Mean	58.83
Std Dev	10.27



B0L001S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U15-ch15, adc0

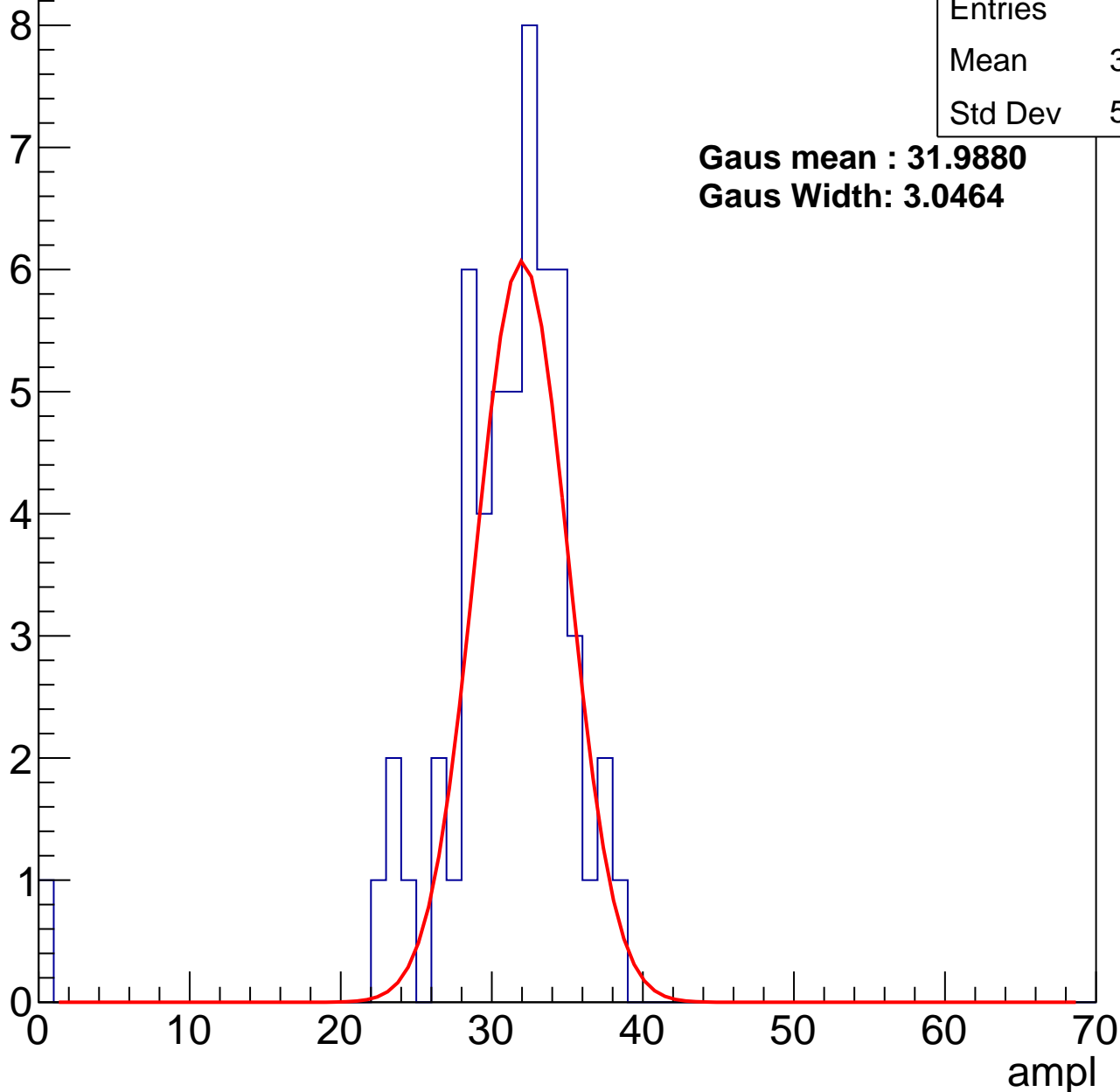
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	30.38
Std Dev	5.425

Gaus mean : 31.9880

Gaus Width: 3.0464



B0L001S, U15-ch15, adc1

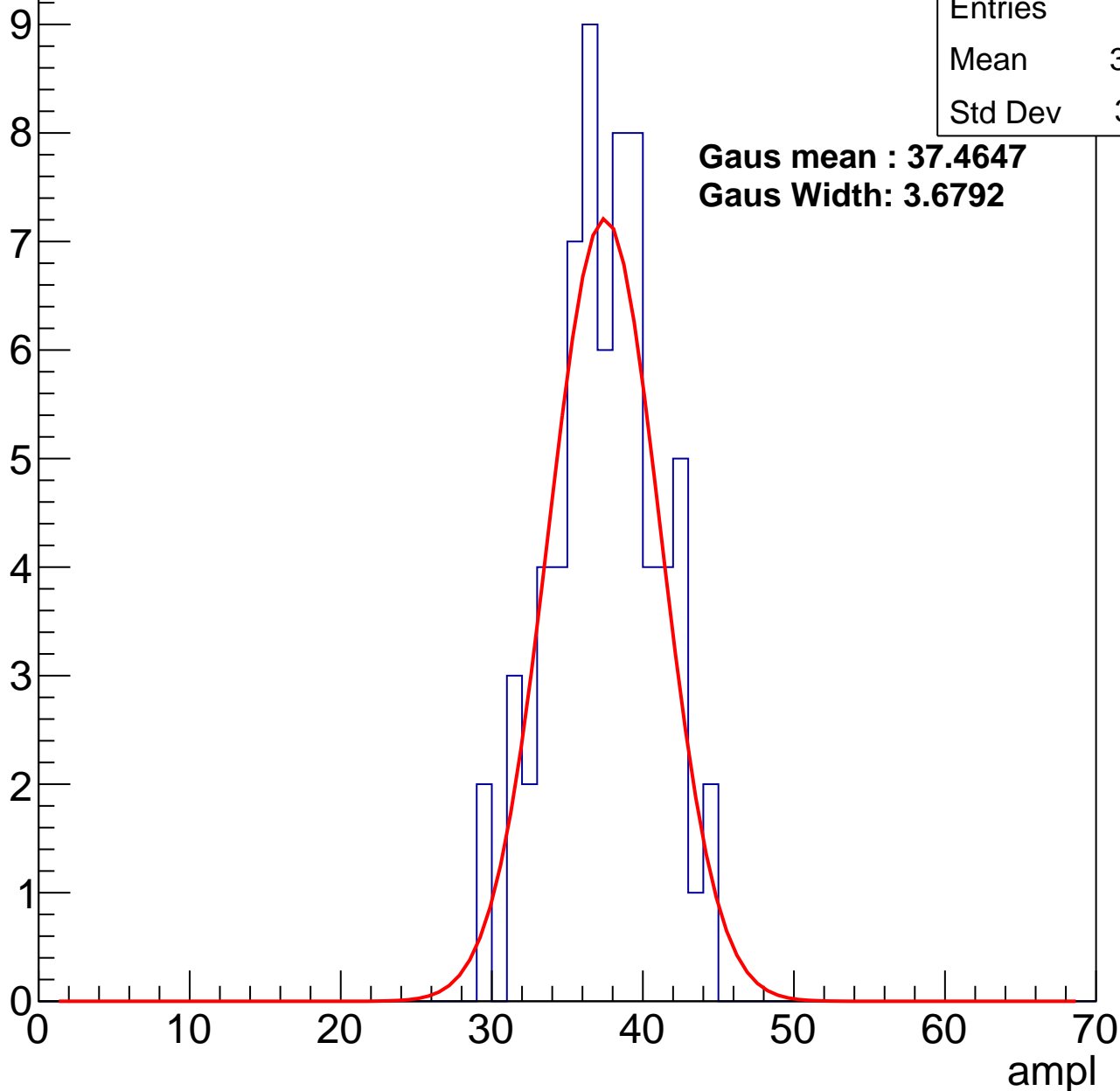
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.03
Std Dev	3.451

Gaus mean : 37.4647

Gaus Width: 3.6792



B0L001S, U15-ch15, adc2

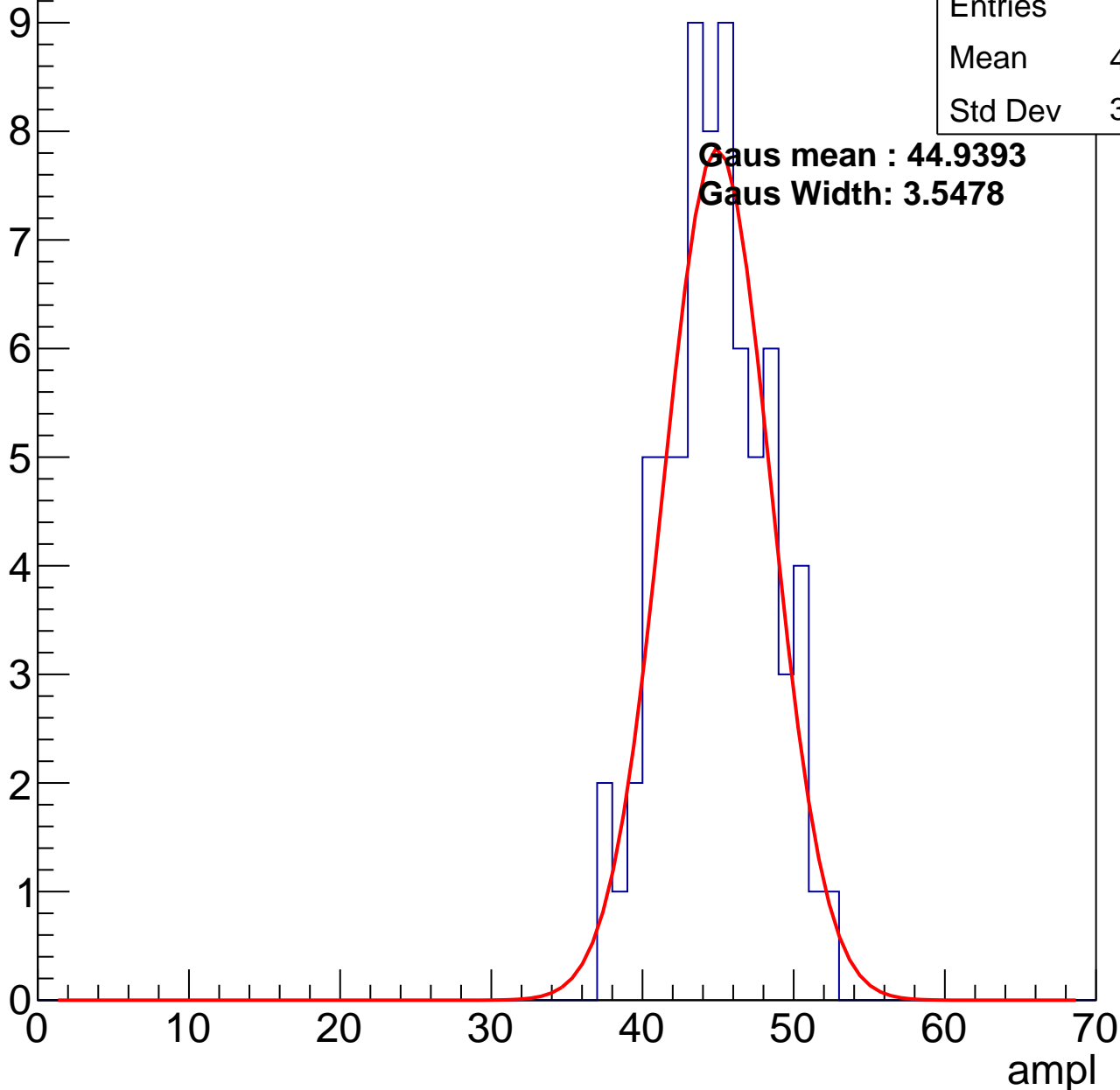
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	44.42
Std Dev	3.398

Gaus mean : 44.9393

Gaus Width: 3.5478

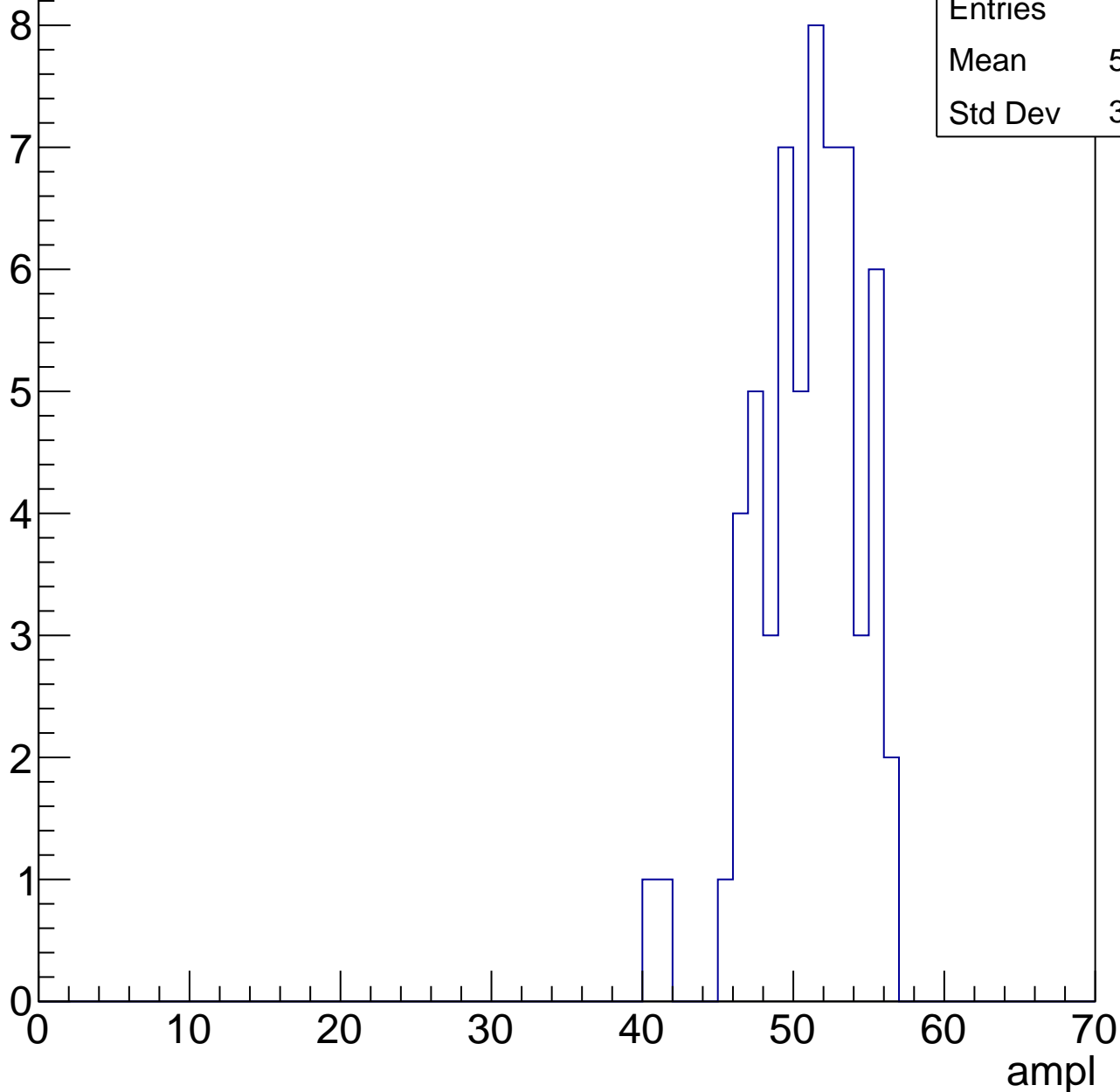


B0L001S, U15-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.48
Std Dev	3.389

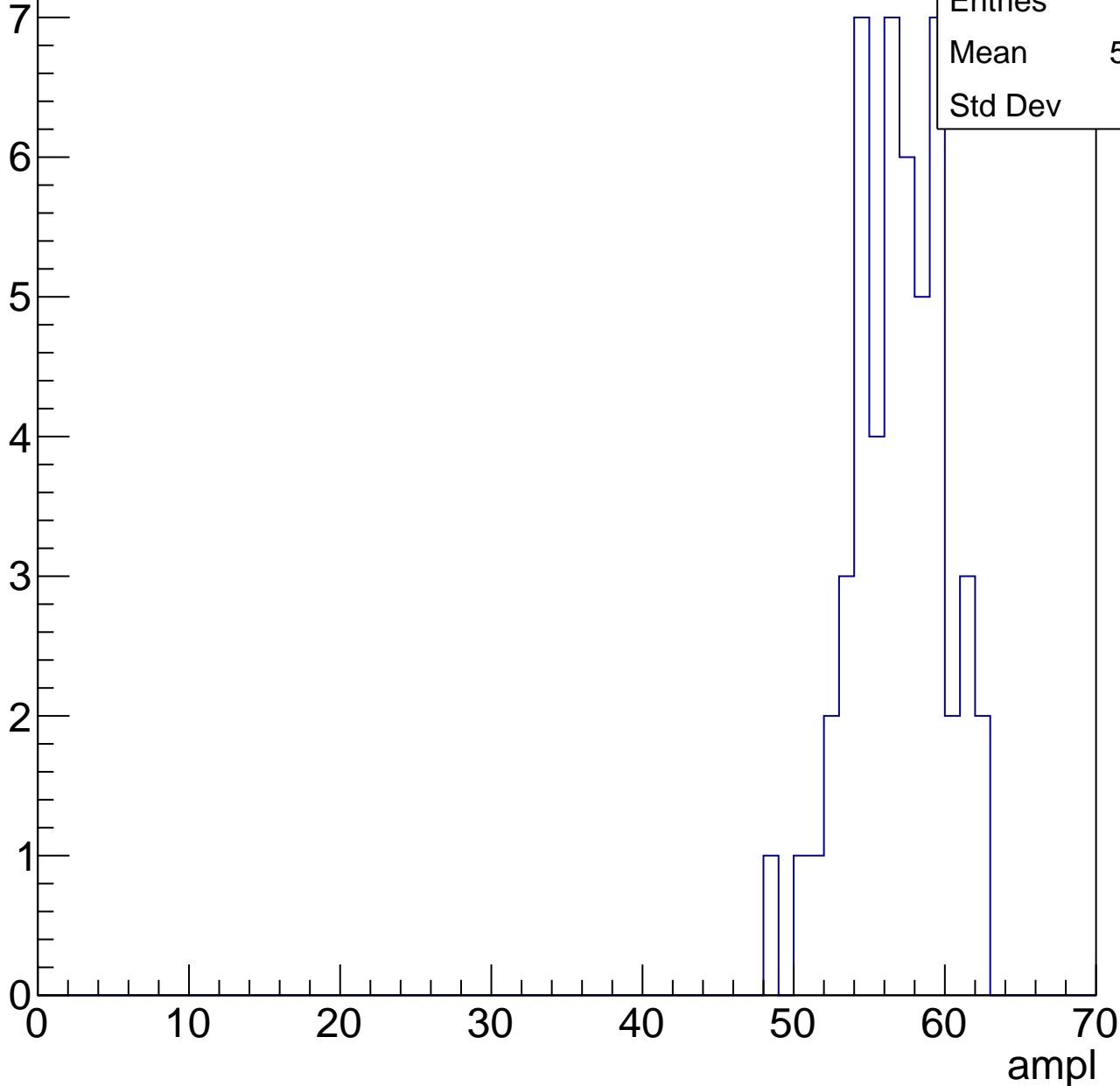


B0L001S, U15-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

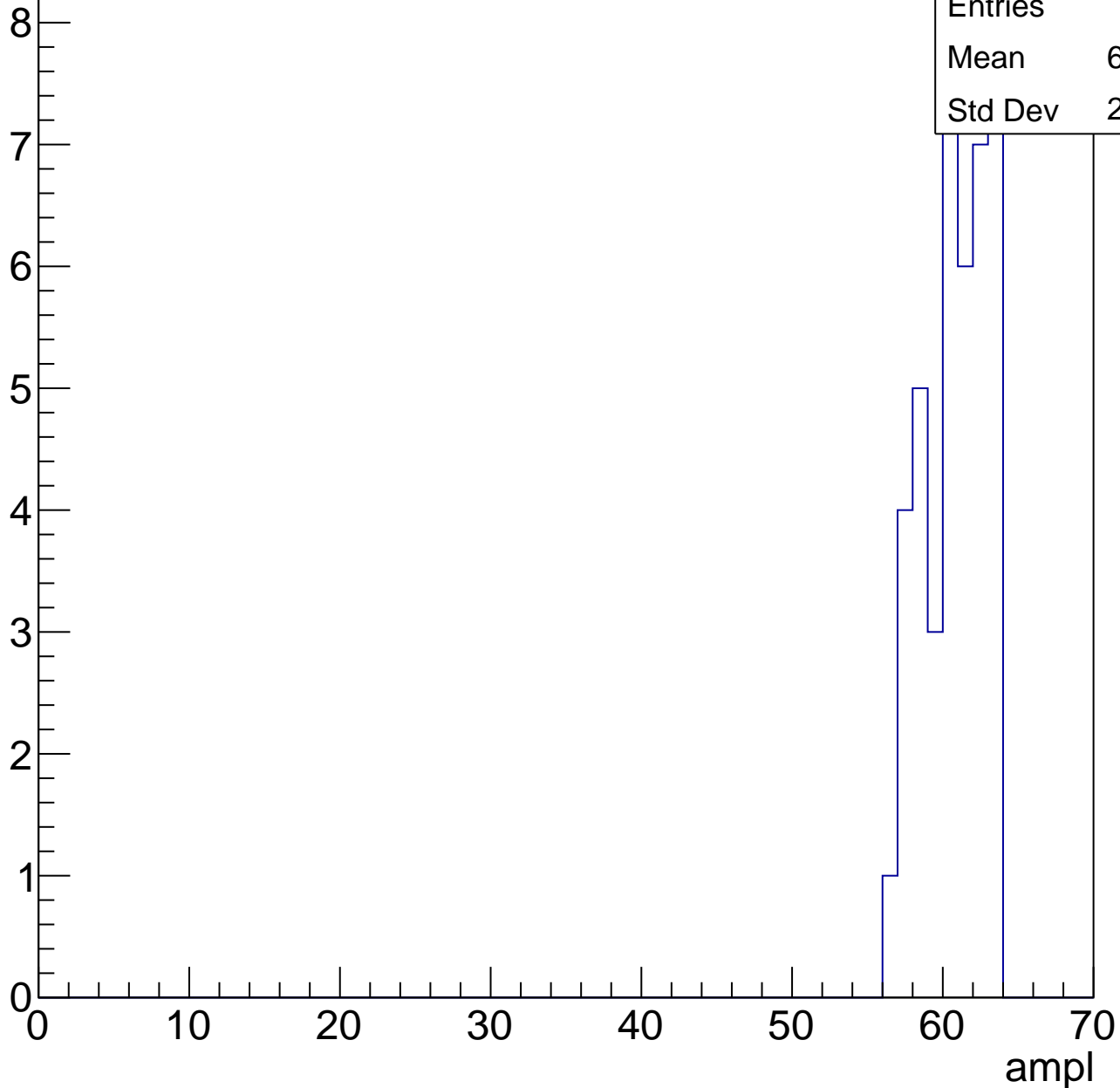
Entries	51
Mean	56.35
Std Dev	3.06



B0L001S, U15-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	42
Mean	60.36
Std Dev	2.045

B0L001S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch16, adc0

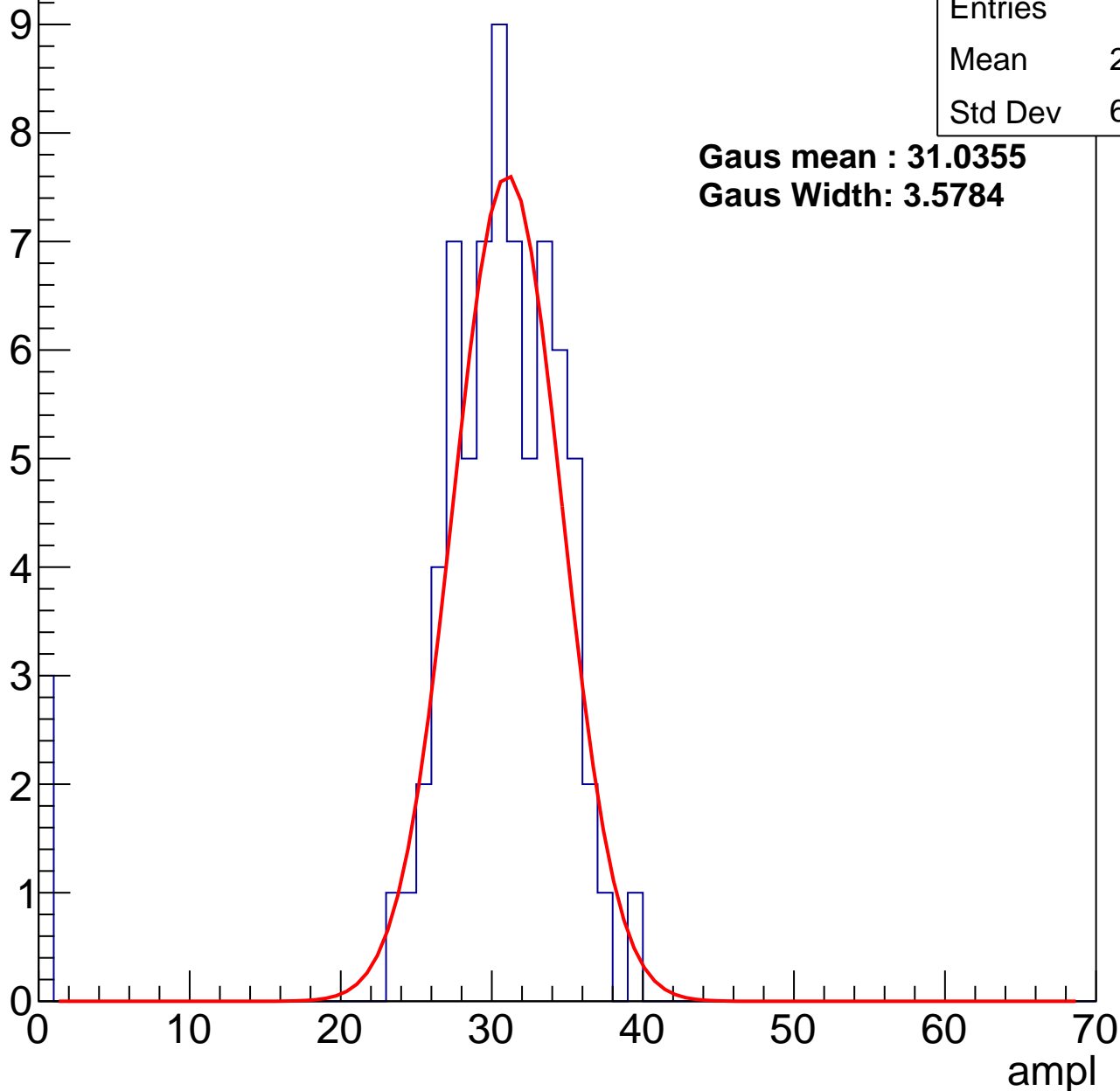
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.29
Std Dev	6.889

Gaus mean : 31.0355

Gaus Width: 3.5784

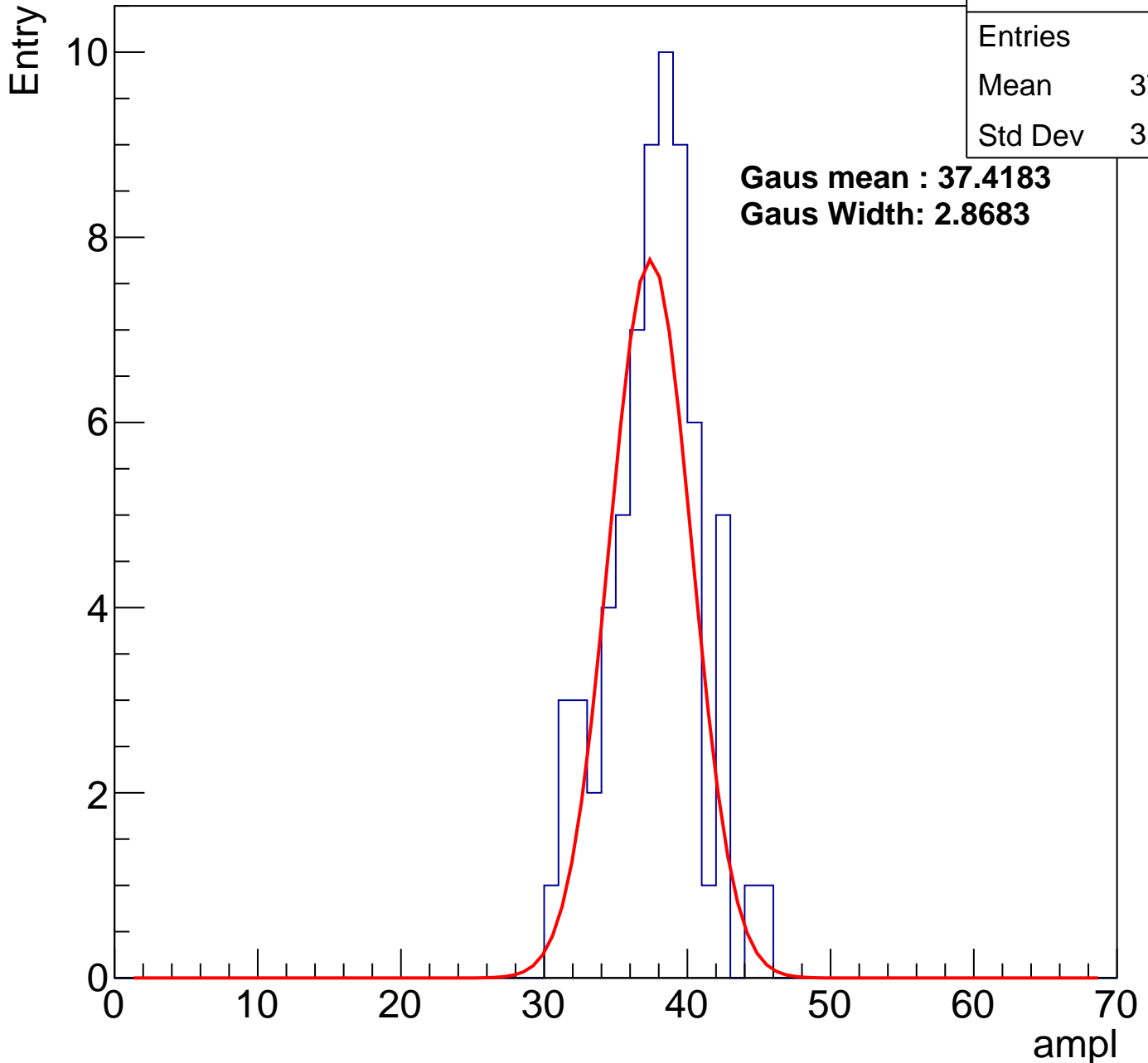


B0L001S, U15-ch16, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37.19
Std Dev	3.182

Gaus mean : 37.4183
Gaus Width: 2.8683



B0L001S, U15-ch16, adc2

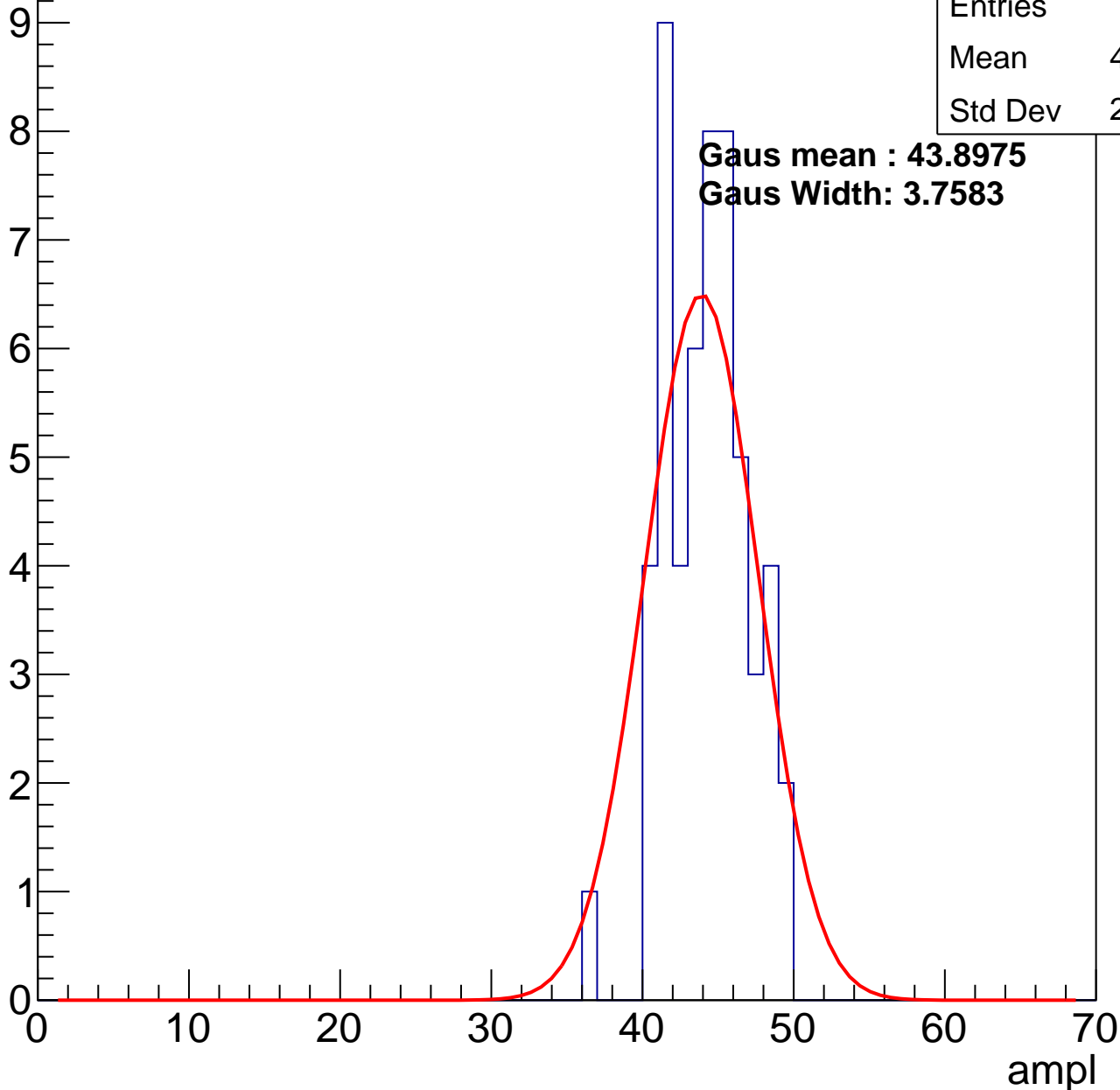
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.78
Std Dev	2.713

Gaus mean : 43.8975

Gaus Width: 3.7583

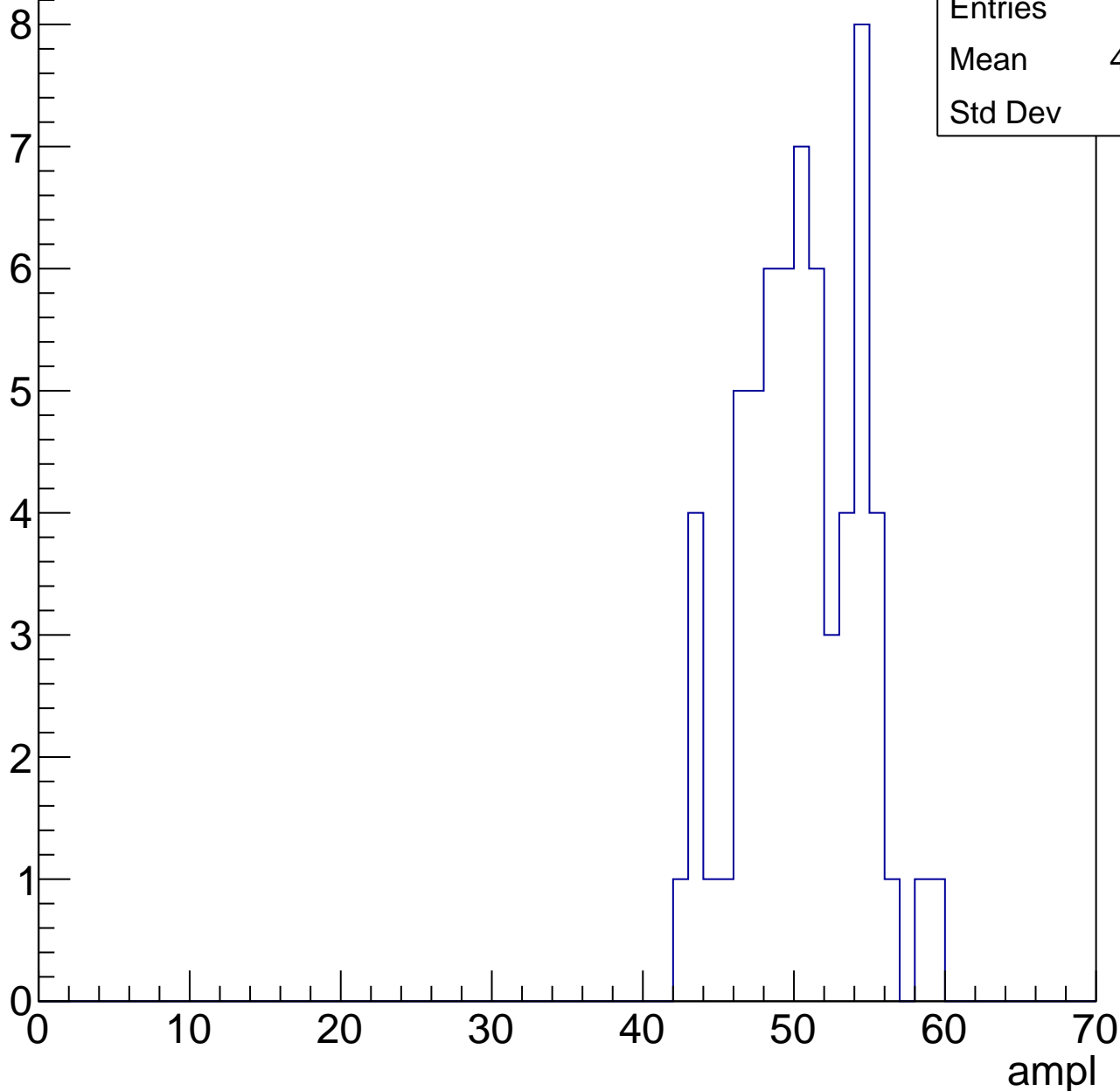


B0L001S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.98
Std Dev	3.83

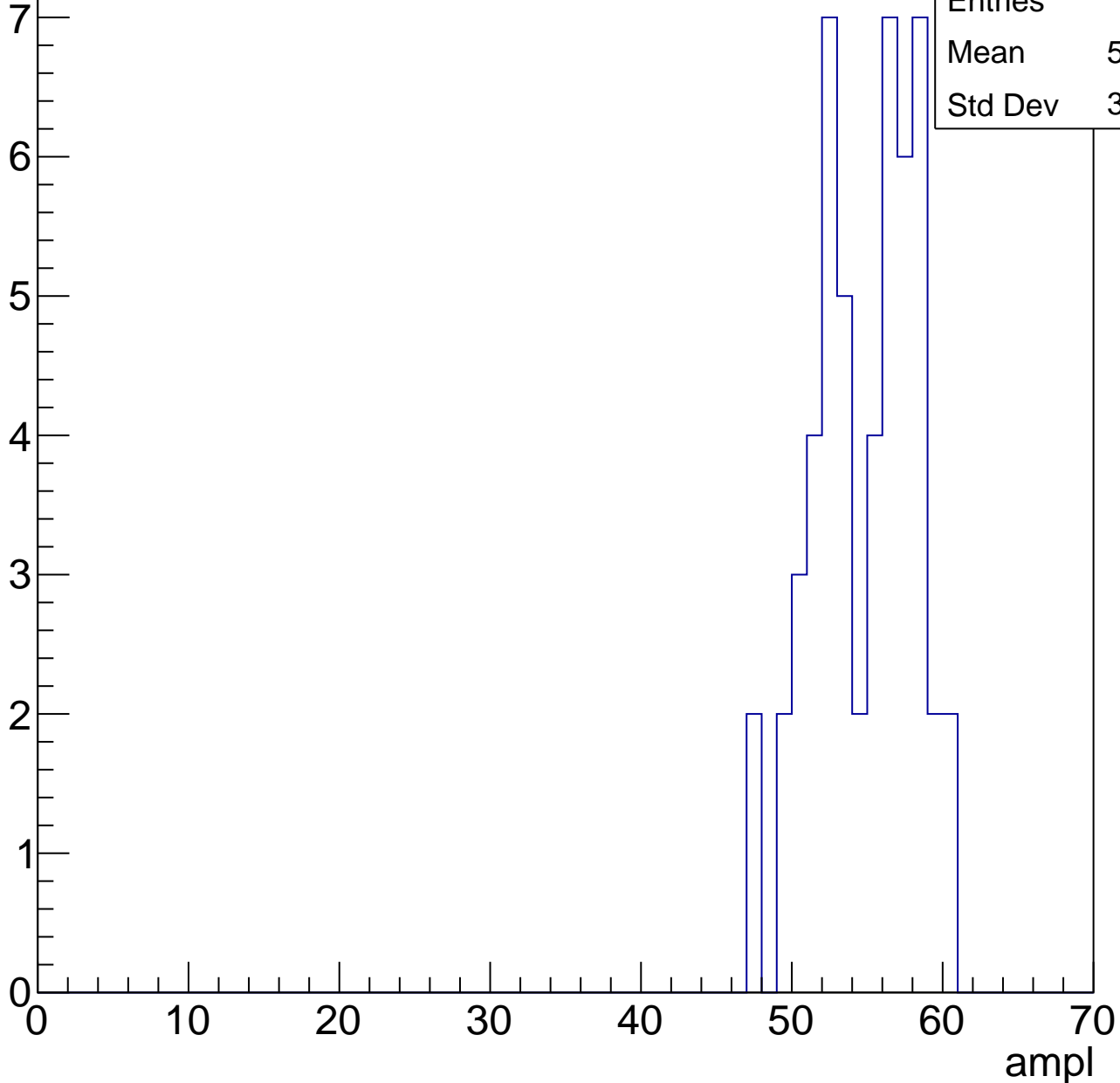


B0L001S, U15-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	54.36
Std Dev	3.303

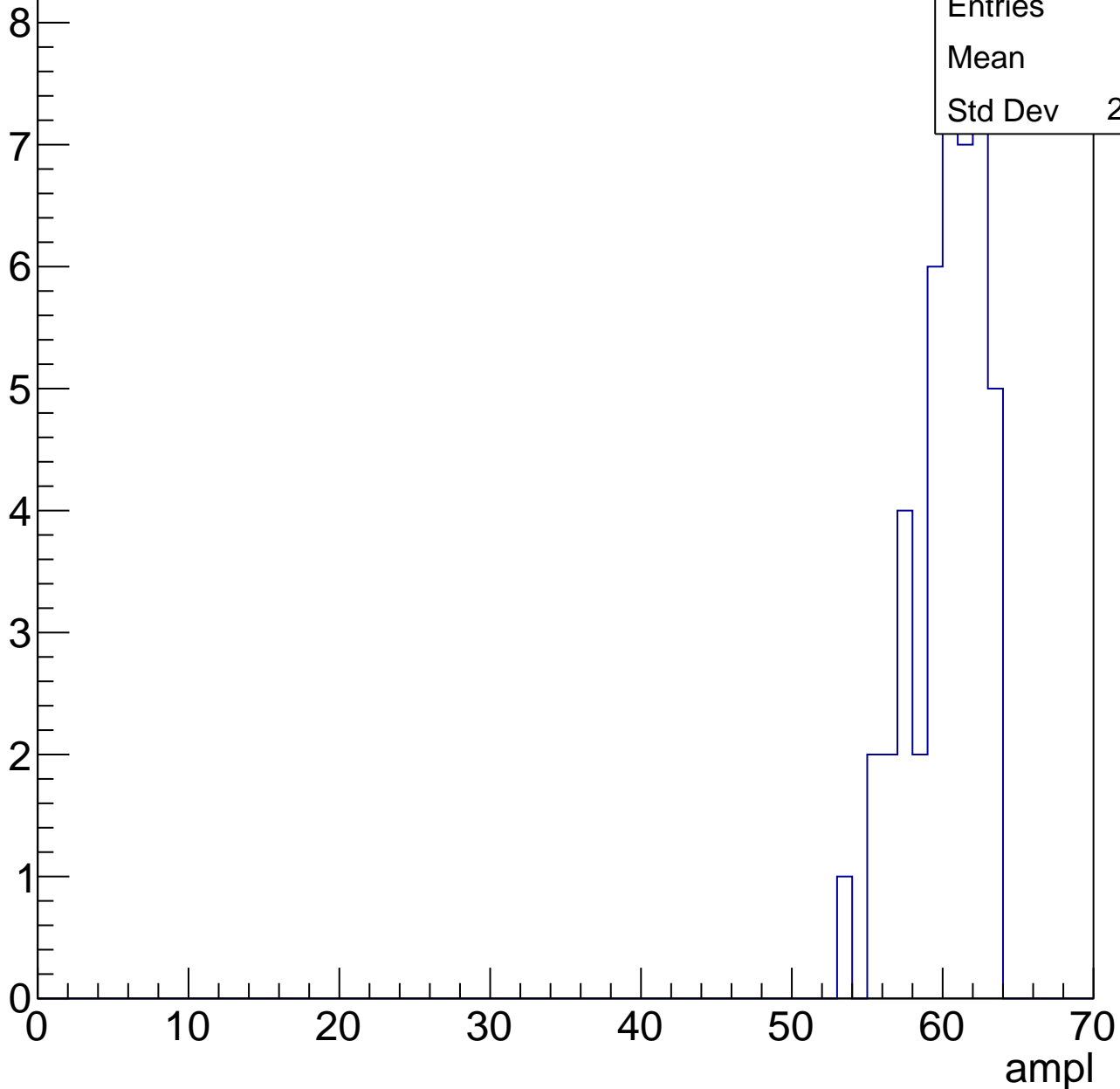


B0L001S, U15-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	59.8
Std Dev	2.418

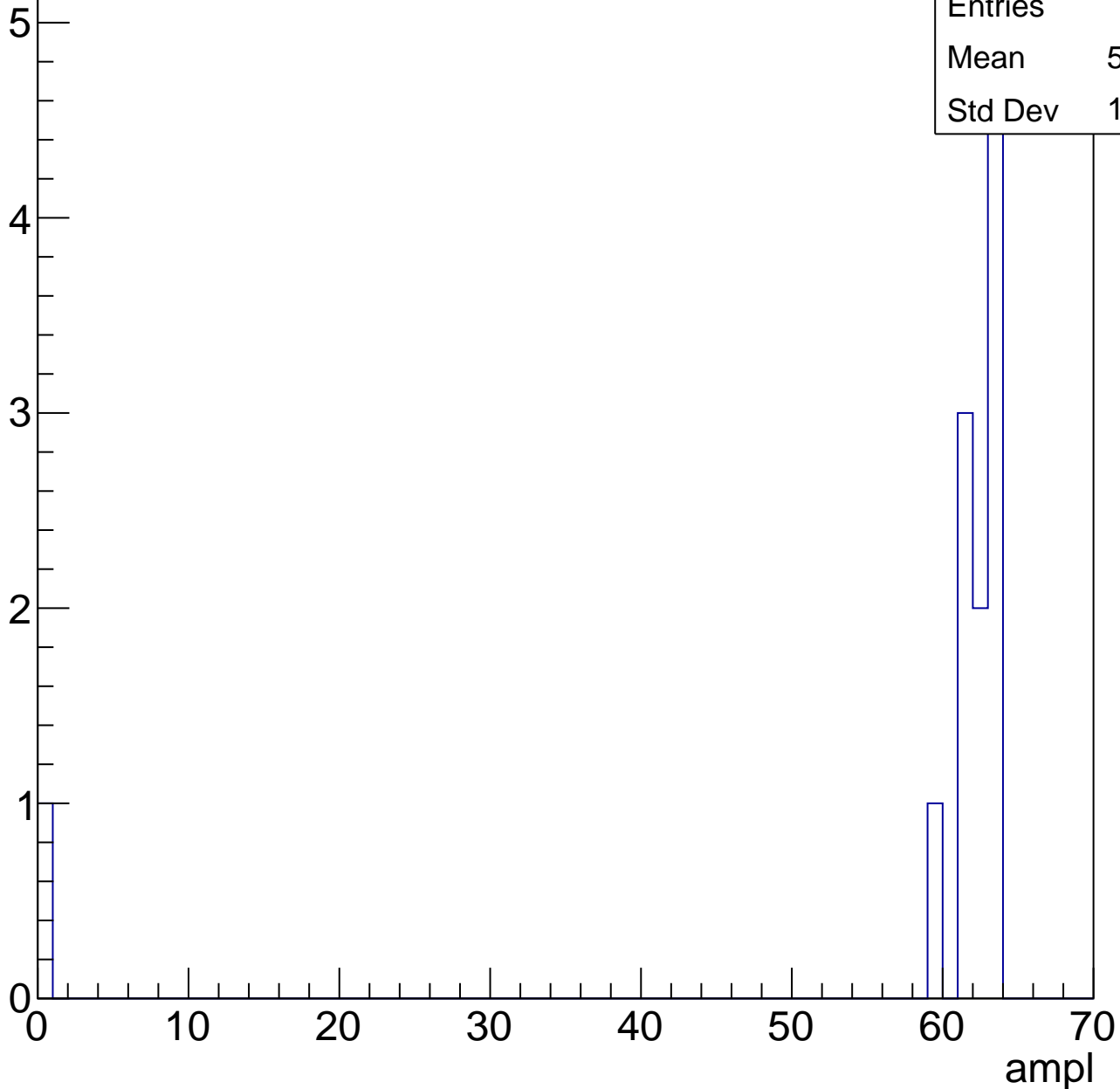


B0L001S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	12
Mean	56.75
Std Dev	17.15



B0L001S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch17, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	30.34
Std Dev	3.308

Gaus mean : 30.7083

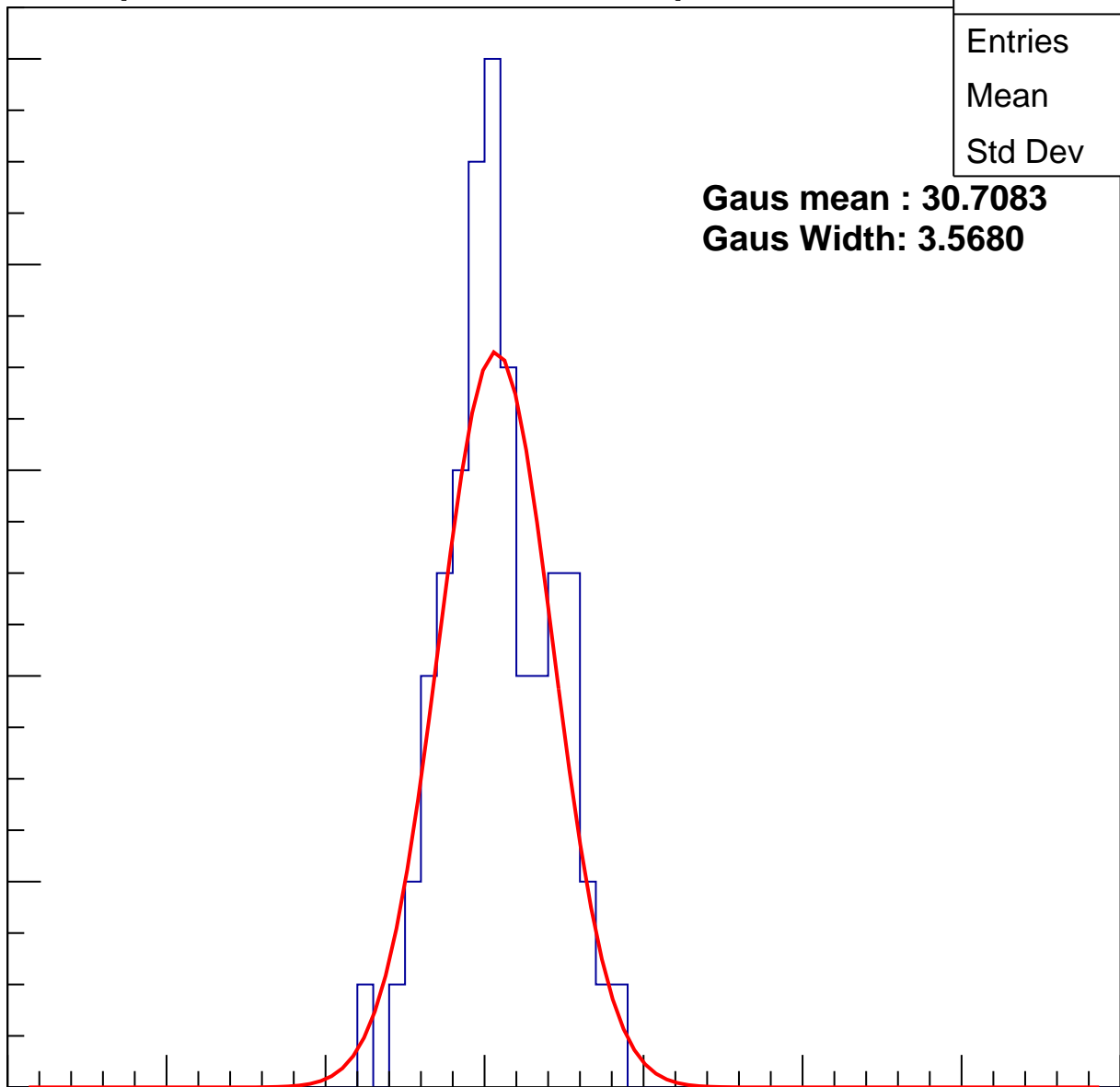
Gaus Width: 3.5680

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch17, adc1

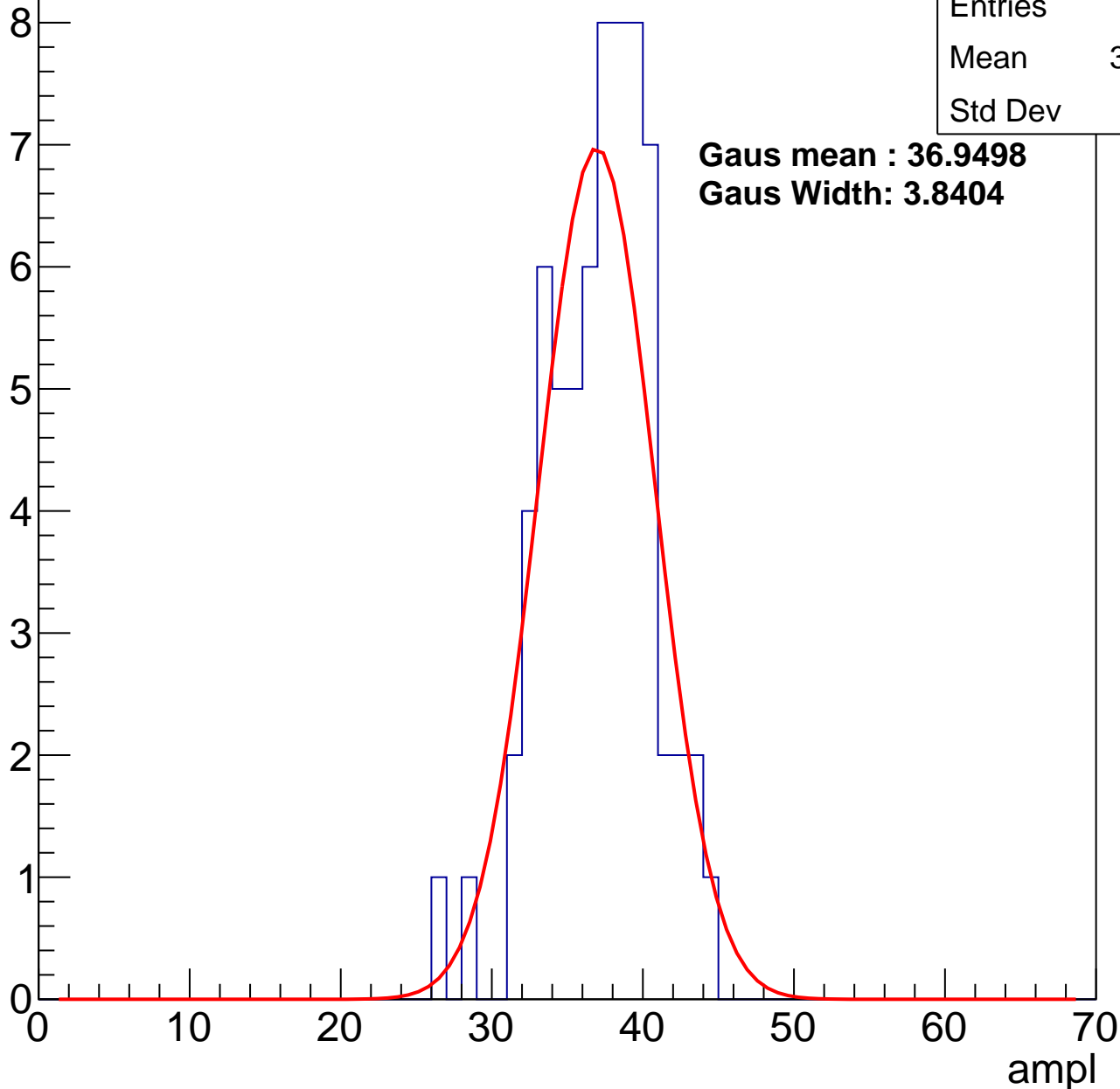
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.63
Std Dev	3.51

Gaus mean : 36.9498

Gaus Width: 3.8404



B0L001S, U15-ch17, adc2

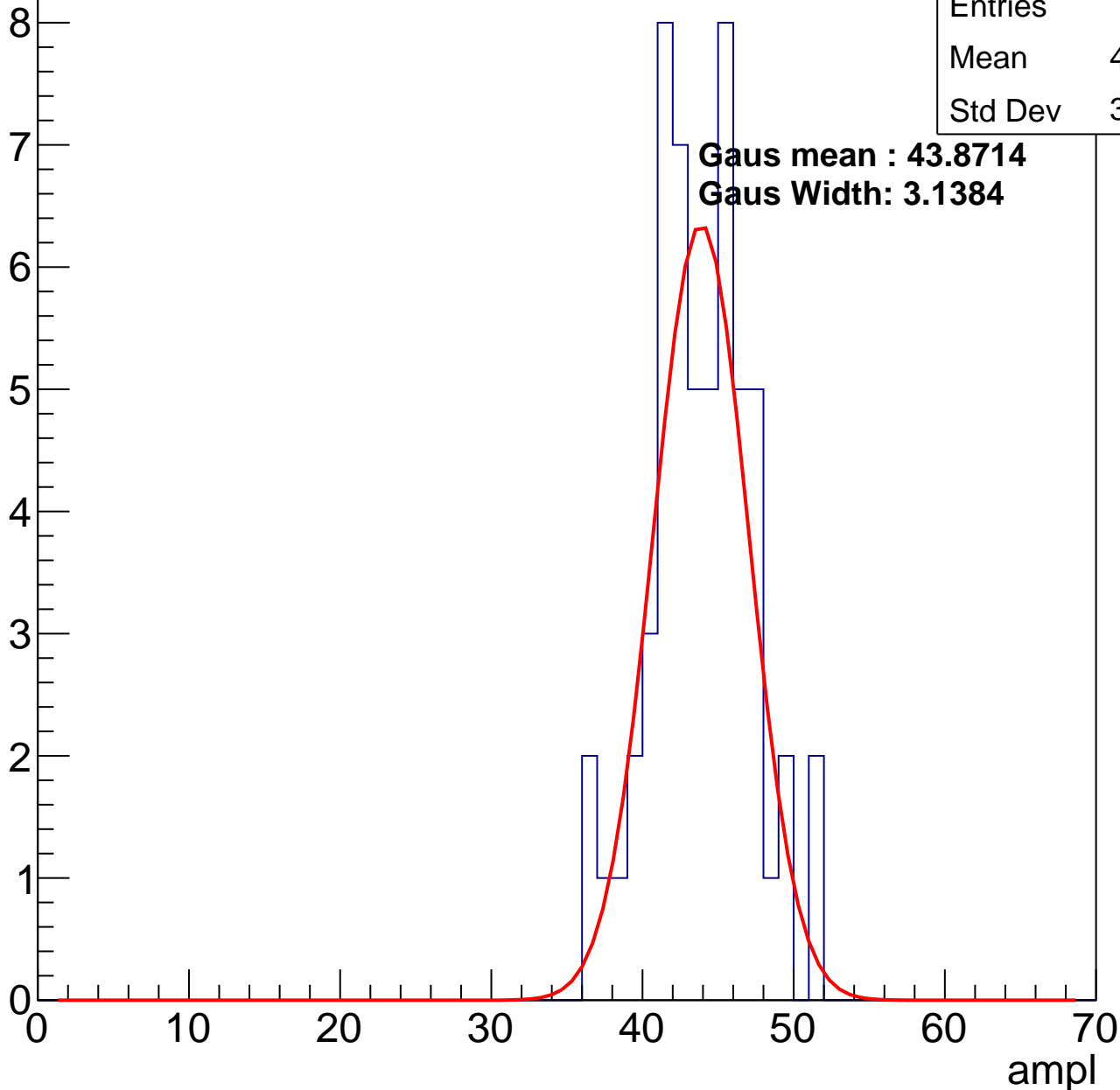
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.42
Std Dev	3.335

Gaus mean : 43.8714

Gaus Width: 3.1384

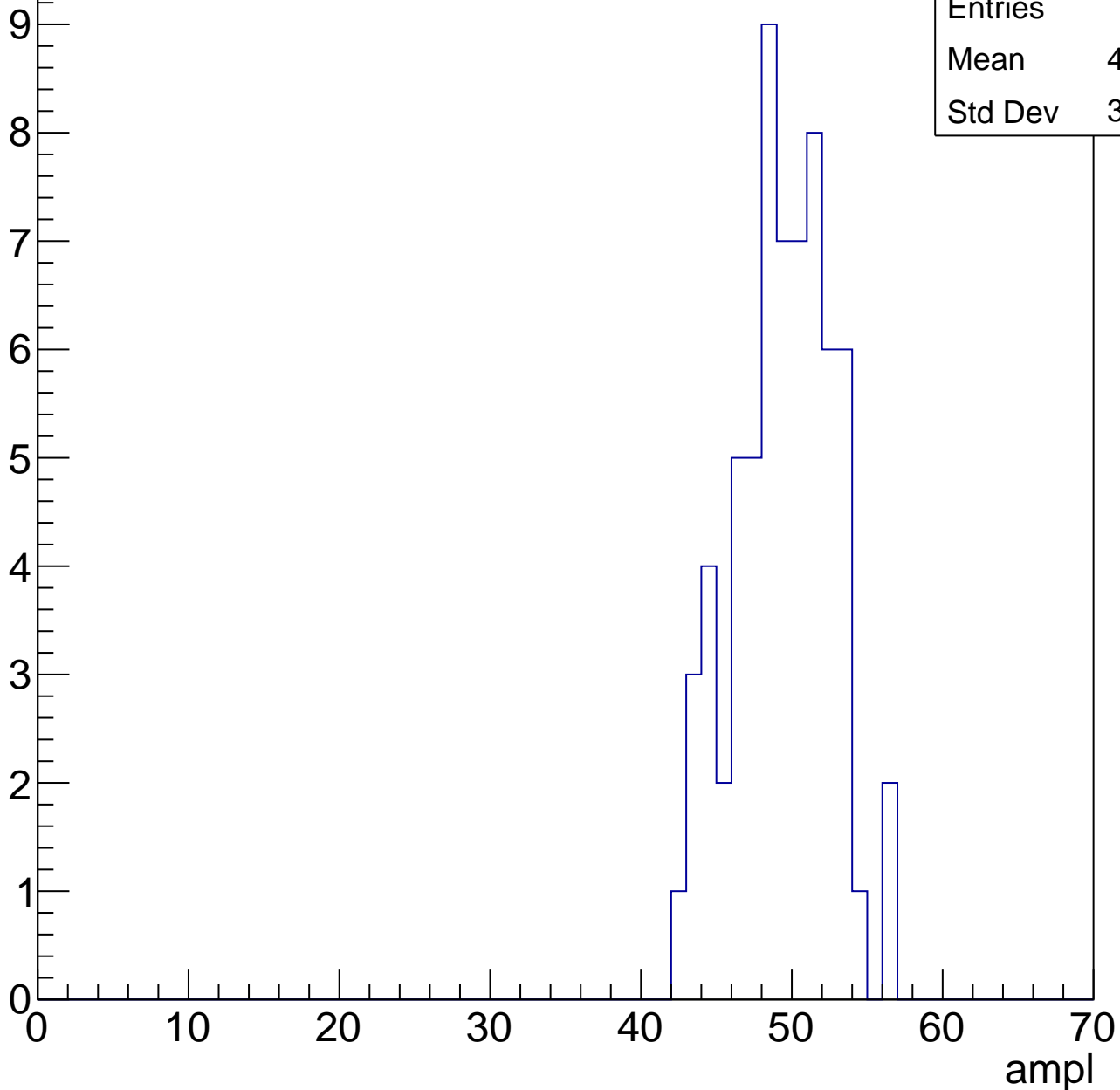


B0L001S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	48.95
Std Dev	3.198

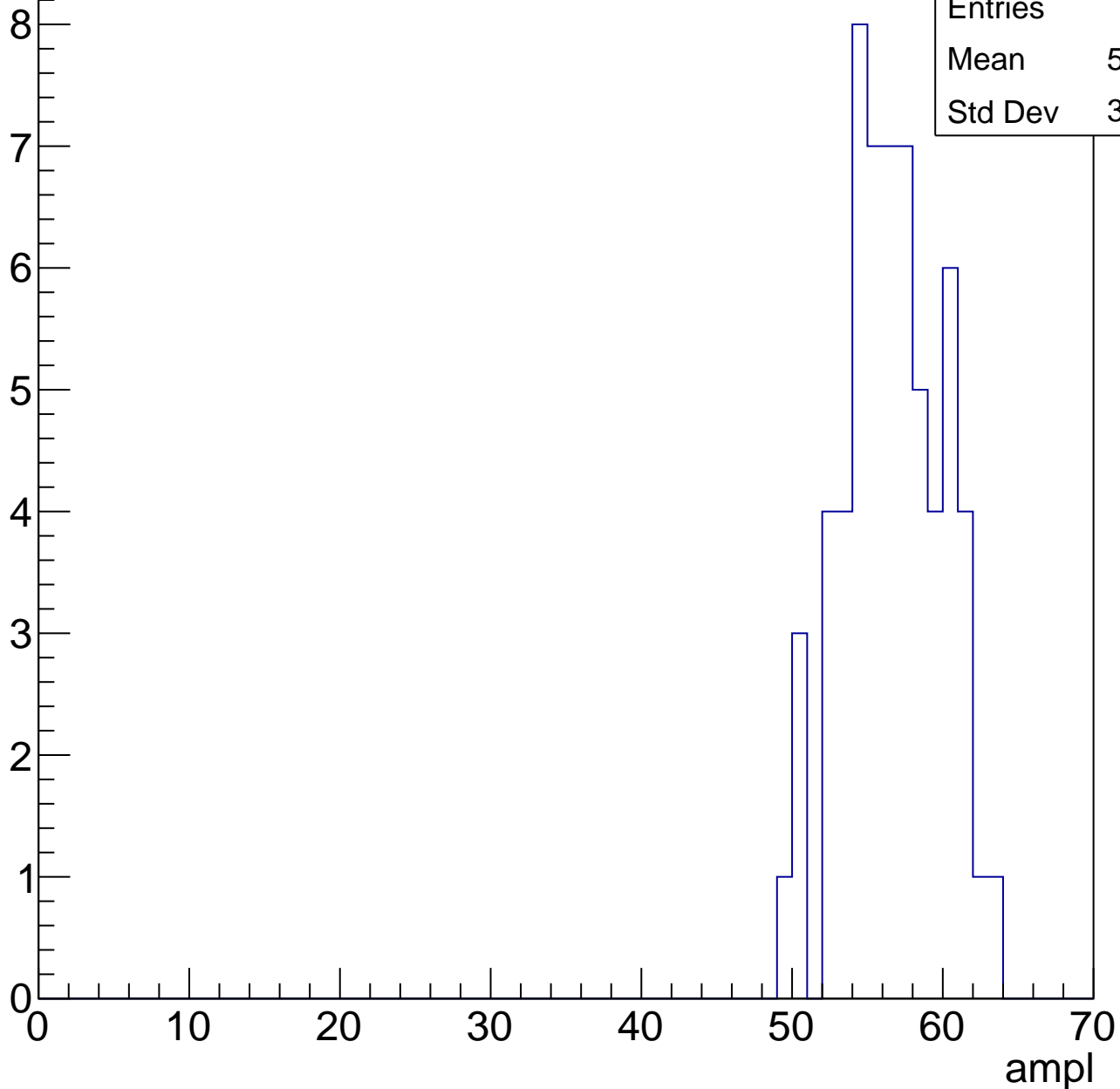


B0L001S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.16
Std Dev	3.209

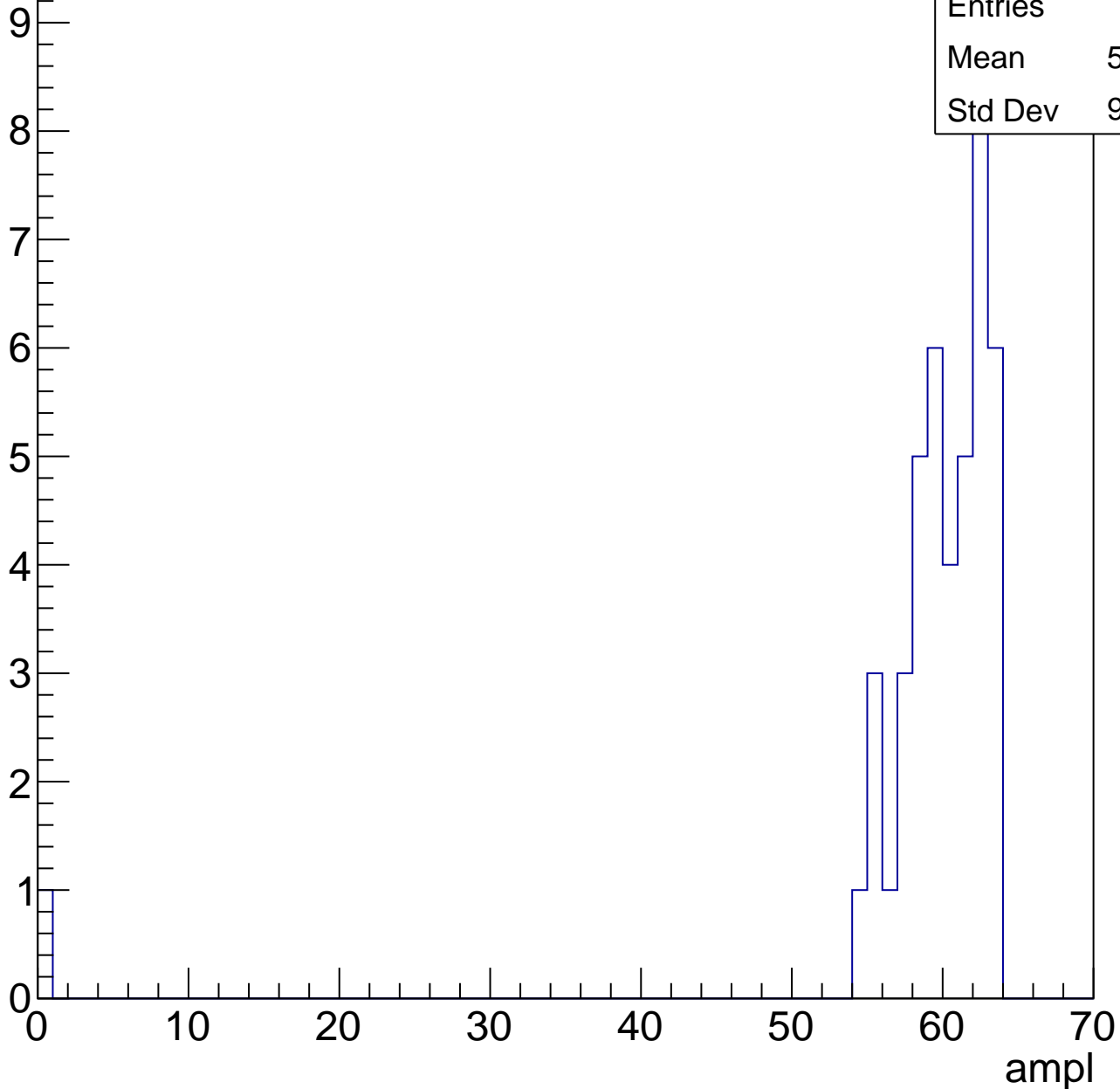


B0L001S, U15-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.43
Std Dev	9.253



B0L001S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch18, adc0

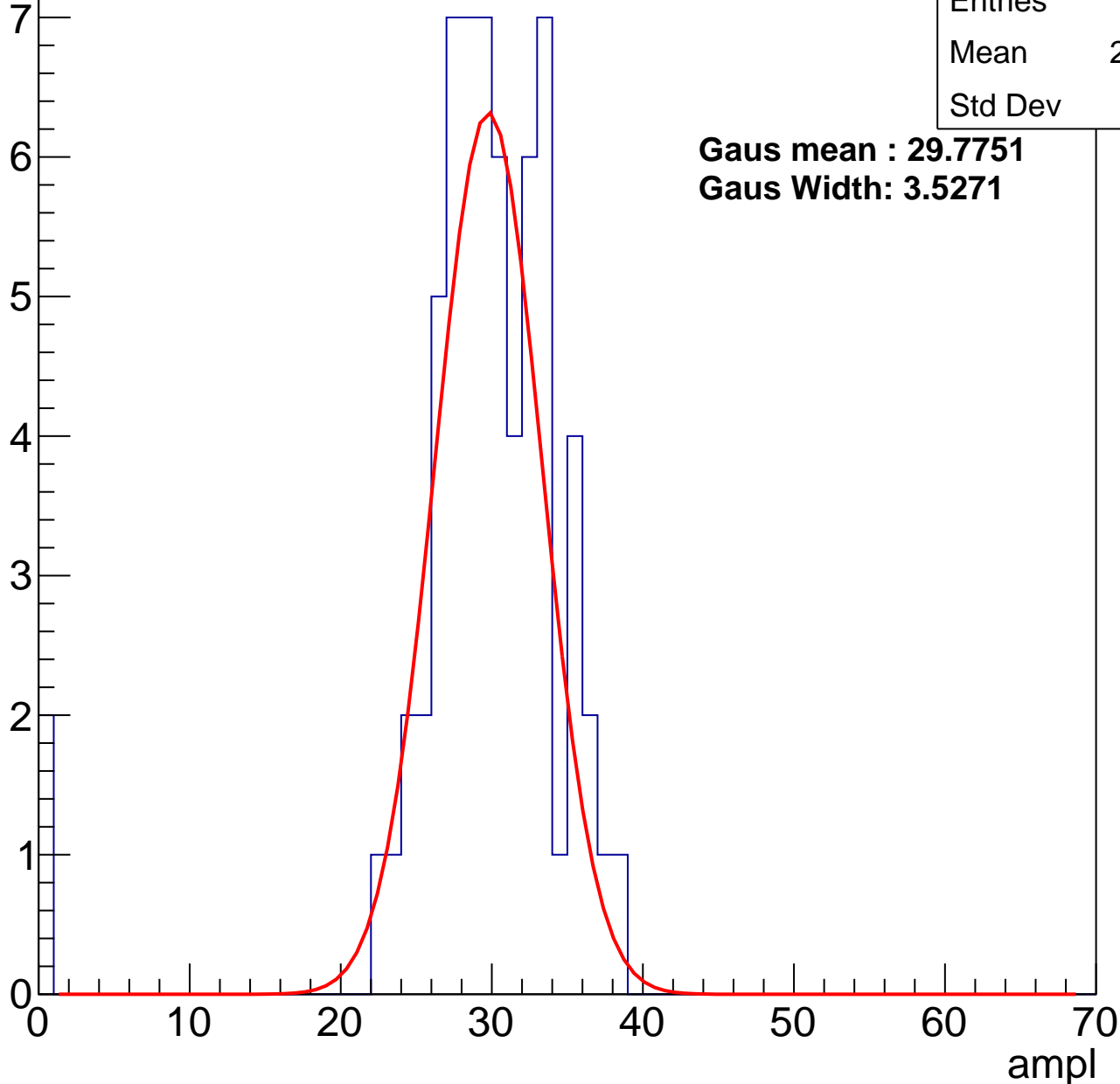
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	28.92
Std Dev	6.19

Gaus mean : 29.7751

Gaus Width: 3.5271



B0L001S, U15-ch18, adc1

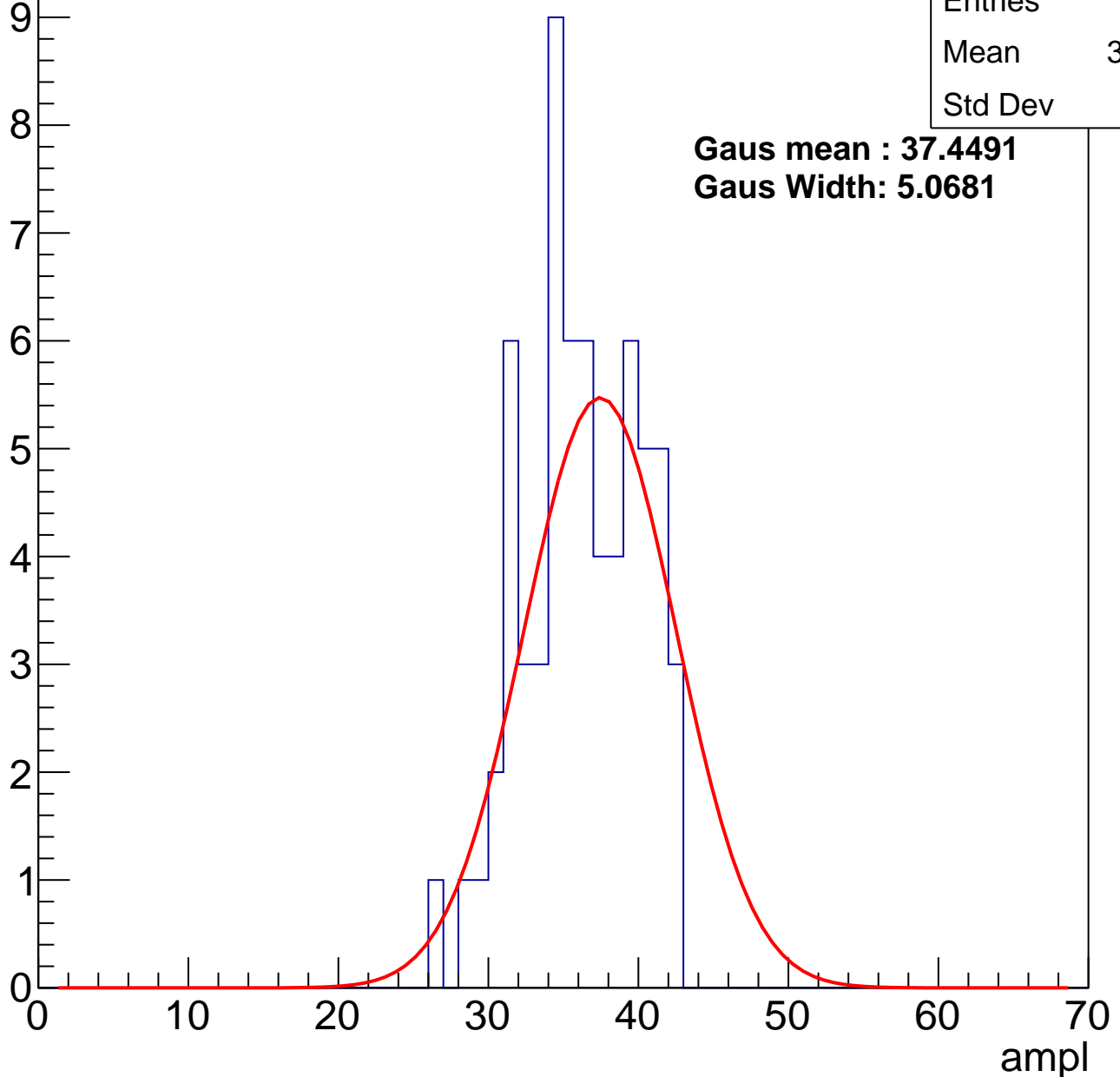
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	35.71
Std Dev	3.79

Gaus mean : 37.4491

Gaus Width: 5.0681



B0L001S, U15-ch18, adc2

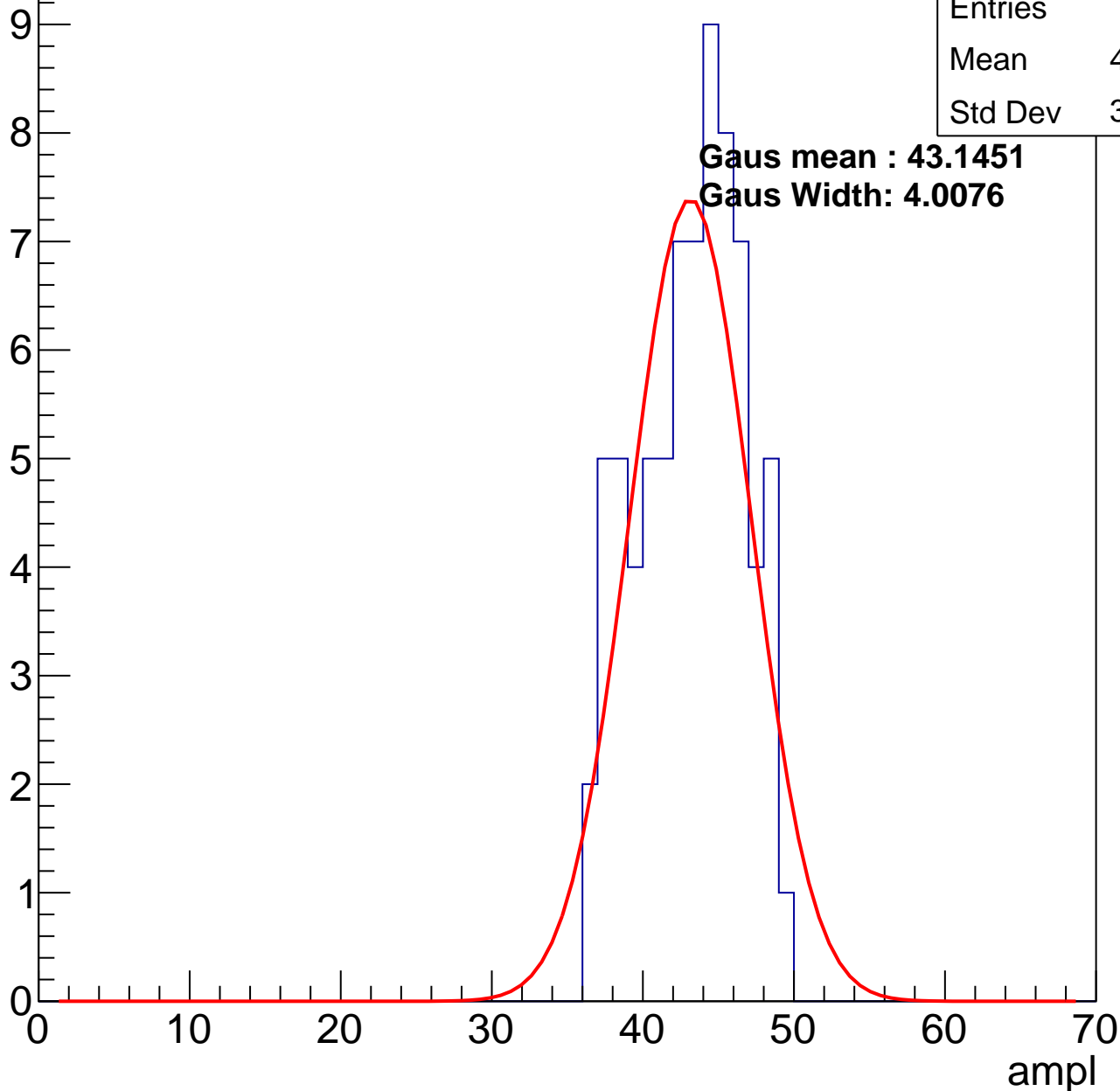
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	42.68
Std Dev	3.409

Gaus mean : 43.1451

Gaus Width: 4.0076

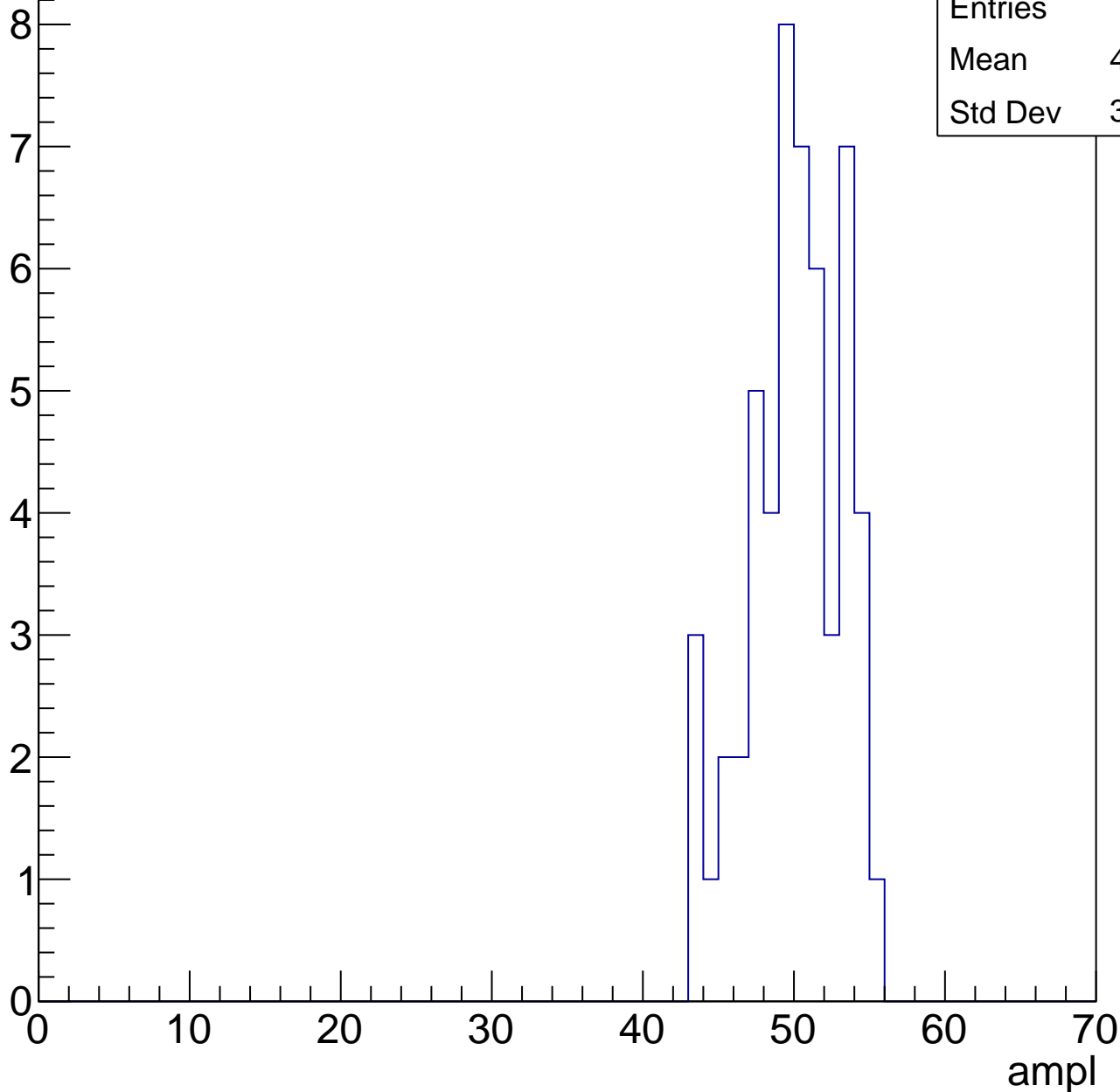


B0L001S, U15-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	49.58
Std Dev	3.056

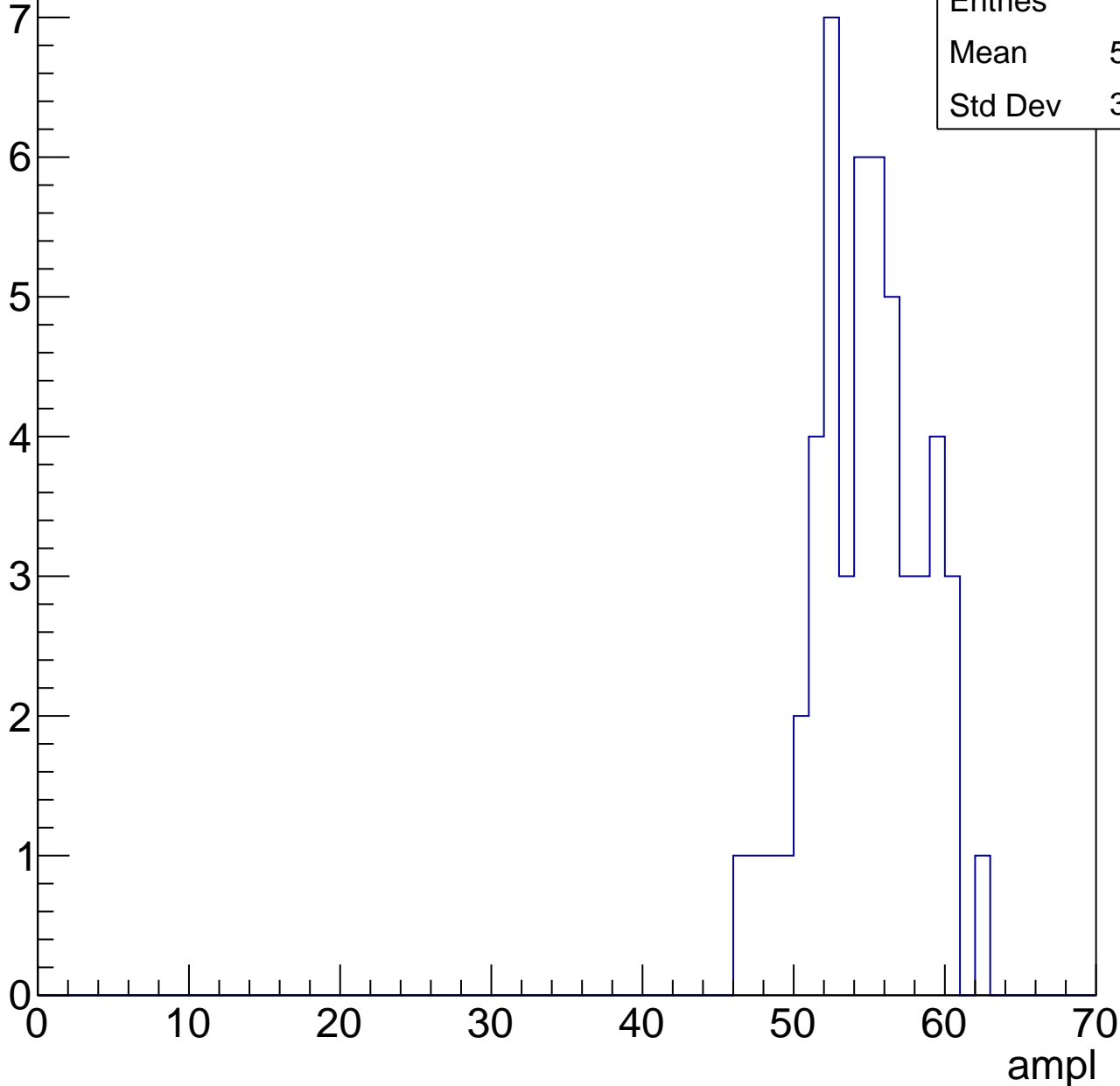


B0L001S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	54.39
Std Dev	3.537

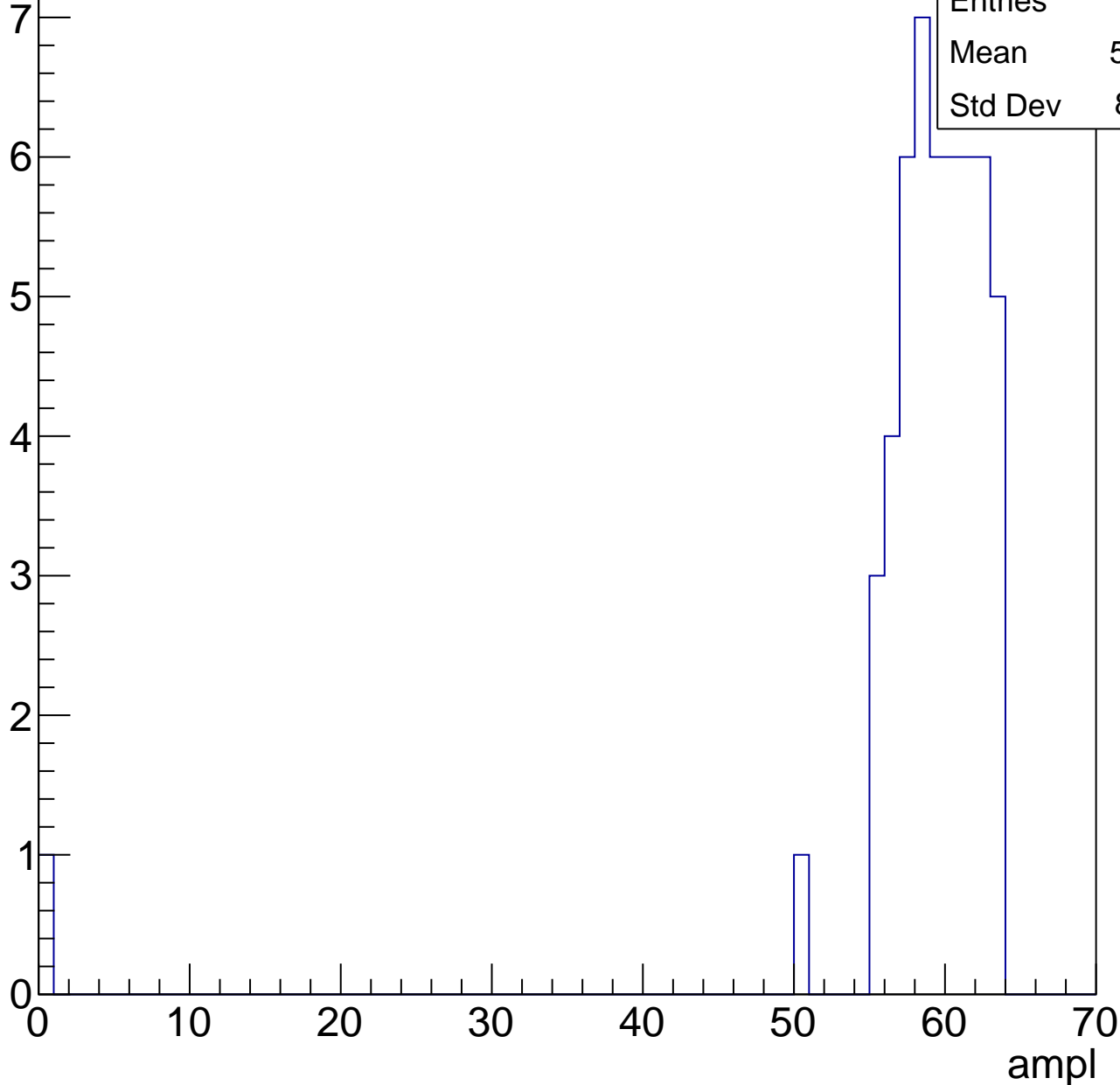


B0L001S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.92
Std Dev	8.611

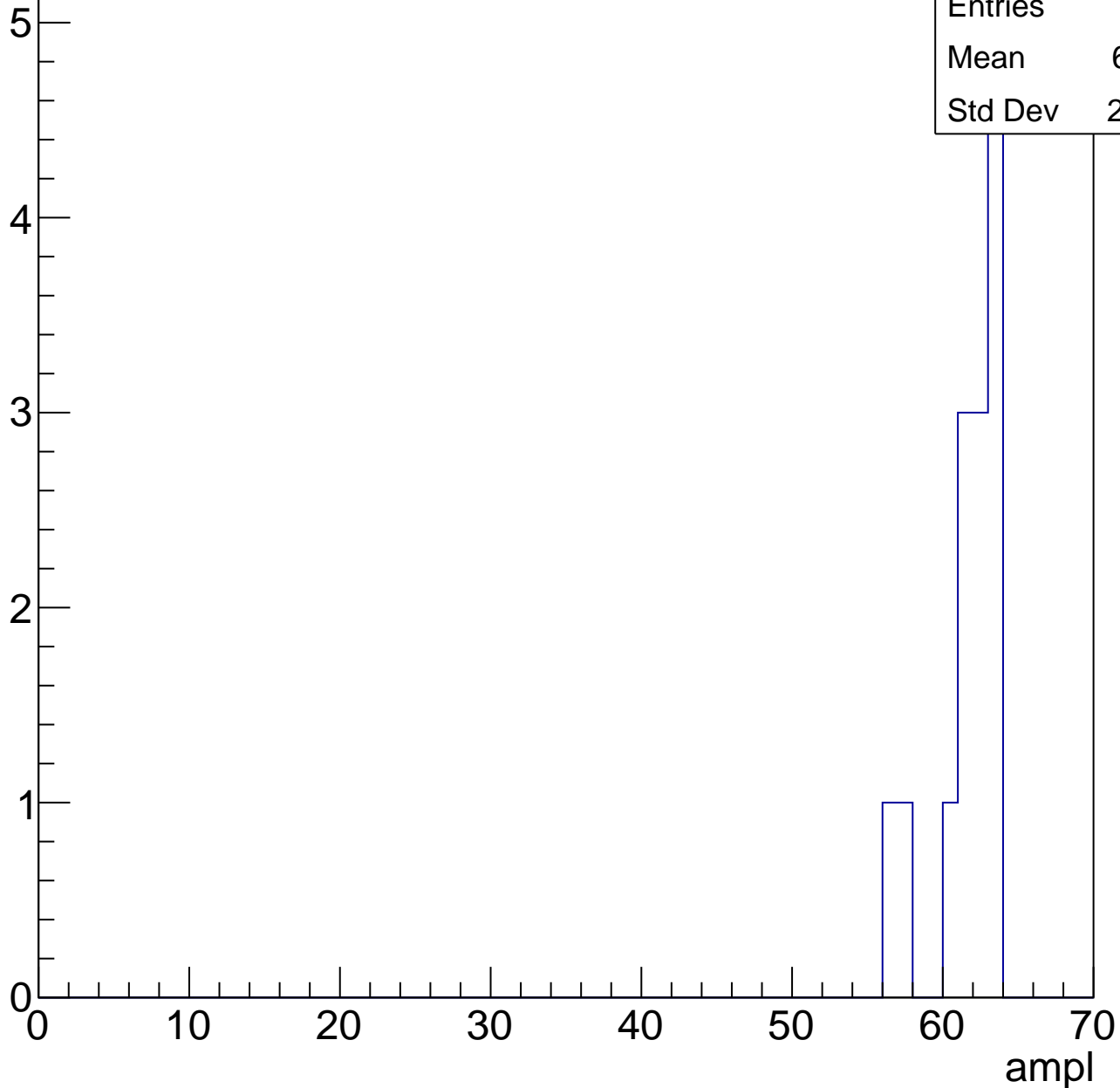


B0L001S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	61.21
Std Dev	2.144



B0L001S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B0L001S, U15-ch19, adc0

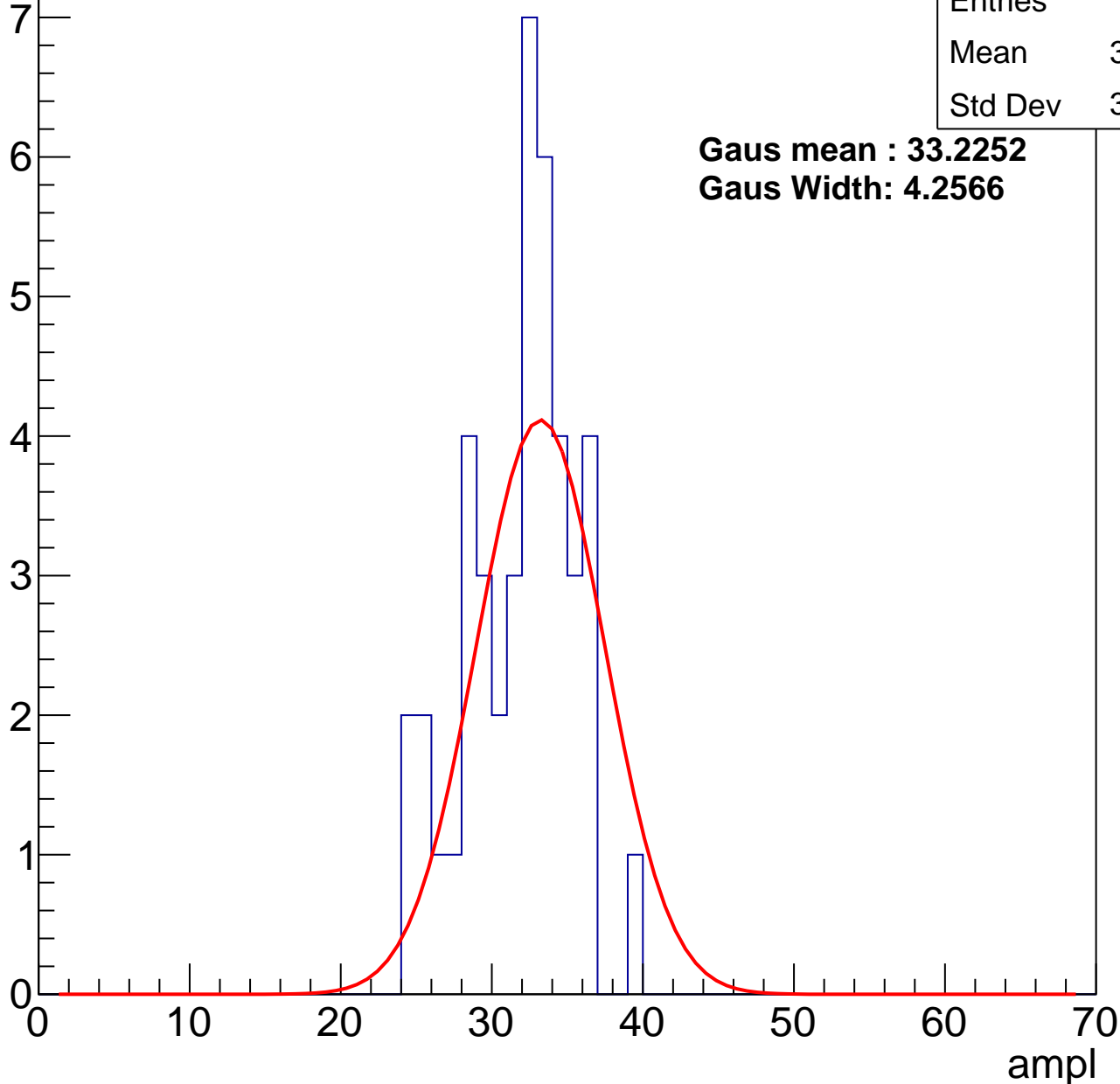
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	31.37
Std Dev	3.524

Gaus mean : 33.2252

Gaus Width: 4.2566

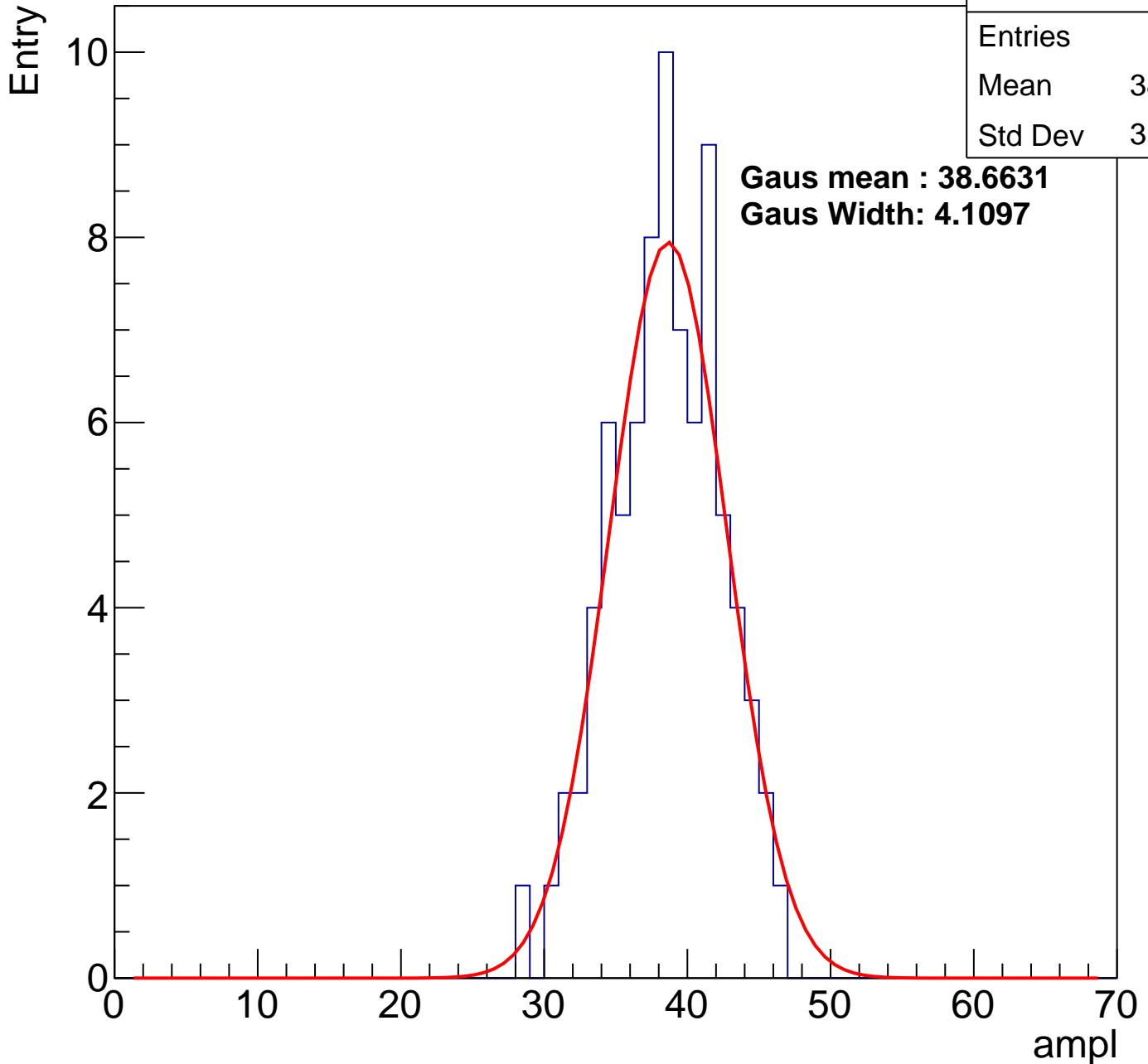


B0L001S, U15-ch19, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	38.04
Std Dev	3.776

Gaus mean : 38.6631
Gaus Width: 4.1097



B0L001S, U15-ch19, adc2

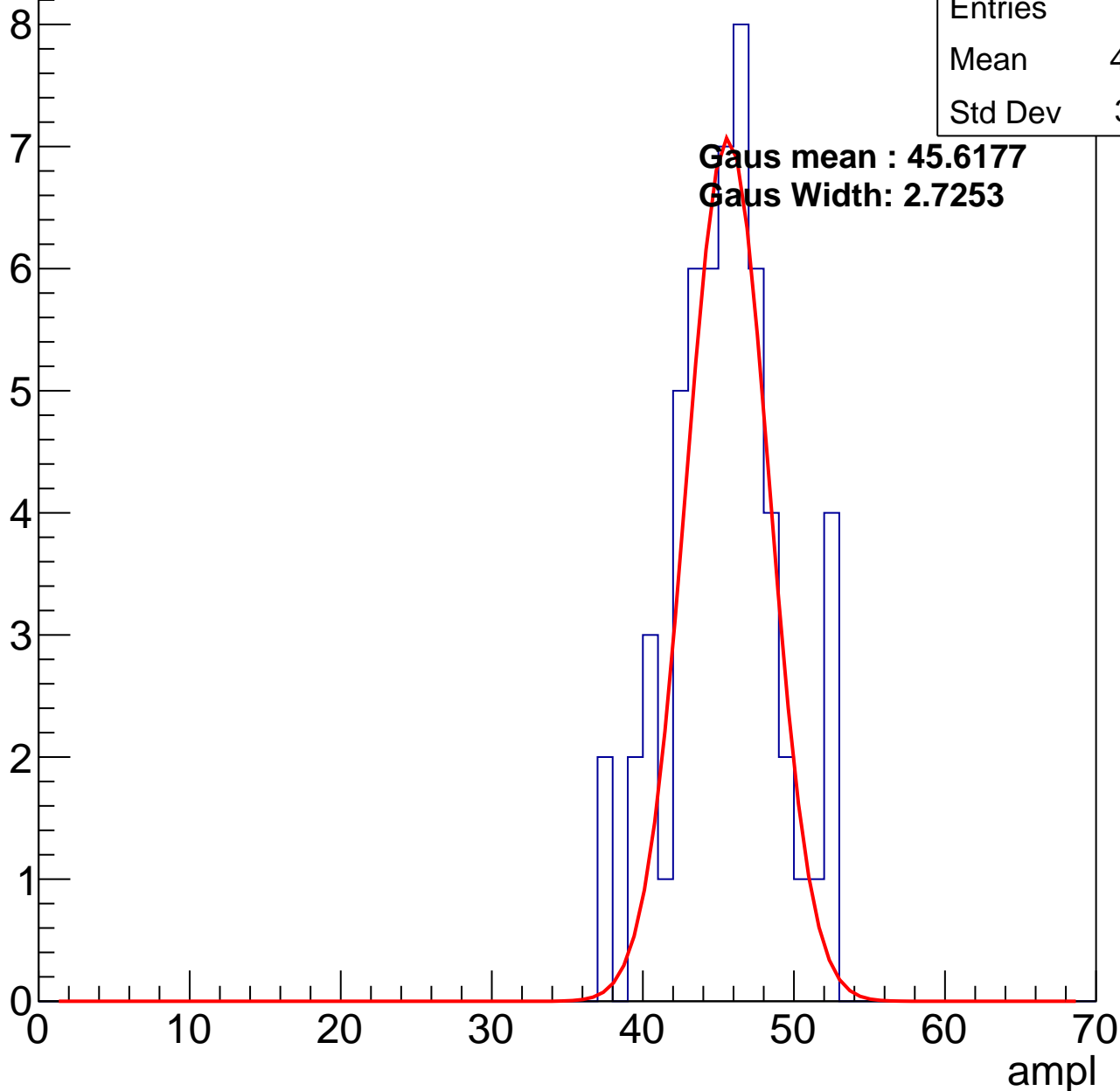
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.98
Std Dev	3.531

Gaus mean : 45.6177

Gaus Width: 2.7253

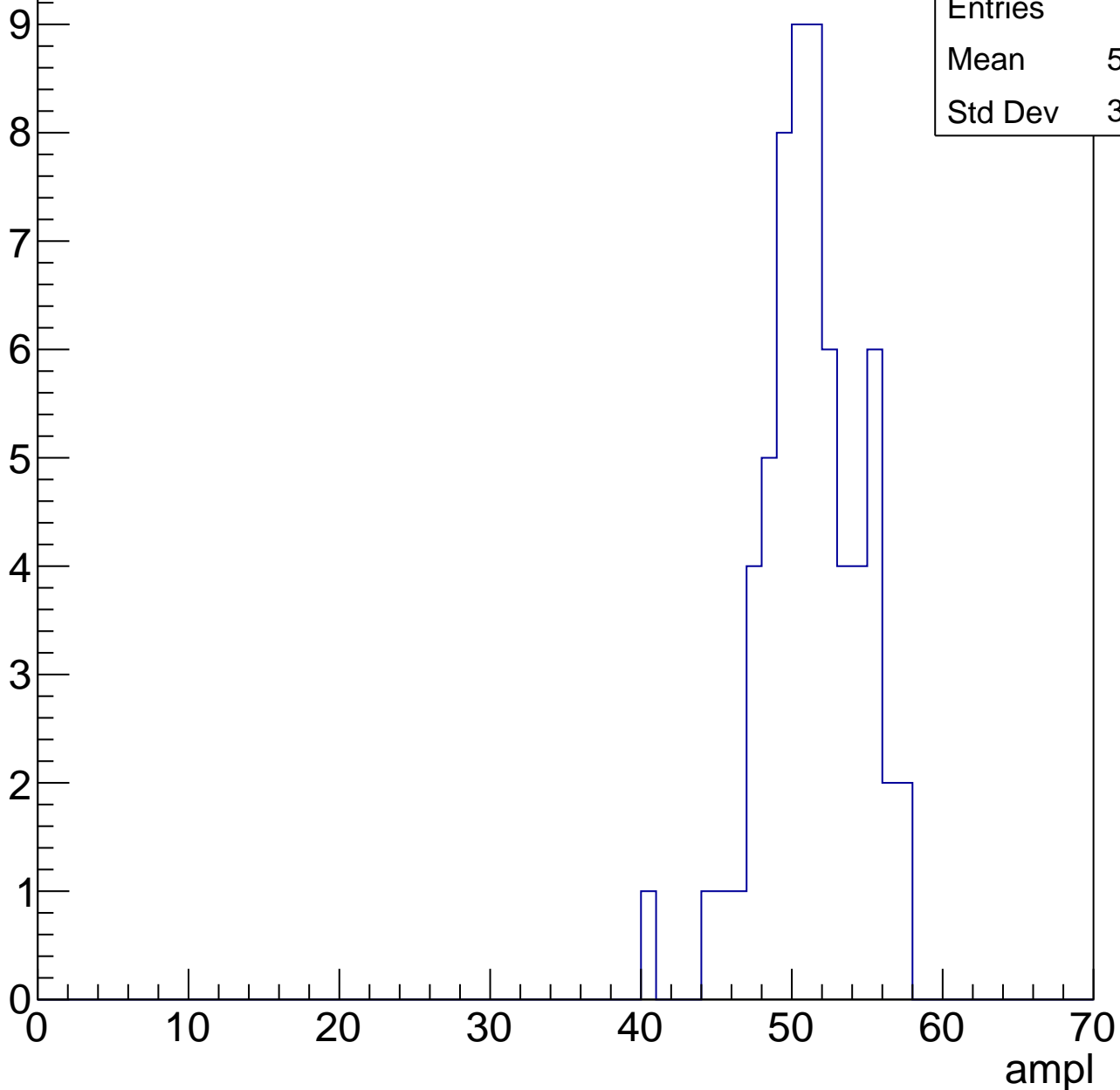


B0L001S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

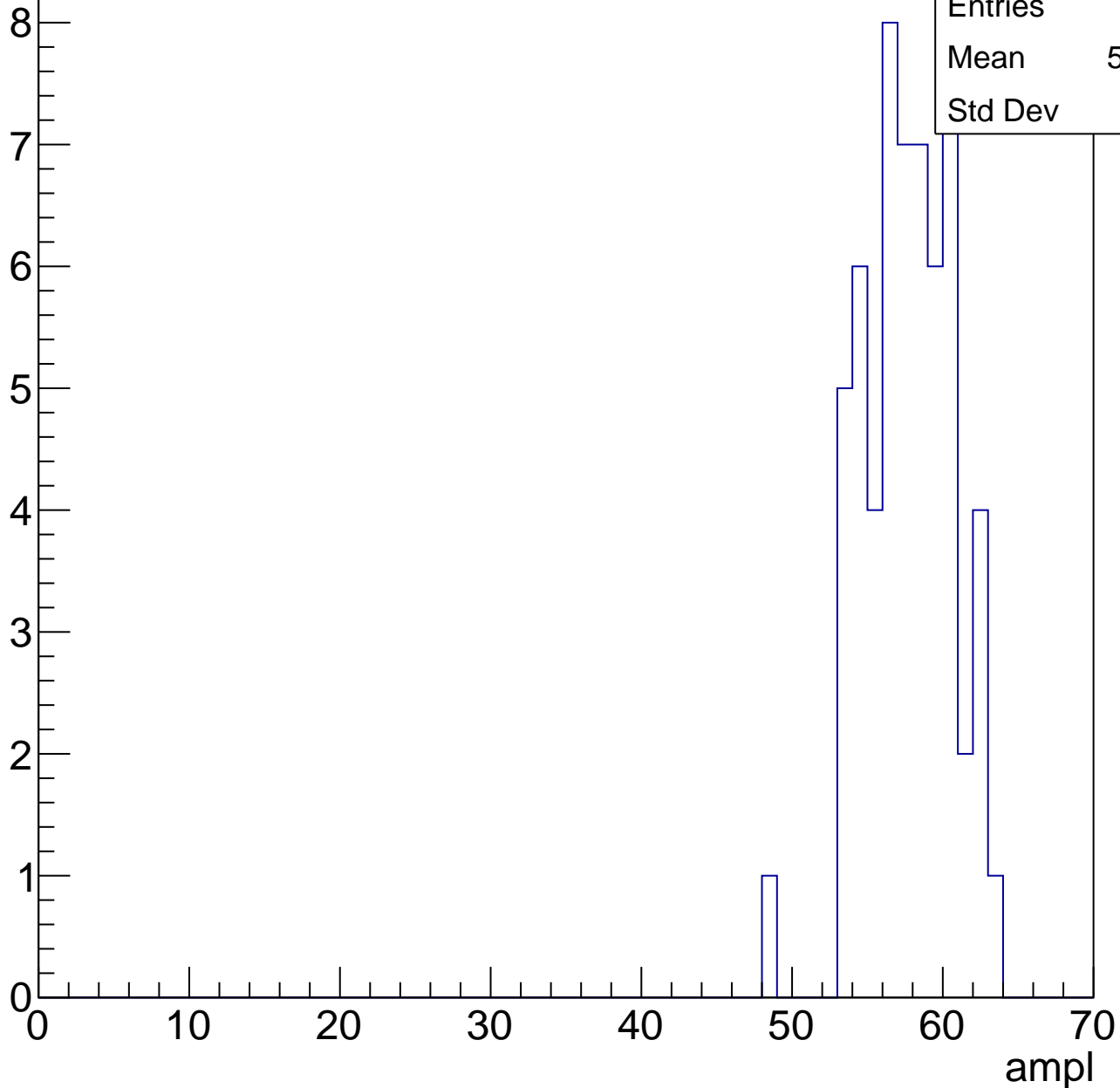
Entries	63
Mean	50.79
Std Dev	3.223



B0L001S, U15-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

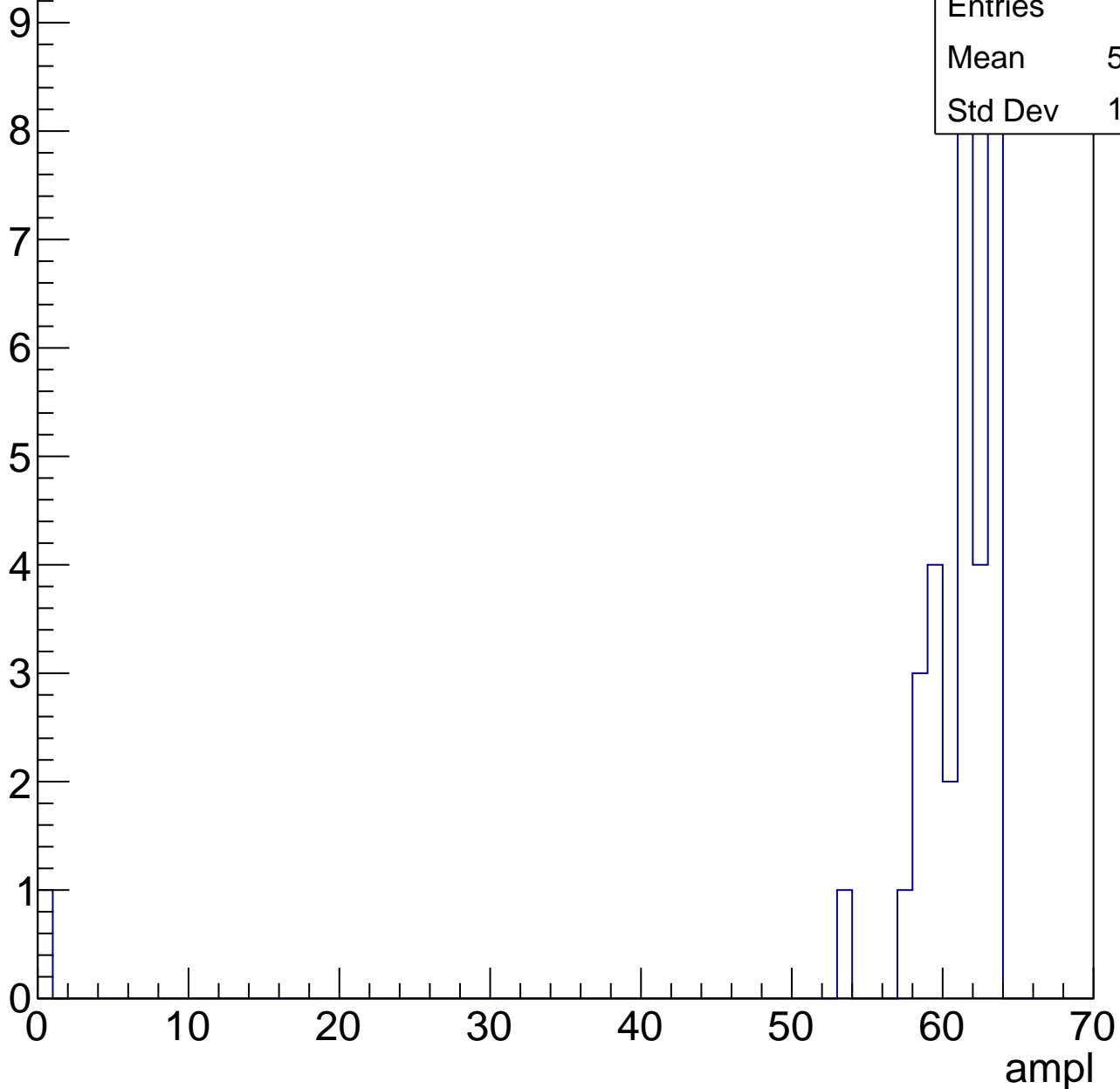


B0L001S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.88
Std Dev	10.64



B0L001S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B0L001S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch20, adc0

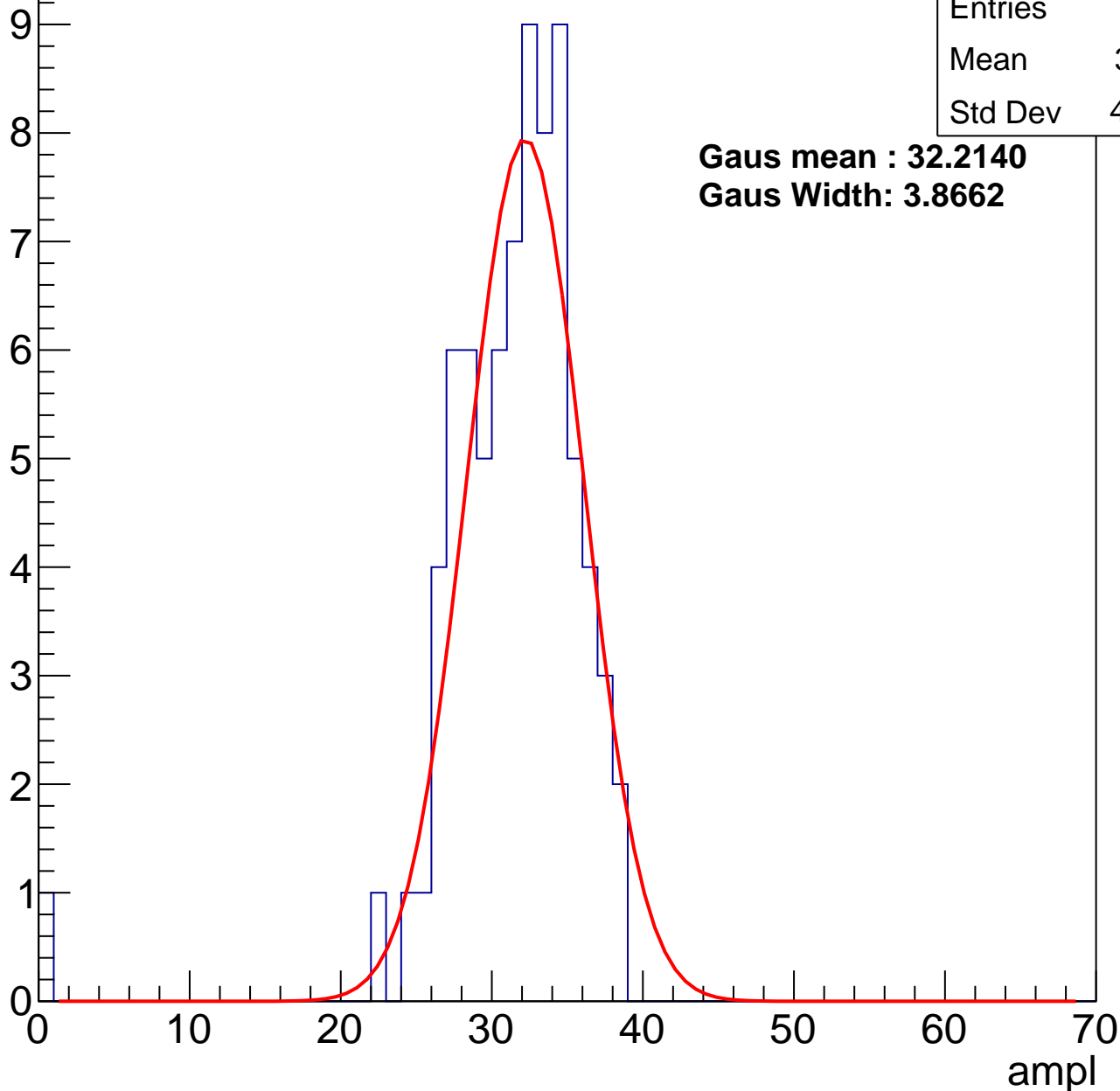
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.91
Std Dev	4.949

Gaus mean : 32.2140

Gaus Width: 3.8662



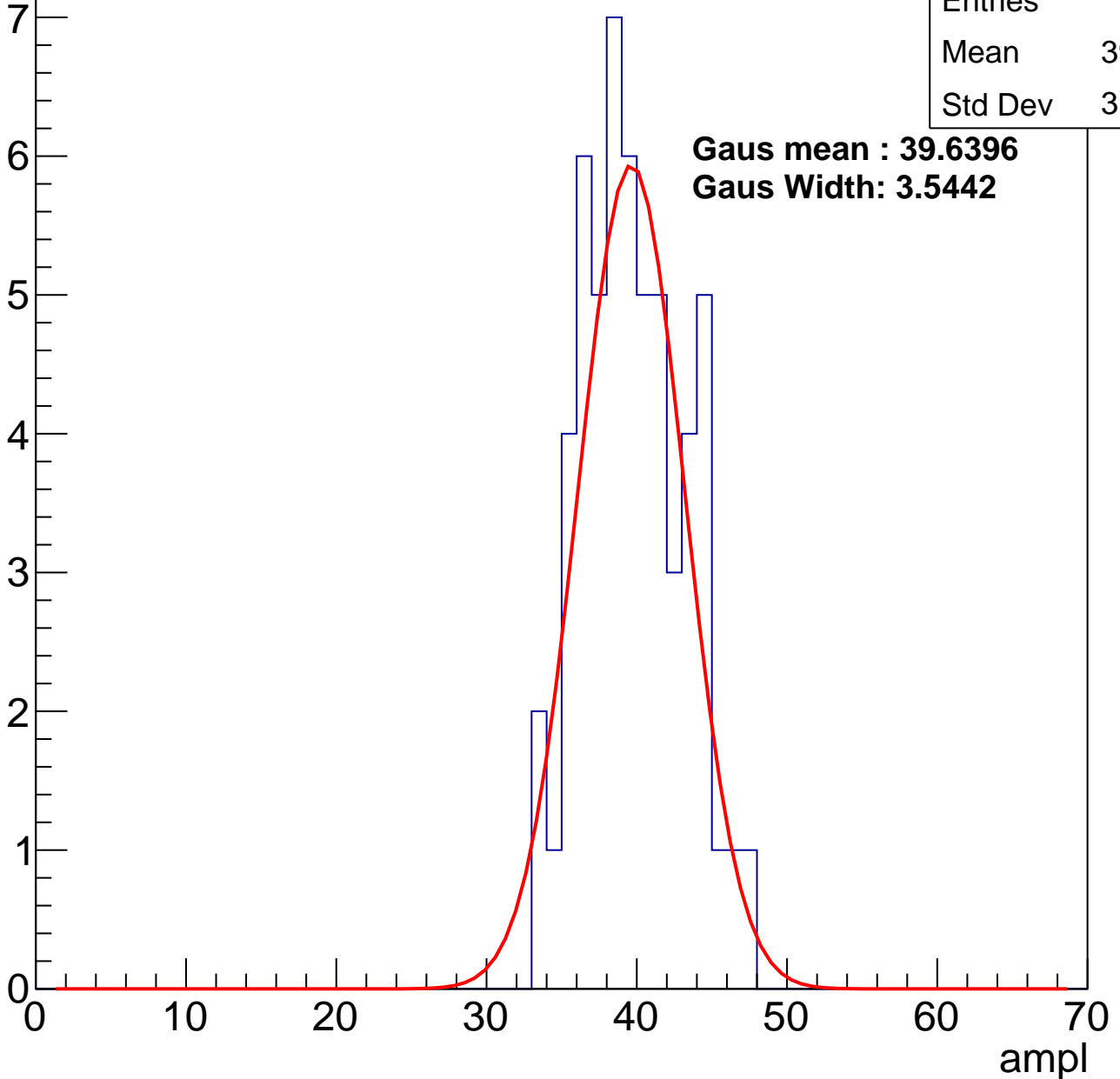
B0L001S, U15-ch20, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	39.32
Std Dev	3.344

Gaus mean : 39.6396
Gaus Width: 3.5442



B0L001S, U15-ch20, adc2

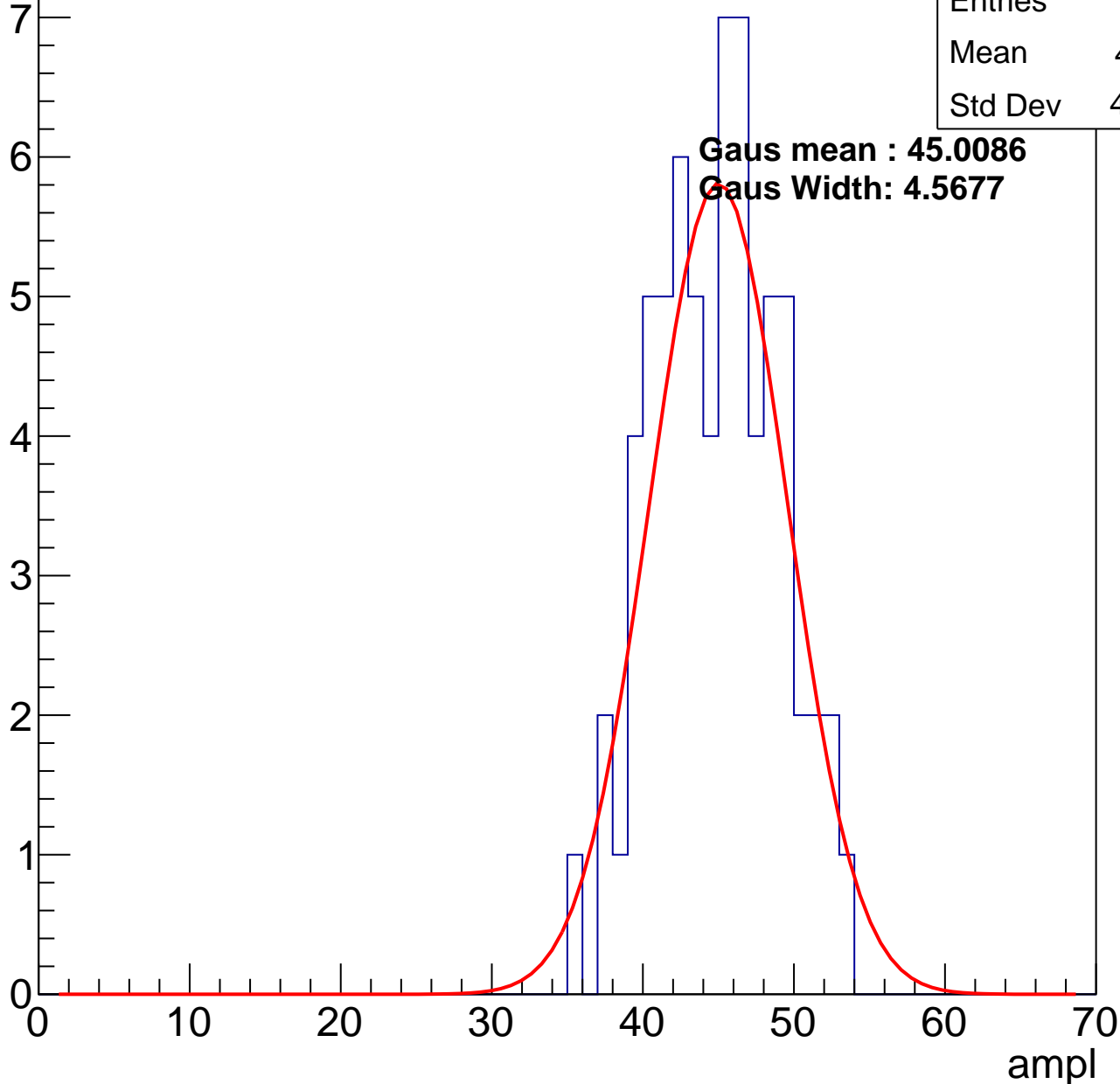
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.41
Std Dev	4.045

Gaus mean : 45.0086

Gaus Width: 4.5677

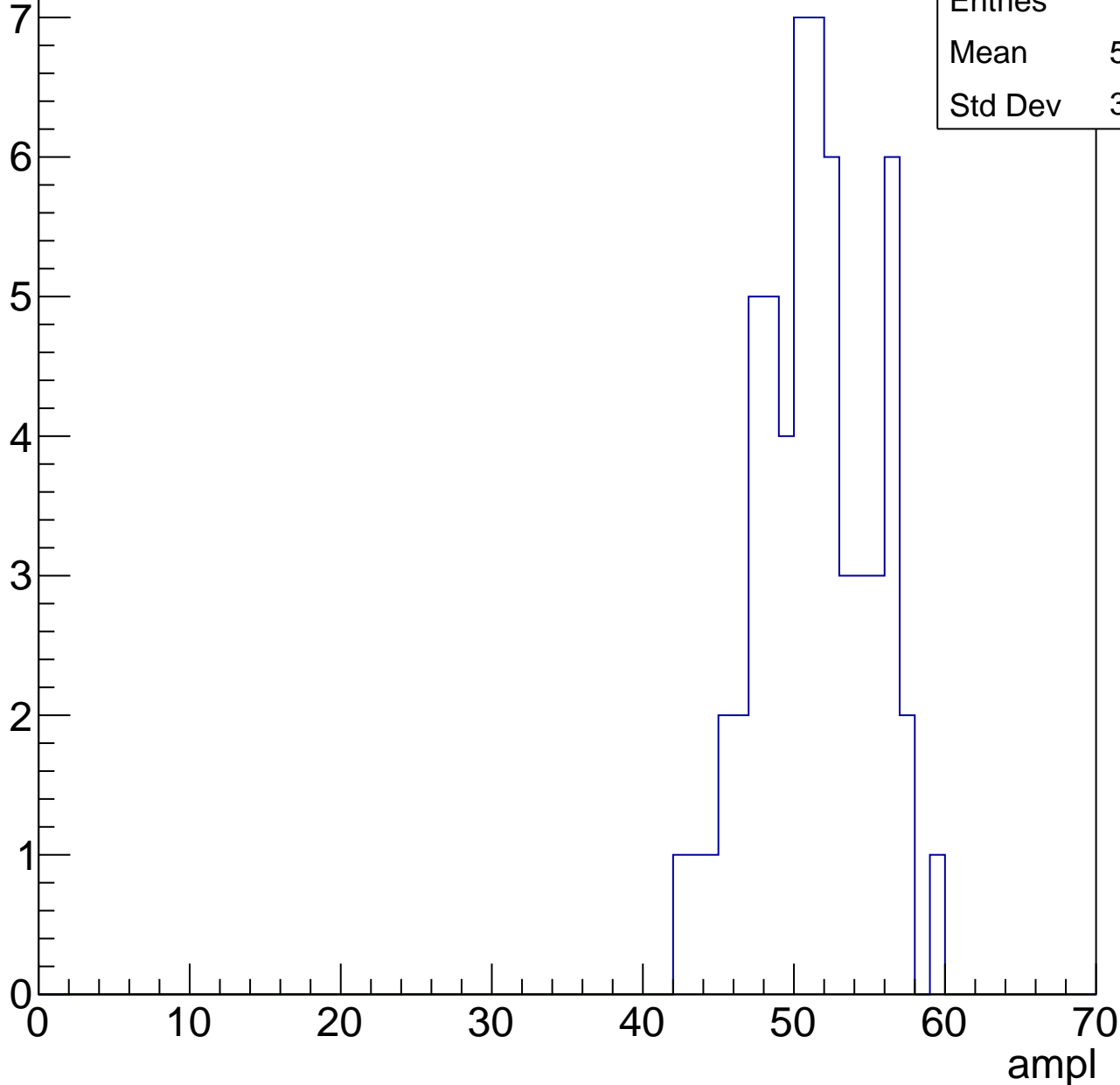


B0L001S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.78
Std Dev	3.774

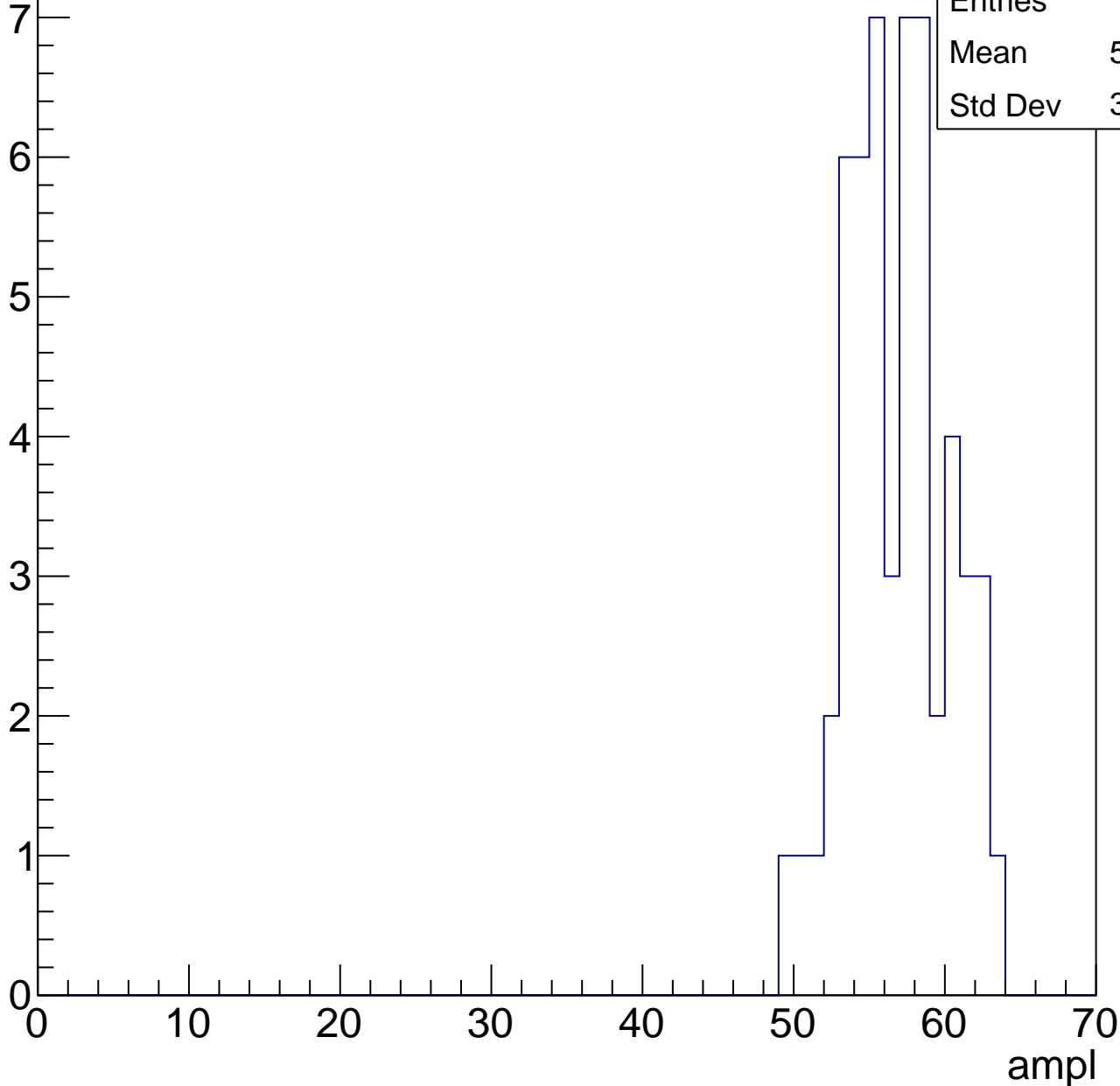


B0L001S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.37
Std Dev	3.245



B0L001S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries

39

Mean

58.85

Std Dev

9.773

0

0

10

20

30

40

50

60

70

ampl

1

2

3

4

5

6

7

B0L001S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch21, adc0

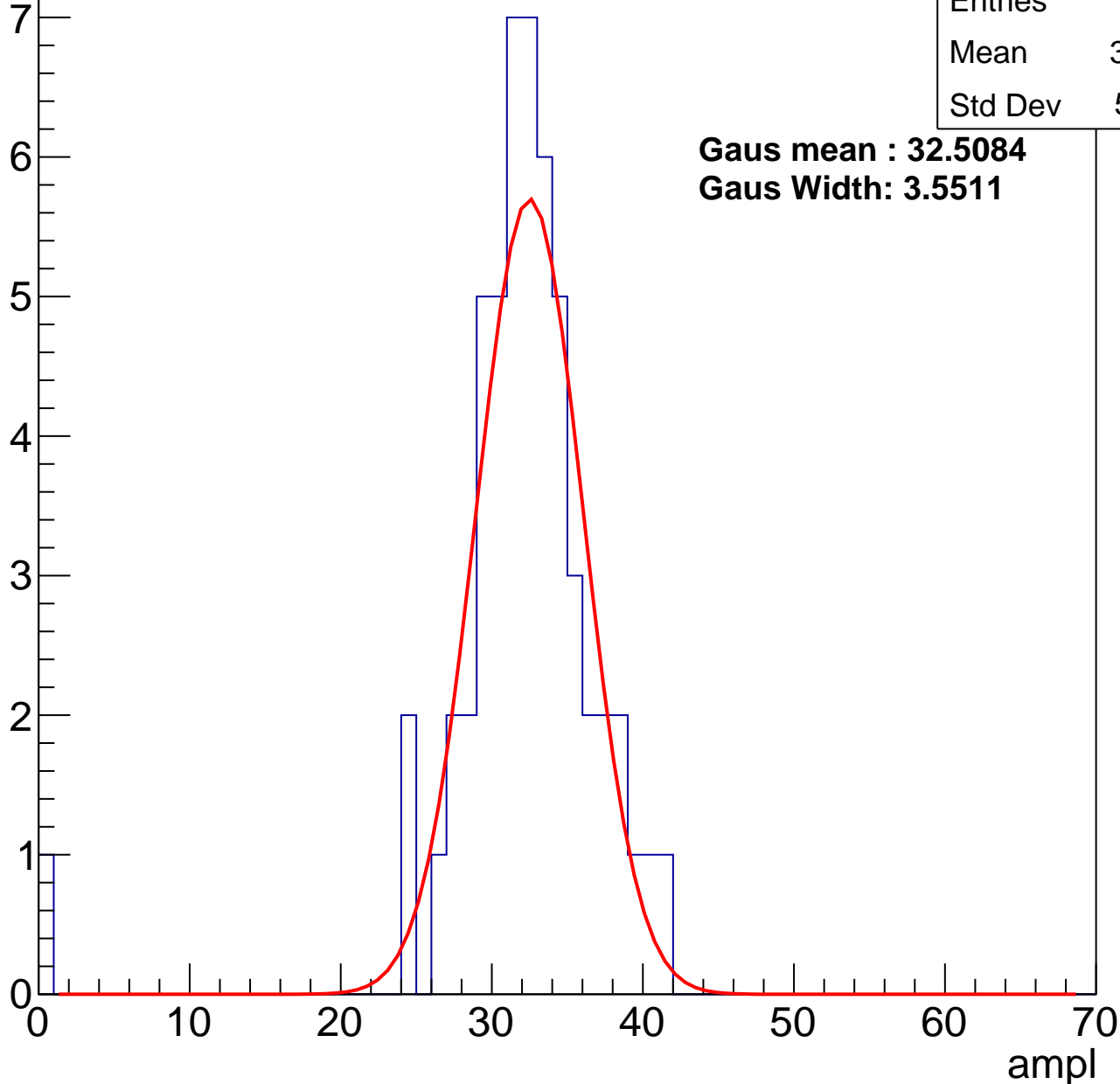
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.55
Std Dev	5.611

Gaus mean : 32.5084

Gaus Width: 3.5511



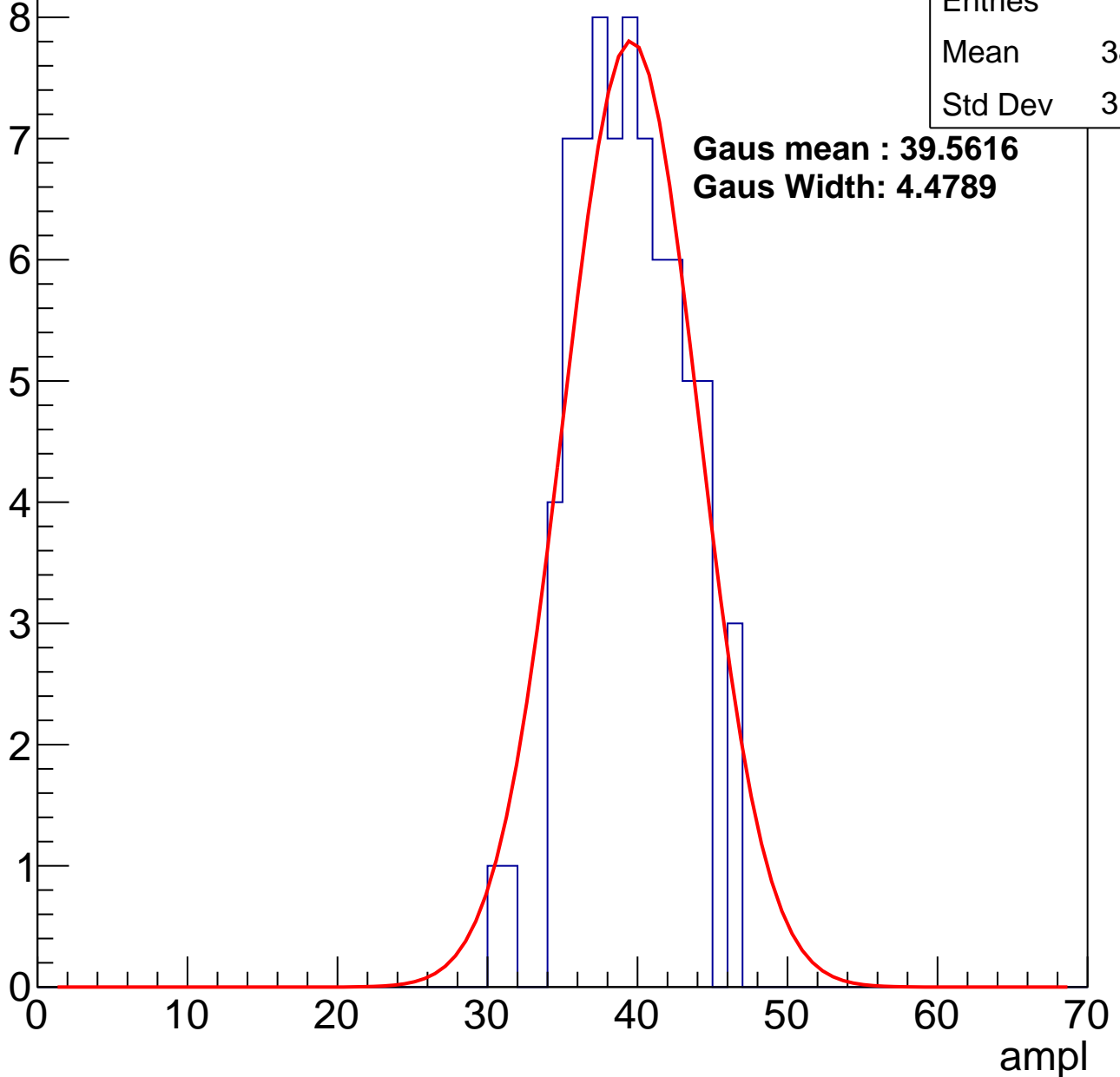
B0L001S, U15-ch21, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	38.92
Std Dev	3.455

Gaus mean : 39.5616
Gaus Width: 4.4789



B0L001S, U15-ch21, adc2

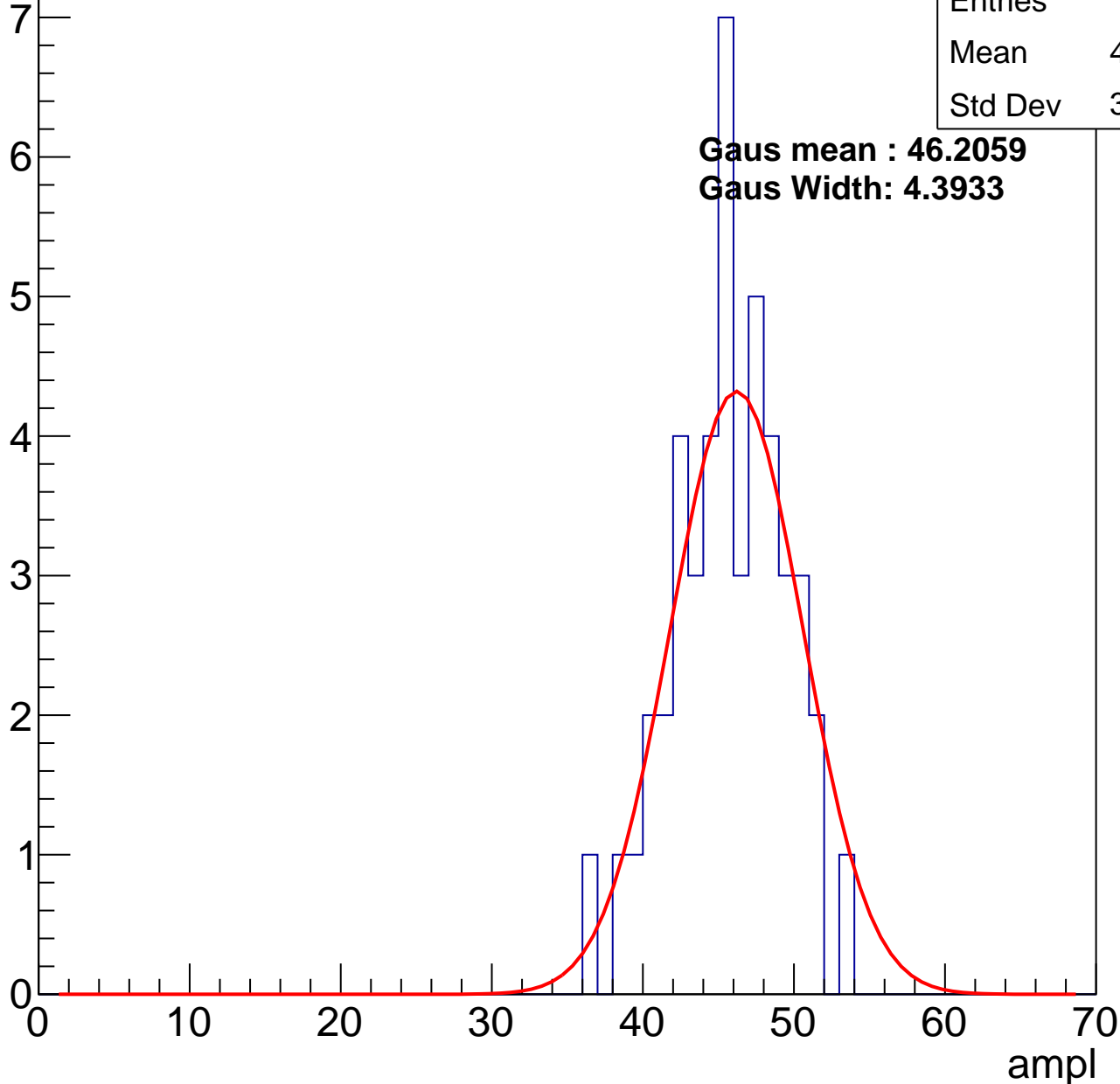
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	45.22
Std Dev	3.659

Gaus mean : 46.2059

Gaus Width: 4.3933

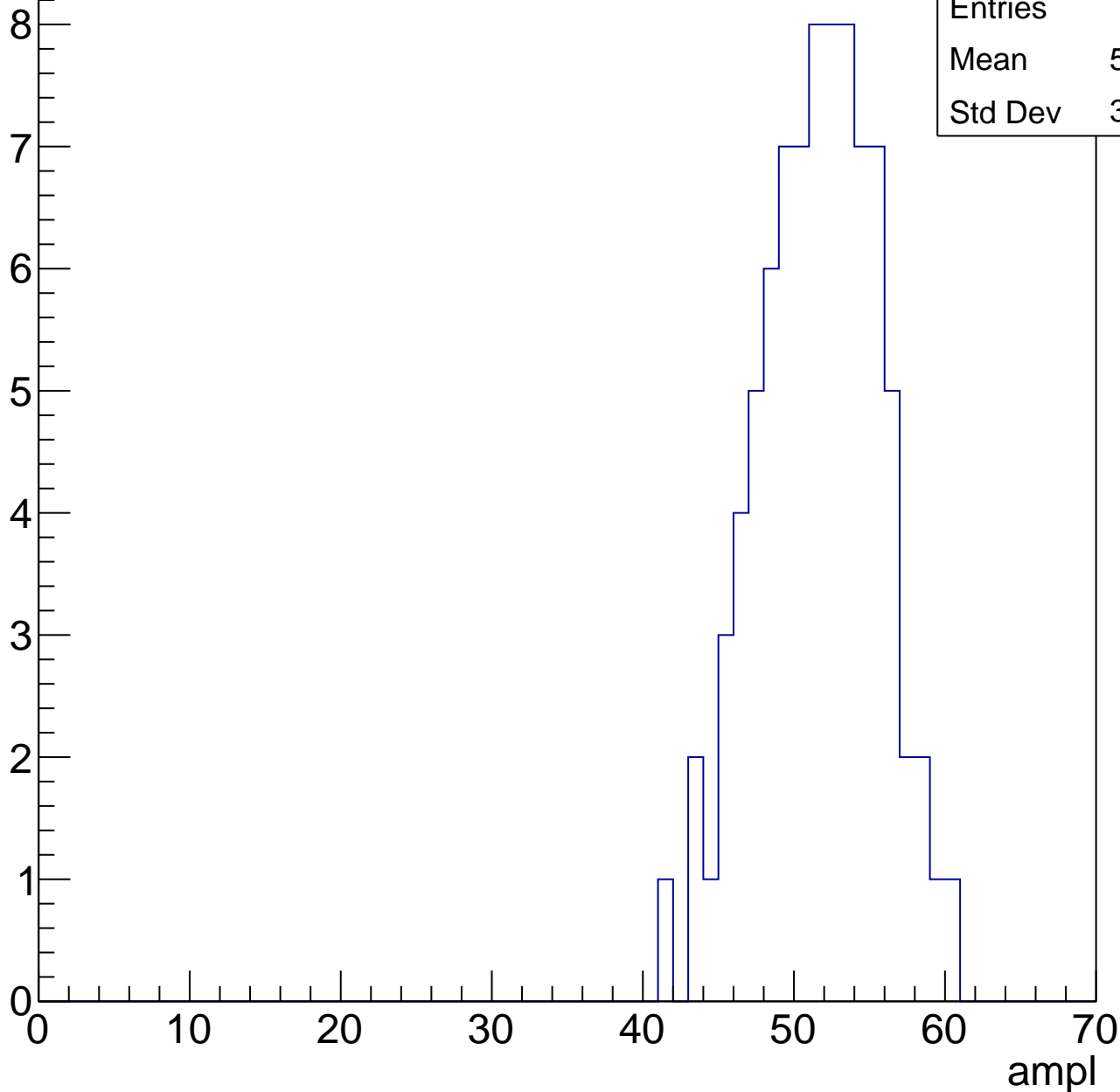


B0L001S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	51.13
Std Dev	3.922

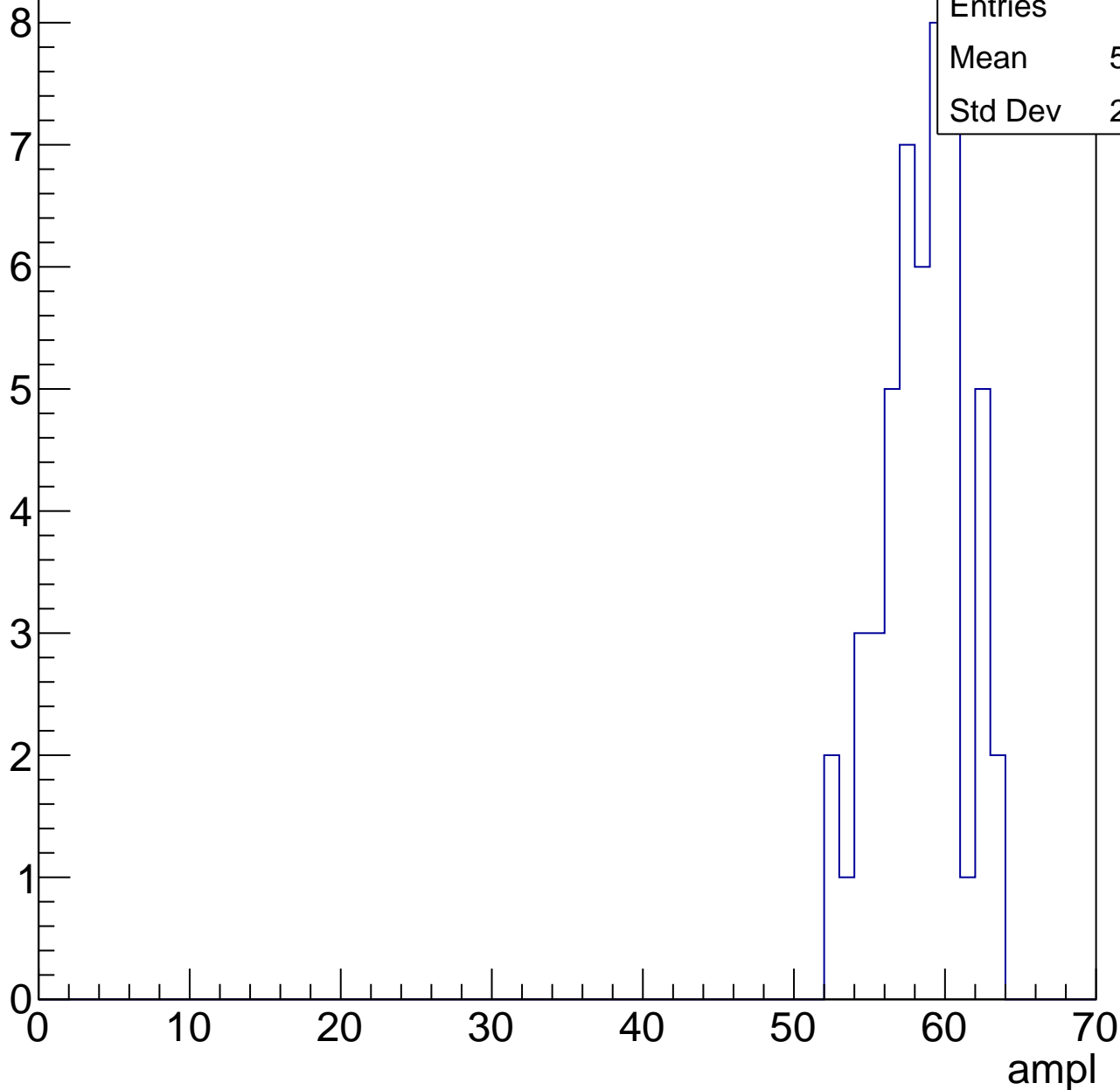


B0L001S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	58.04
Std Dev	2.722

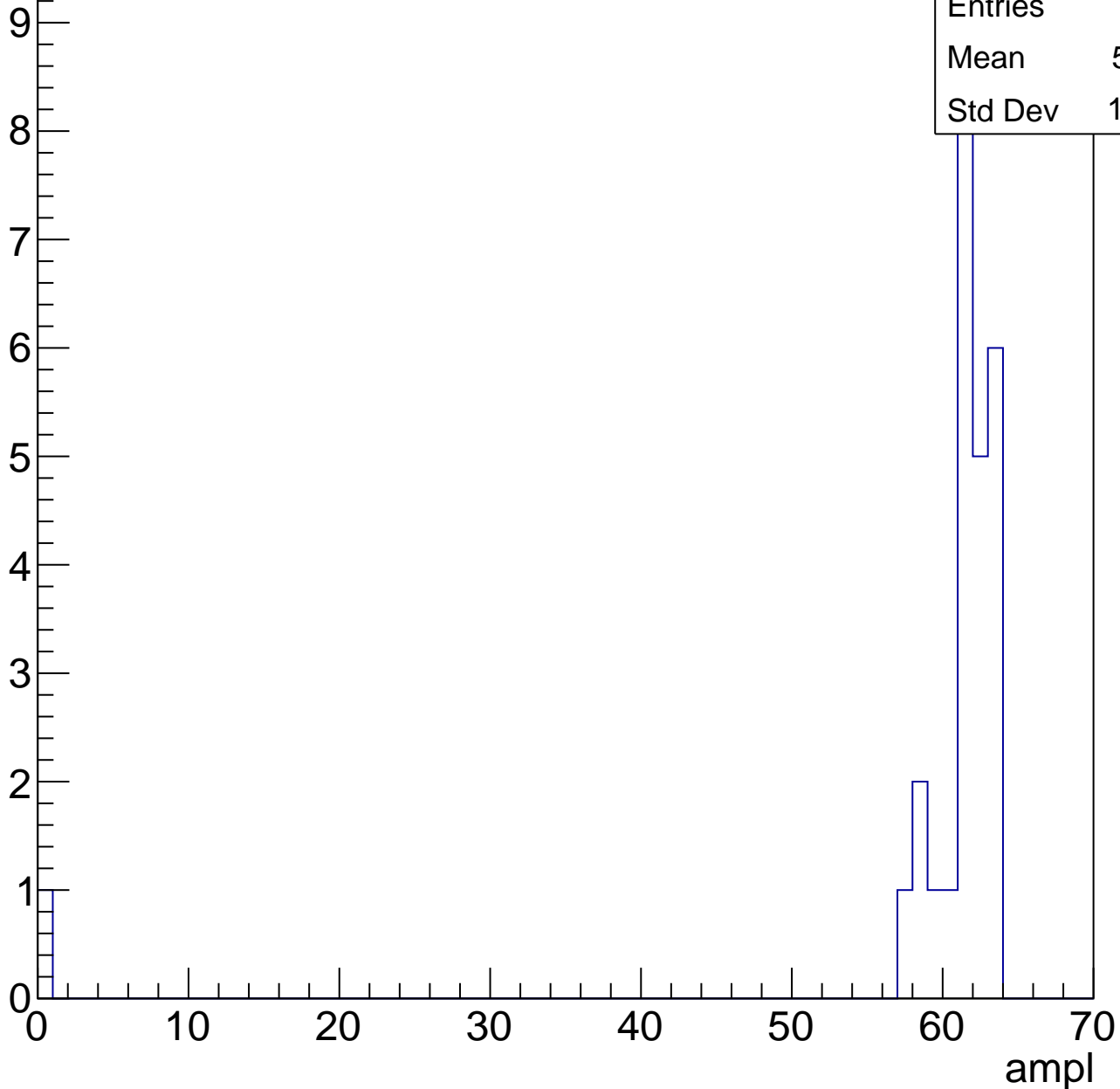


B0L001S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.81
Std Dev	11.87



B0L001S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch22, adc0

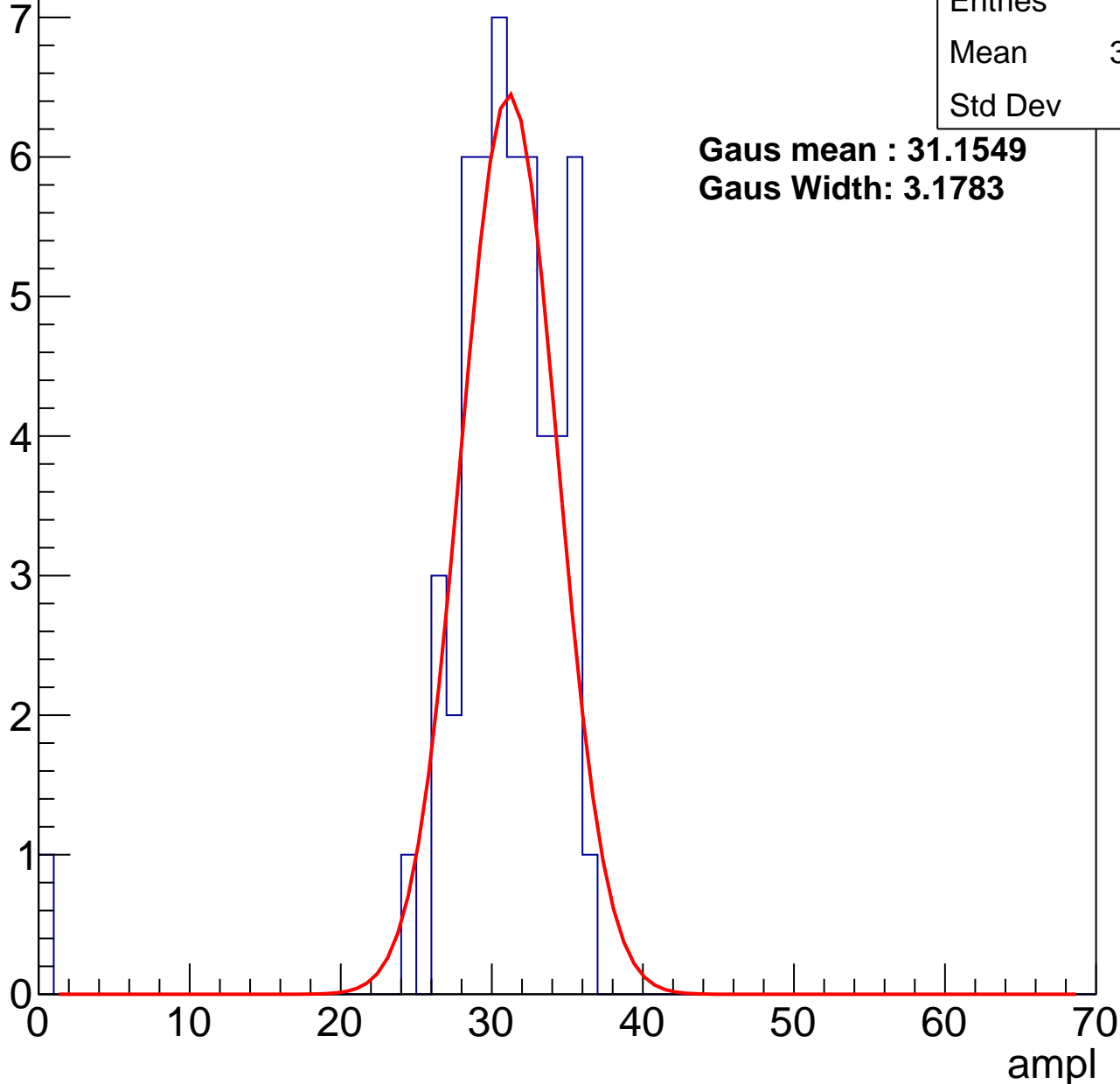
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.19
Std Dev	5.04

Gaus mean : 31.1549

Gaus Width: 3.1783

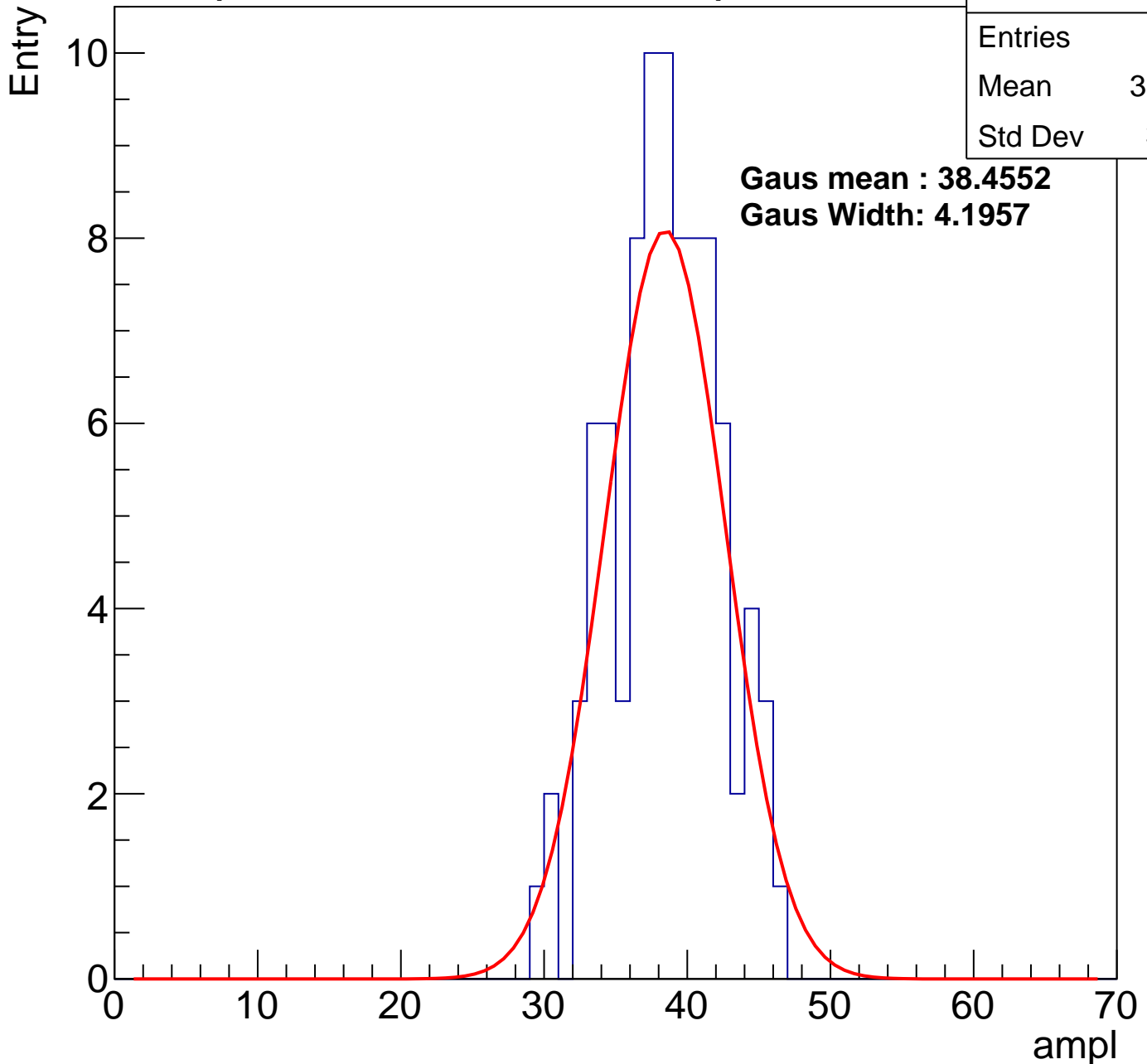


B0L001S, U15-ch22, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	89
Mean	38.03
Std Dev	3.74

Gaus mean : 38.4552
Gaus Width: 4.1957



B0L001S, U15-ch22, adc2

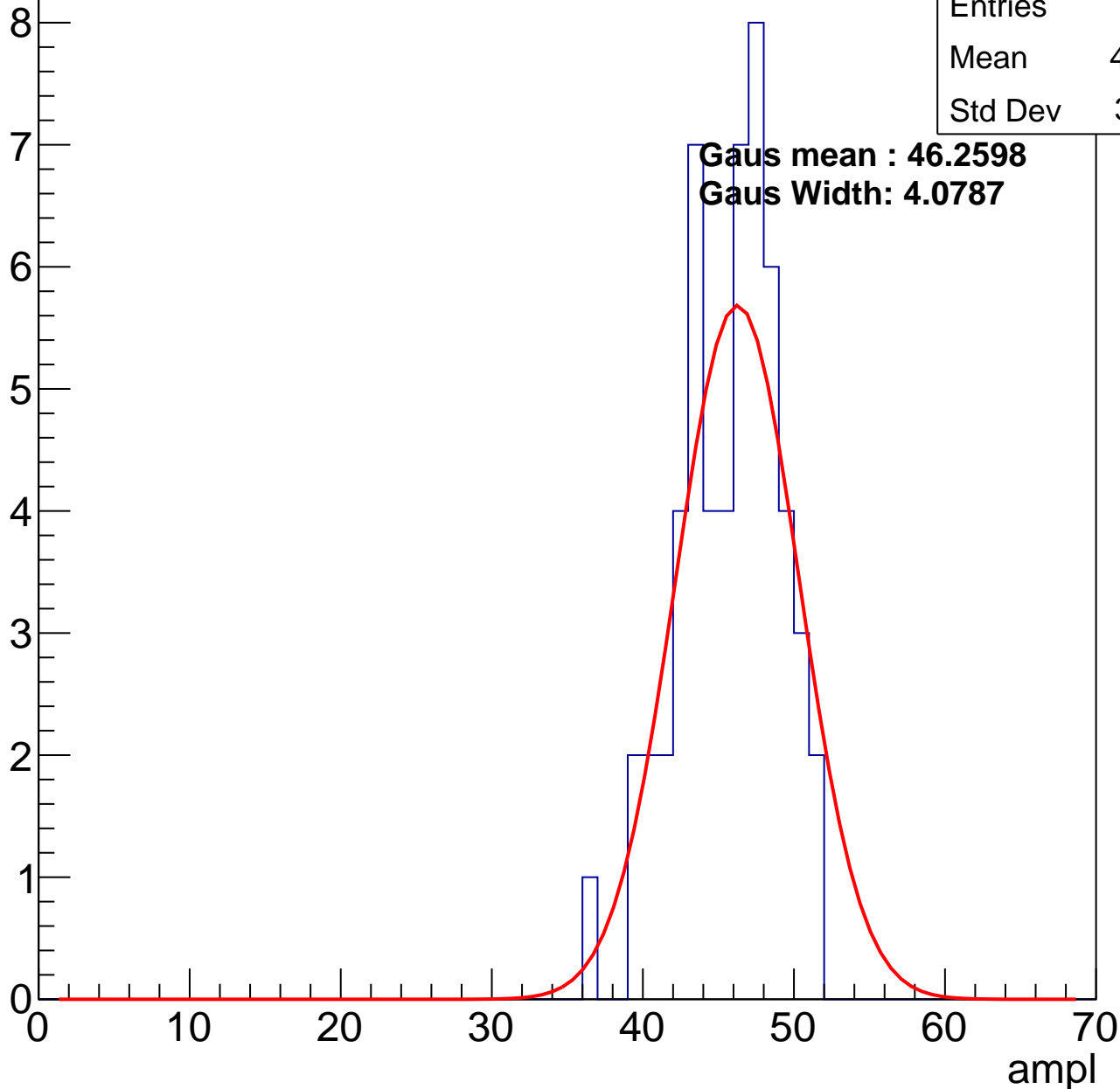
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	45.27
Std Dev	3.281

Gaus mean : 46.2598

Gaus Width: 4.0787

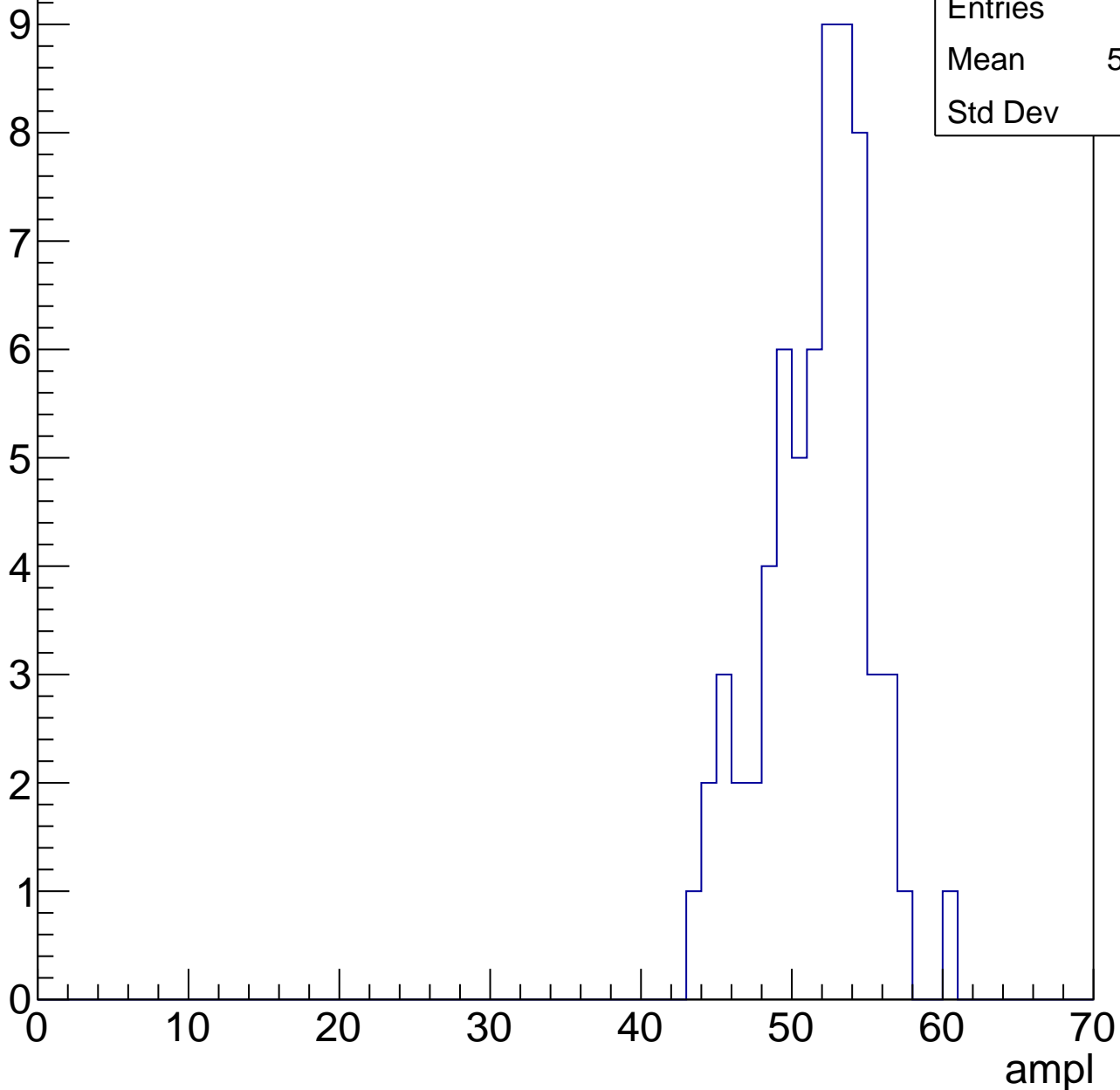


B0L001S, U15-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.09
Std Dev	3.45

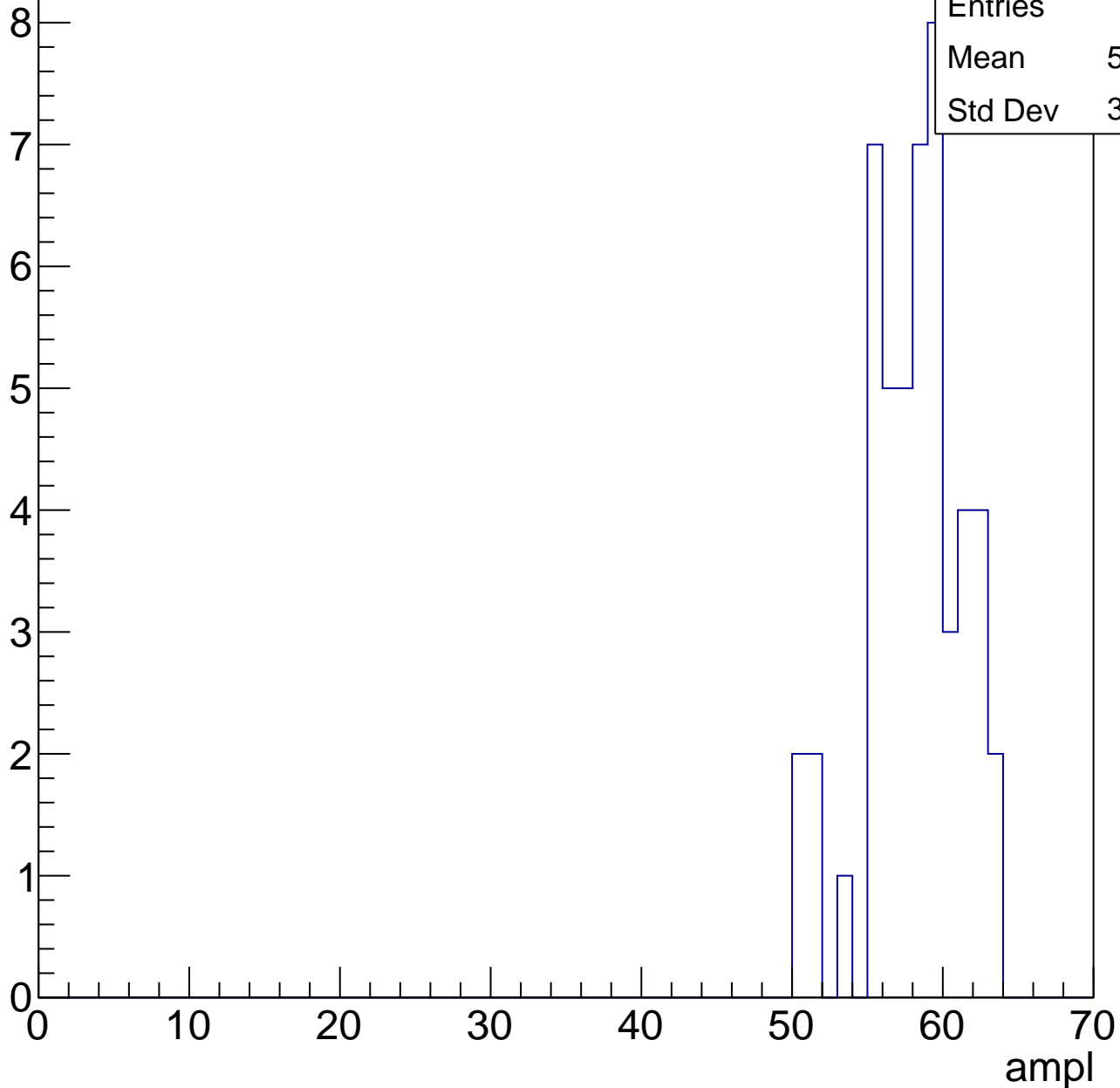


B0L001S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.62
Std Dev	3.168

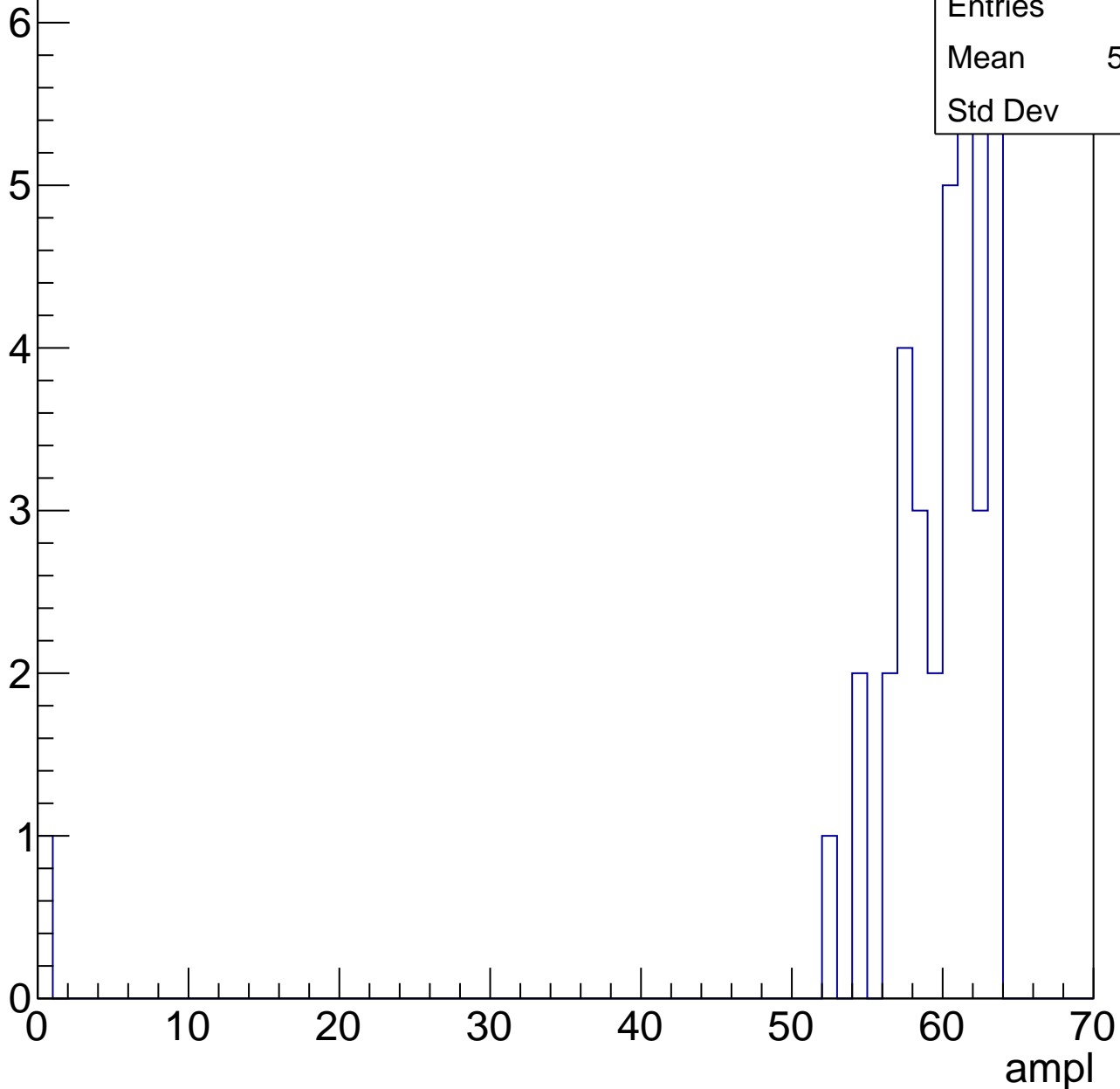


B0L001S, U15-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	57.77
Std Dev	10.3



B0L001S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

6

Mean

62

Std Dev

1

Entries	6
Mean	62
Std Dev	1

B0L001S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch23, adc0

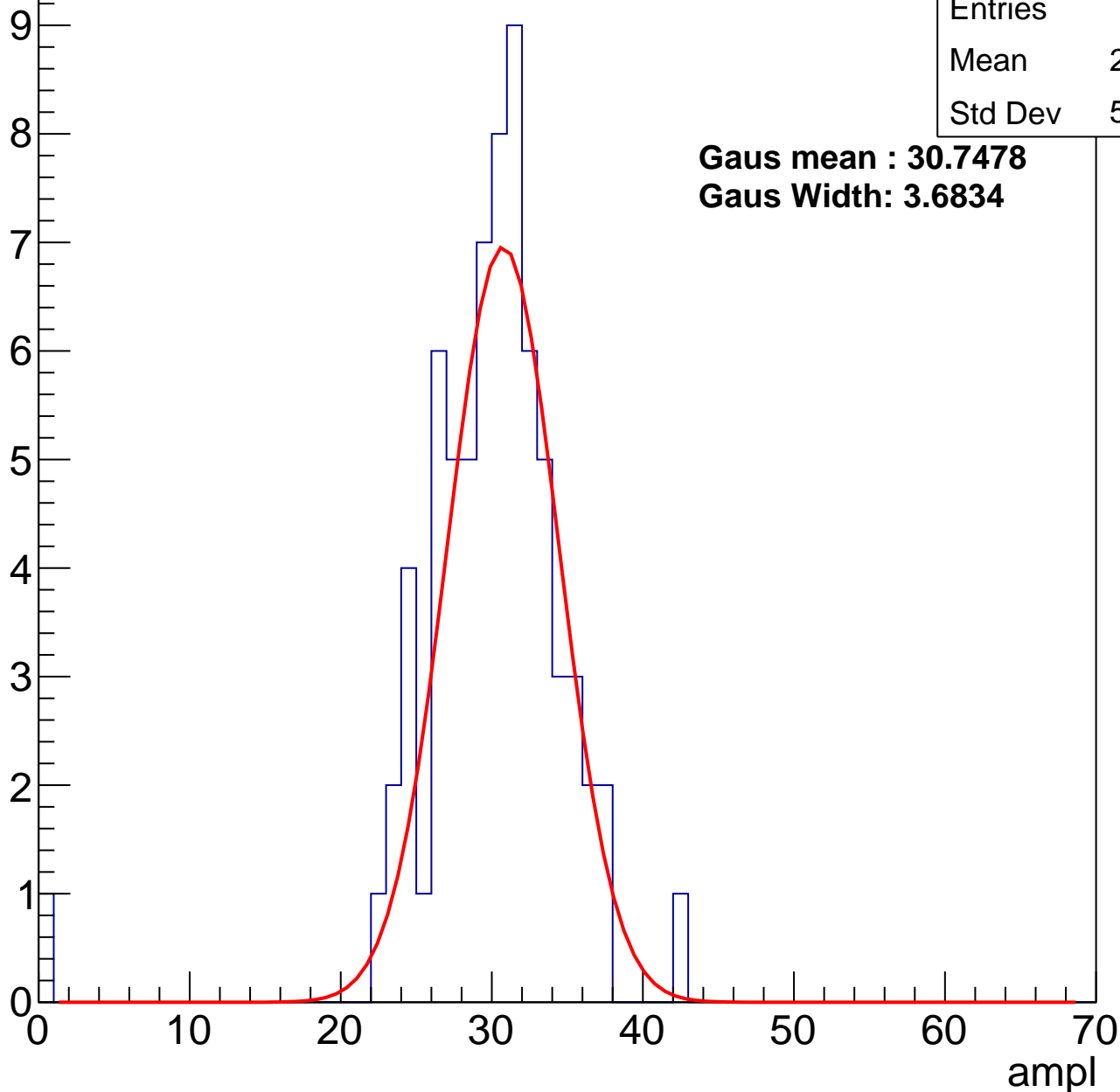
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.49
Std Dev	5.167

Gaus mean : 30.7478

Gaus Width: 3.6834

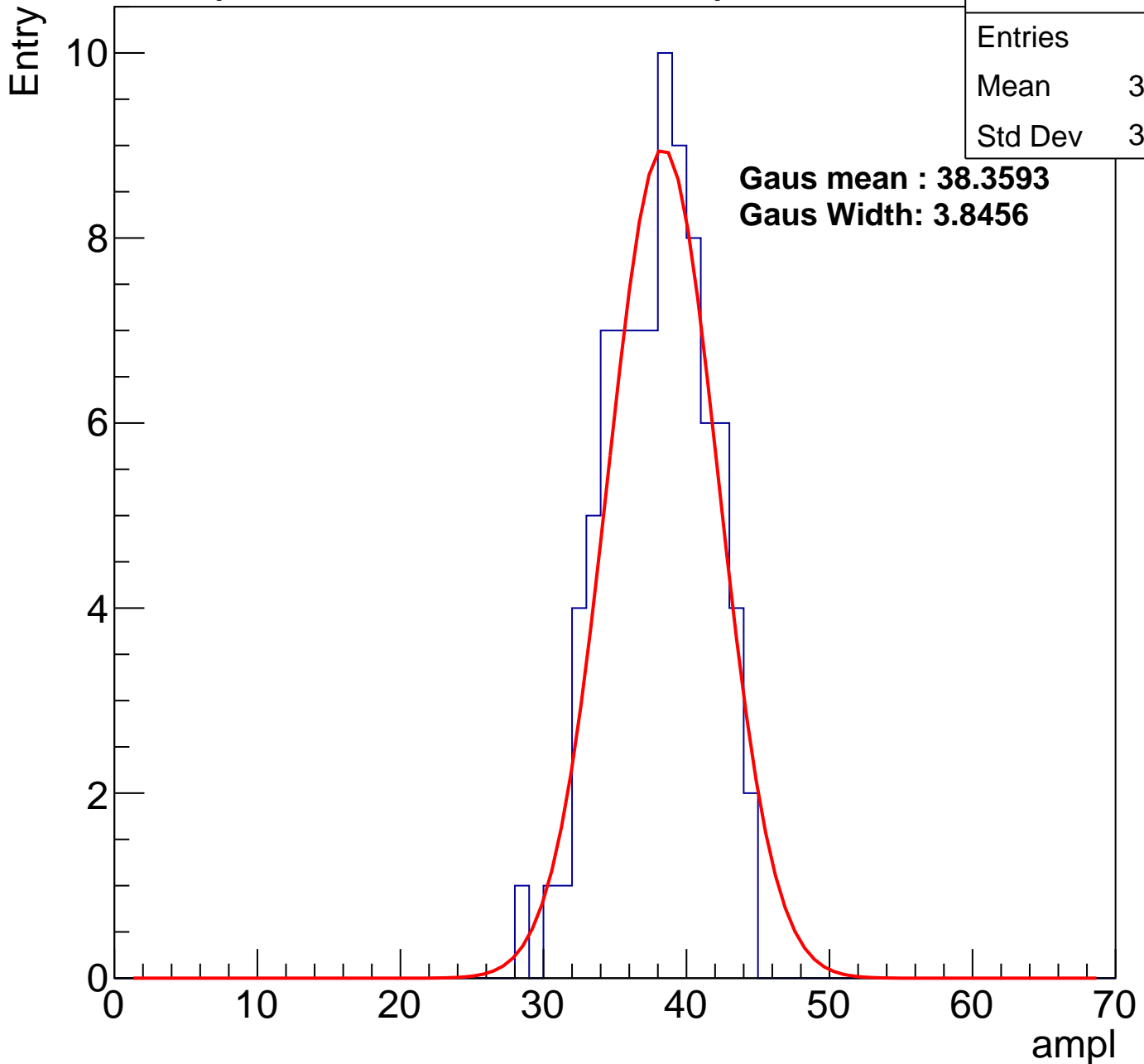


B0L001S, U15-ch23, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	37.47
Std Dev	3.483

Gaus mean : 38.3593
Gaus Width: 3.8456



B0L001S, U15-ch23, adc2

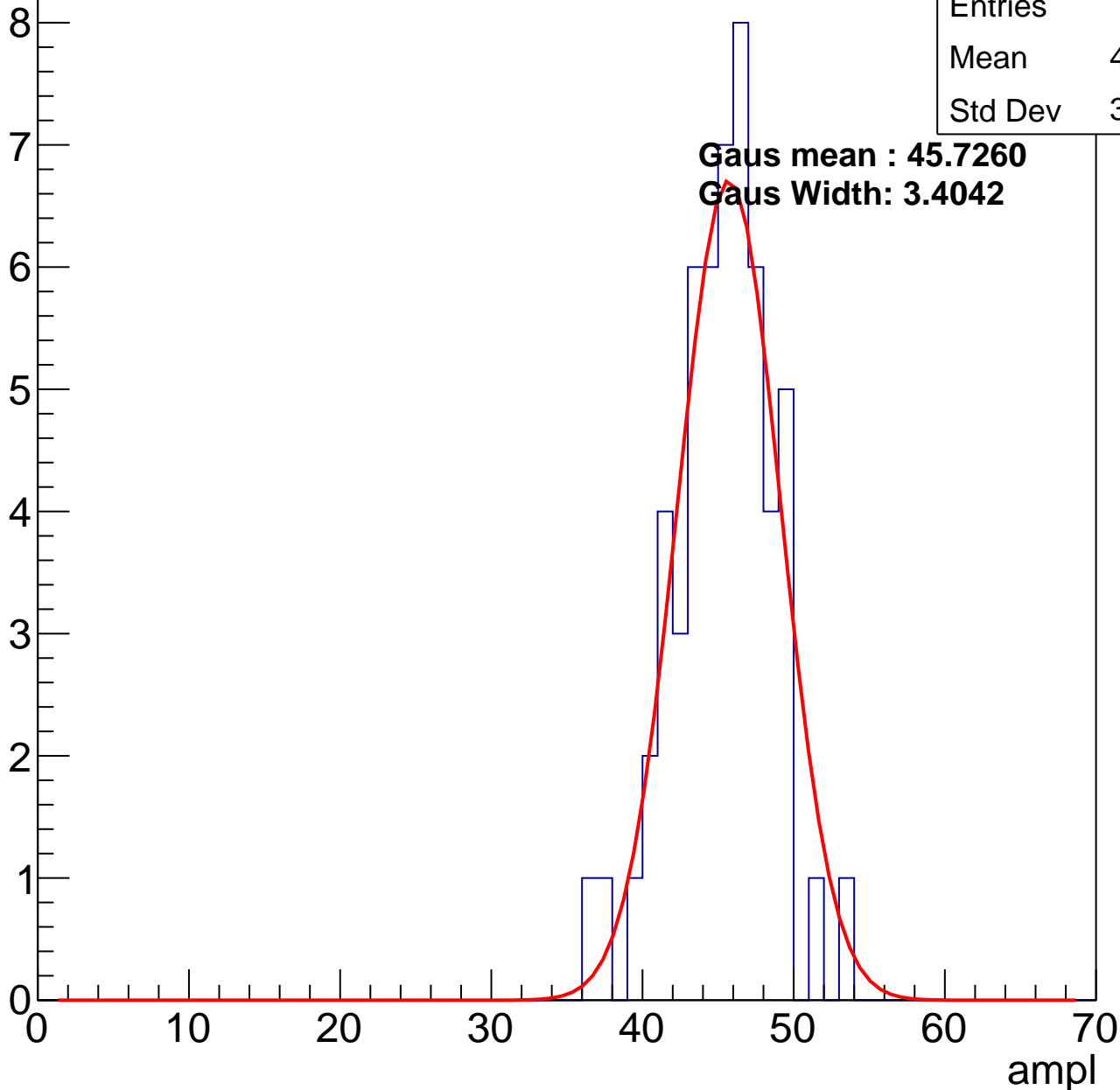
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.82
Std Dev	3.268

Gaus mean : 45.7260

Gaus Width: 3.4042



B0L001S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	50.3
Std Dev	3.069

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

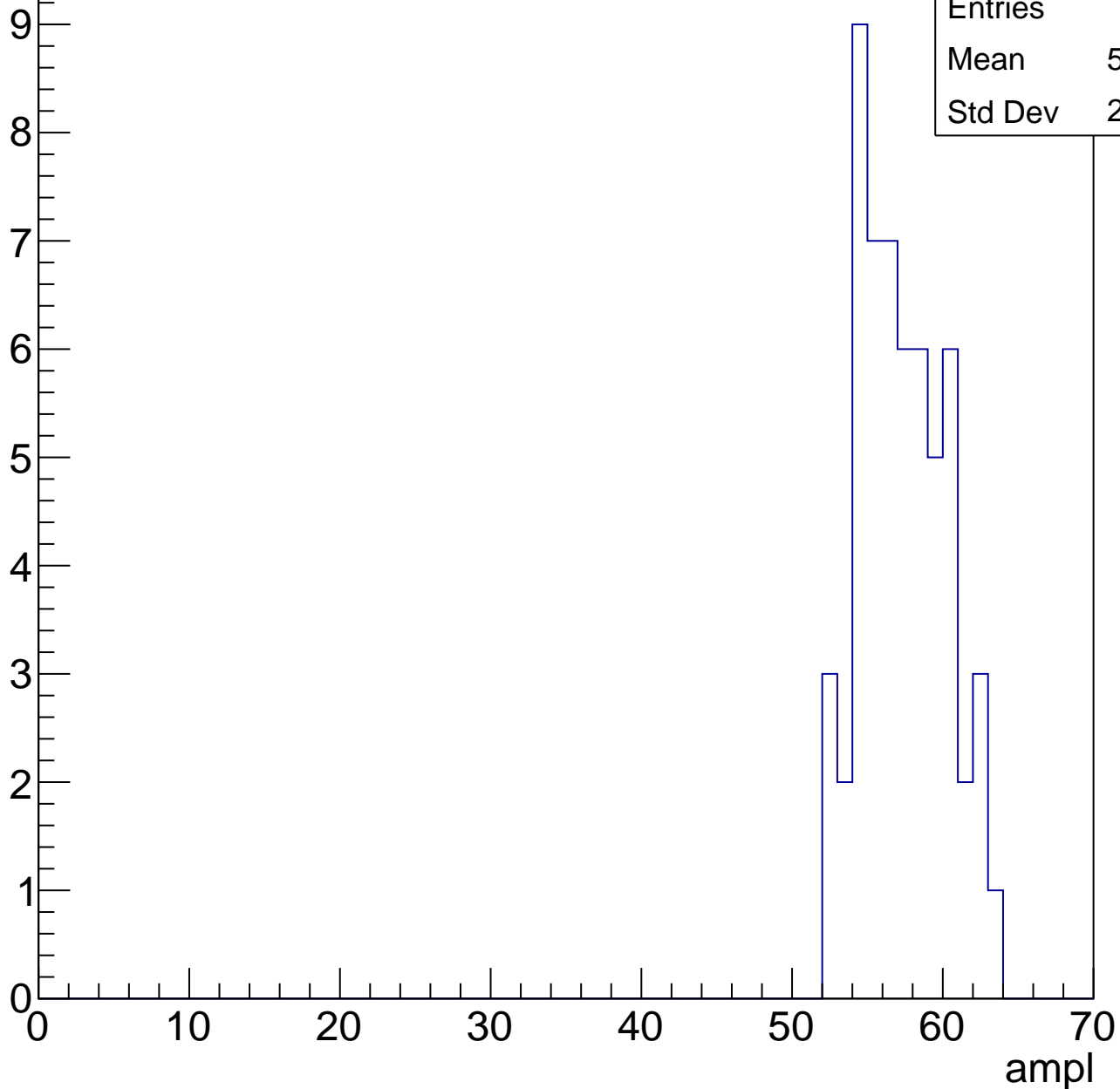
10

B0L001S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.86
Std Dev	2.794

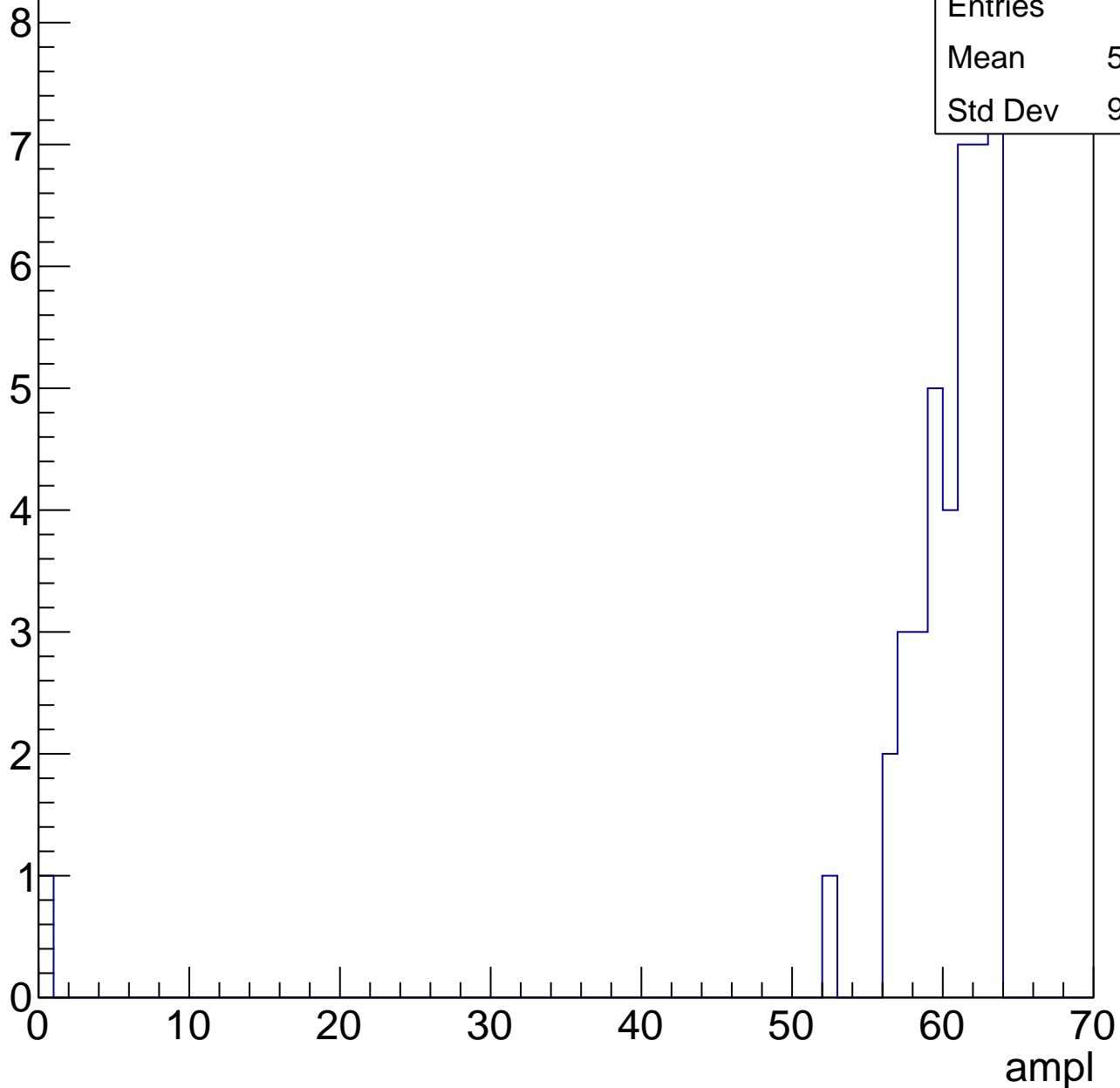


B0L001S, U15-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.76
Std Dev	9.606



B0L001S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch24, adc0

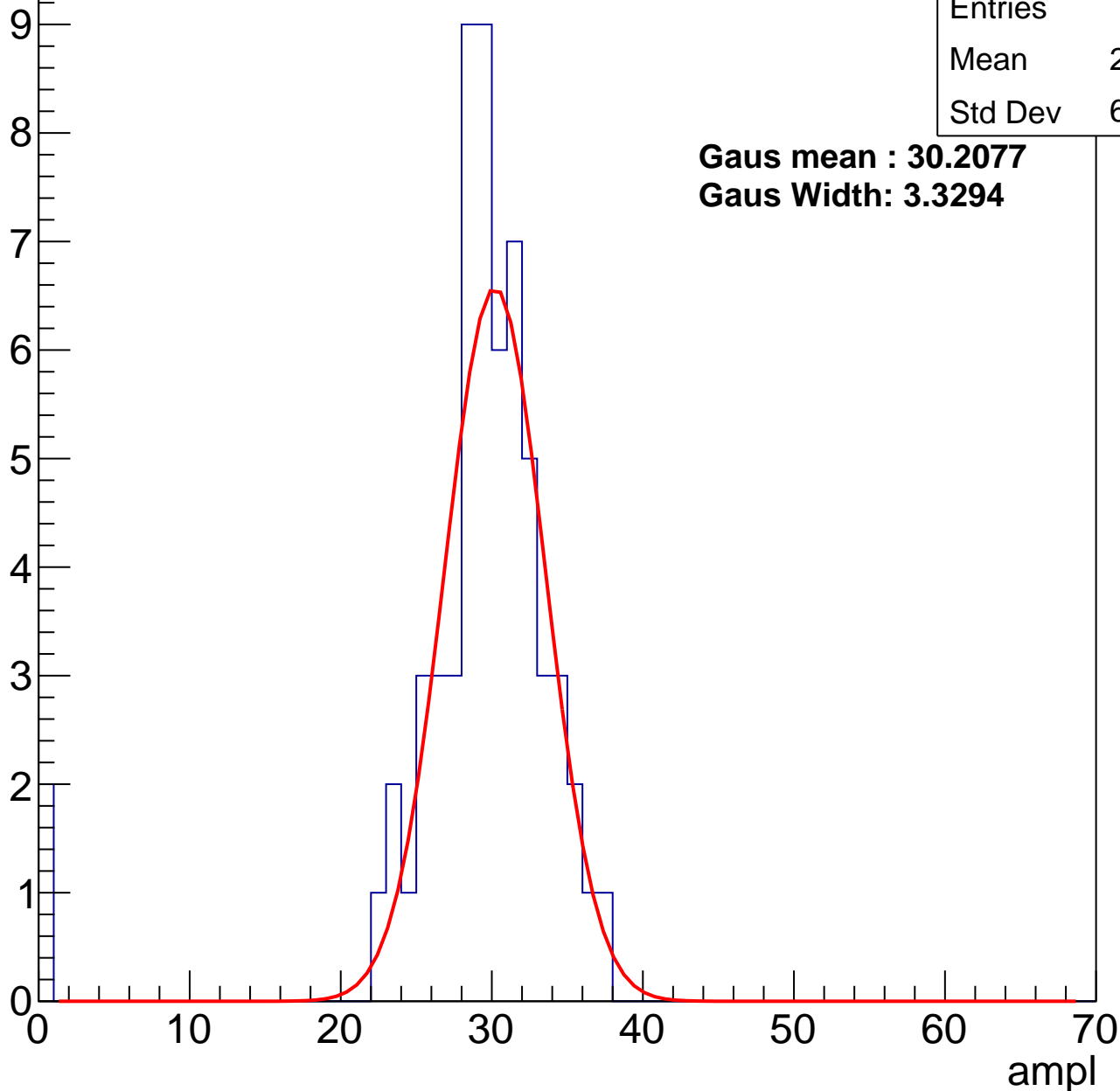
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.52
Std Dev	6.137

Gaus mean : 30.2077

Gaus Width: 3.3294



B0L001S, U15-ch24, adc1

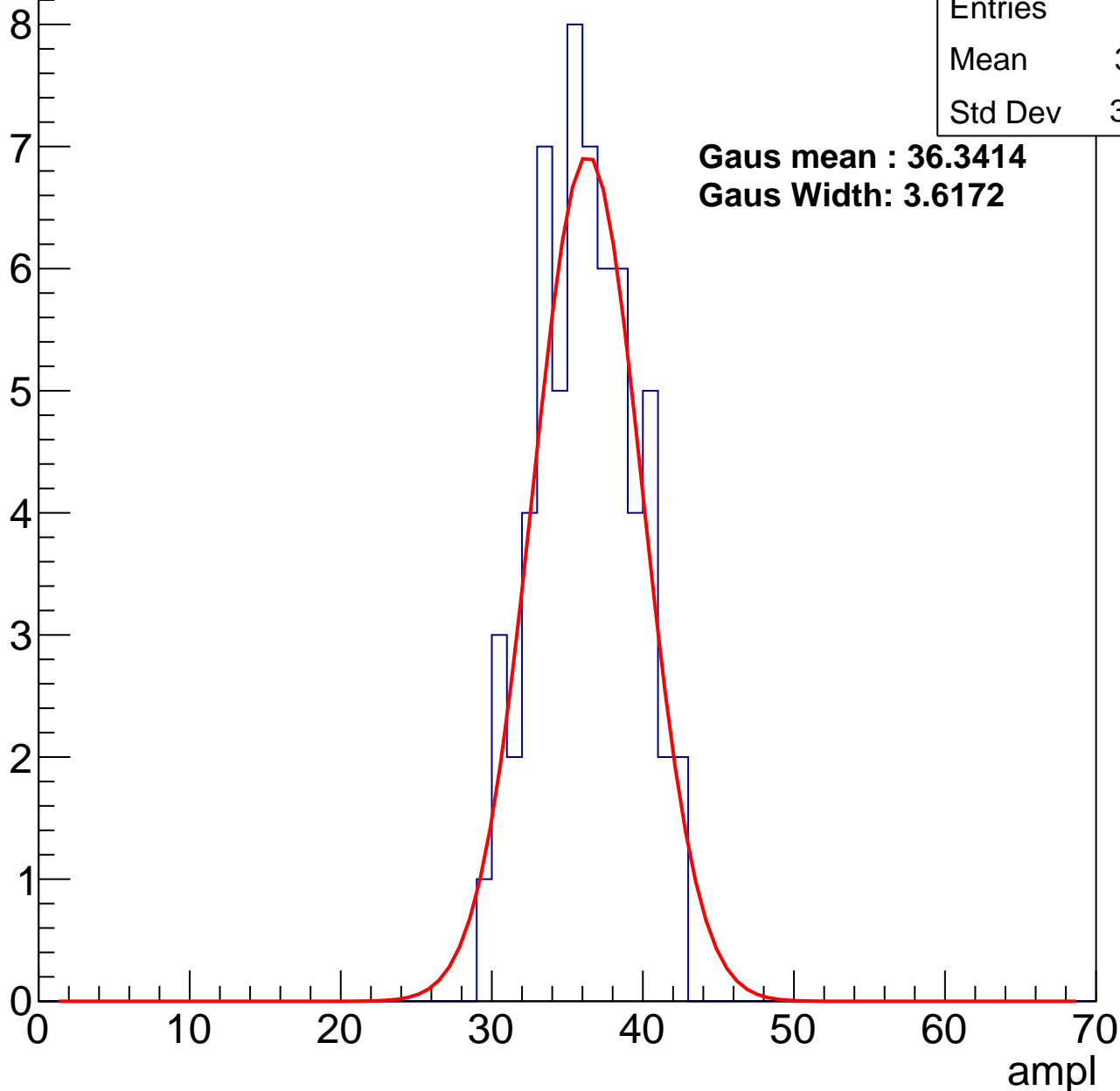
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	35.71
Std Dev	3.174

Gaus mean : 36.3414

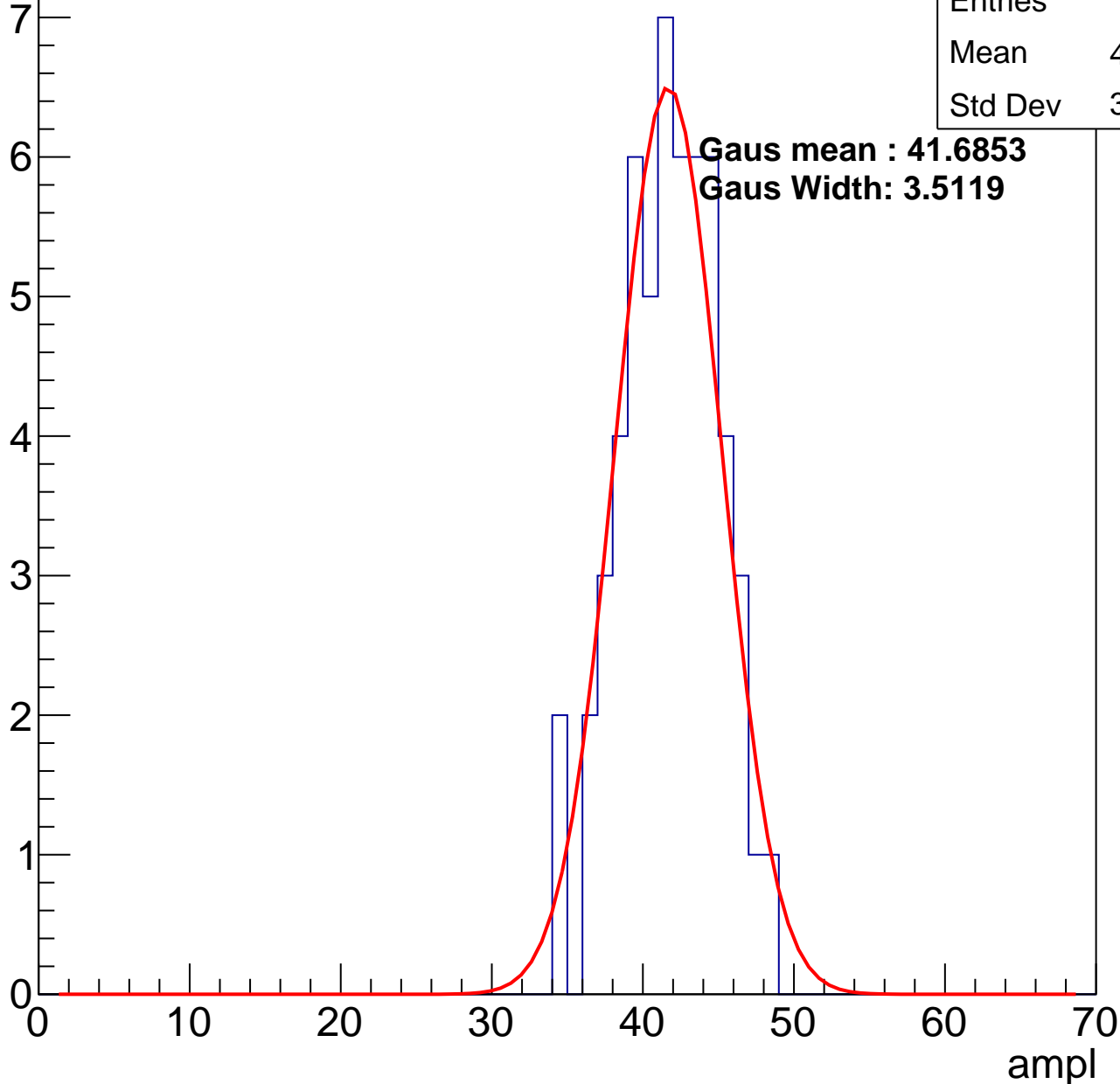
Gaus Width: 3.6172



B0L001S, U15-ch24, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

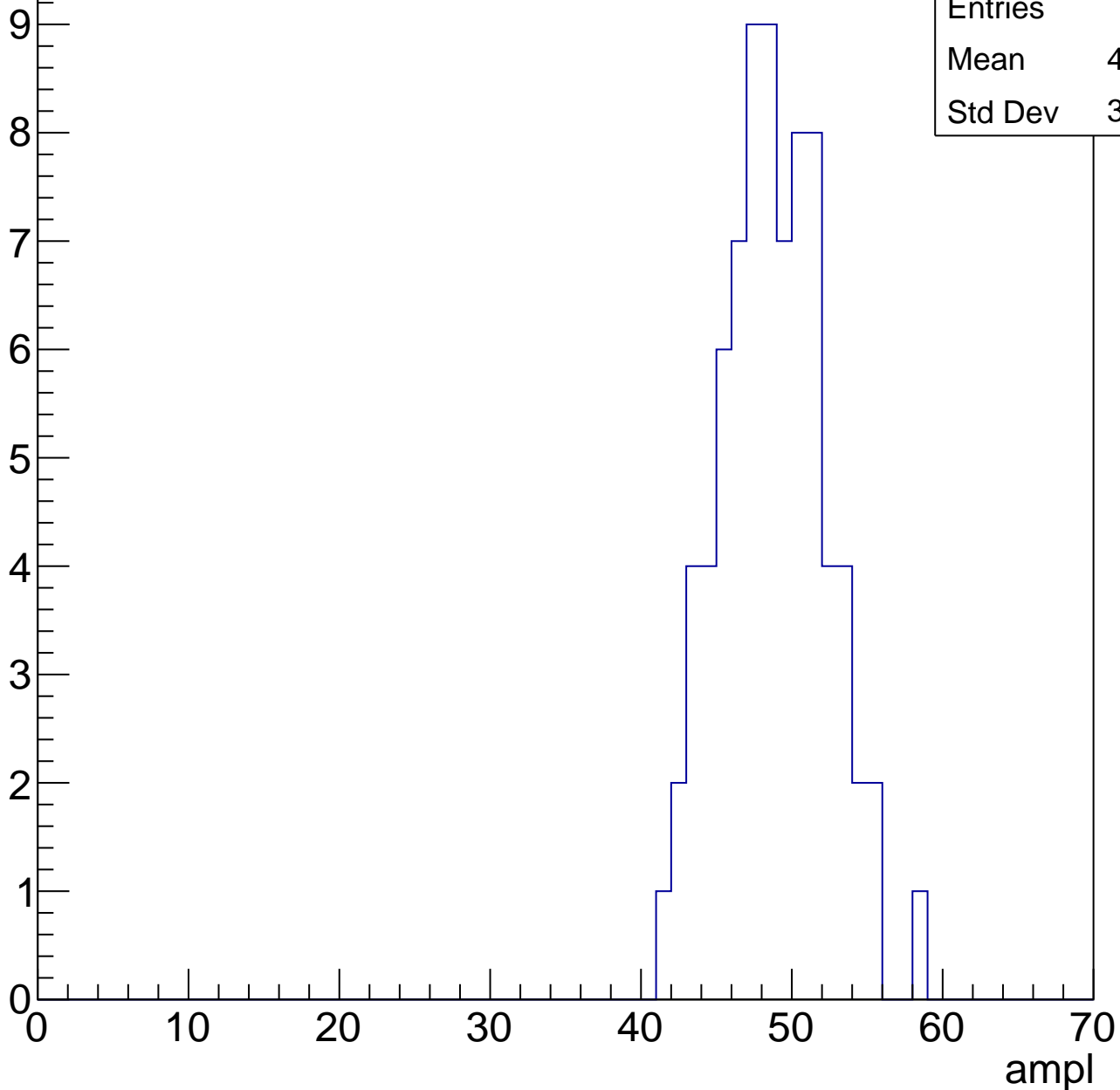


B0L001S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	48.29
Std Dev	3.424

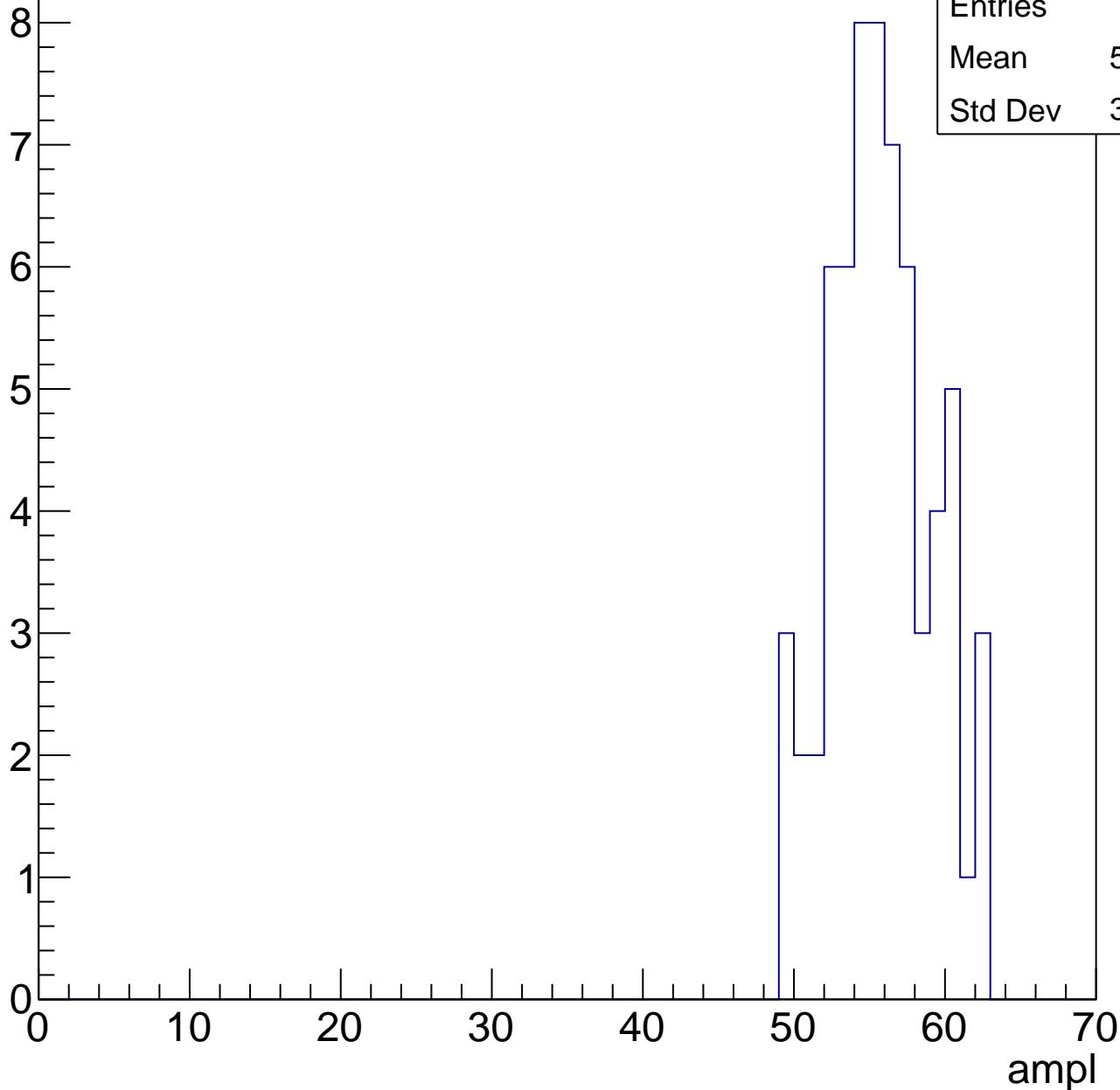


B0L001S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.34
Std Dev	3.303

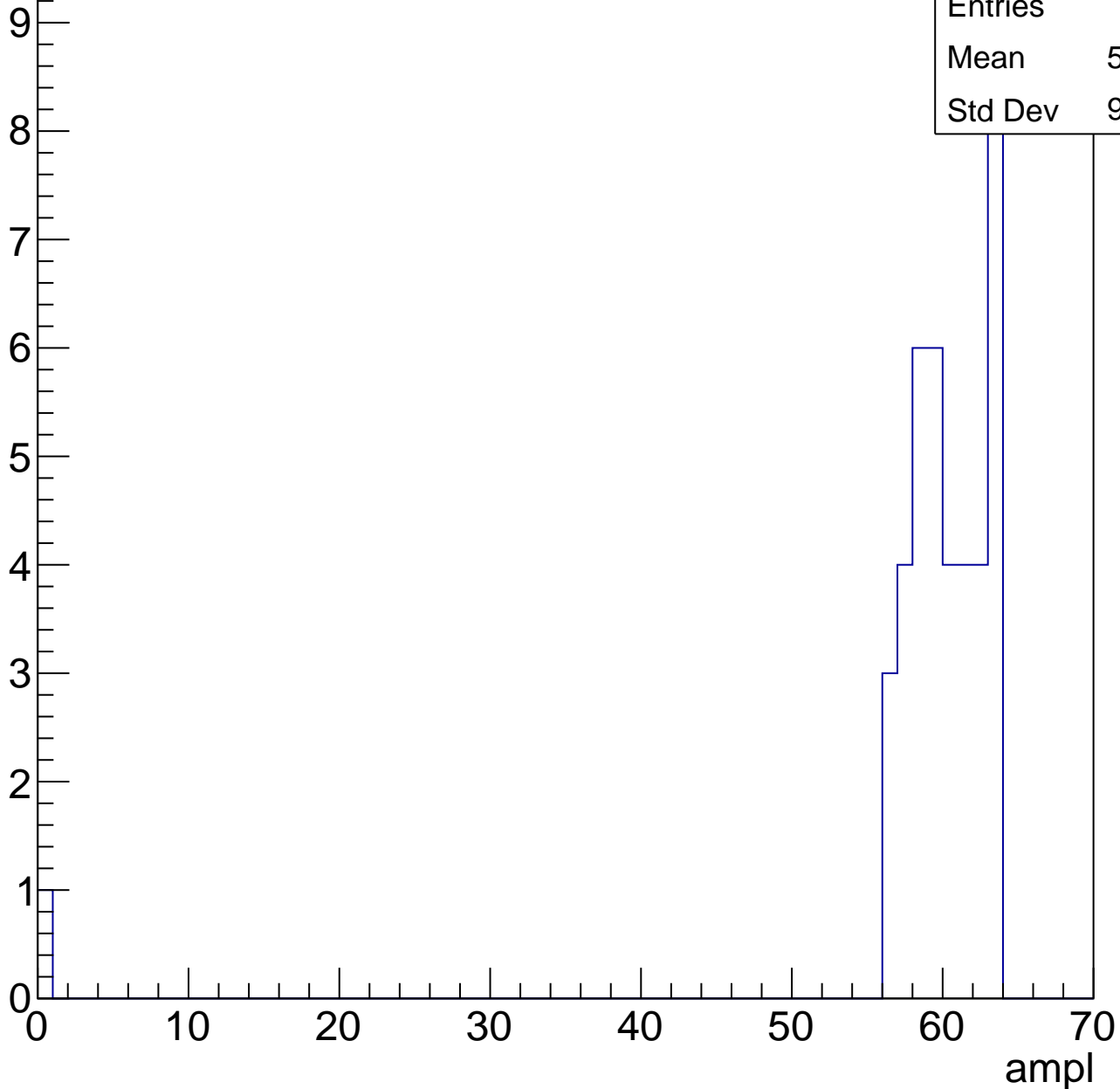


B0L001S, U15-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

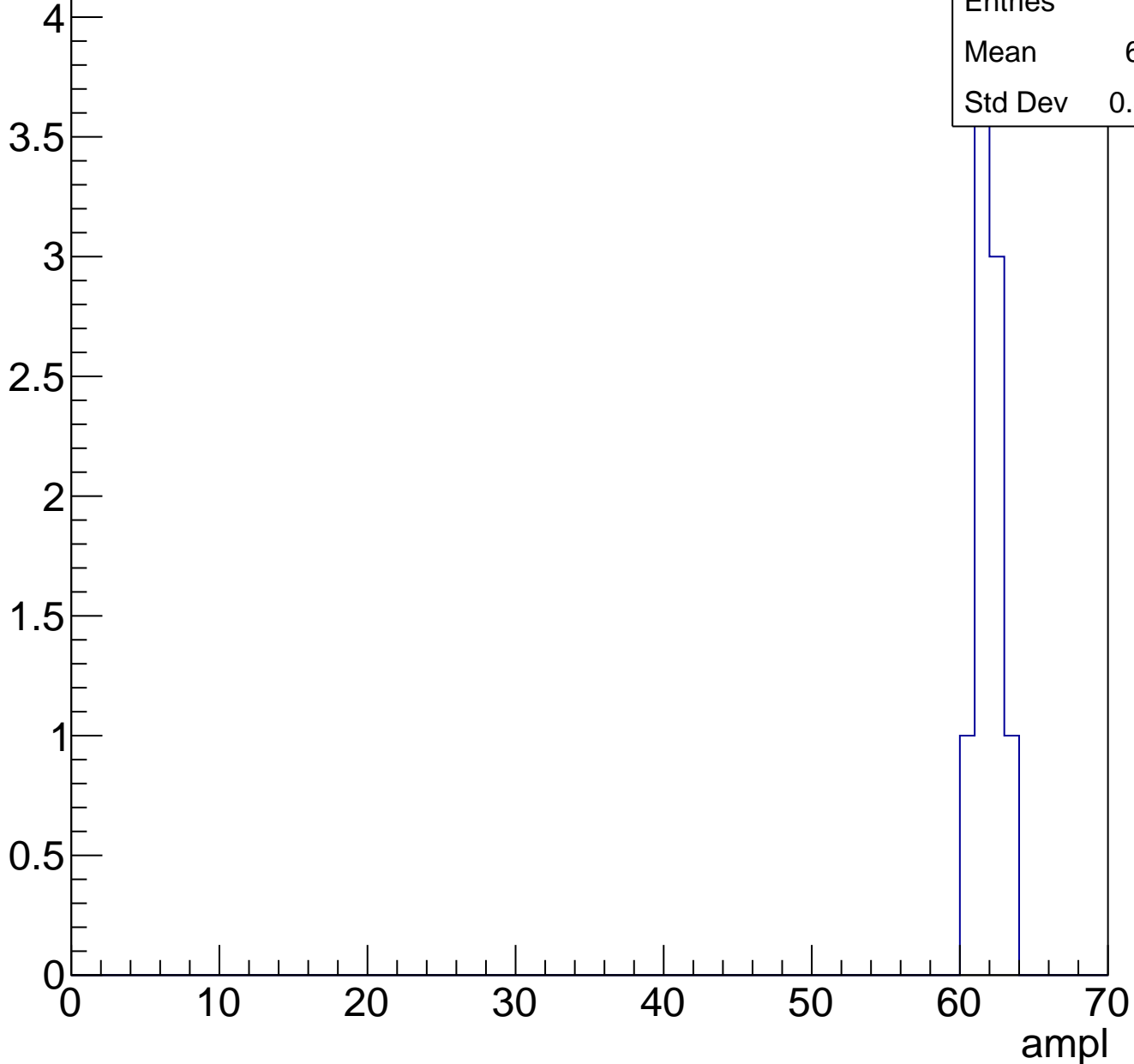
Entries	41
Mean	58.46
Std Dev	9.523



B0L001S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

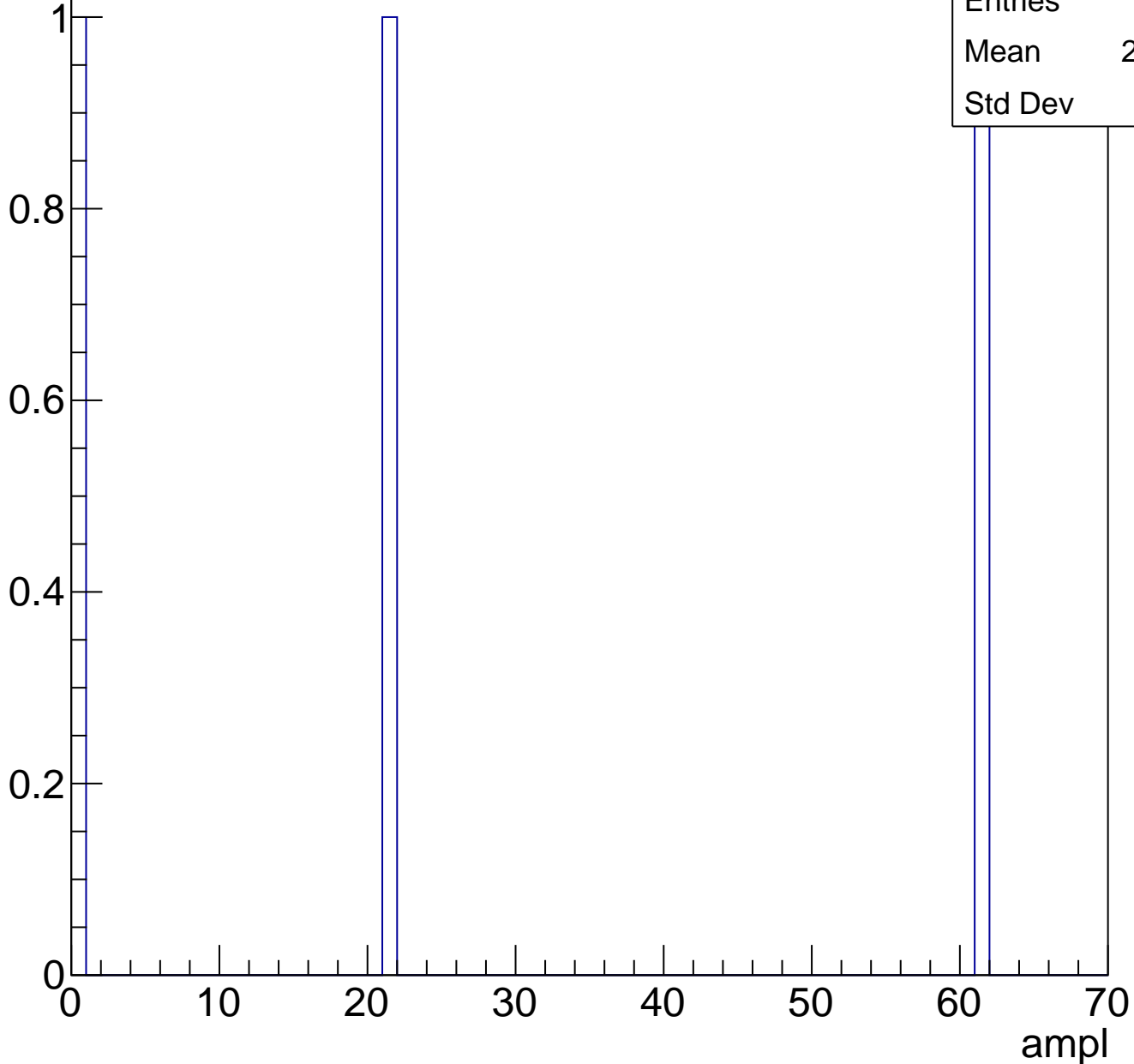
Entry



B0L001S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	27.33
Std Dev	25.3

B0L001S, U15-ch25, adc0

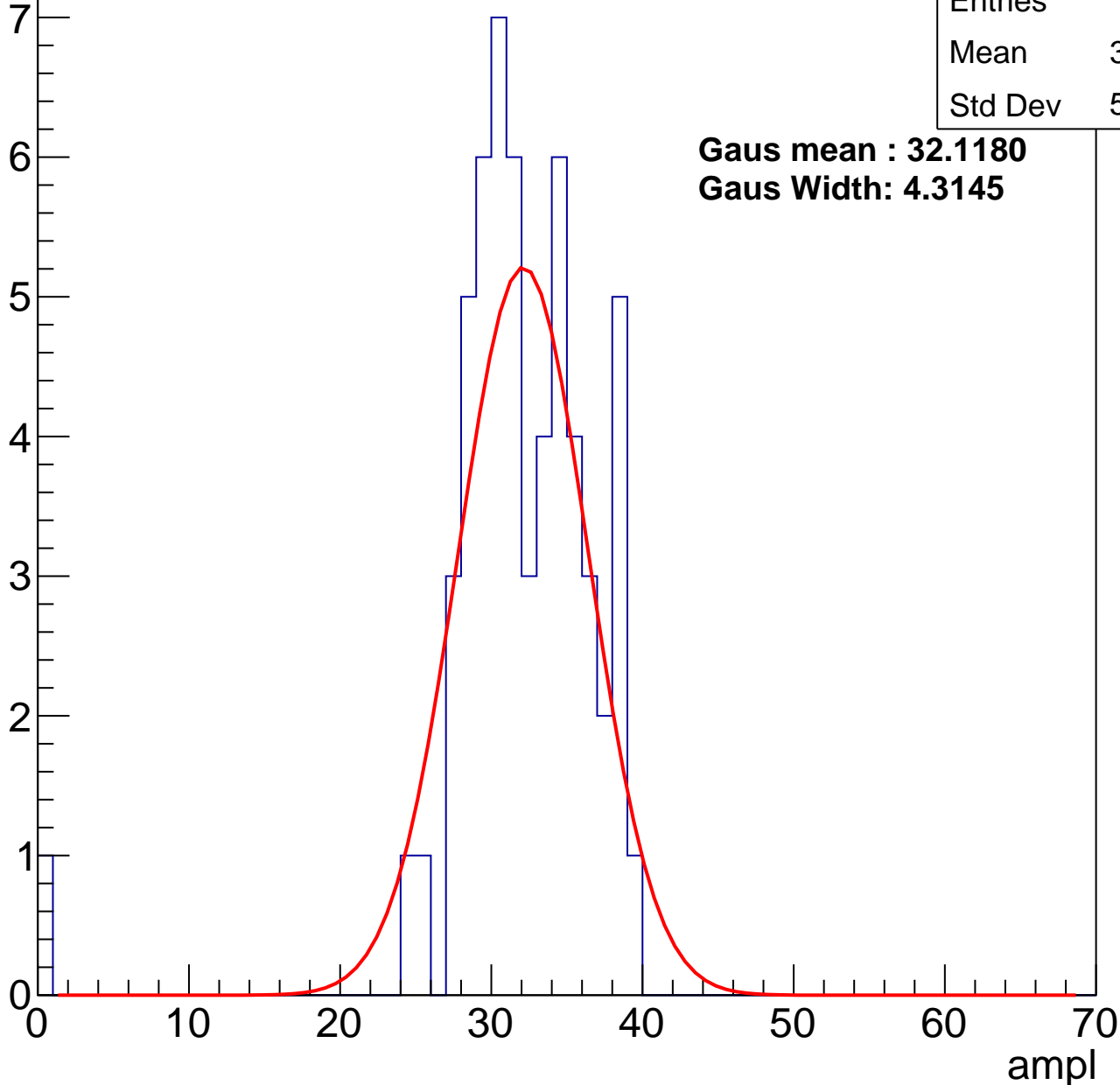
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.43
Std Dev	5.496

Gaus mean : 32.1180

Gaus Width: 4.3145



B0L001S, U15-ch25, adc1

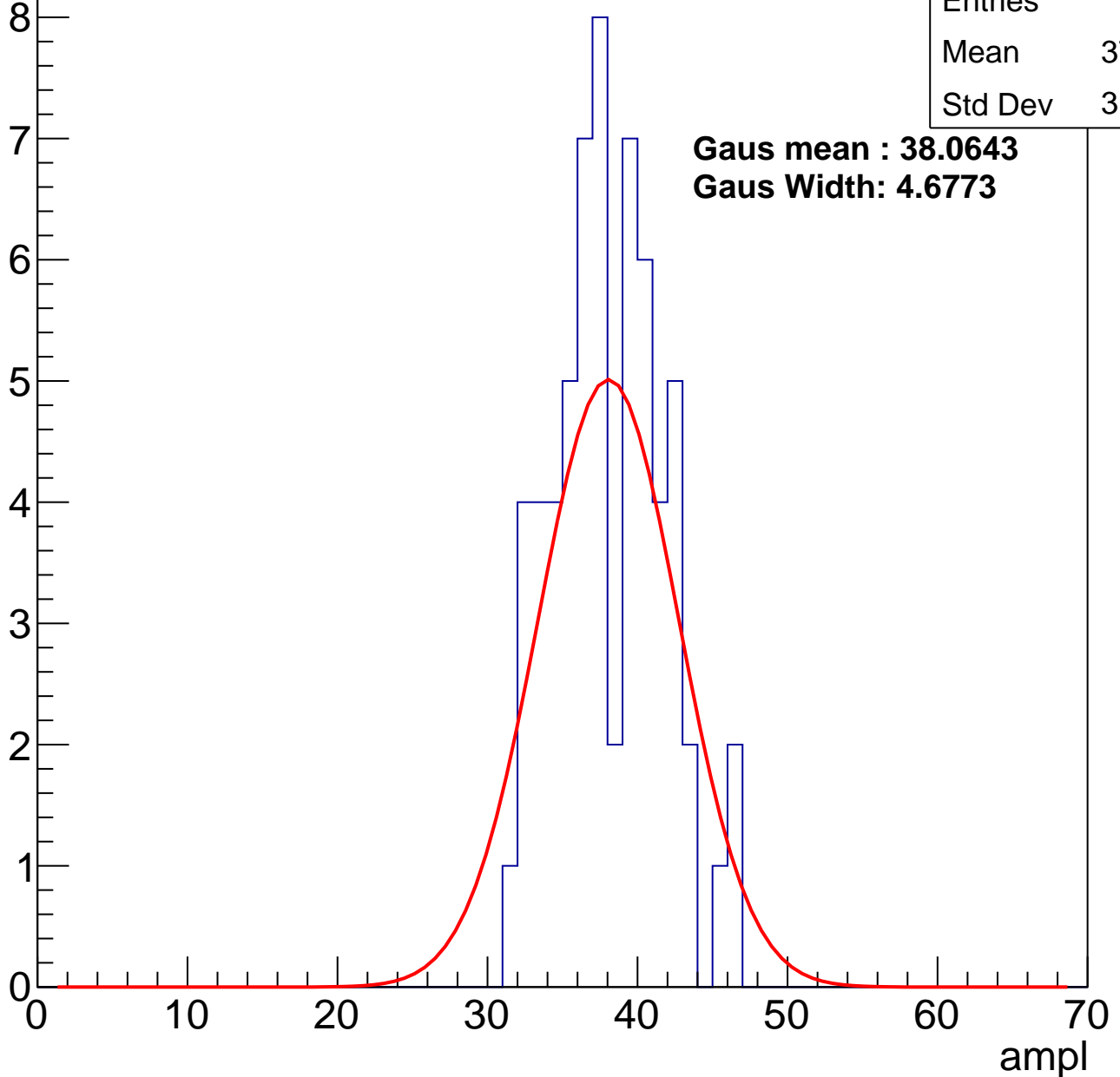
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.68
Std Dev	3.605

Gaus mean : 38.0643

Gaus Width: 4.6773



B0L001S, U15-ch25, adc2

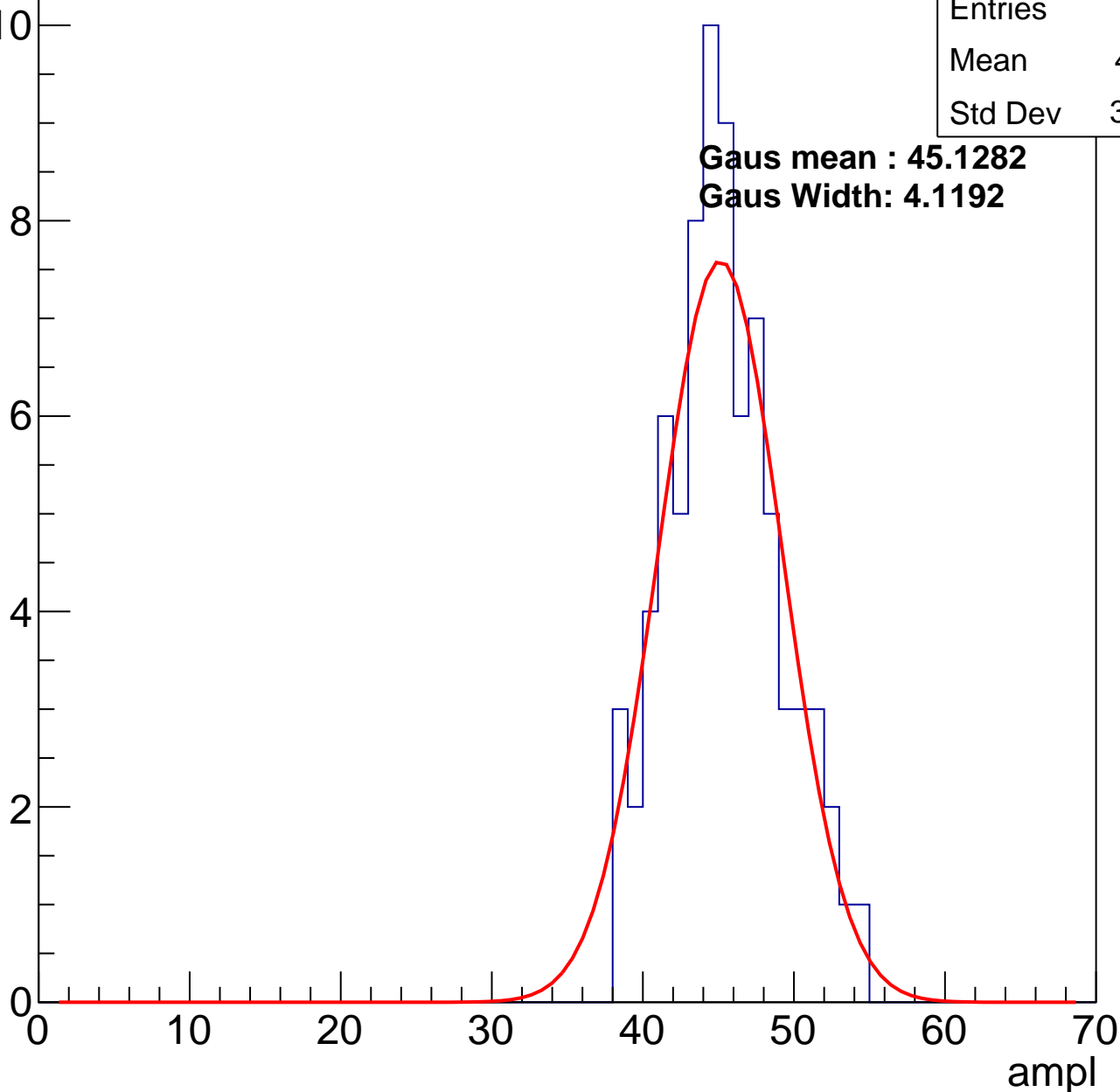
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	44.91
Std Dev	3.677

Gaus mean : 45.1282

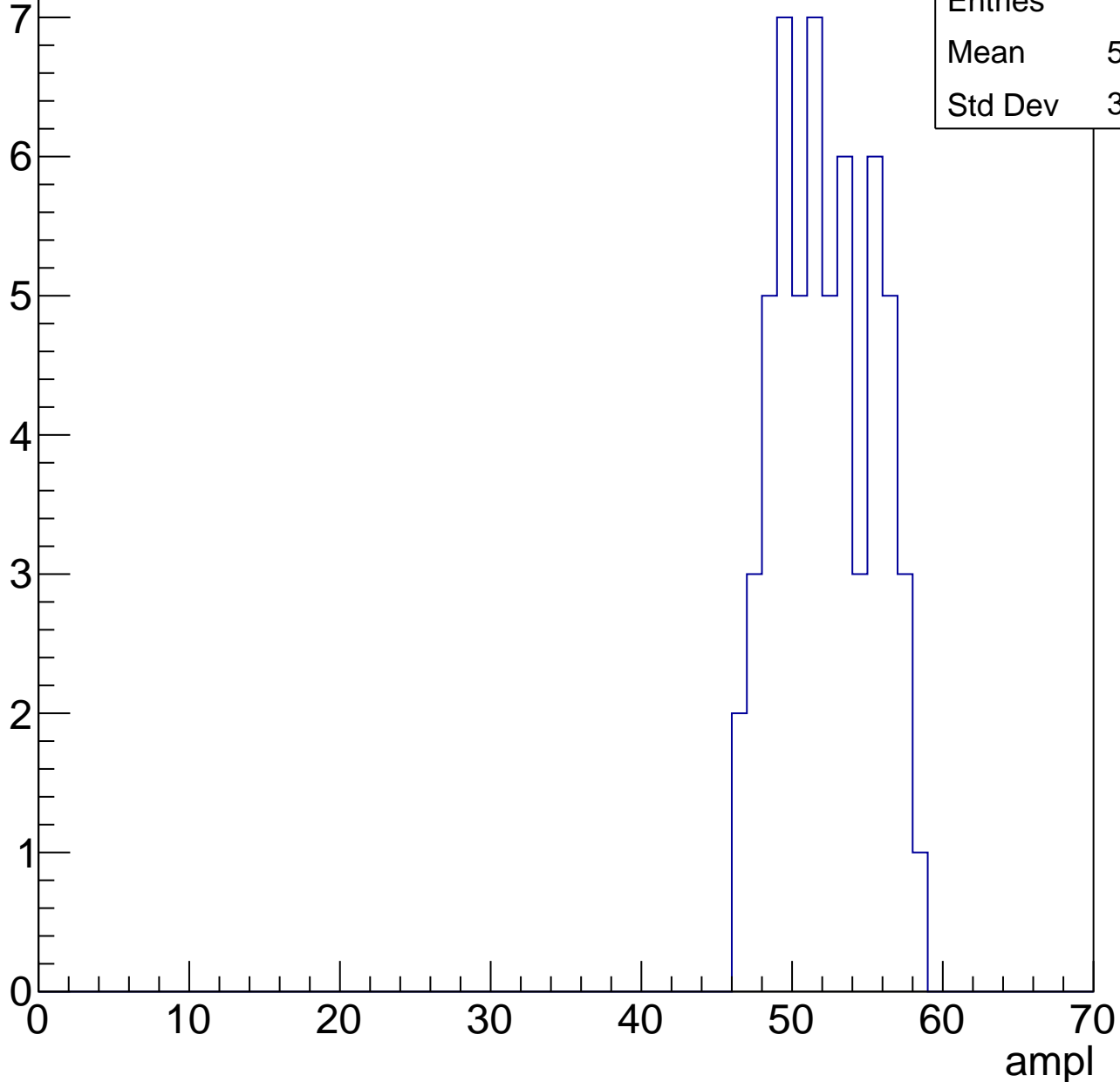
Gaus Width: 4.1192



B0L001S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

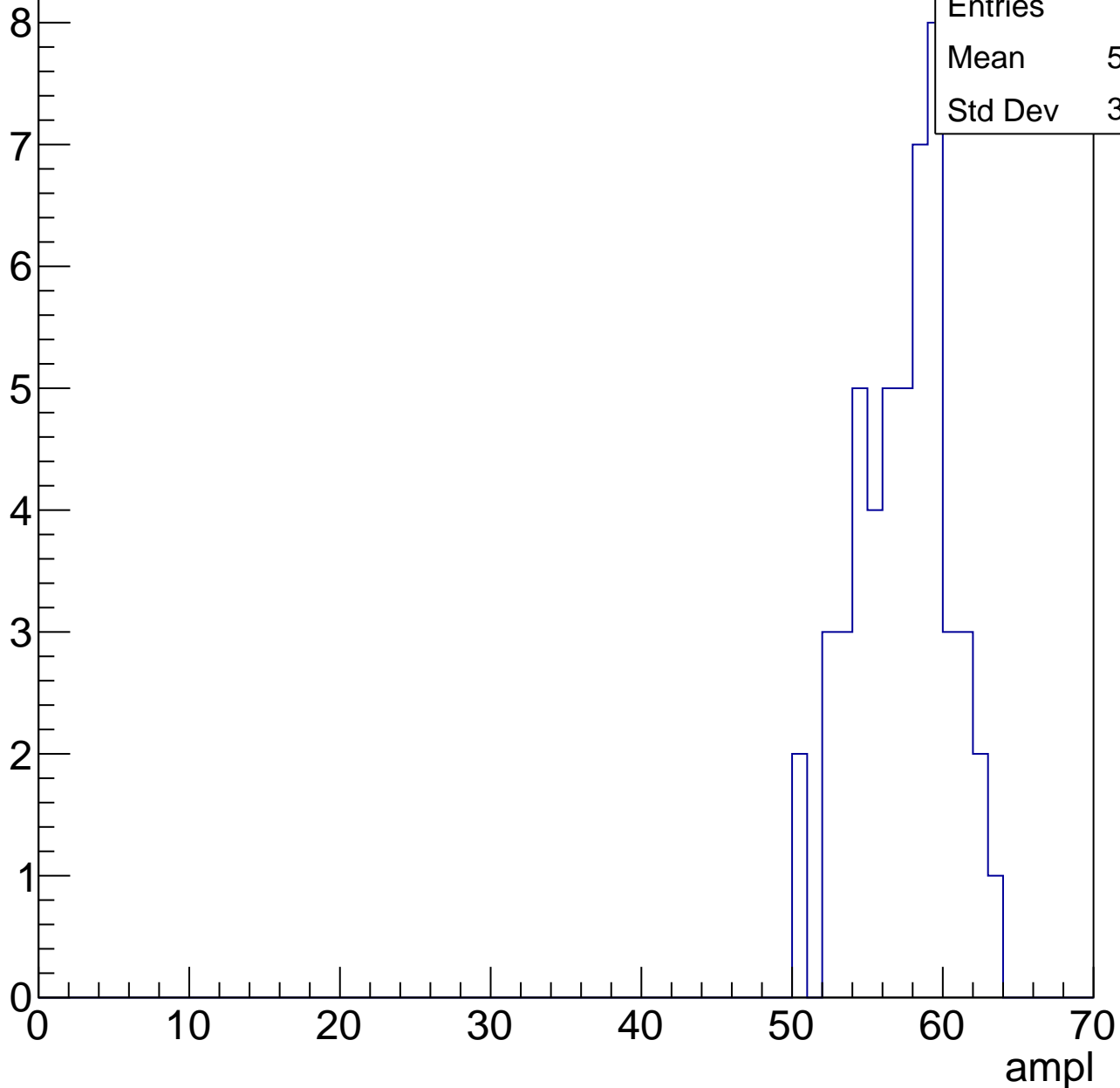
Entry



B0L001S, U15-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



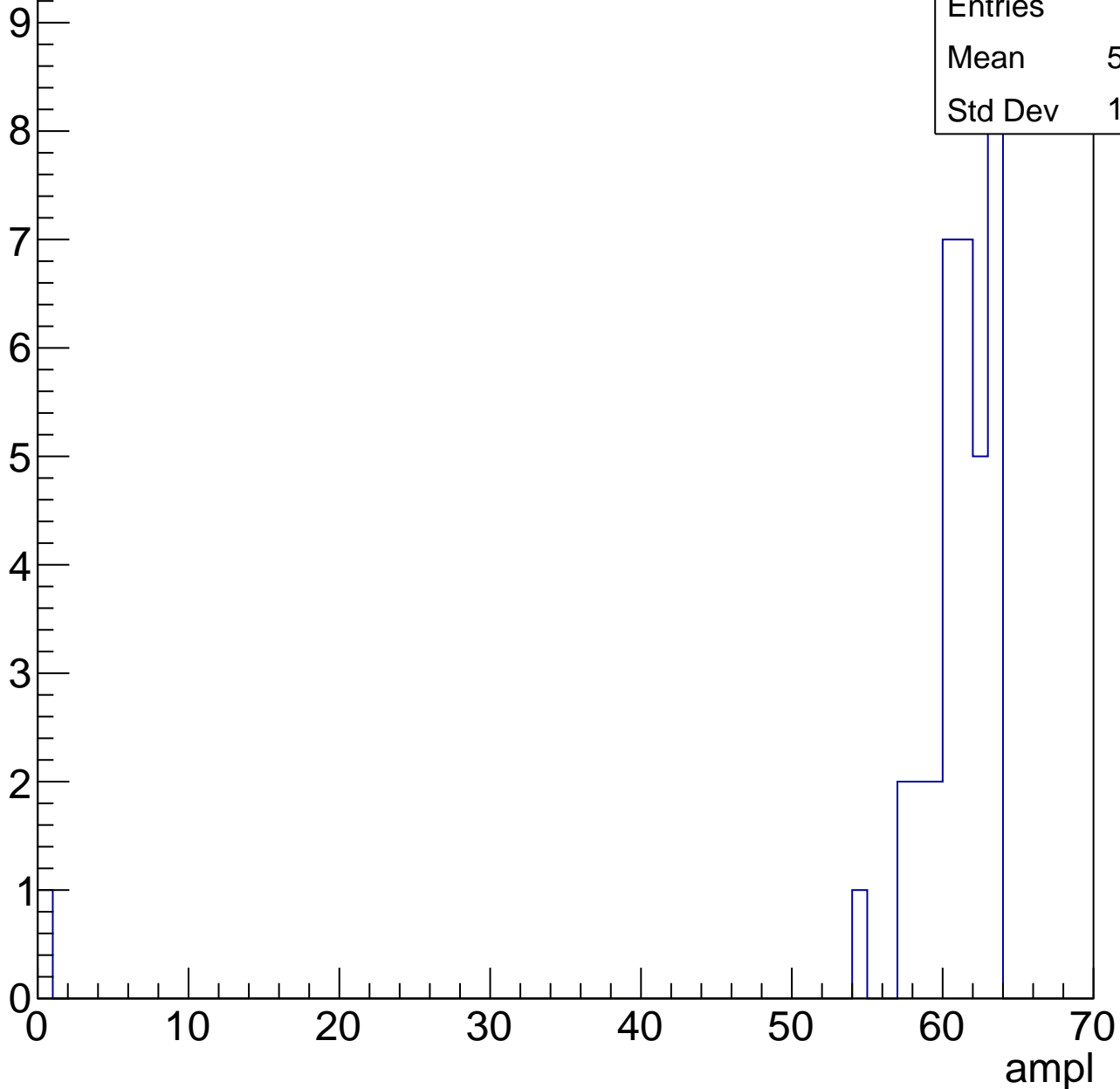
Entries	51
Mean	56.82
Std Dev	3.085

B0L001S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.06
Std Dev	10.19



B0L001S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch26, adc0

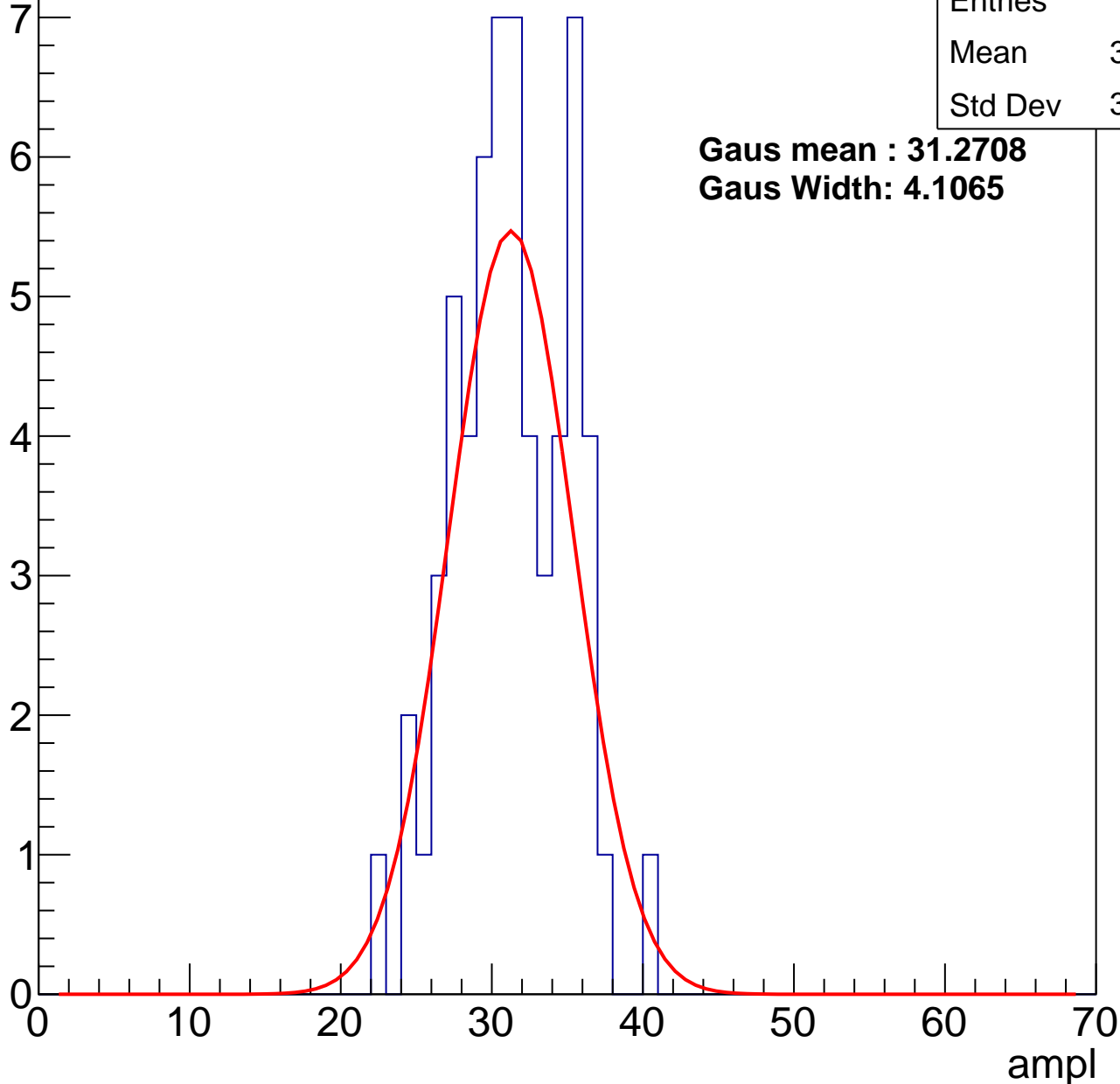
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.83
Std Dev	3.689

Gaus mean : 31.2708

Gaus Width: 4.1065



B0L001S, U15-ch26, adc1

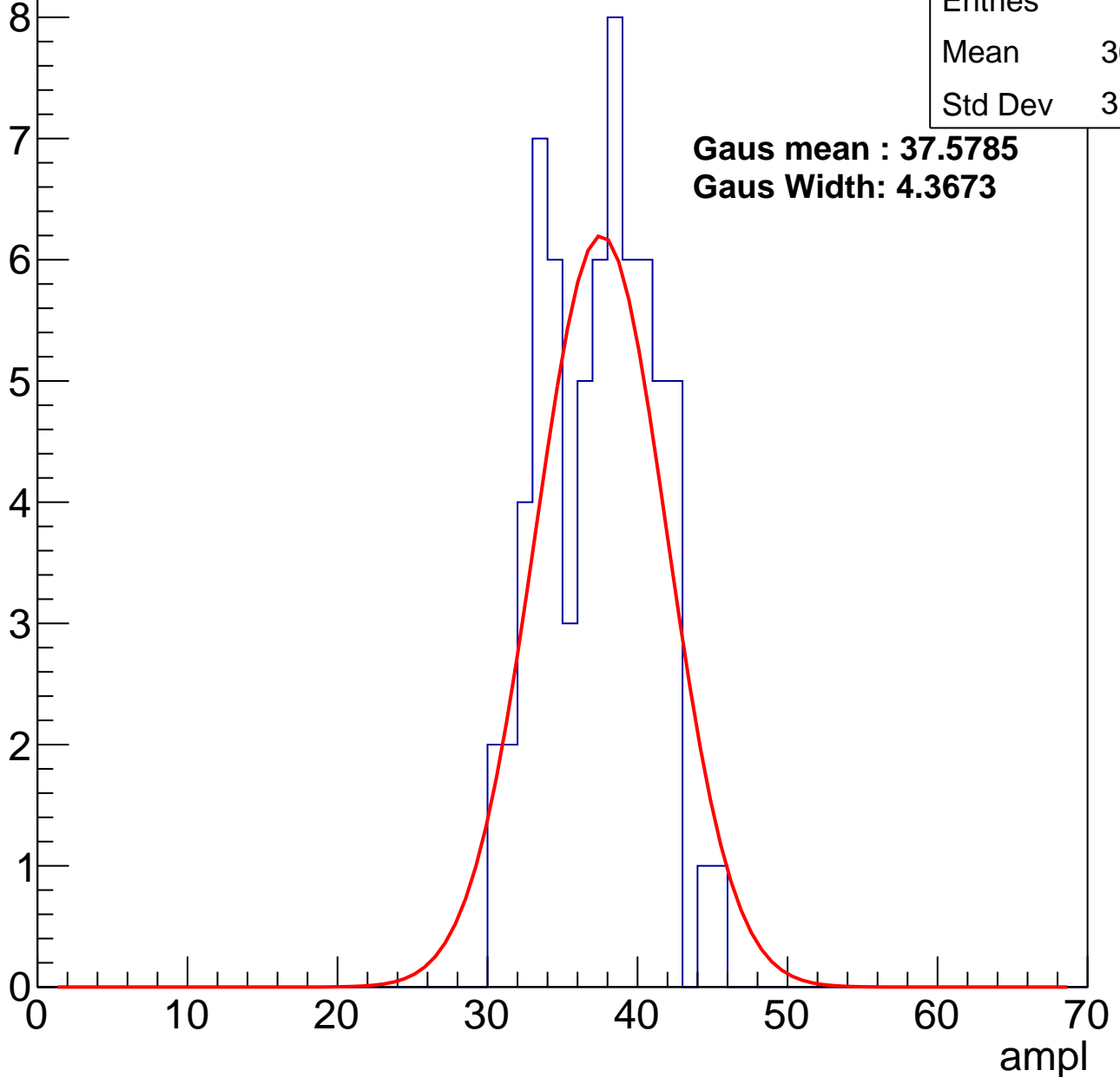
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.93
Std Dev	3.576

Gaus mean : 37.5785

Gaus Width: 4.3673



B0L001S, U15-ch26, adc2

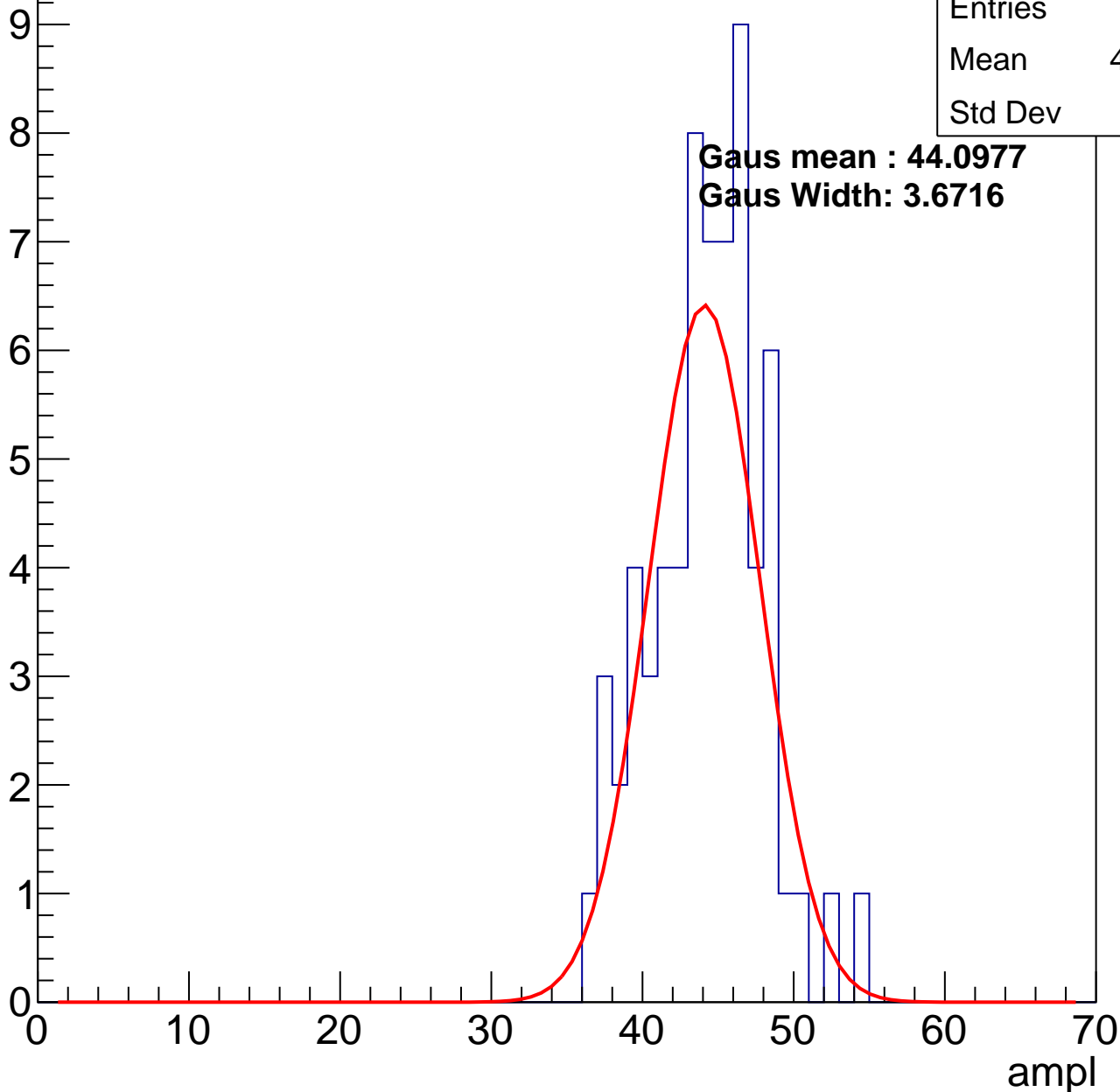
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.83
Std Dev	3.67

Gaus mean : 44.0977

Gaus Width: 3.6716

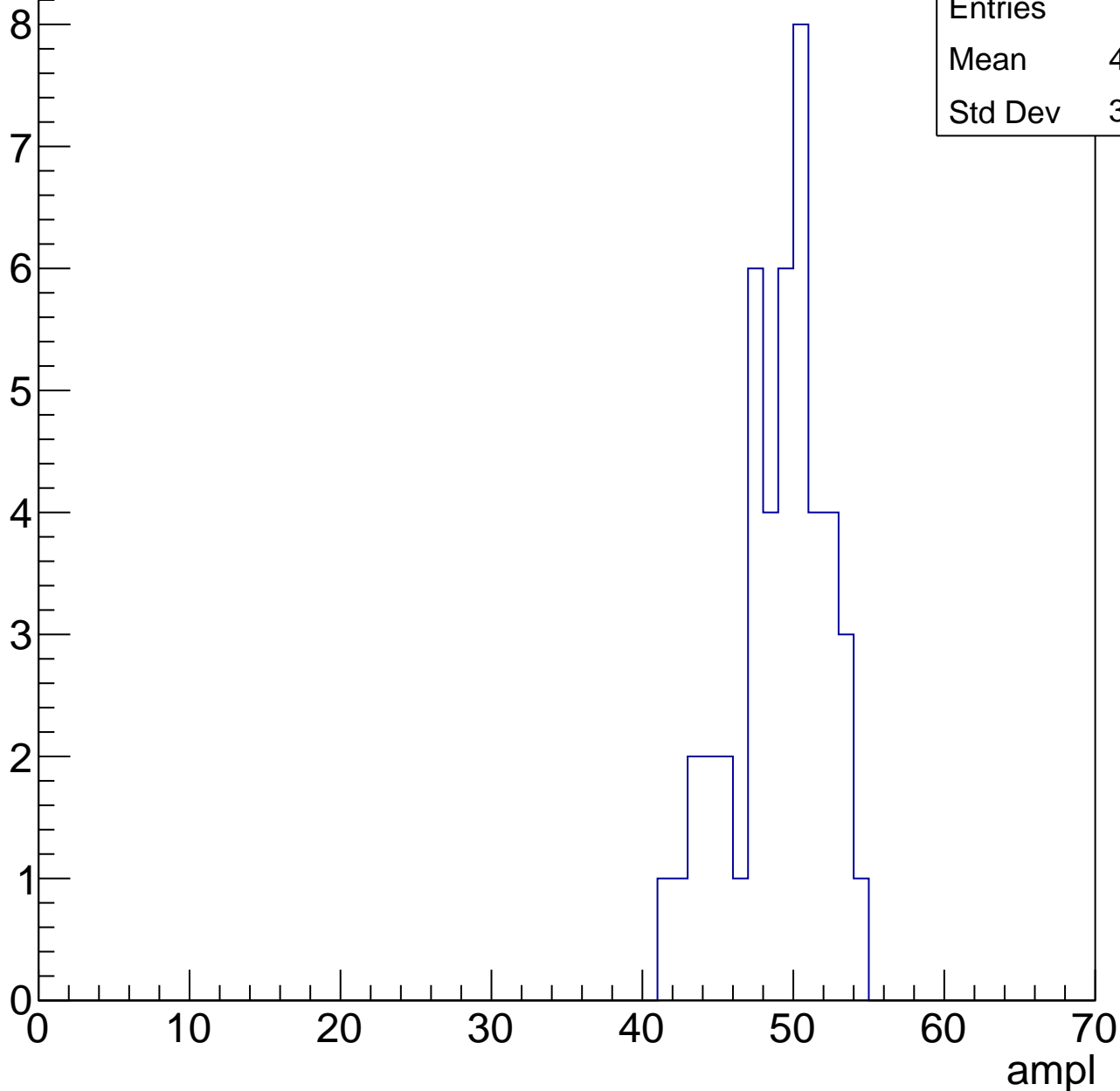


B0L001S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	48.58
Std Dev	3.088

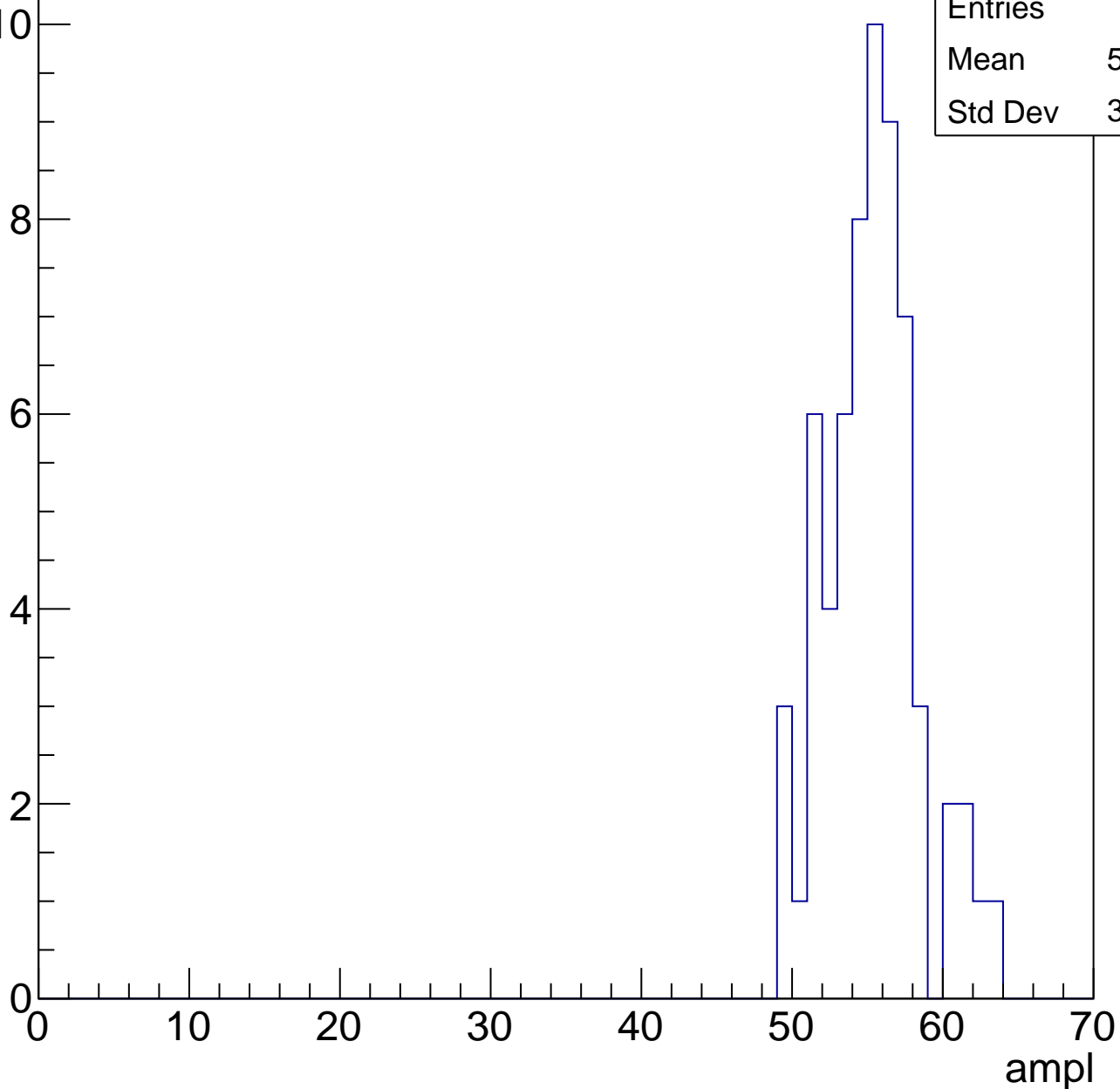


B0L001S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	54.84
Std Dev	3.072

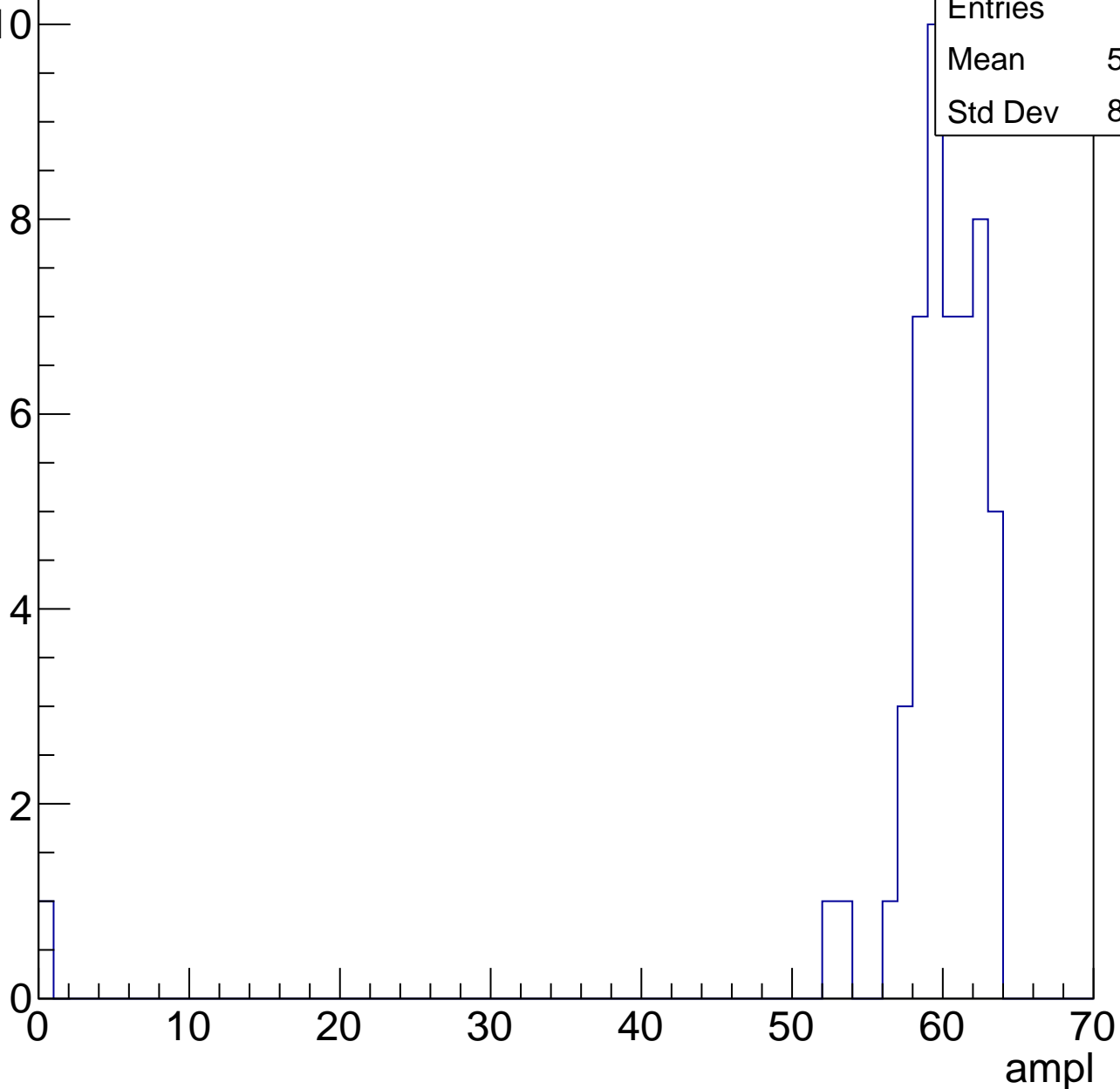


B0L001S, U15-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	58.55
Std Dev	8.598



B0L001S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch27, adc0

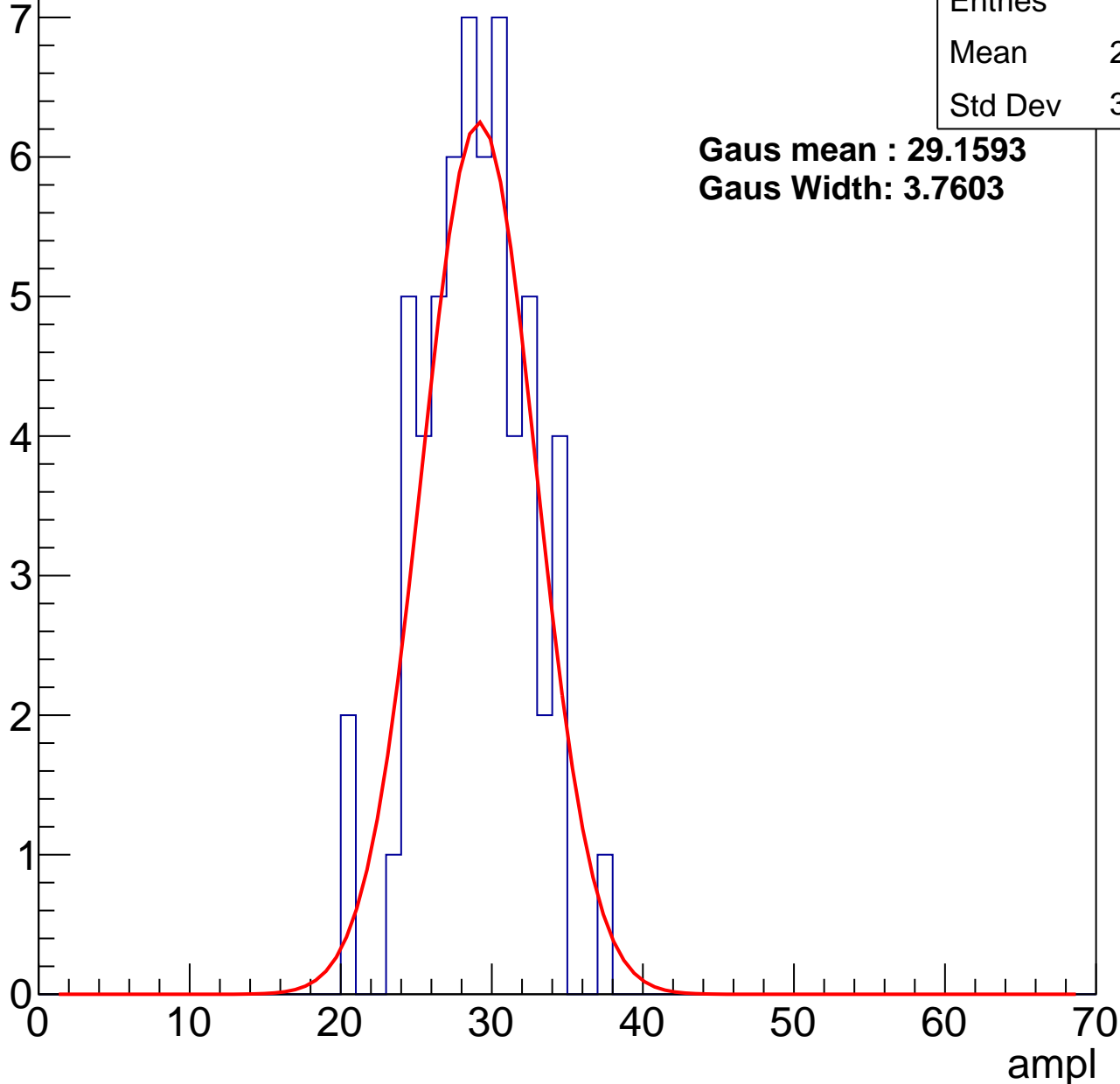
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	28.44
Std Dev	3.465

Gaus mean : 29.1593

Gaus Width: 3.7603

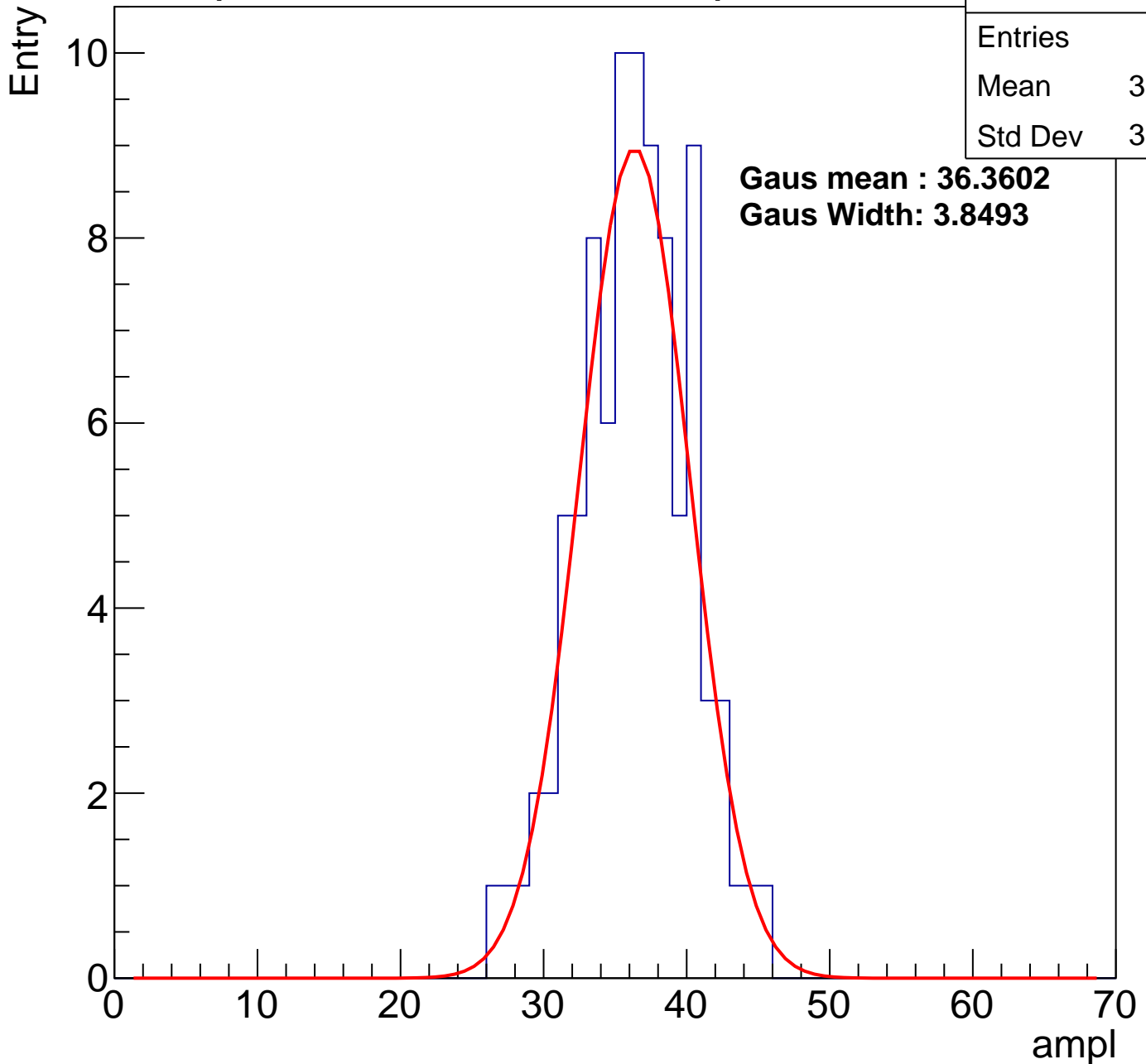


B0L001S, U15-ch27, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	91
Mean	35.88
Std Dev	3.825

Gaus mean : 36.3602
Gaus Width: 3.8493



B0L001S, U15-ch27, adc2

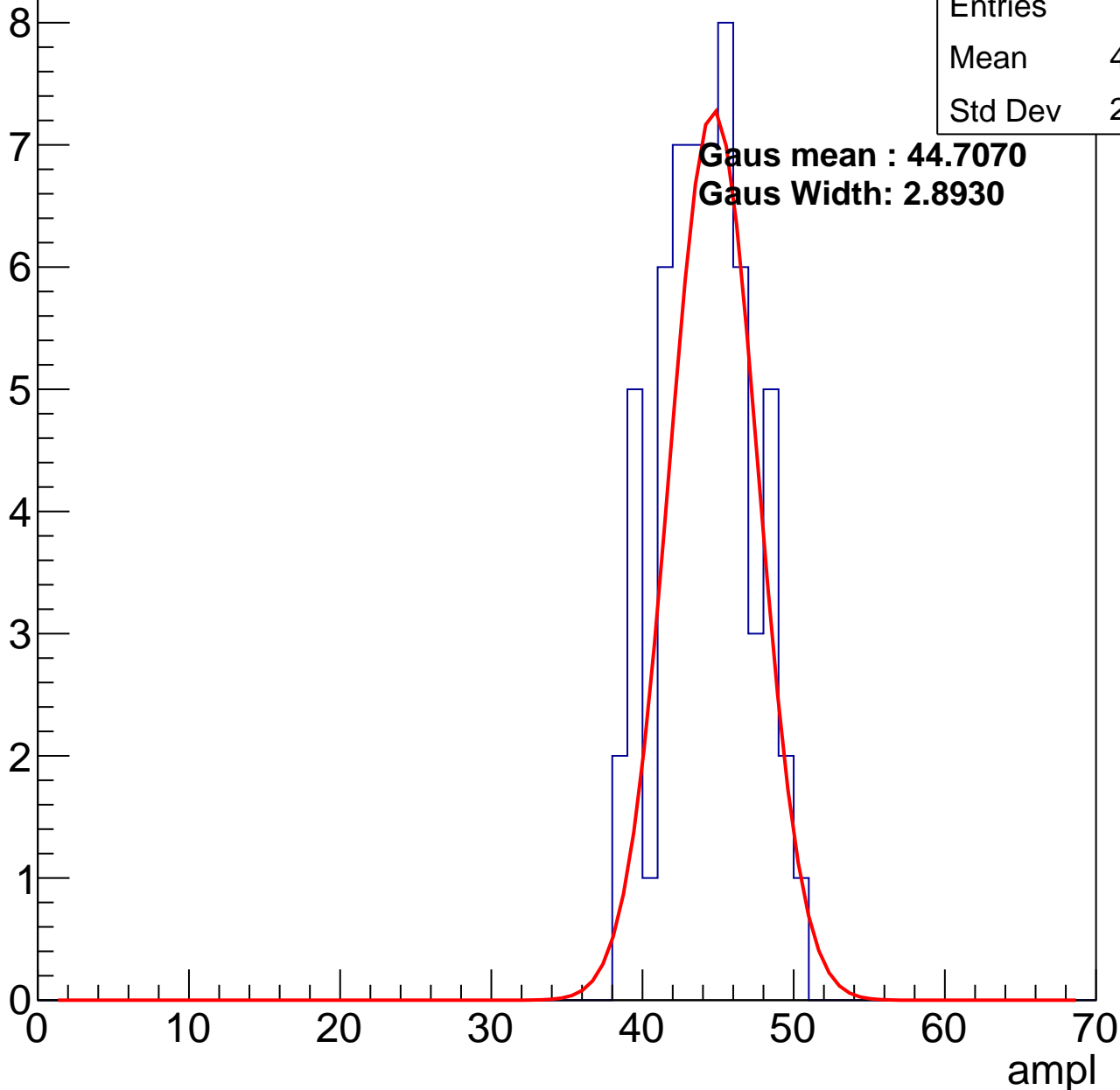
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.75
Std Dev	2.953

Gaus mean : 44.7070

Gaus Width: 2.8930

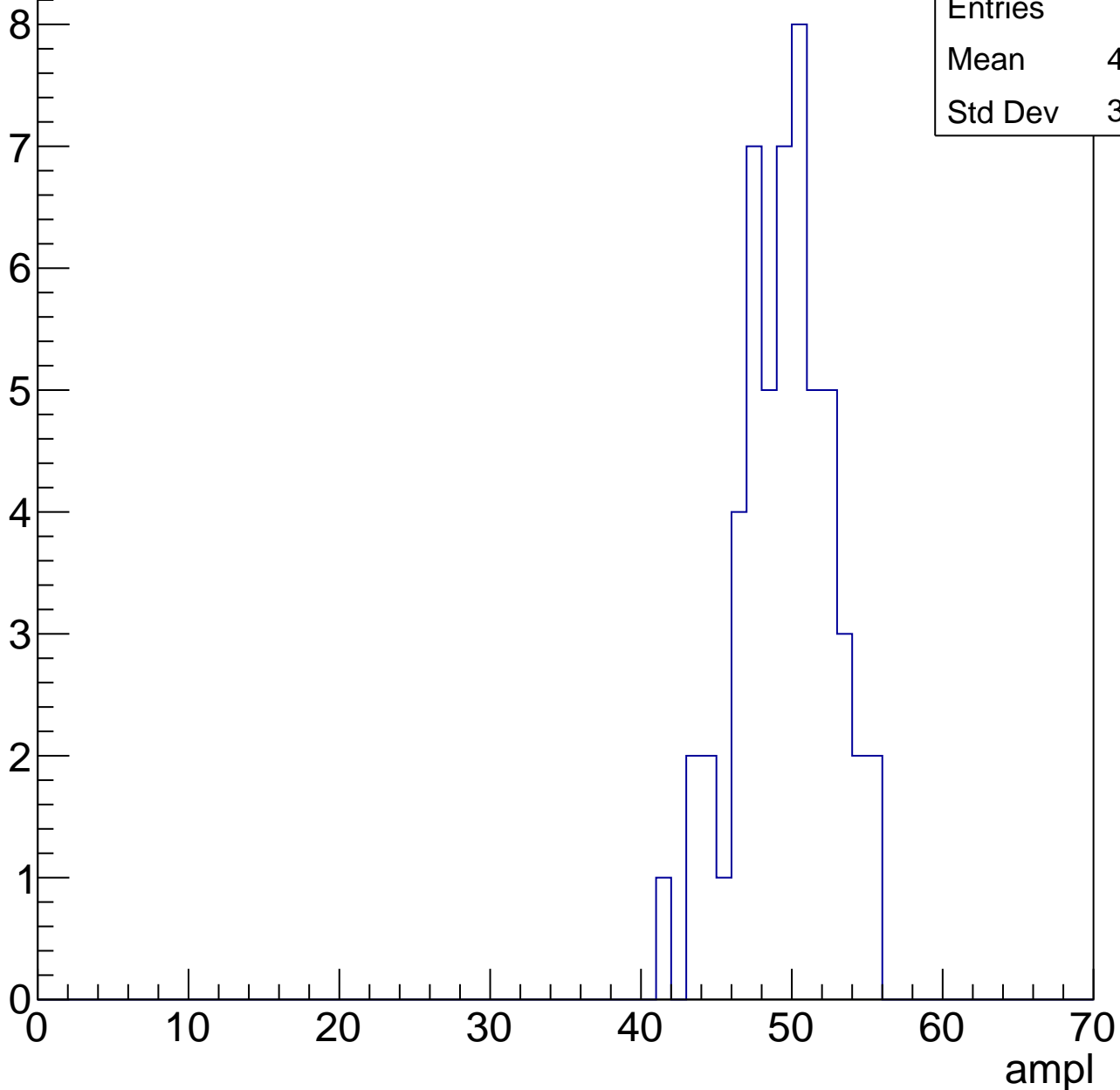


B0L001S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

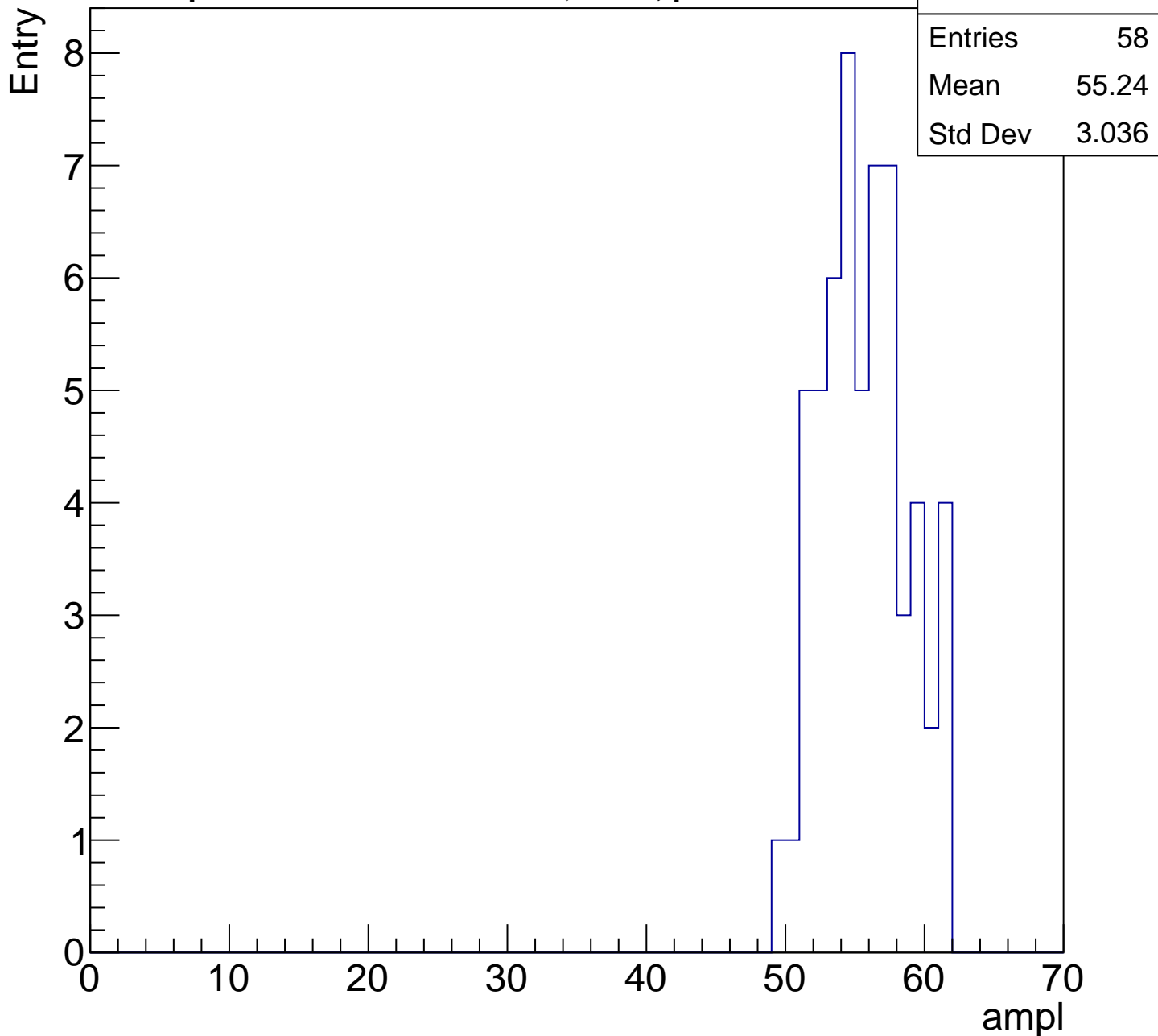
Entry

Entries	54
Mean	49.04
Std Dev	3.085



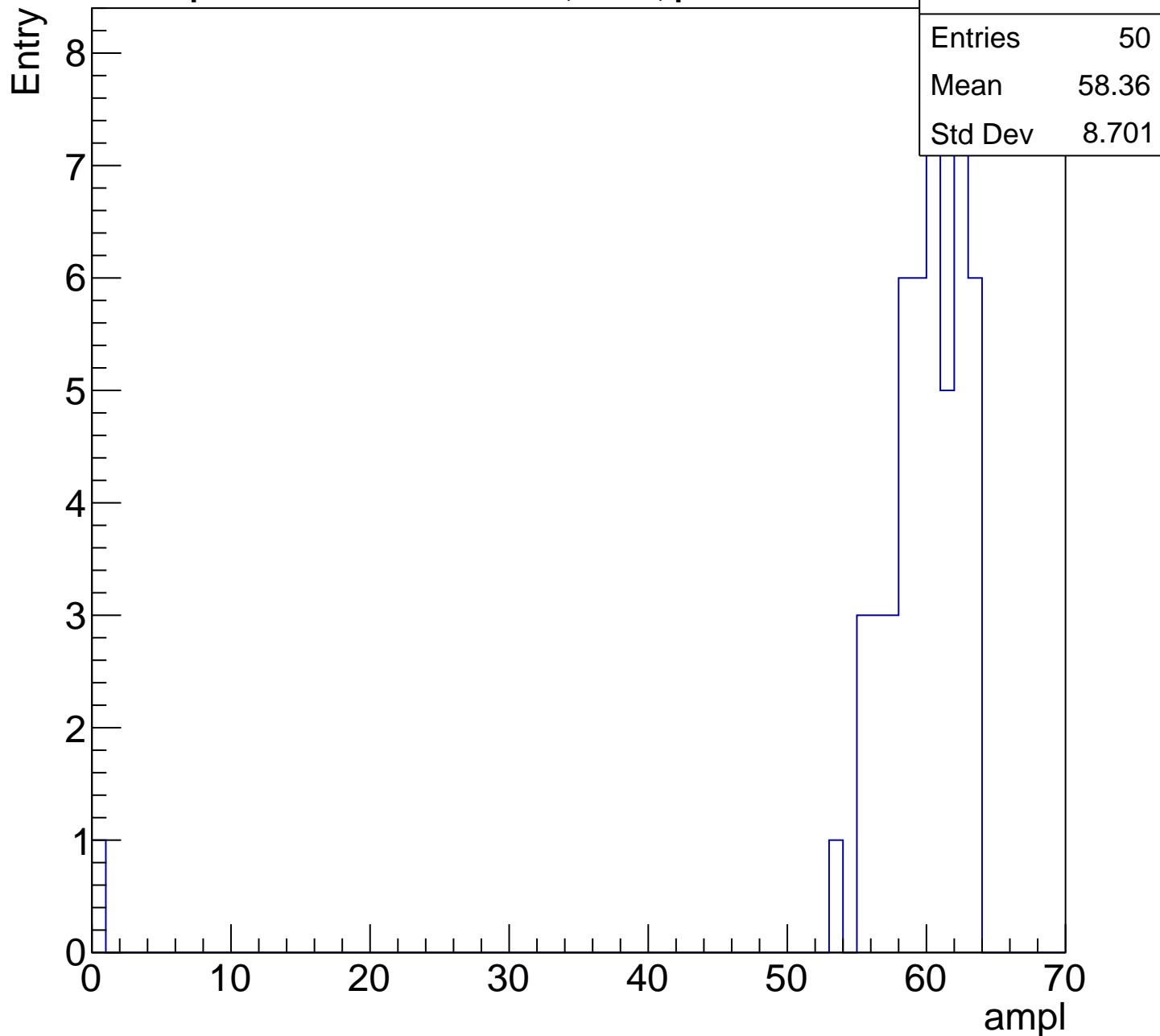
B0L001S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U15-ch27, adc5

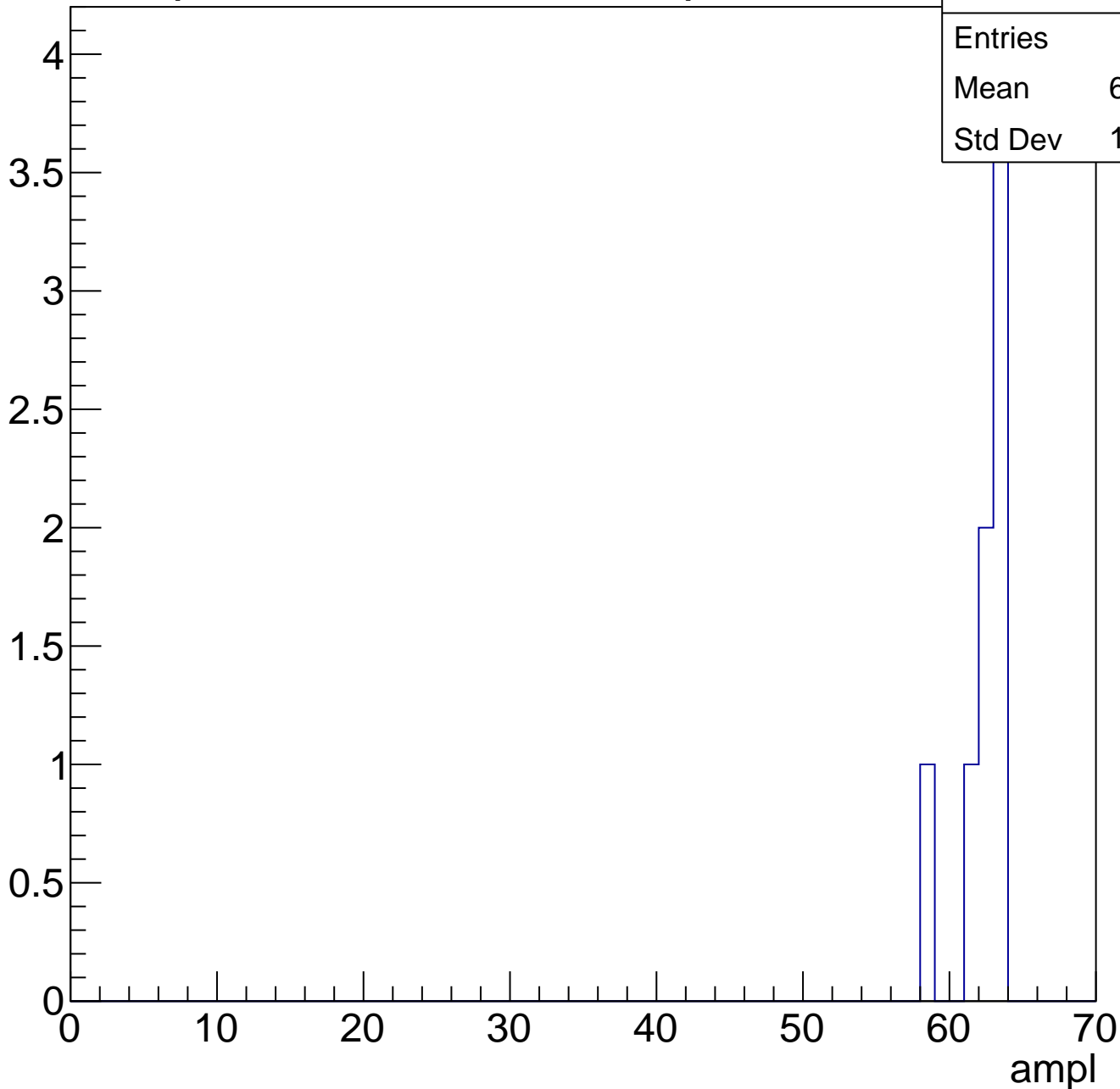
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch28, adc0

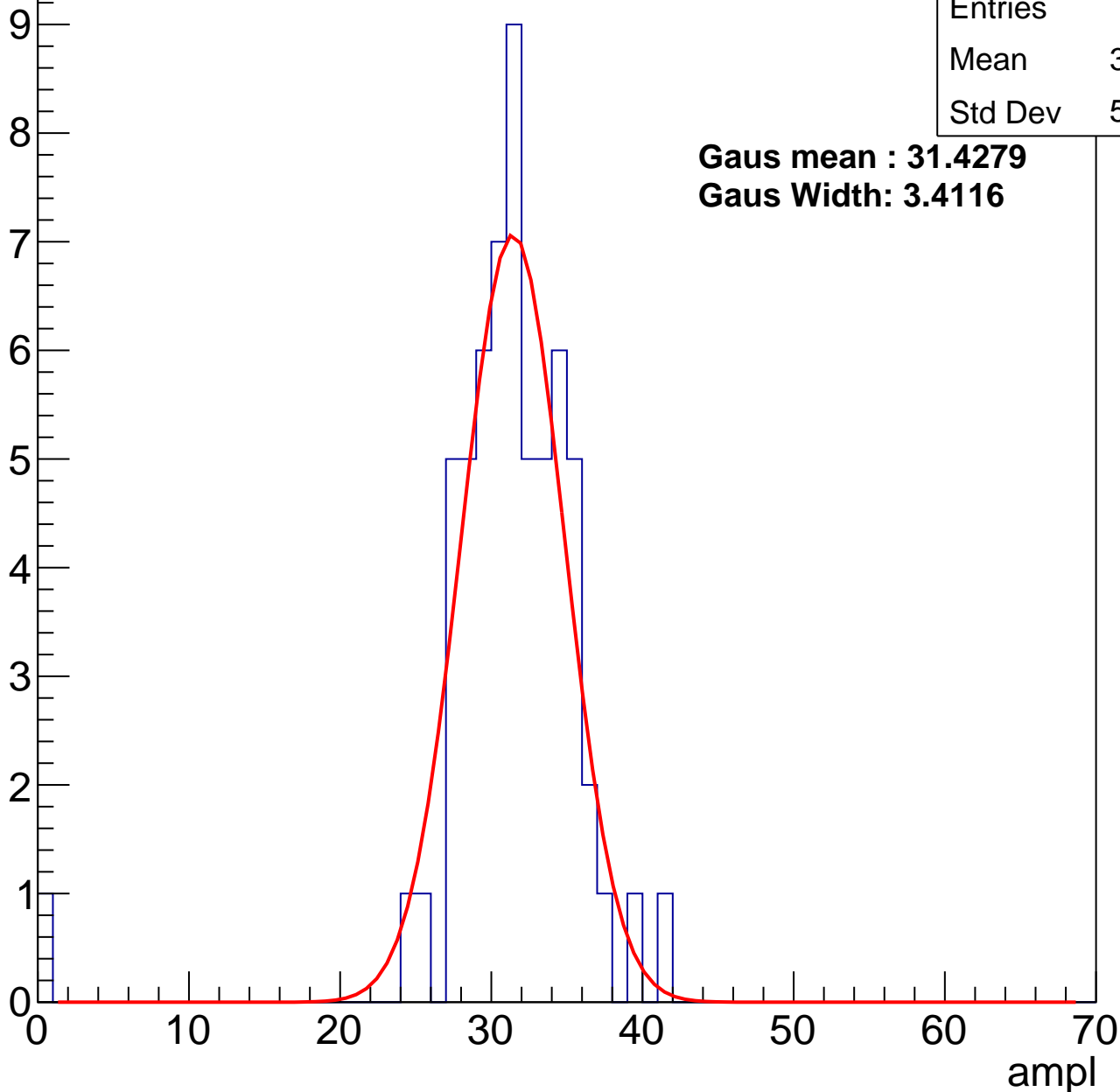
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.82
Std Dev	5.136

Gaus mean : 31.4279

Gaus Width: 3.4116



B0L001S, U15-ch28, adc1

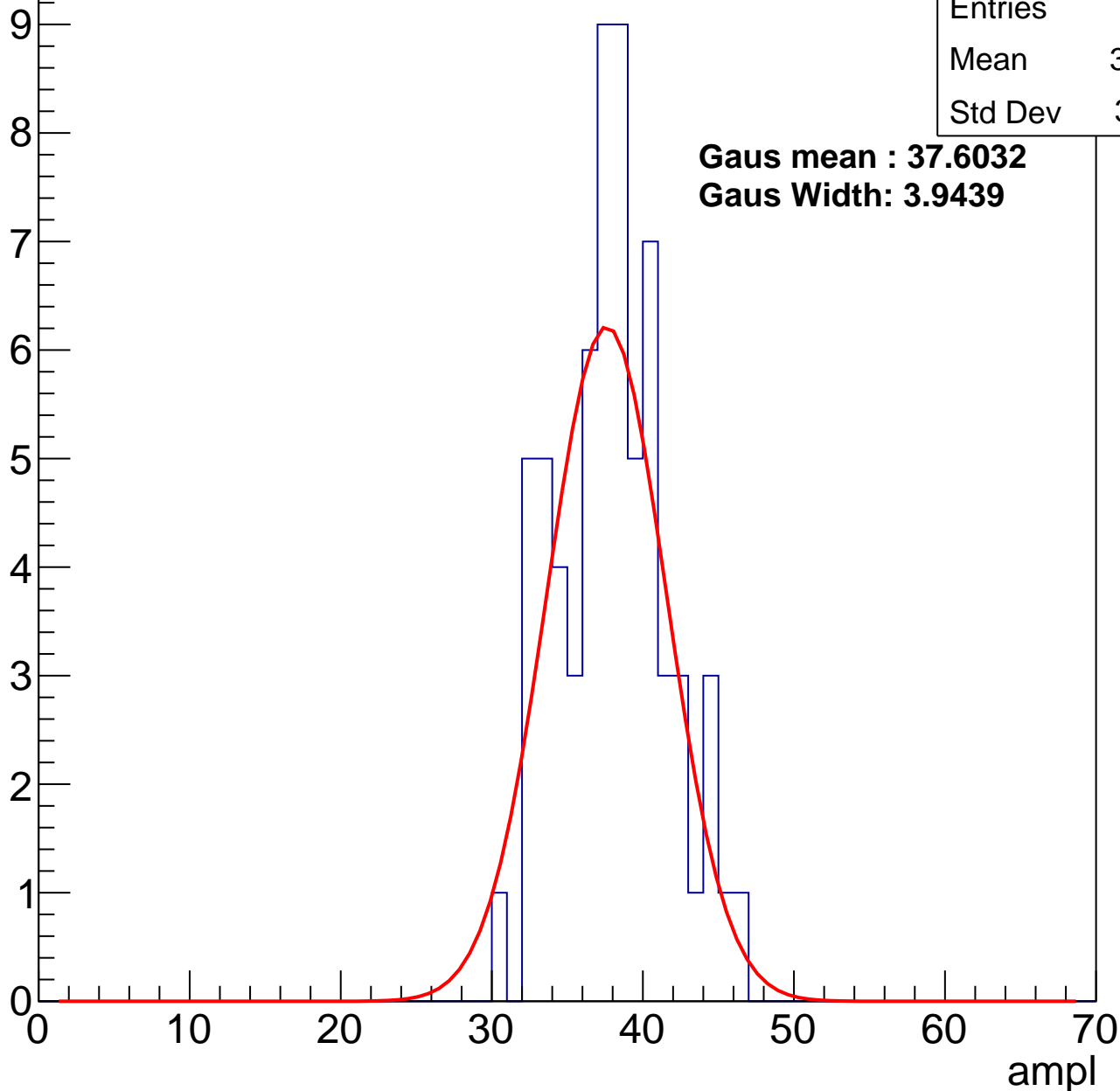
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.53
Std Dev	3.551

Gaus mean : 37.6032

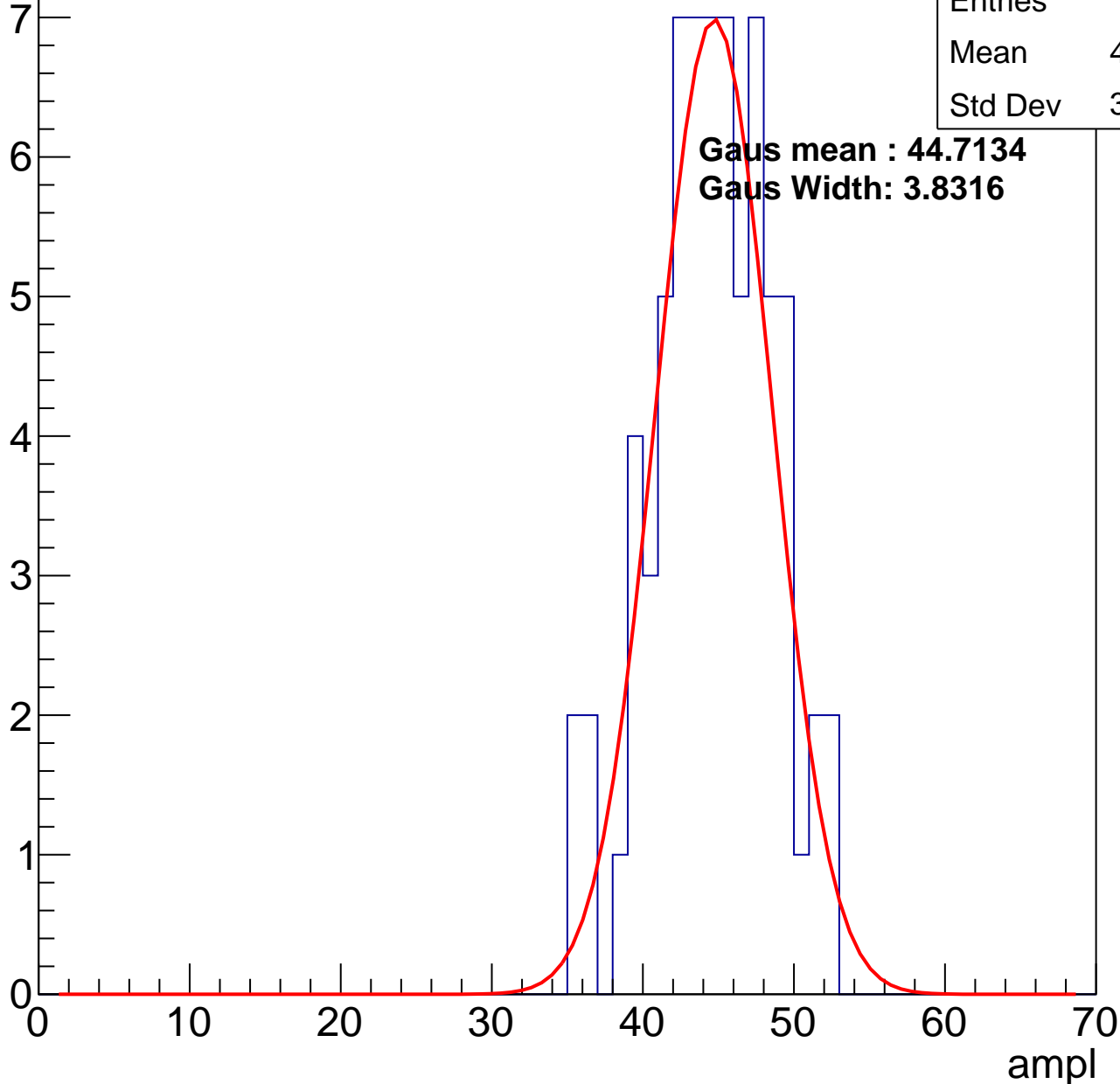
Gaus Width: 3.9439



B0L001S, U15-ch28, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry



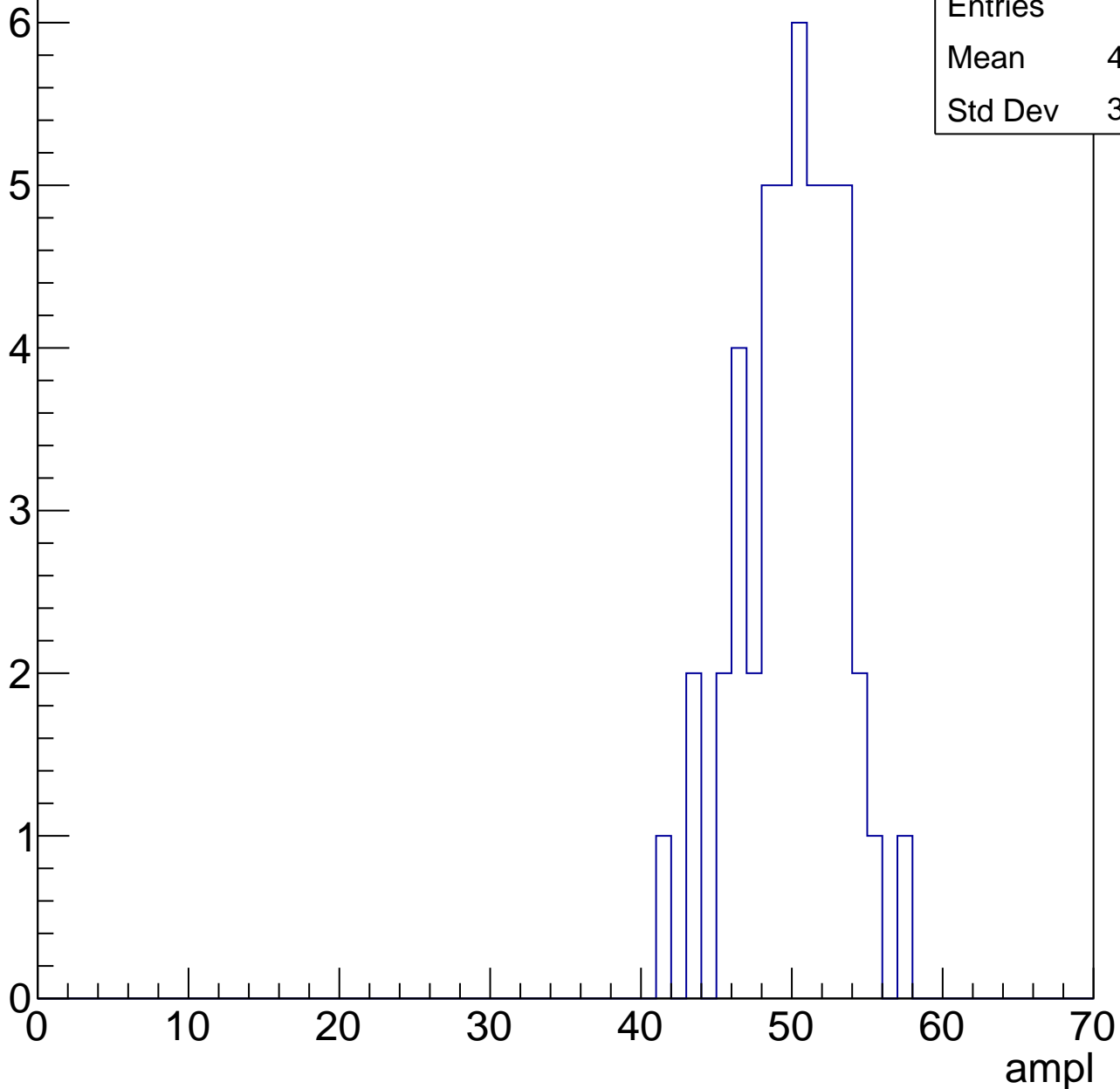
Entries	72
Mean	44.15
Std Dev	3.925

B0L001S, U15-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	49.57
Std Dev	3.308

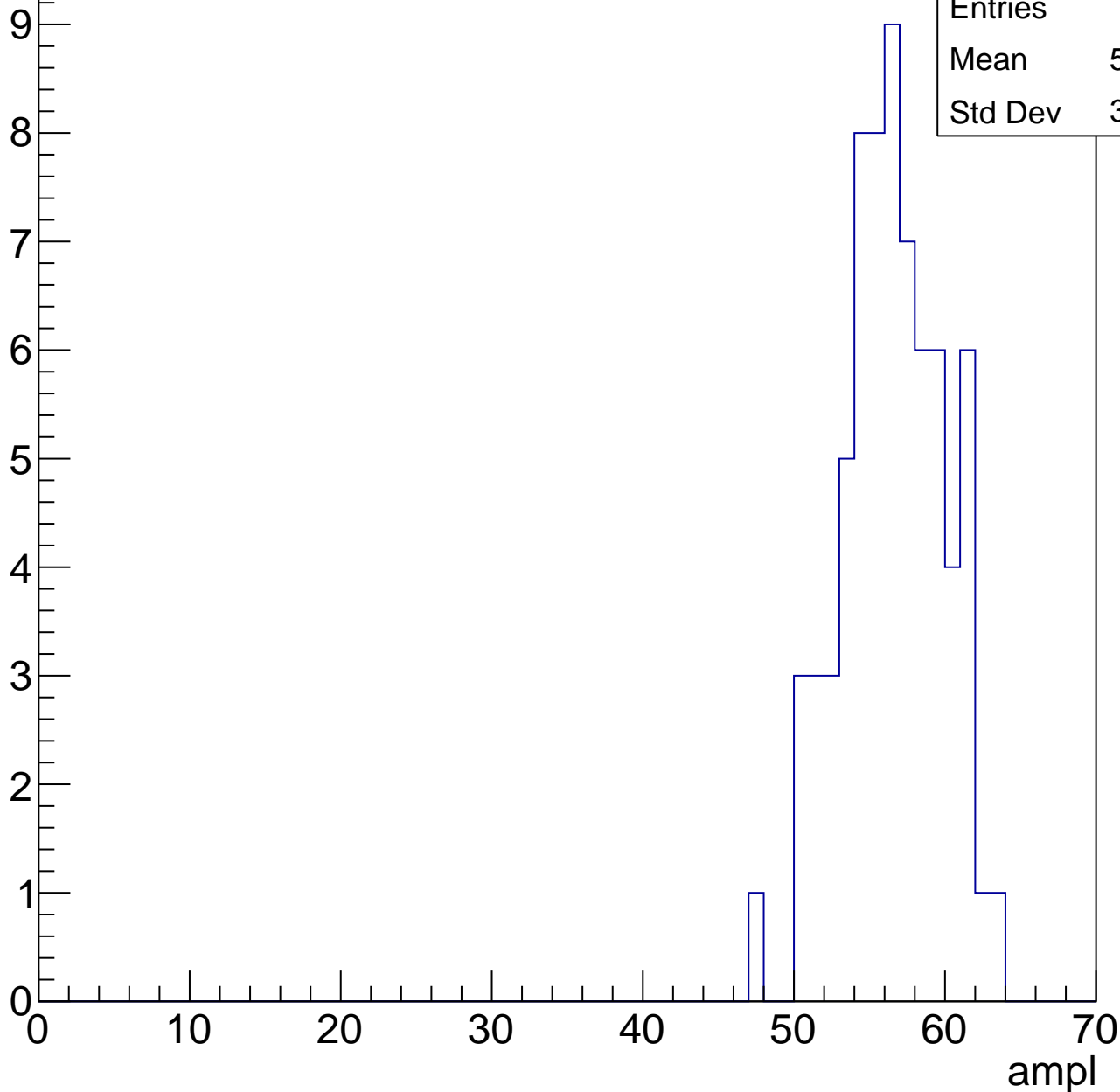


B0L001S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.04
Std Dev	3.316

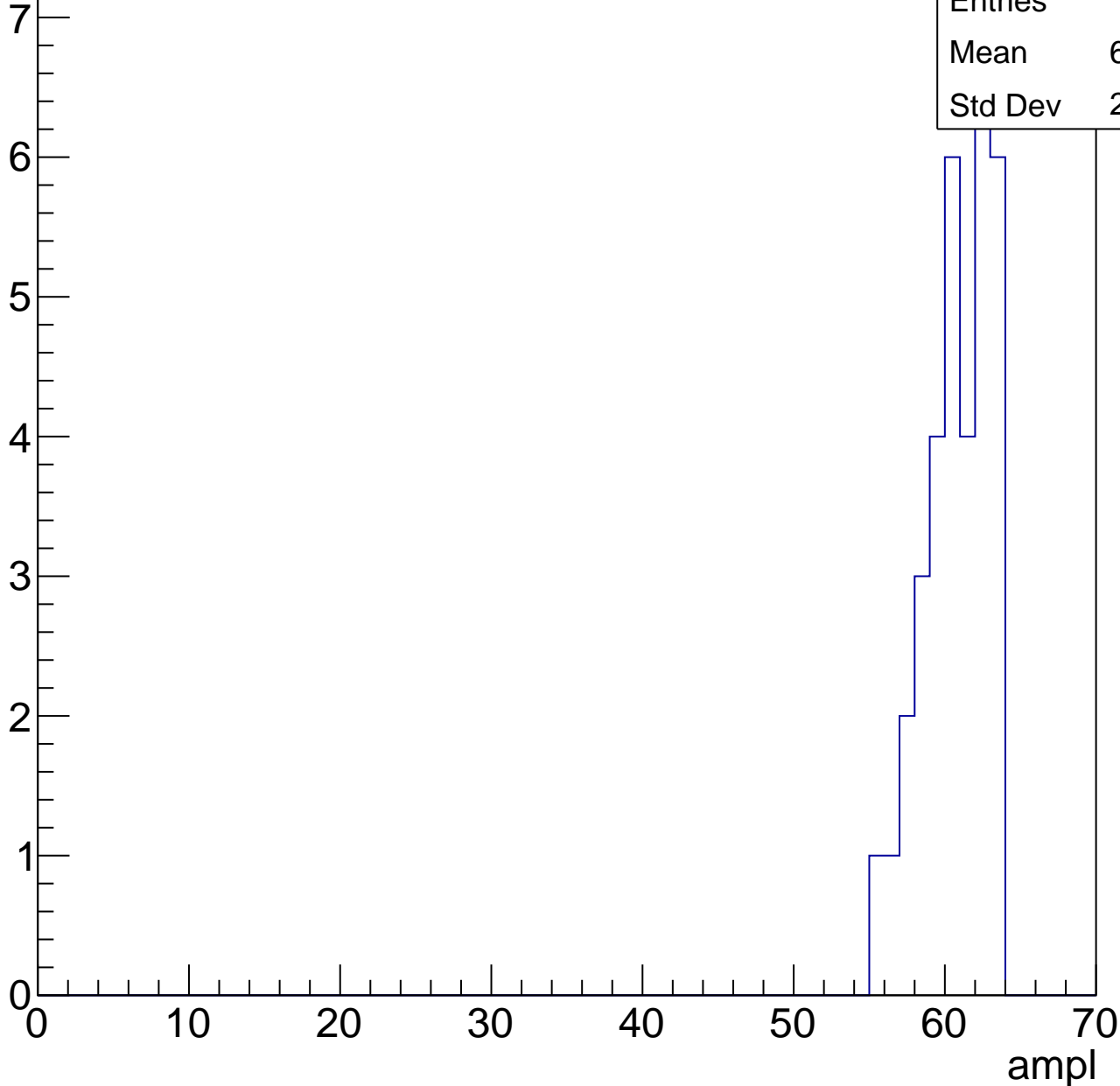


B0L001S, U15-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

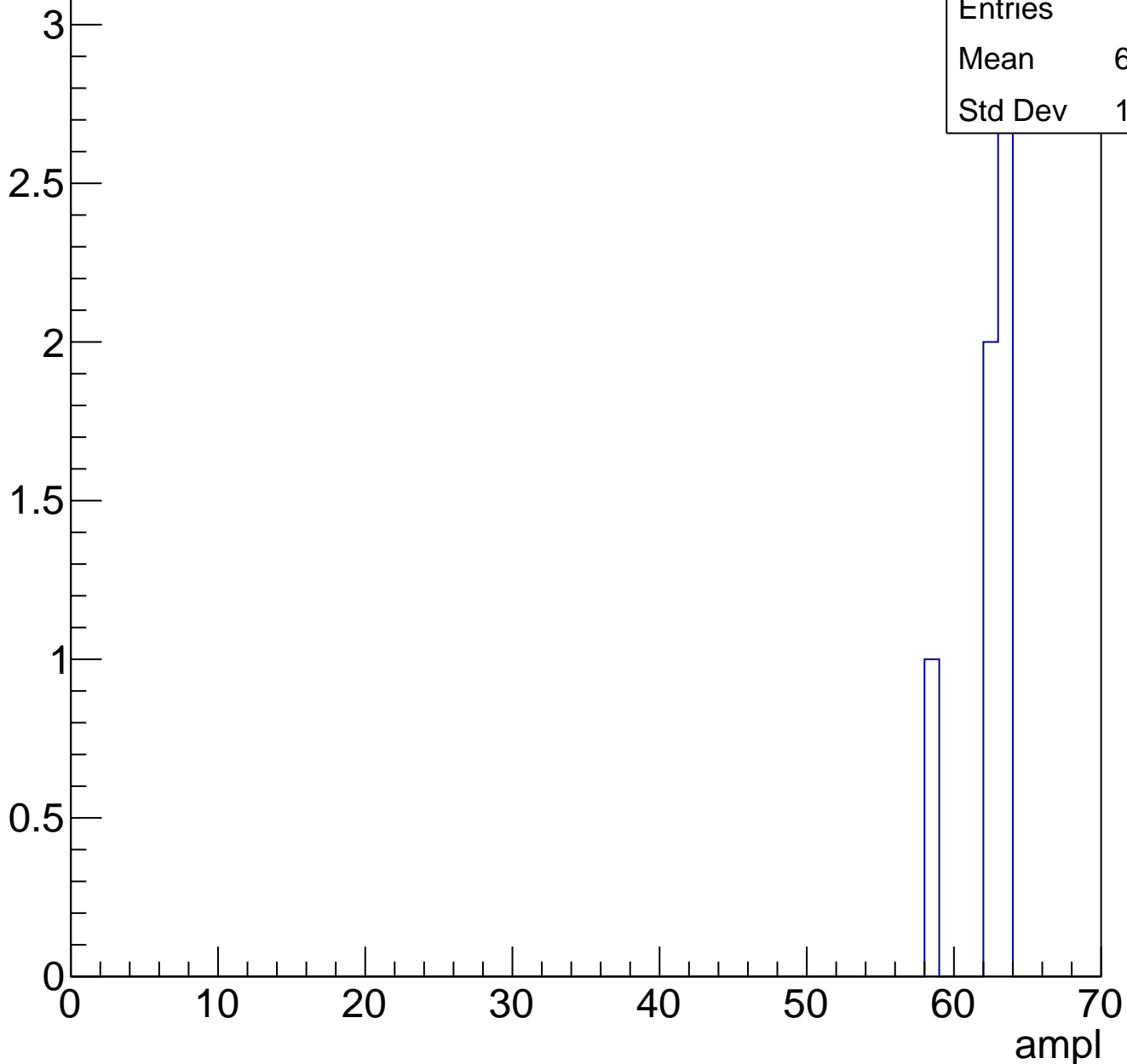
Entries	34
Mean	60.32
Std Dev	2.152



B0L001S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch29, adc0

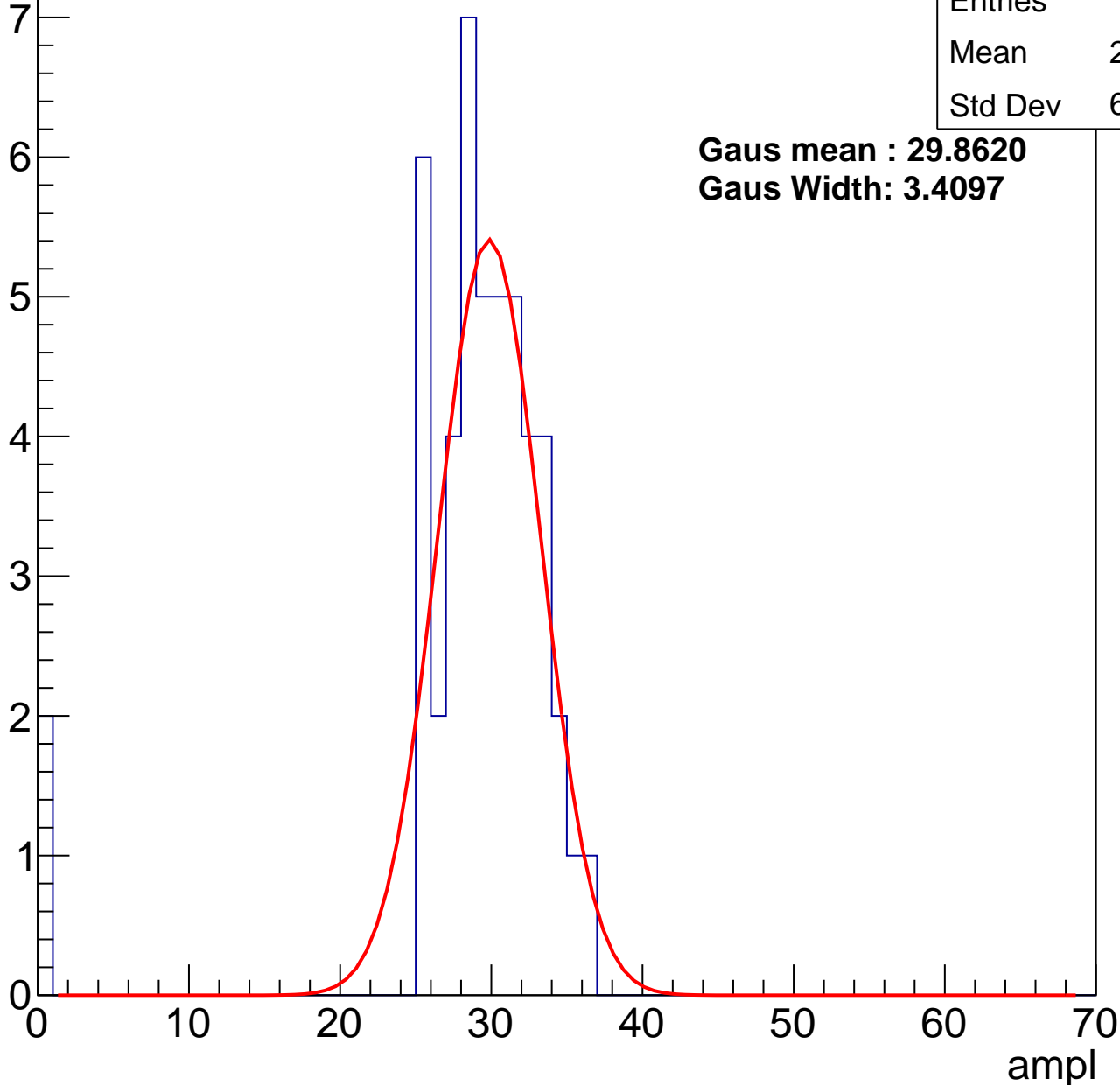
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	28.23
Std Dev	6.536

Gaus mean : 29.8620

Gaus Width: 3.4097



B0L001S, U15-ch29, adc1

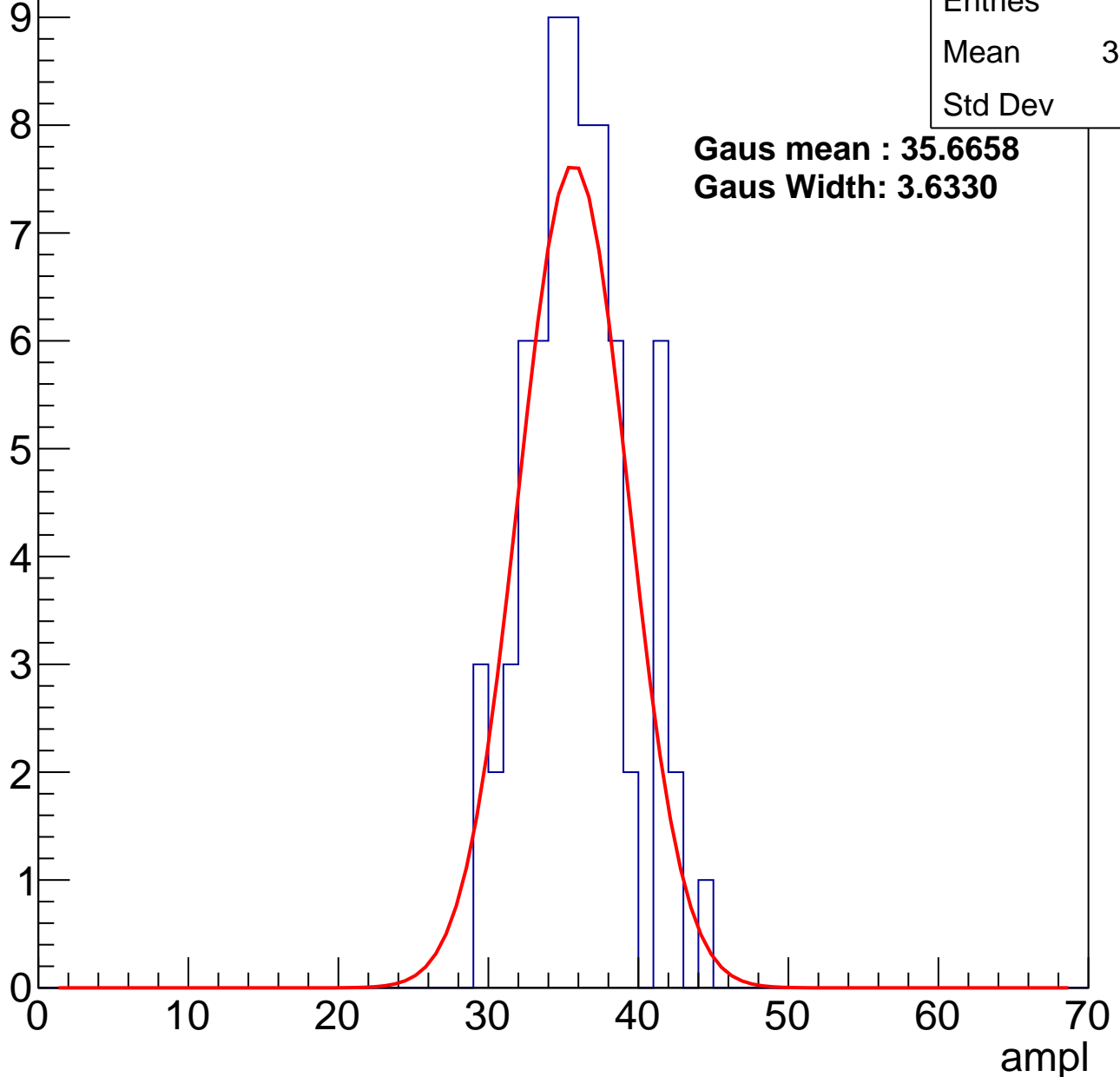
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	35.42
Std Dev	3.36

Gaus mean : 35.6658

Gaus Width: 3.6330



B0L001S, U15-ch29, adc2

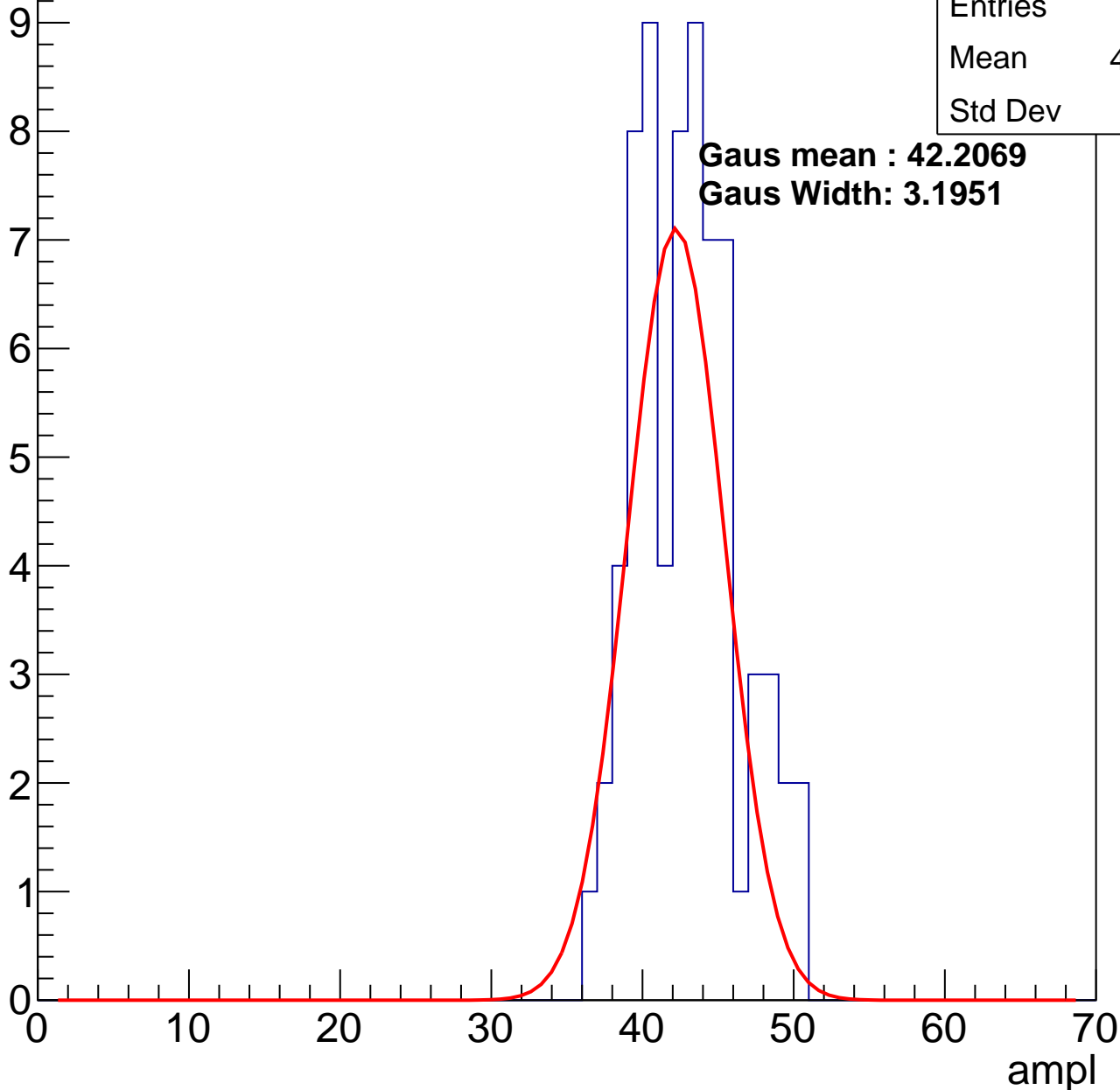
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	42.47
Std Dev	3.32

Gaus mean : 42.2069

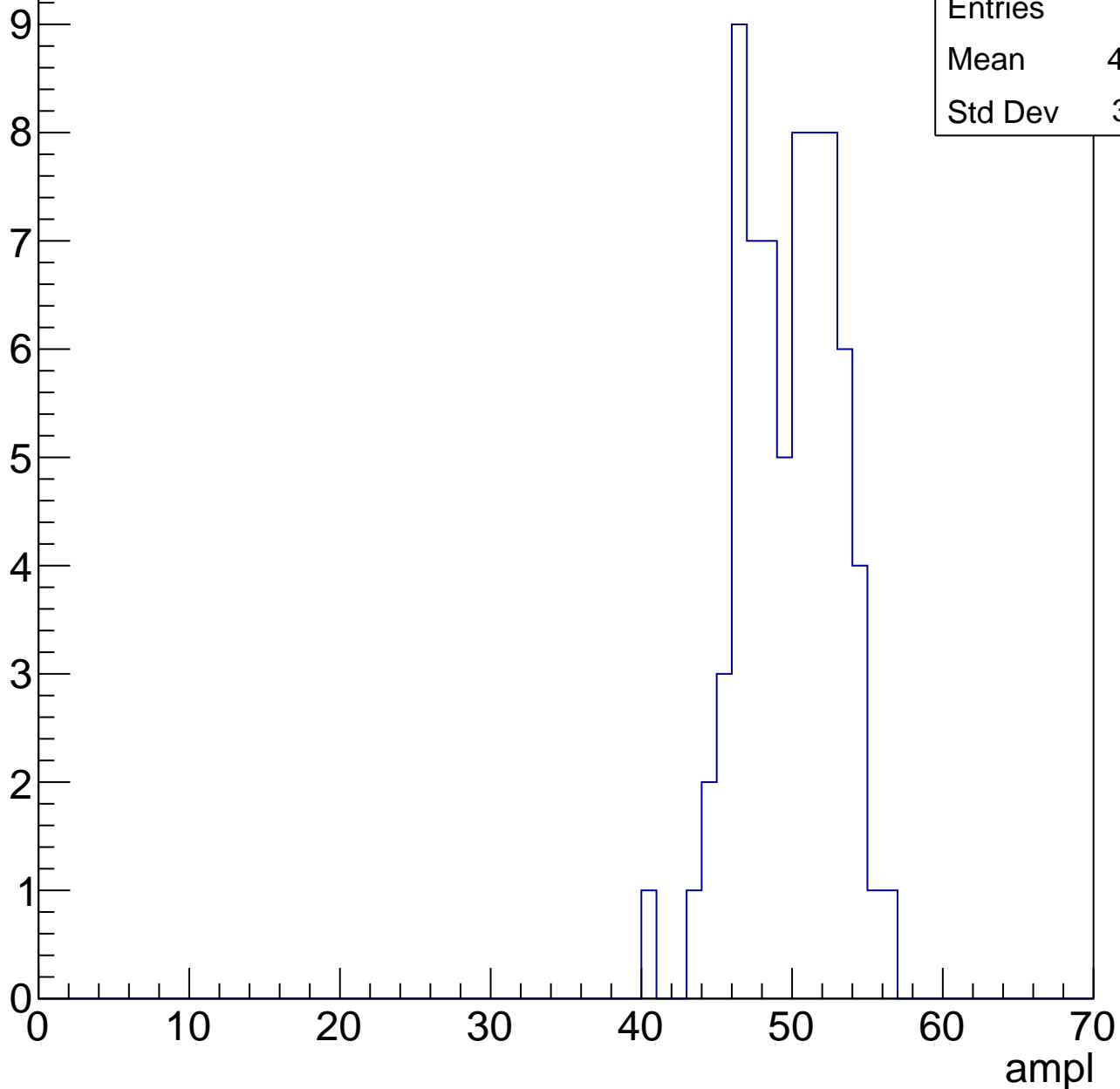
Gaus Width: 3.1951



B0L001S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

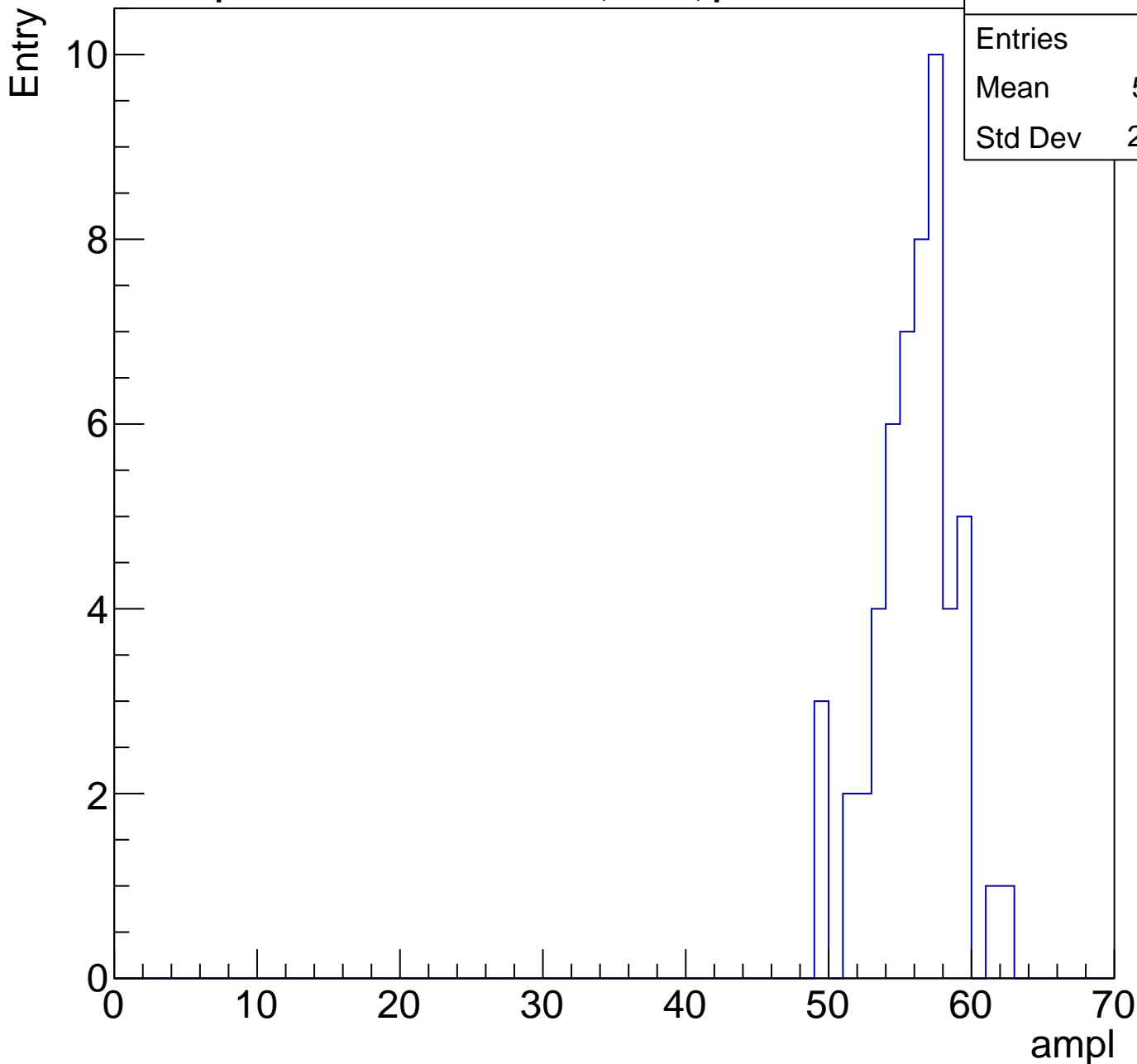
Entry



B0L001S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	53
Mean	55.51
Std Dev	2.799

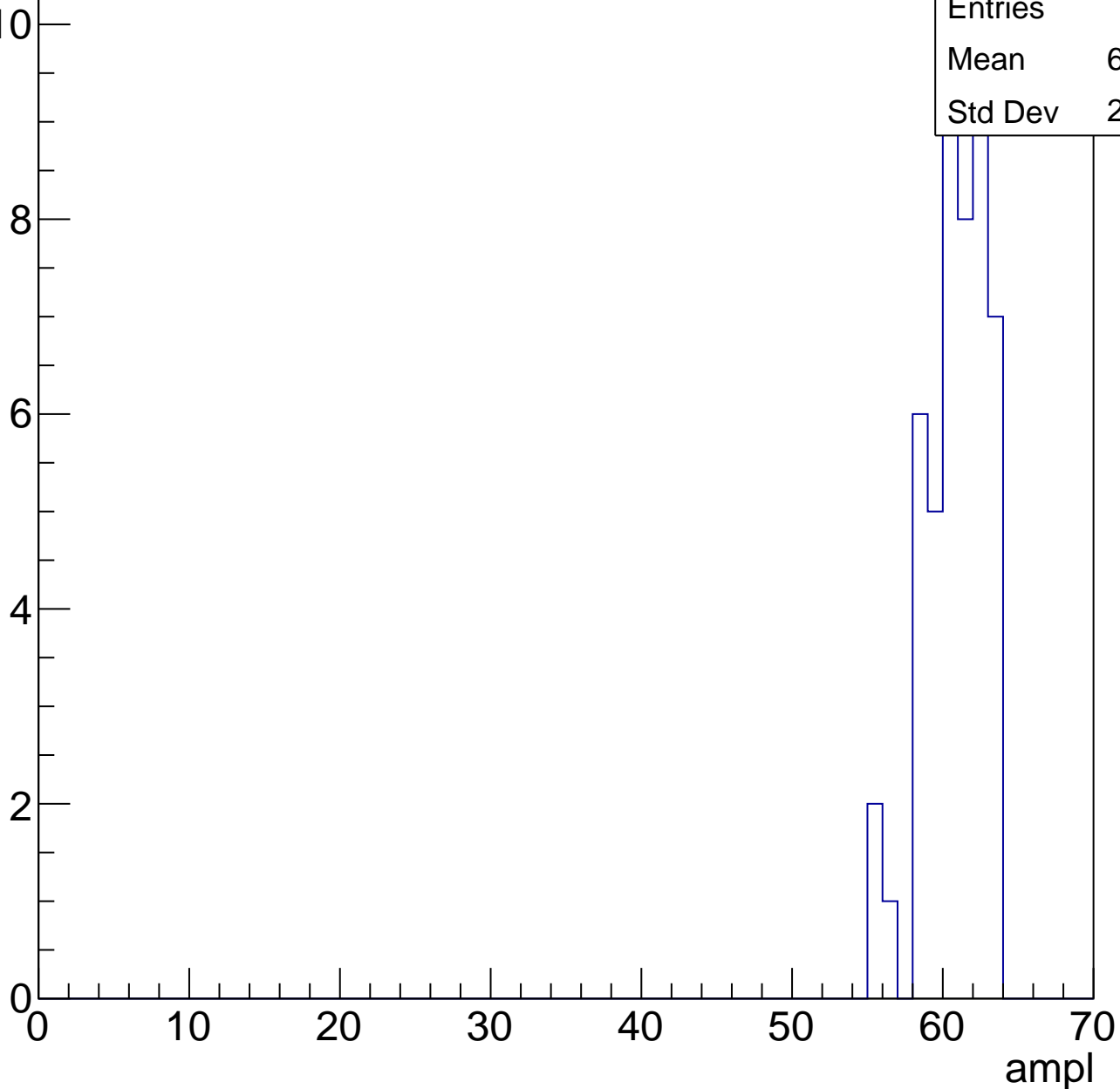


B0L001S, U15-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

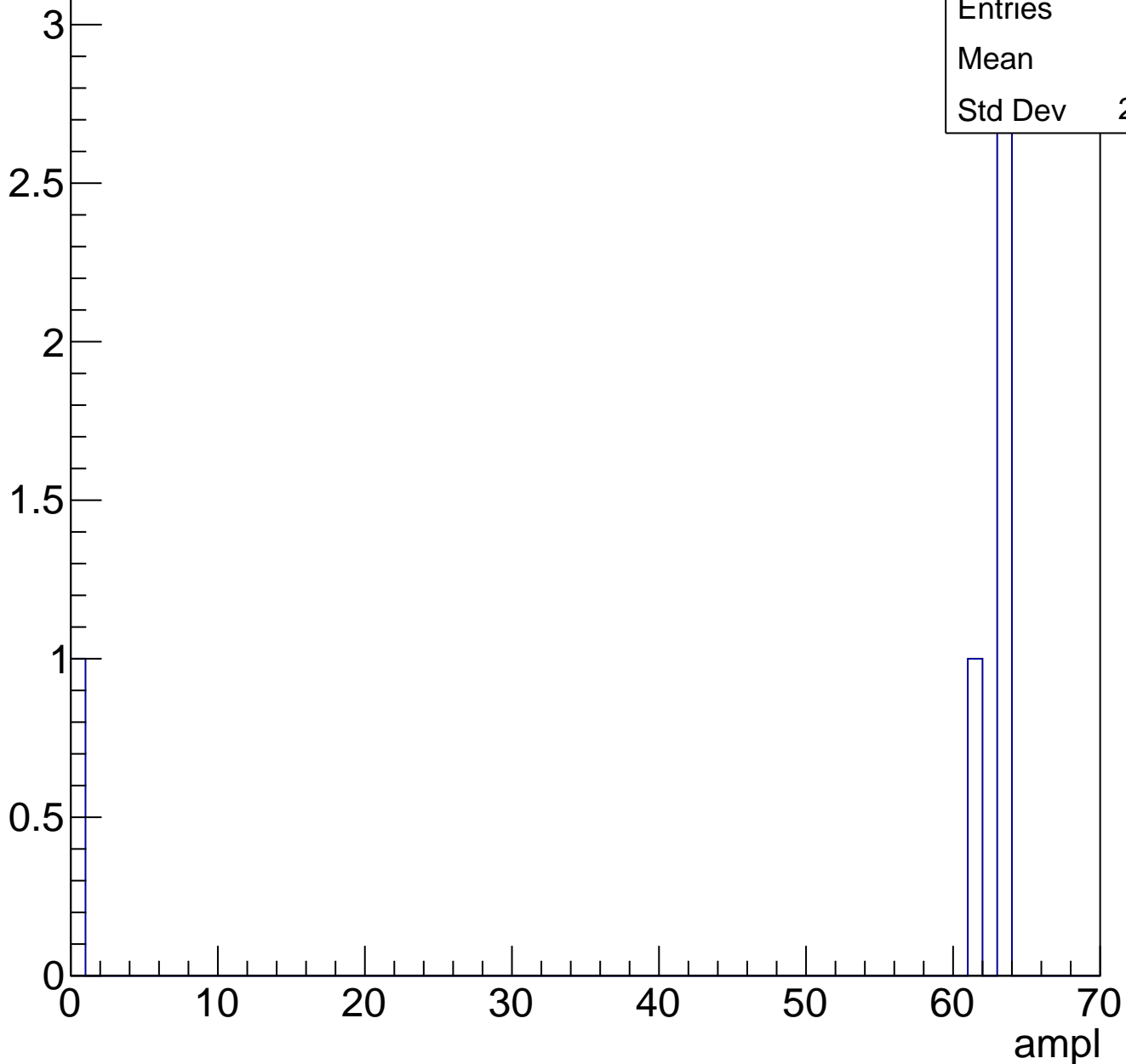
Entries	48
Mean	60.33
Std Dev	2.024



B0L001S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch30, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	28.24
Std Dev	5.824

Gaus mean : 29.7246

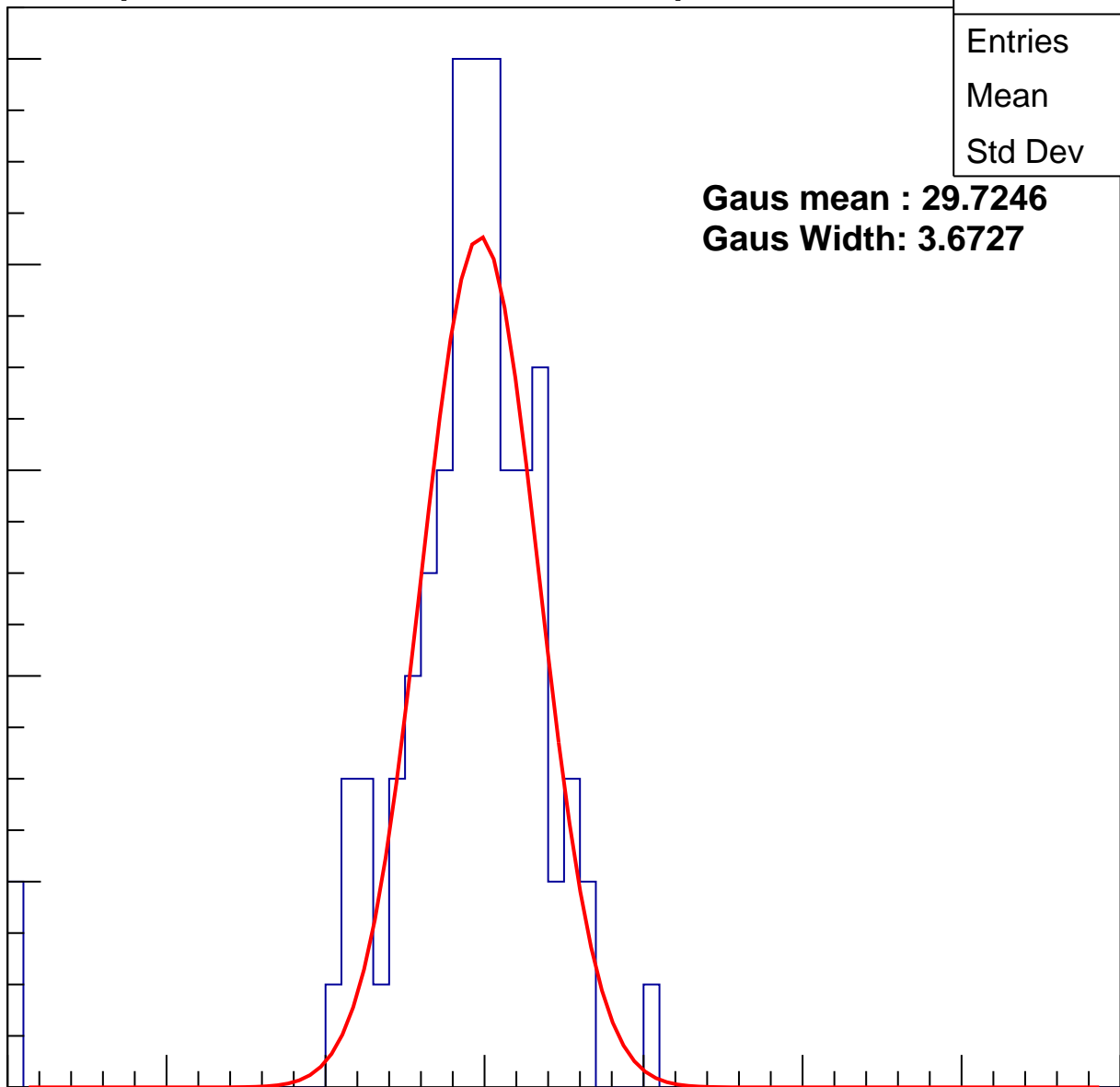
Gaus Width: 3.6727

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch30, adc1

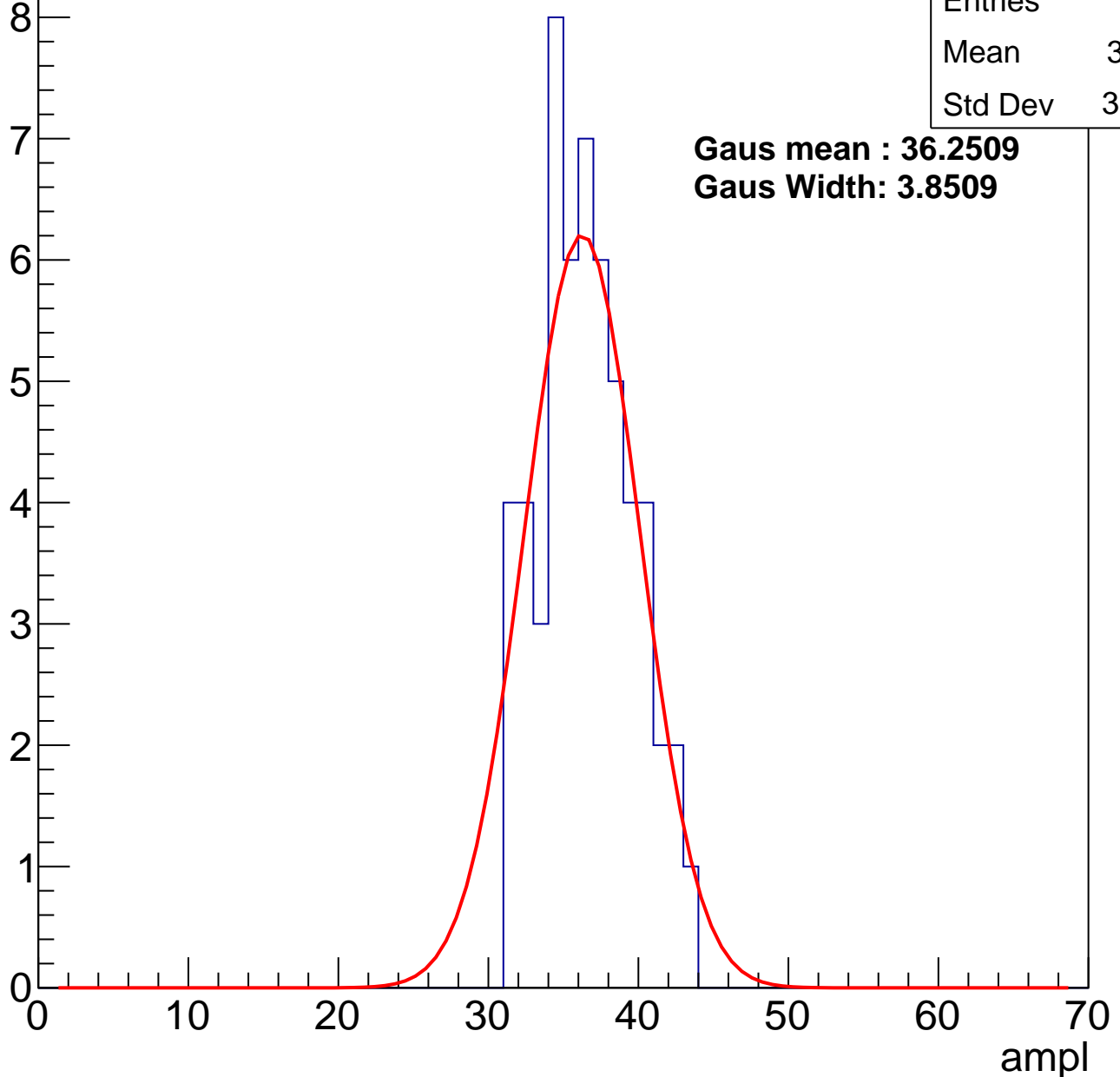
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	36.11
Std Dev	3.063

Gaus mean : 36.2509

Gaus Width: 3.8509



B0L001S, U15-ch30, adc2

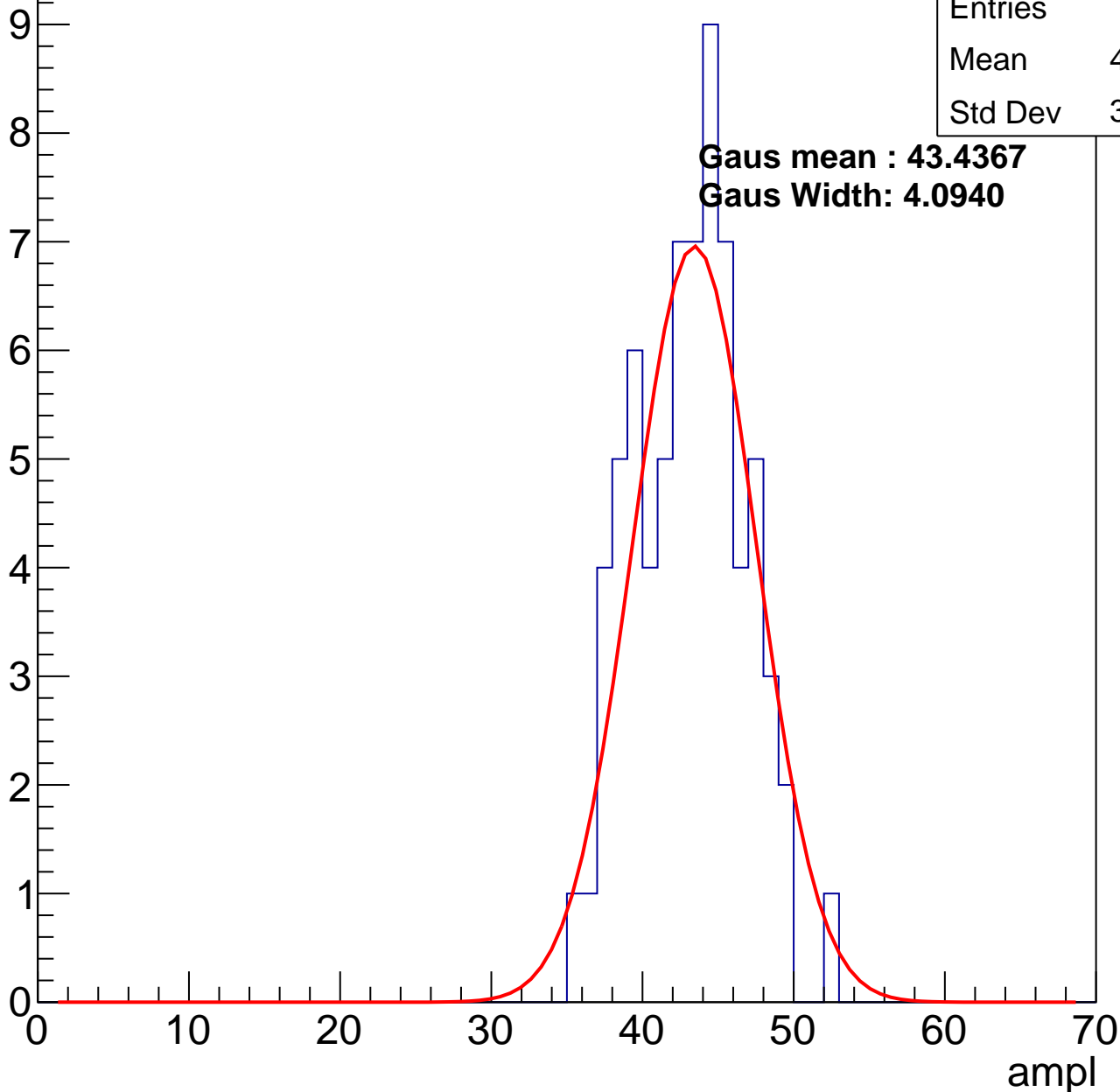
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	42.63
Std Dev	3.585

Gaus mean : 43.4367

Gaus Width: 4.0940

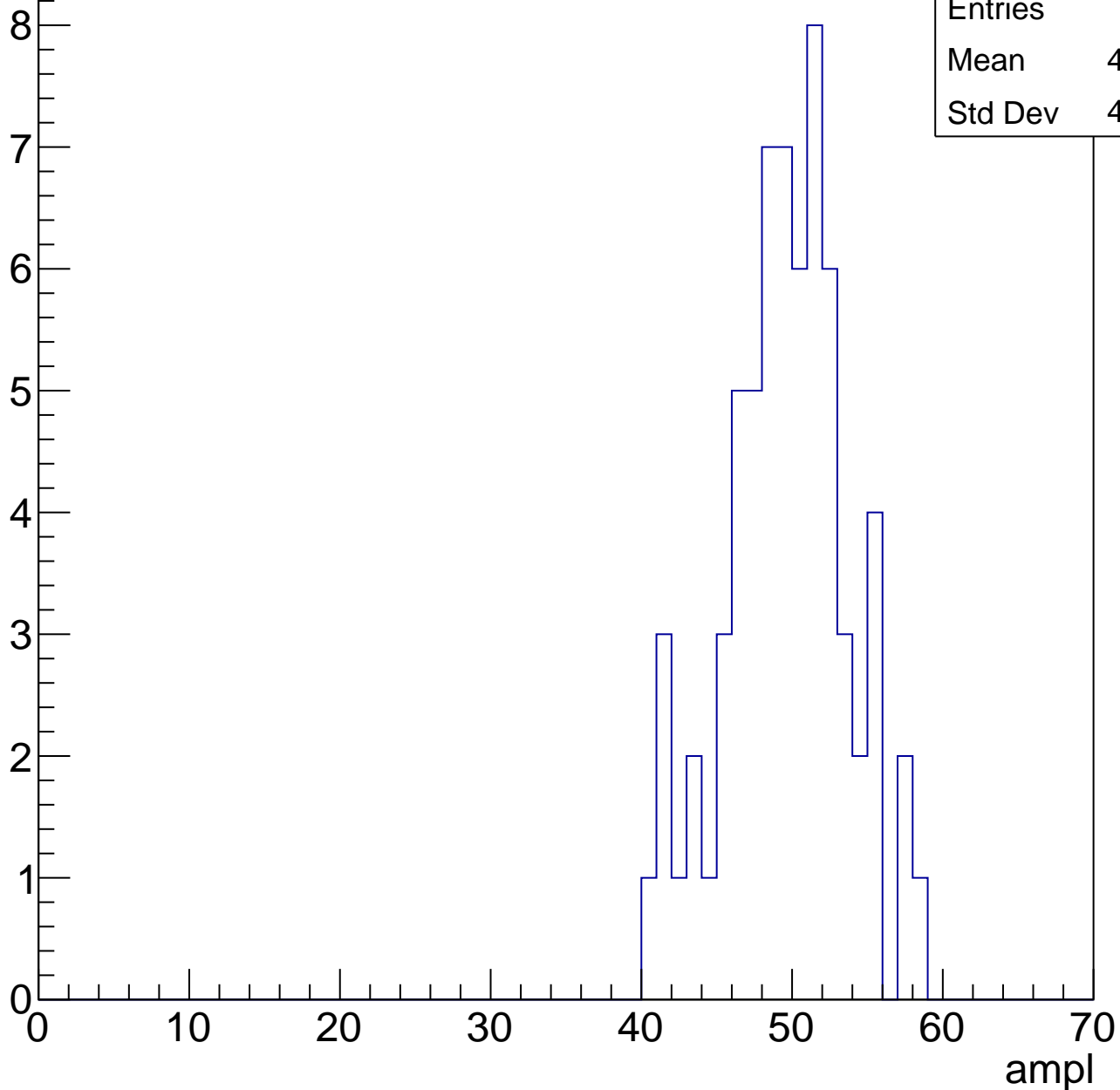


B0L001S, U15-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

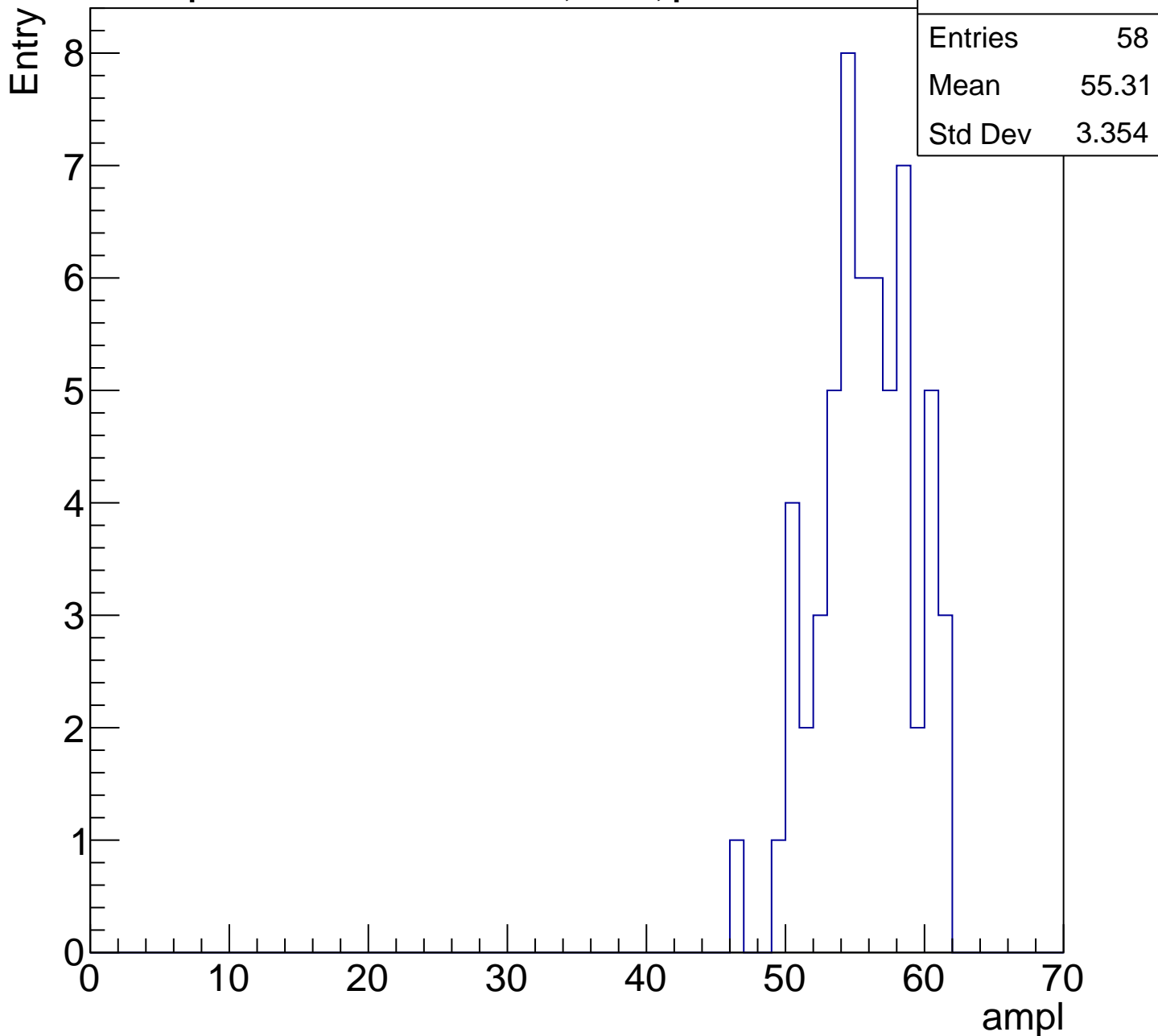
Entry

Entries	67
Mean	49.15
Std Dev	4.016



B0L001S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

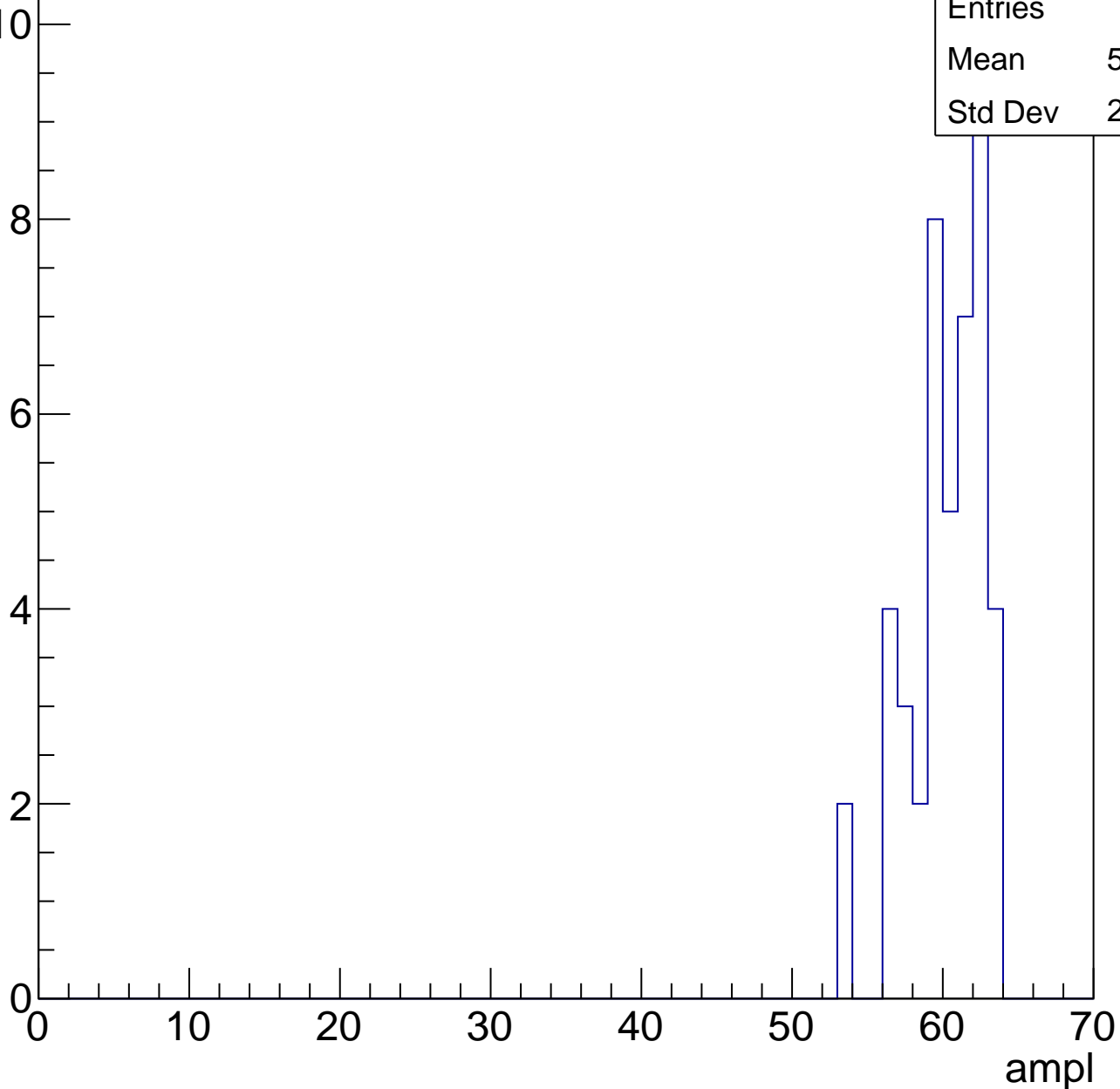


B0L001S, U15-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	59.73
Std Dev	2.516

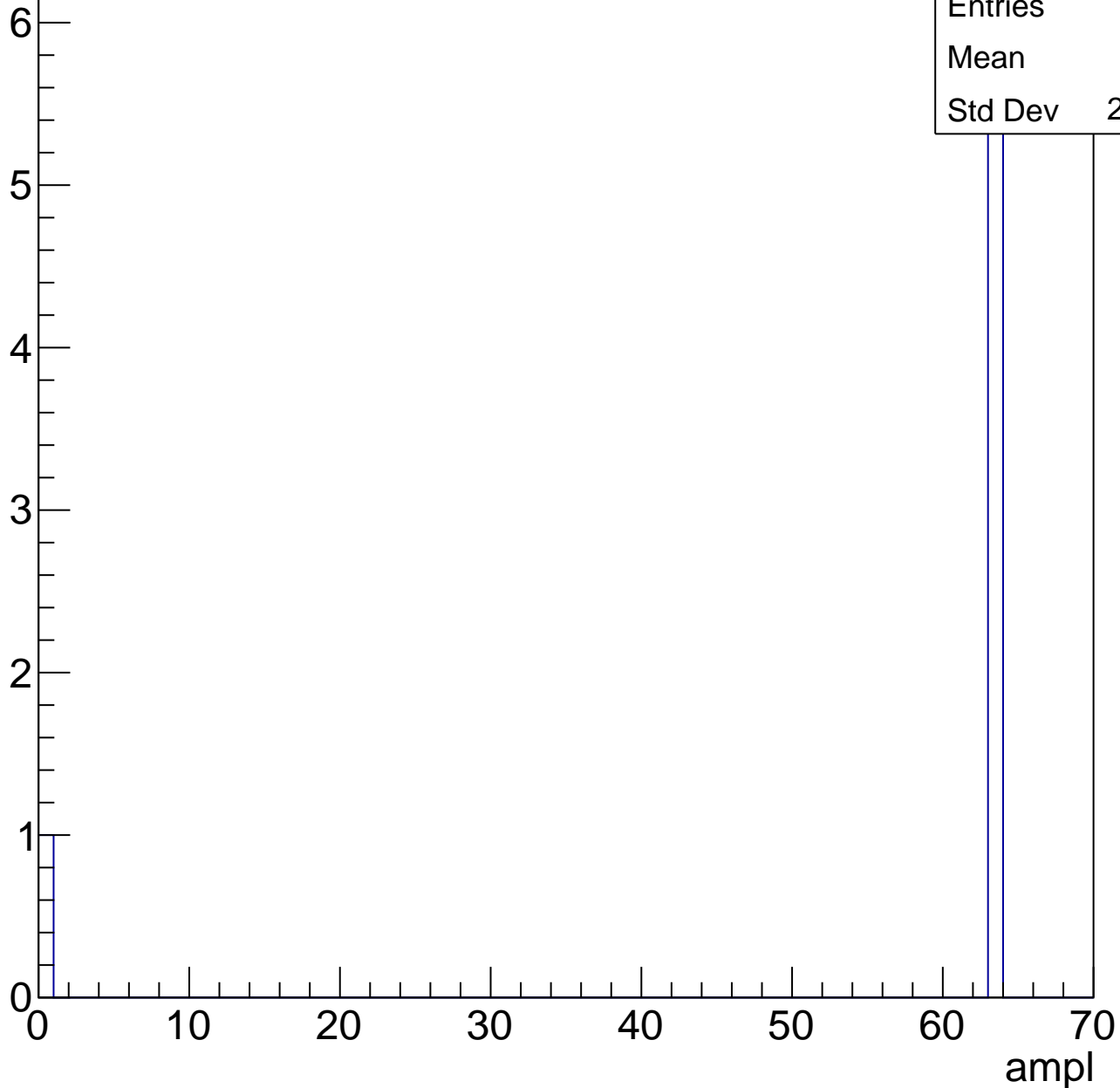


B0L001S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	7
Mean	54
Std Dev	22.05



B0L001S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch31, adc0

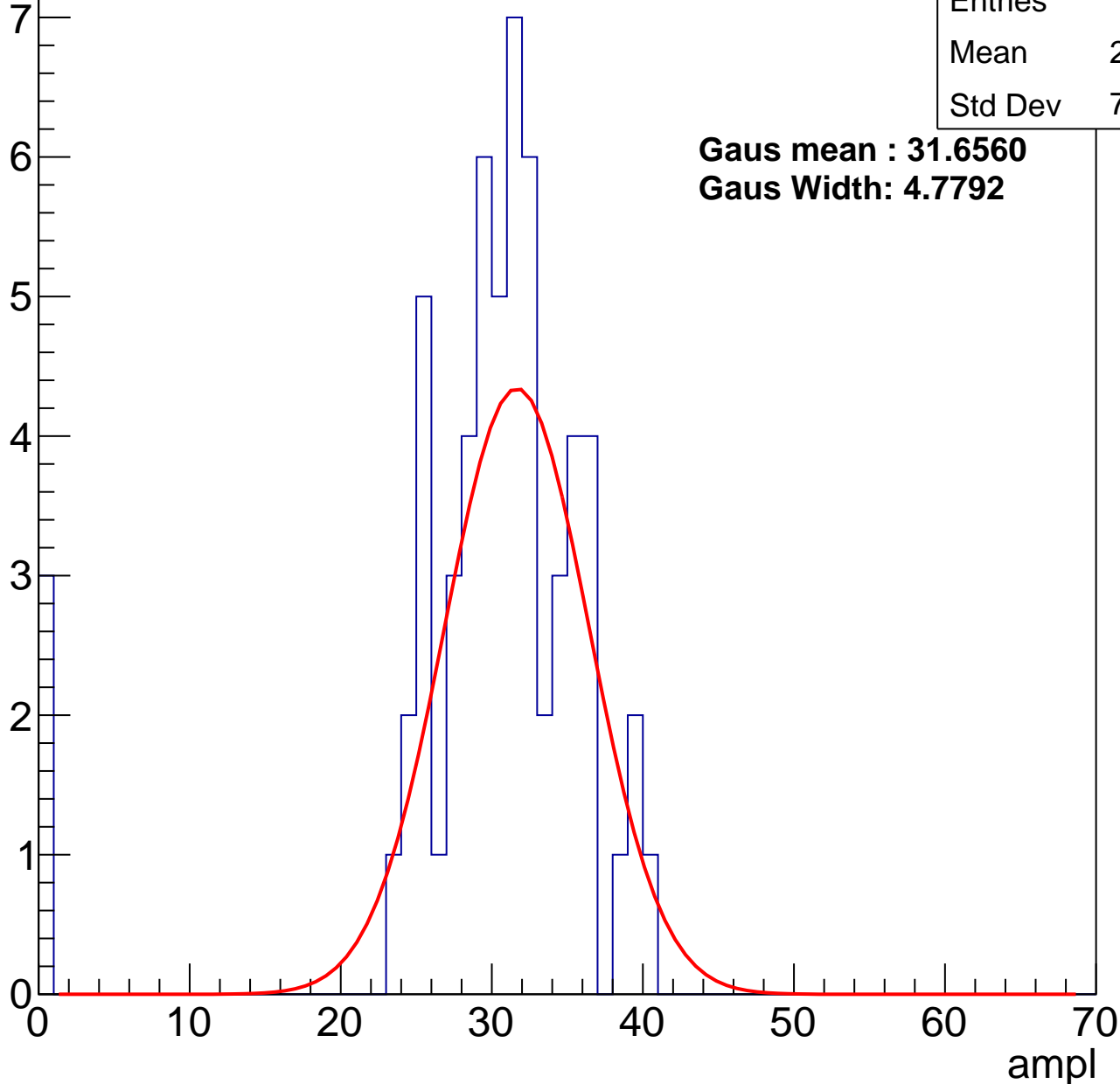
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.27
Std Dev	7.795

Gaus mean : 31.6560

Gaus Width: 4.7792



B0L001S, U15-ch31, adc1

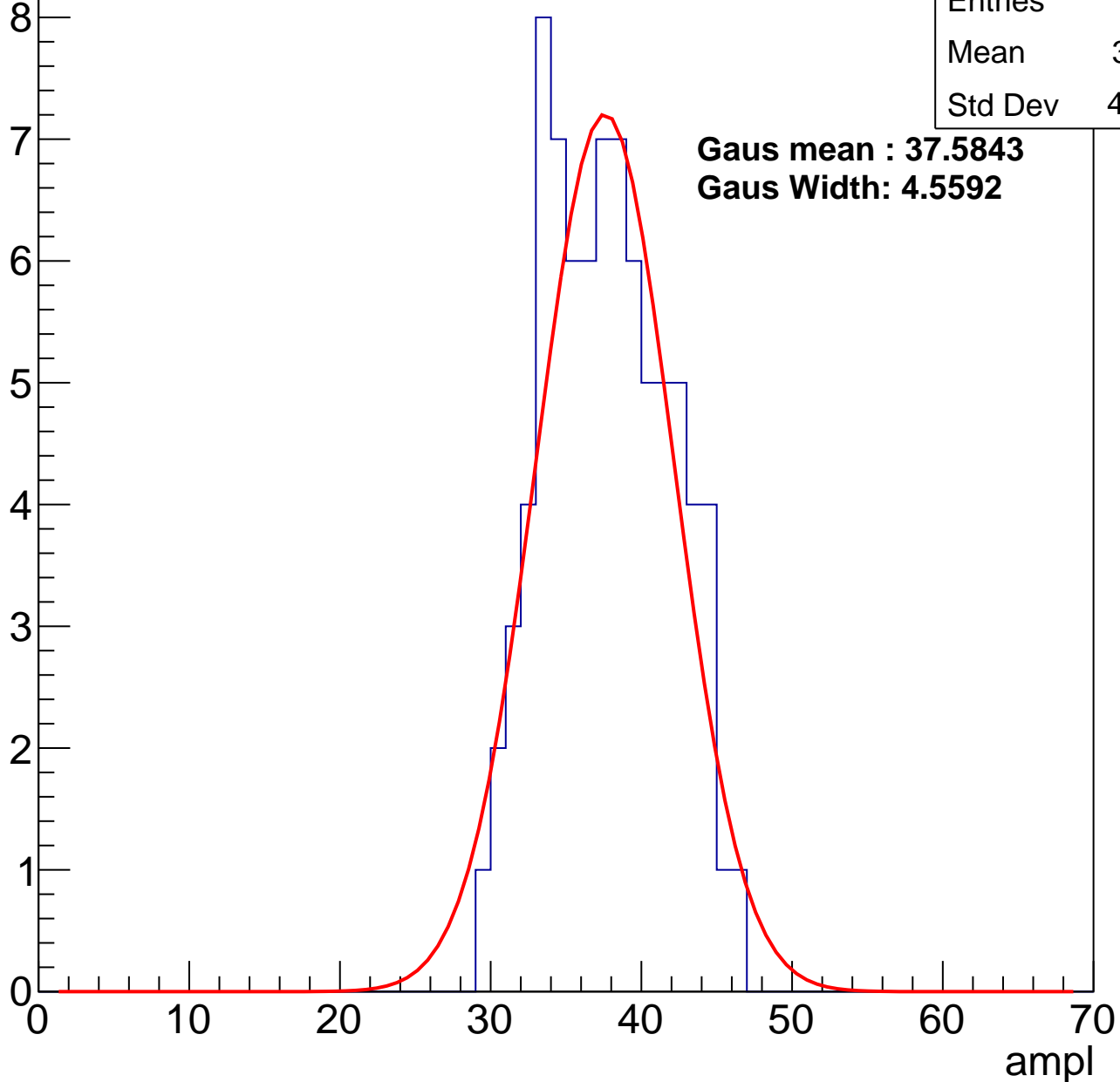
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	37.21
Std Dev	4.063

Gaus mean : 37.5843

Gaus Width: 4.5592



B0L001S, U15-ch31, adc2

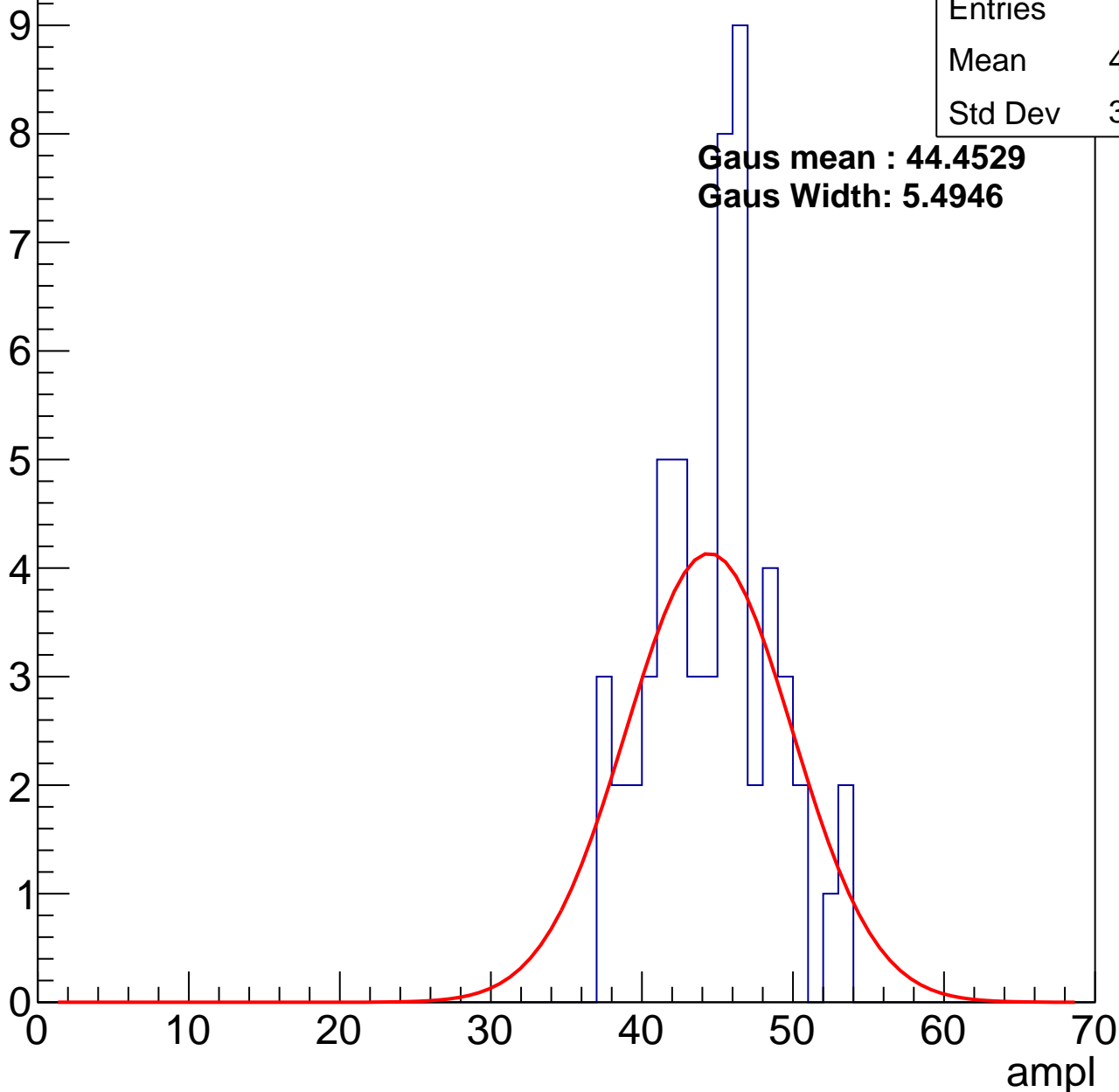
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.32
Std Dev	3.925

Gaus mean : 44.4529

Gaus Width: 5.4946

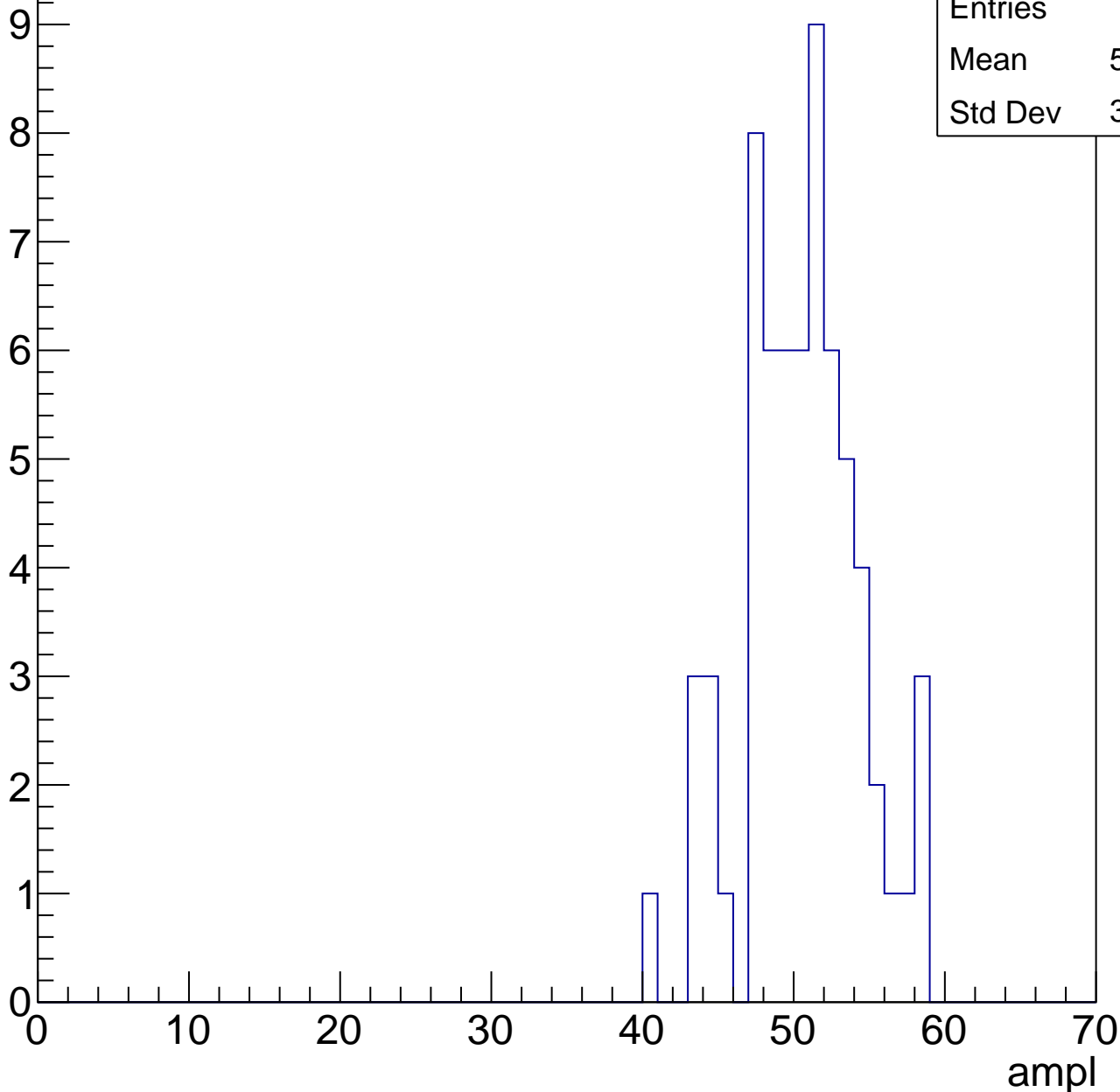


B0L001S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.05
Std Dev	3.825

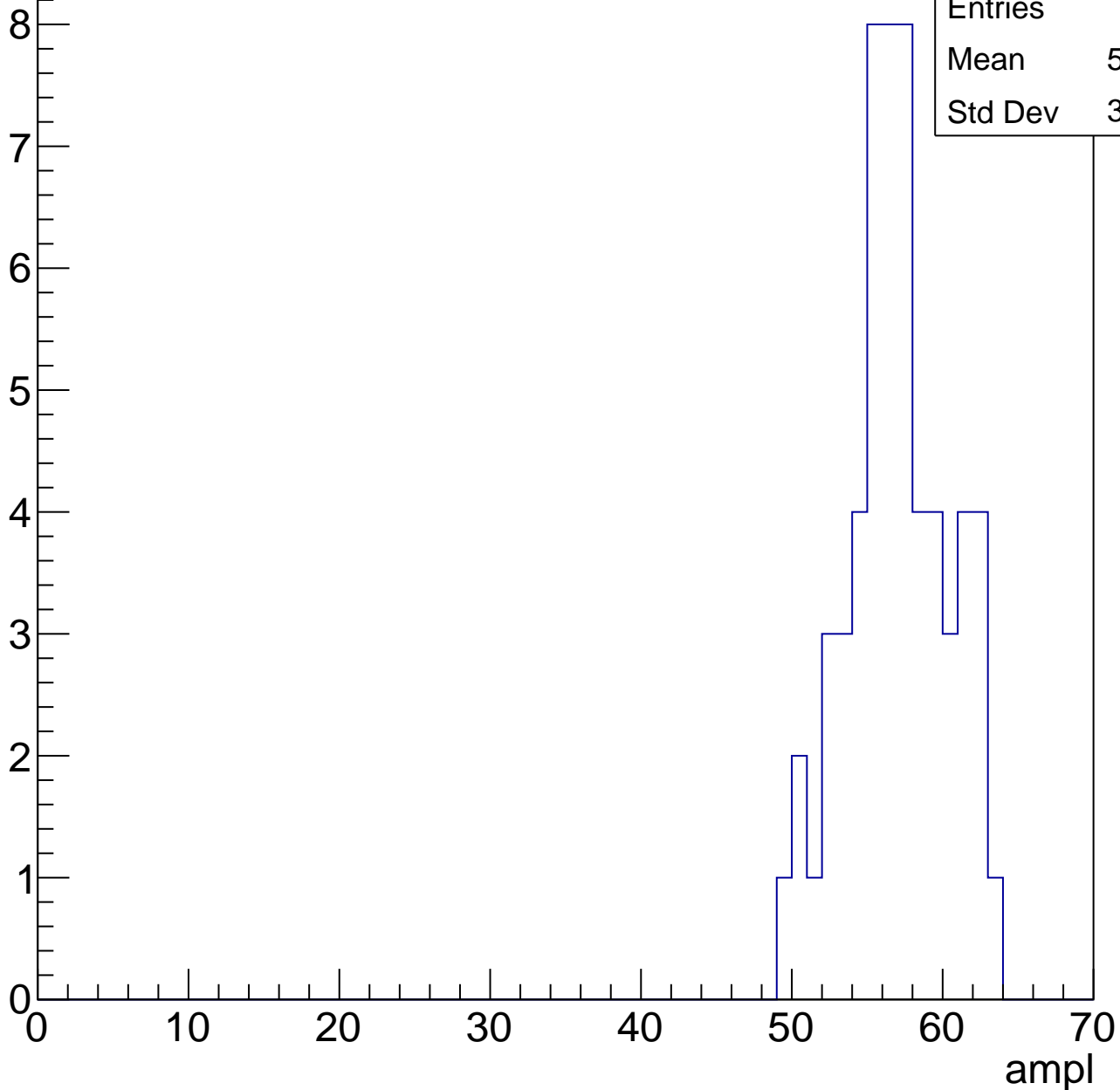


B0L001S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.52
Std Dev	3.297

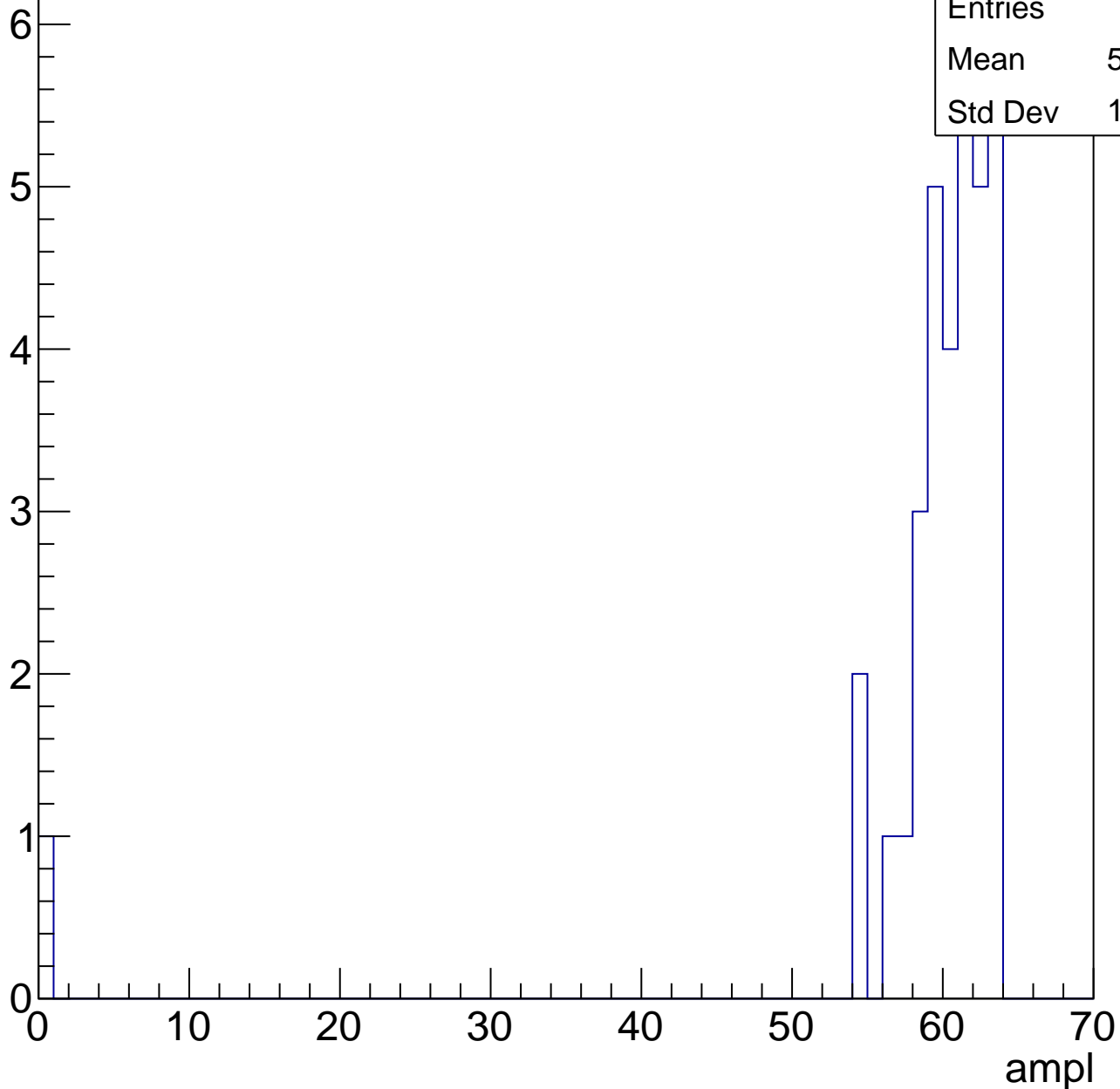


B0L001S, U15-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

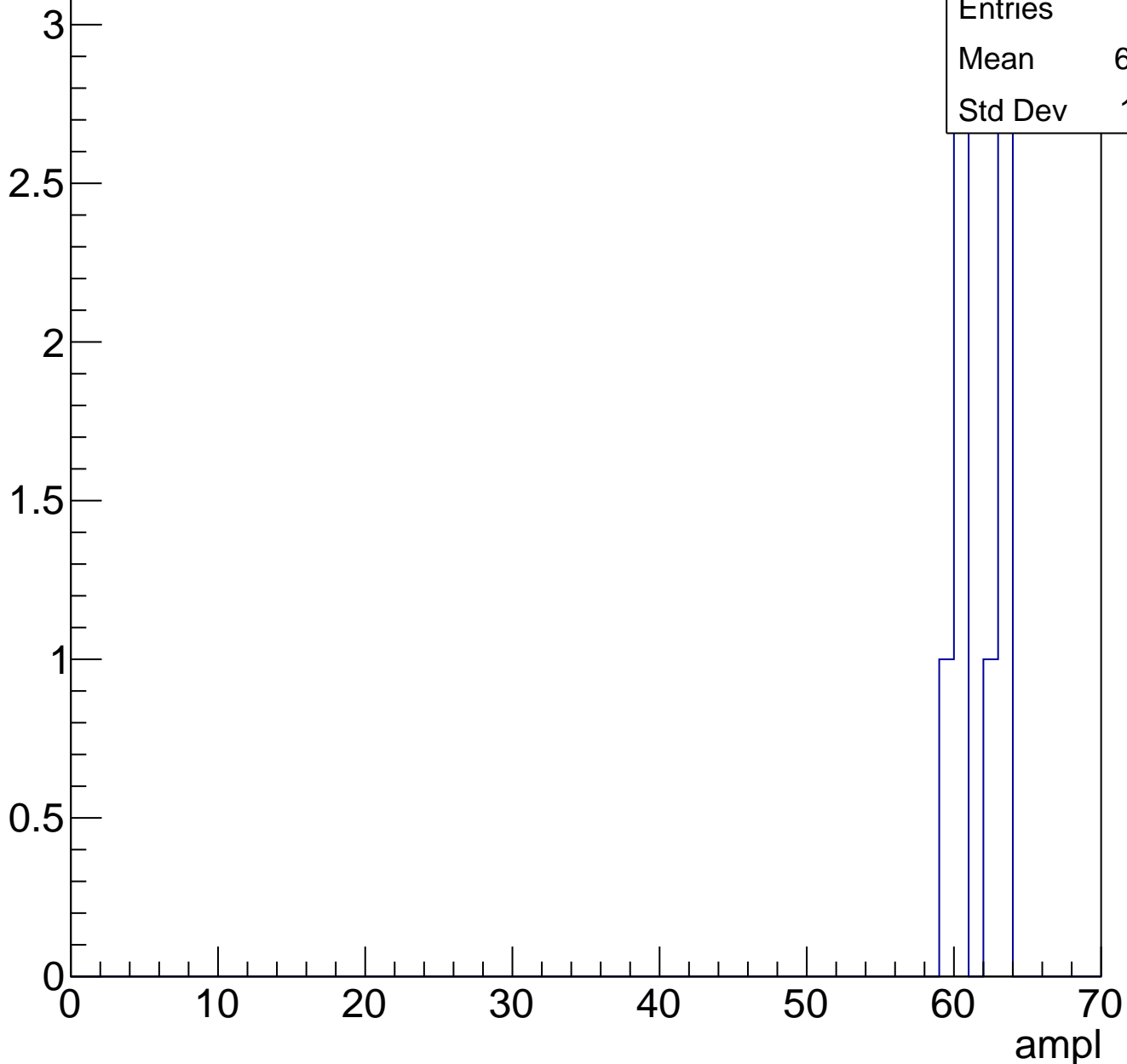
Entries	34
Mean	58.35
Std Dev	10.43



B0L001S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch32, adc0

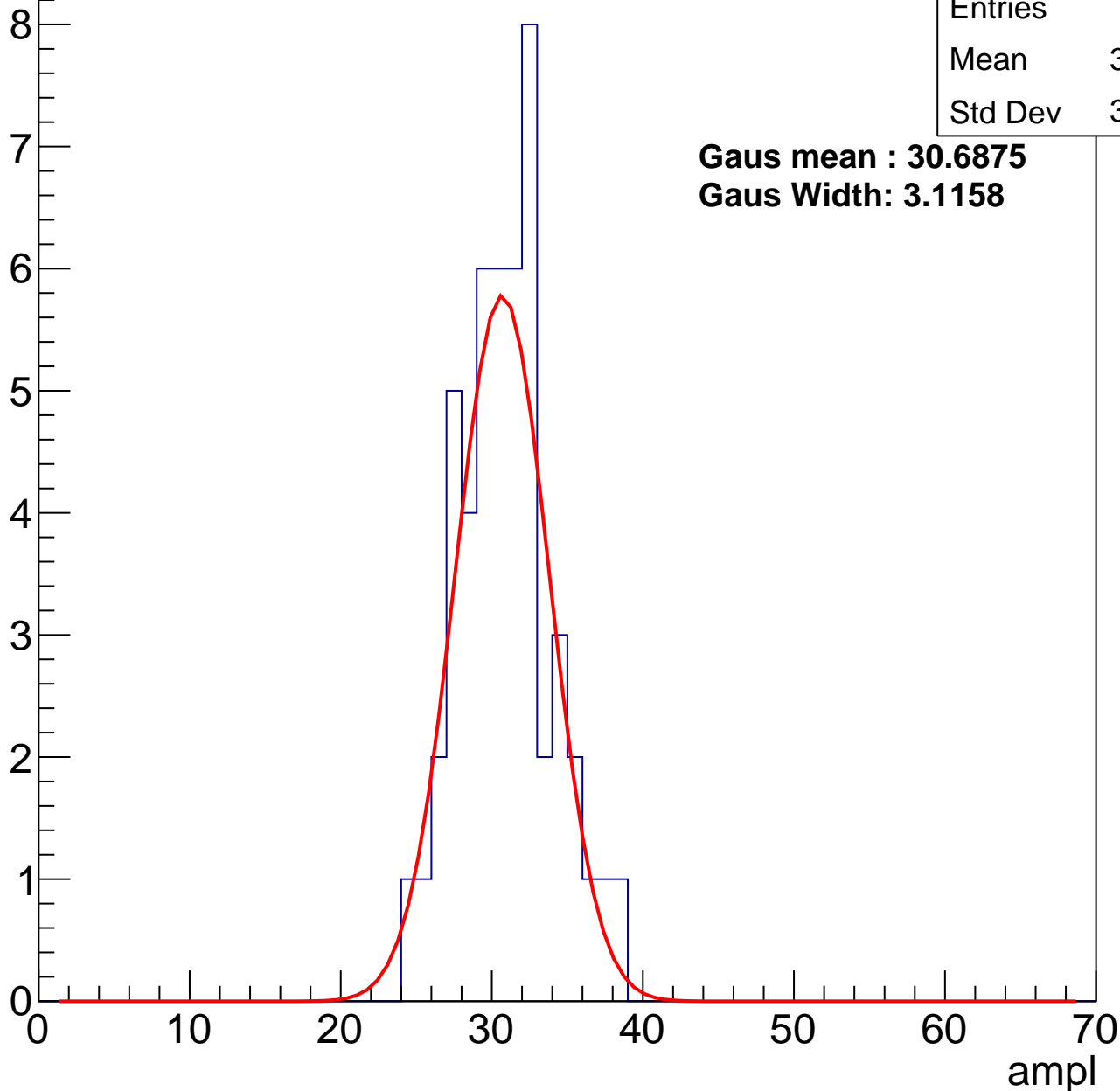
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	30.47
Std Dev	3.038

Gaus mean : 30.6875

Gaus Width: 3.1158



B0L001S, U15-ch32, adc1

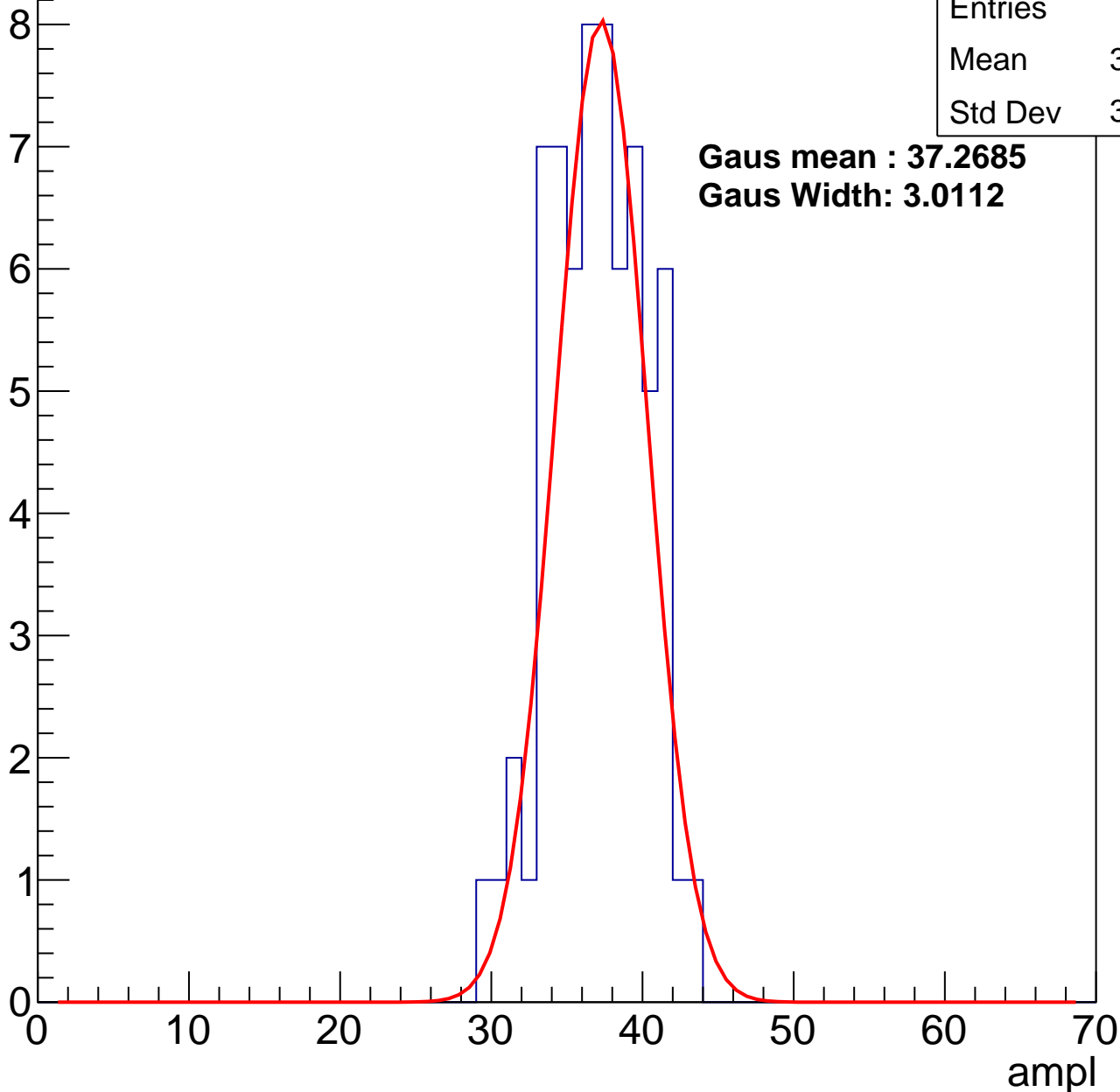
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.54
Std Dev	3.087

Gaus mean : 37.2685

Gaus Width: 3.0112



B0L001S, U15-ch32, adc2

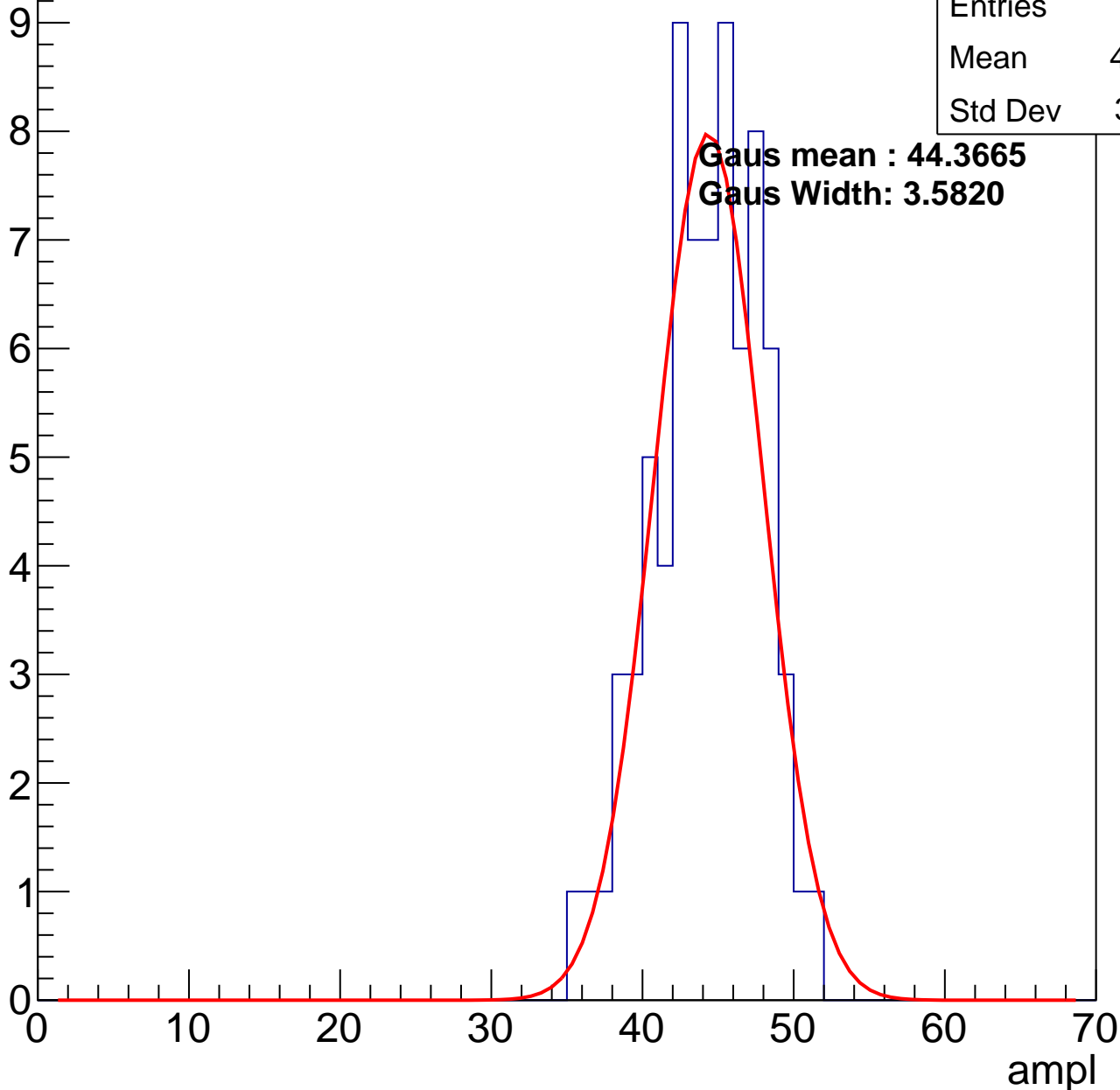
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.77
Std Dev	3.451

Gaus mean : 44.3665

Gaus Width: 3.5820

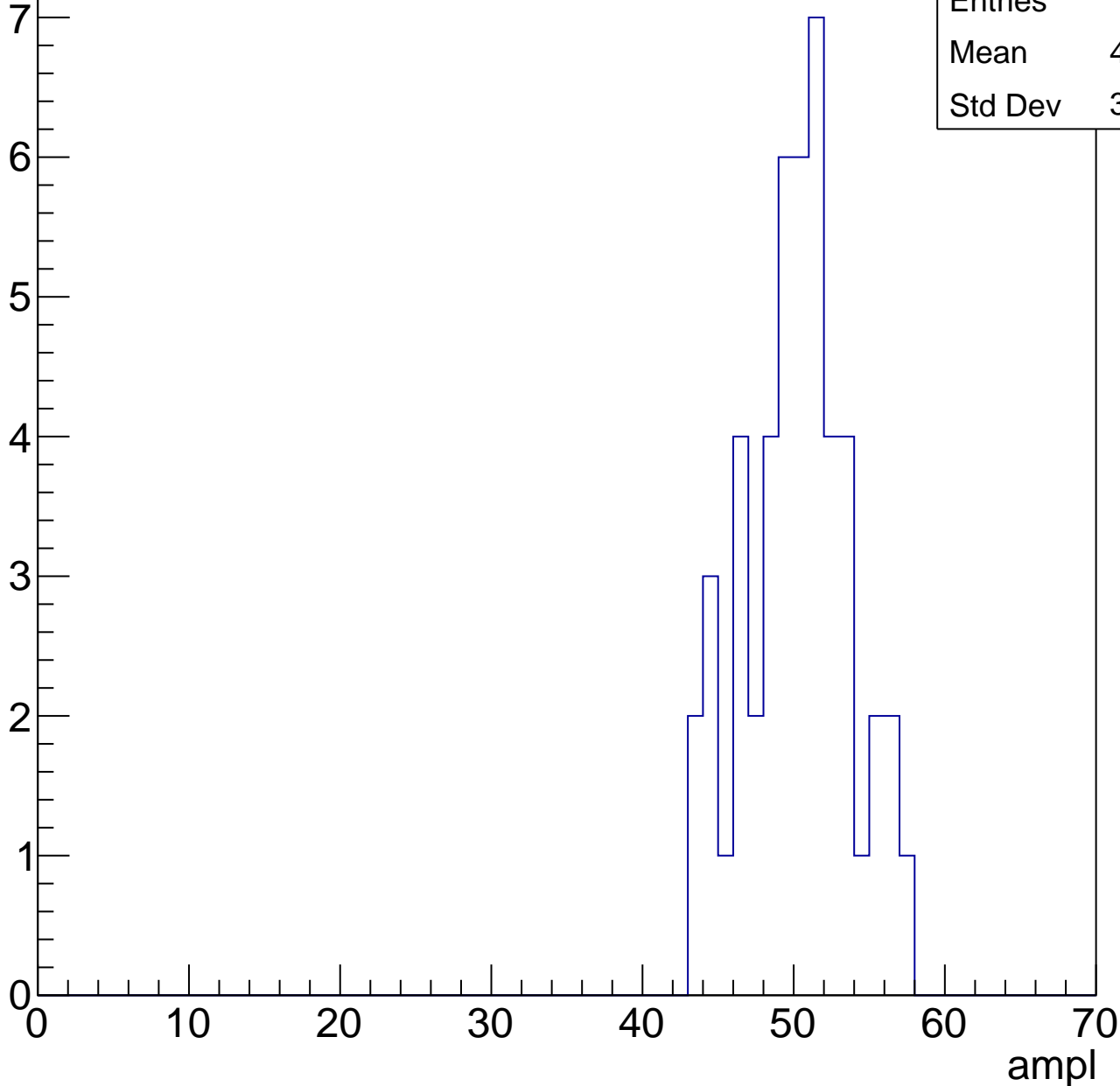


B0L001S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	49.73
Std Dev	3.433

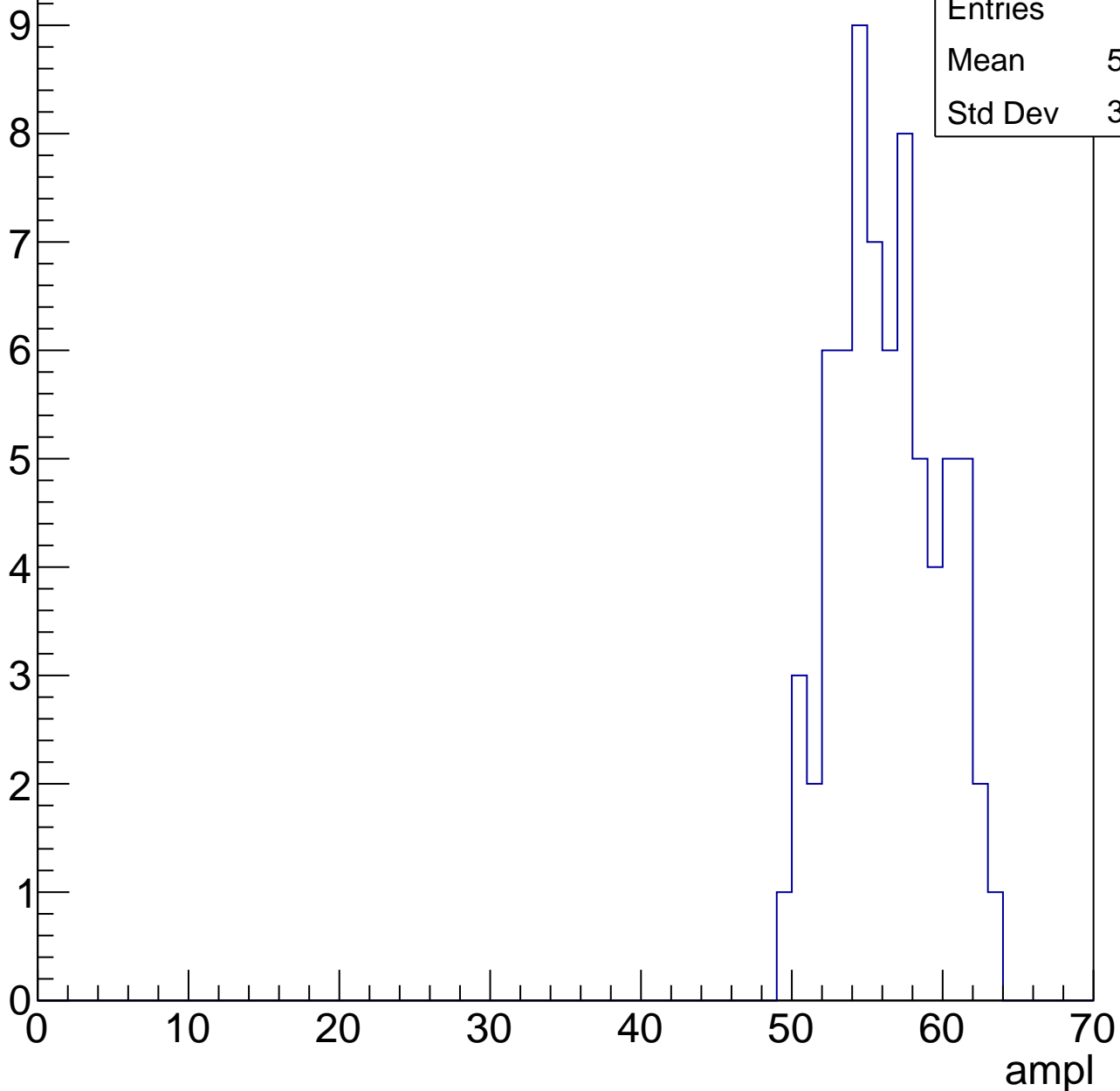


B0L001S, U15-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	55.89
Std Dev	3.357

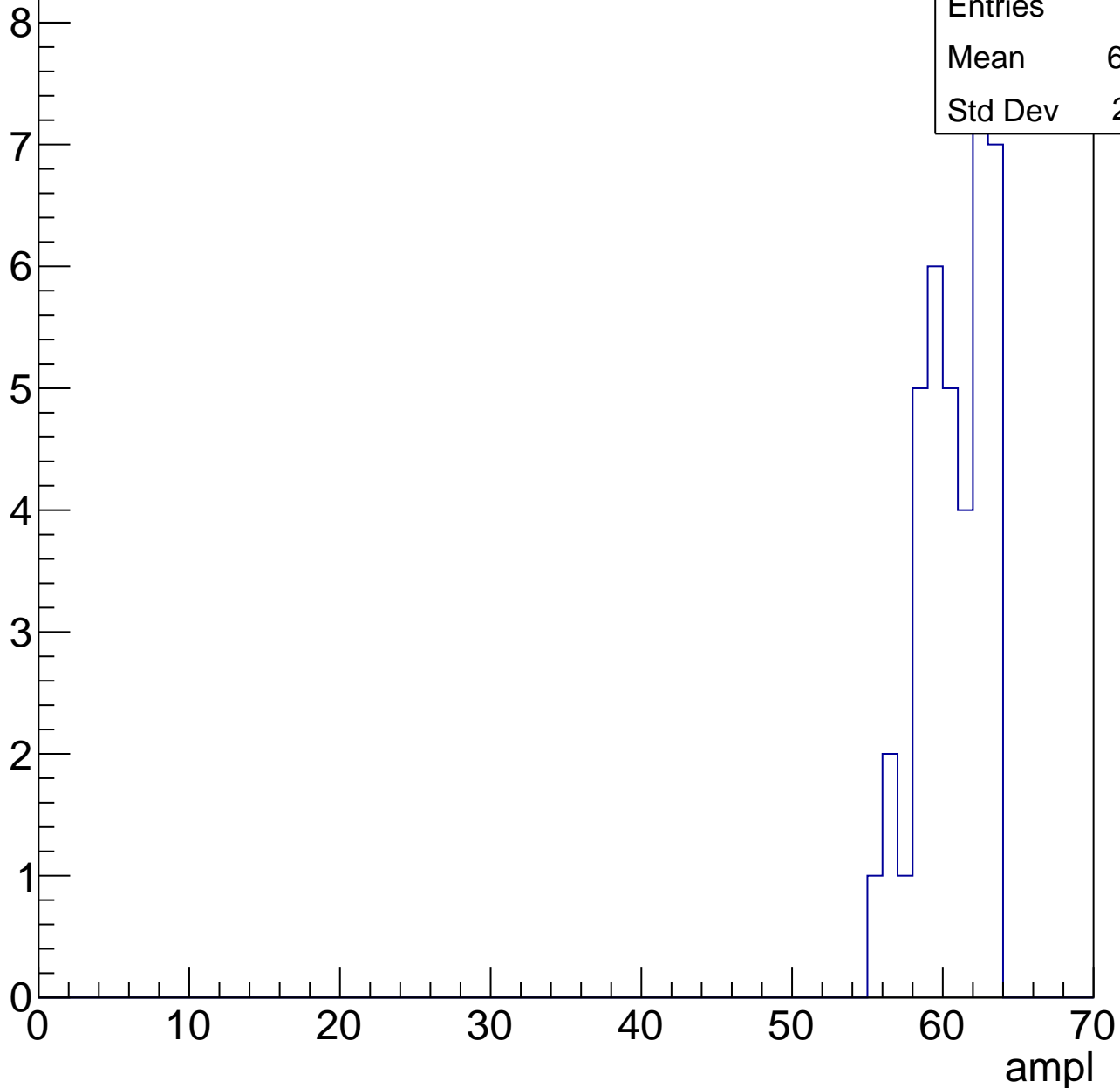


B0L001S, U15-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

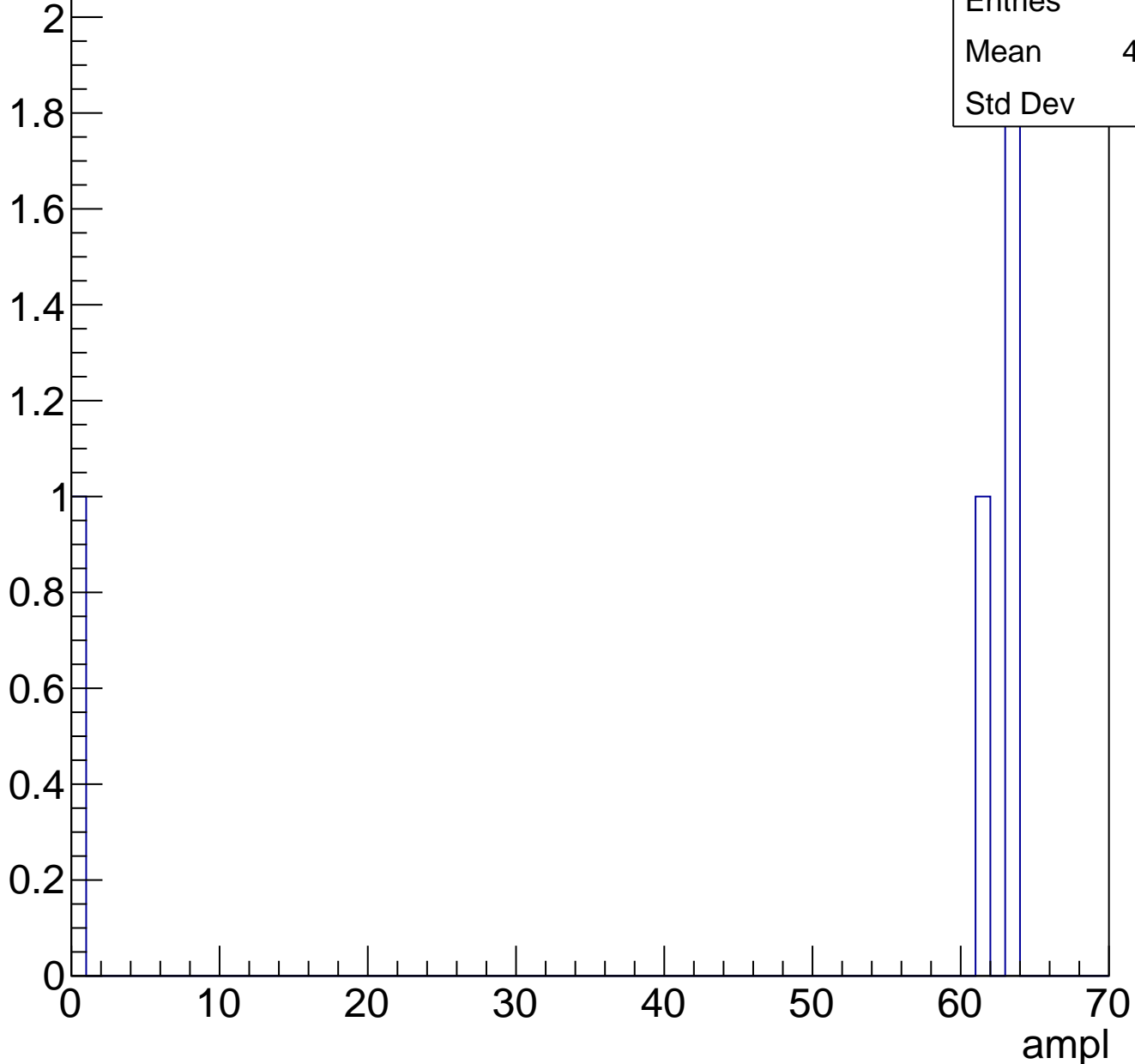
Entries	39
Mean	60.23
Std Dev	2.201



B0L001S, U15-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	46.75
Std Dev	27

B0L001S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch33, adc0

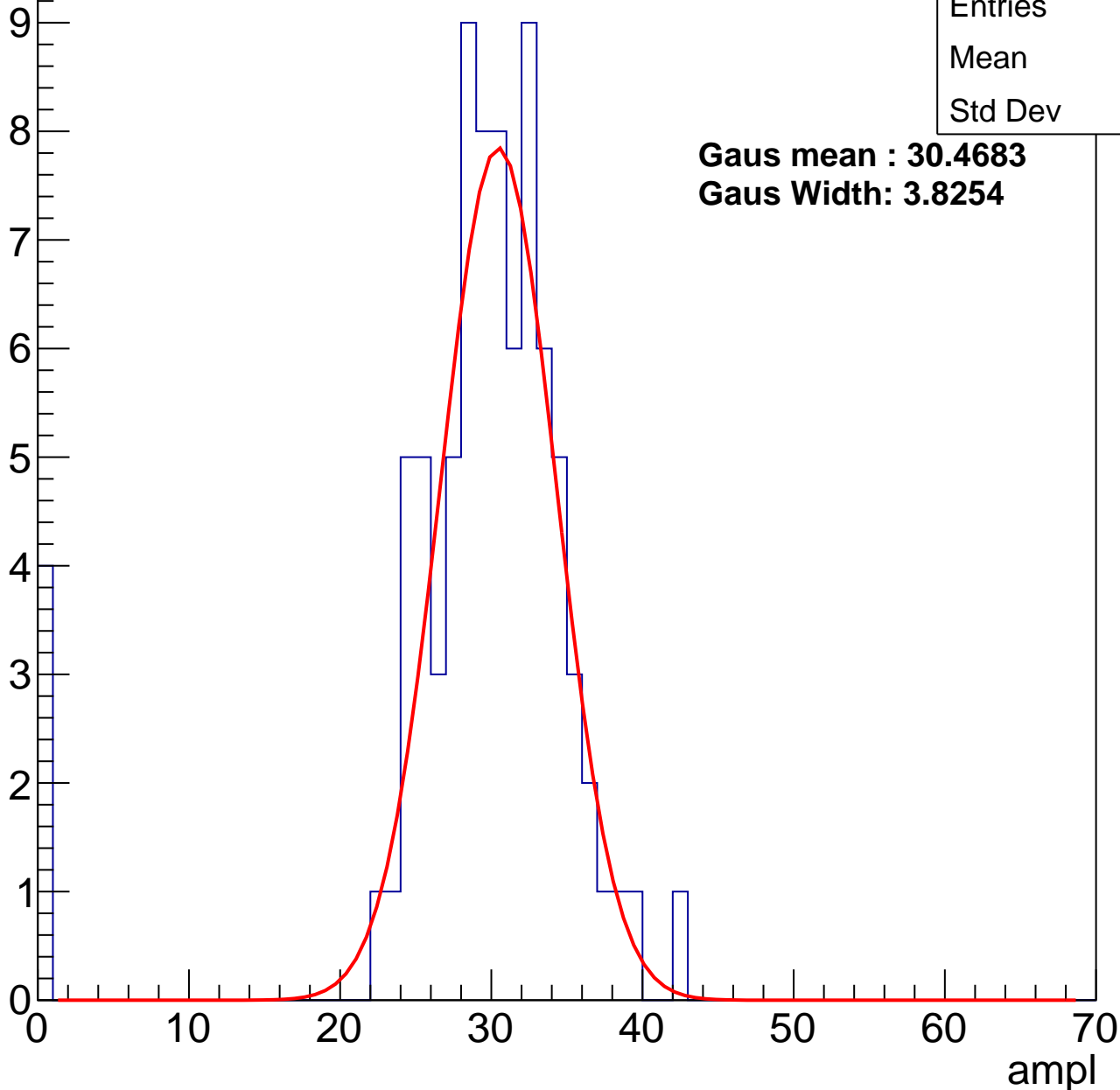
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	28.6
Std Dev	7.44

Gaus mean : 30.4683

Gaus Width: 3.8254



B0L001S, U15-ch33, adc1

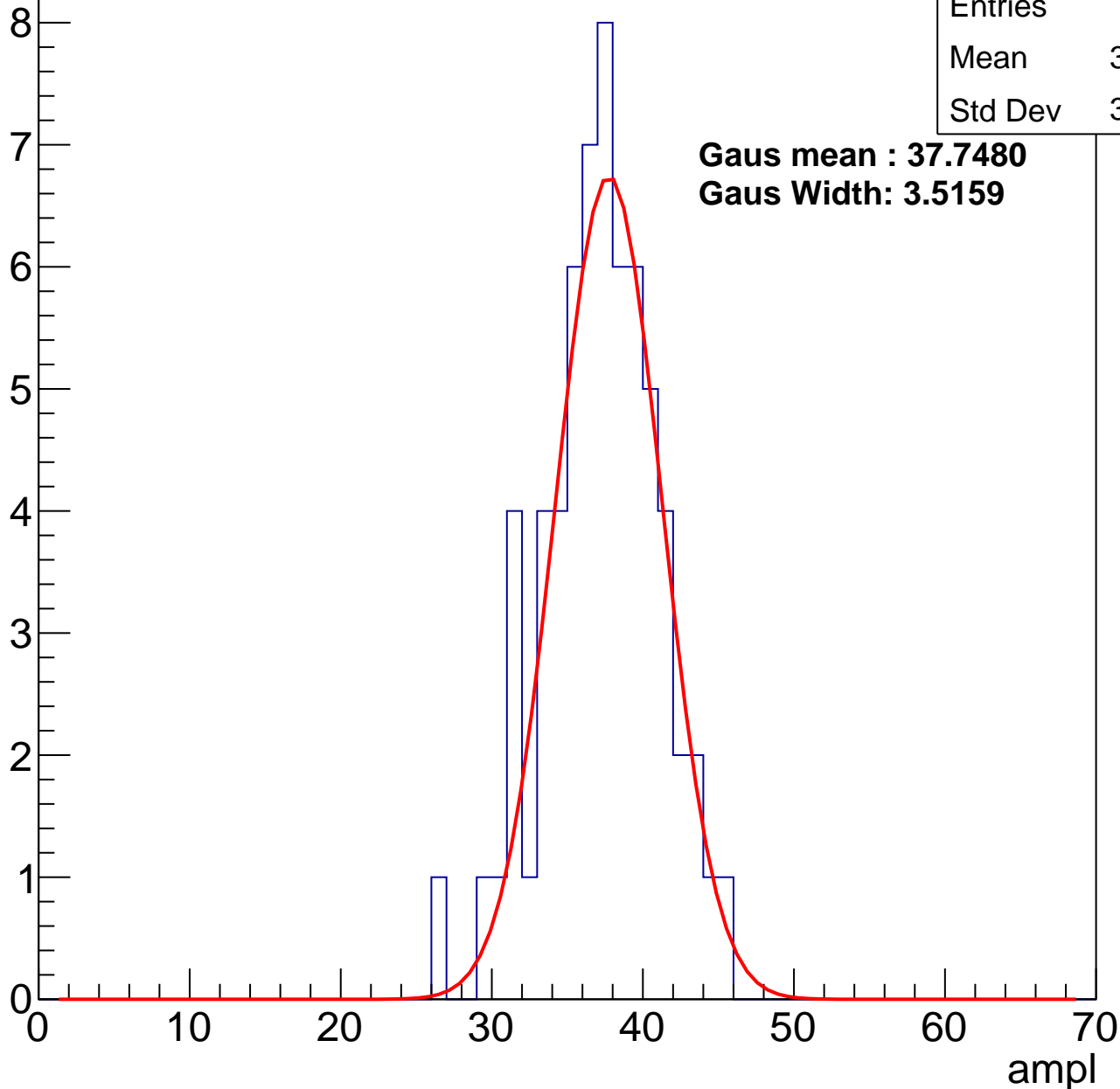
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	36.75
Std Dev	3.758

Gaus mean : 37.7480

Gaus Width: 3.5159



B0L001S, U15-ch33, adc2

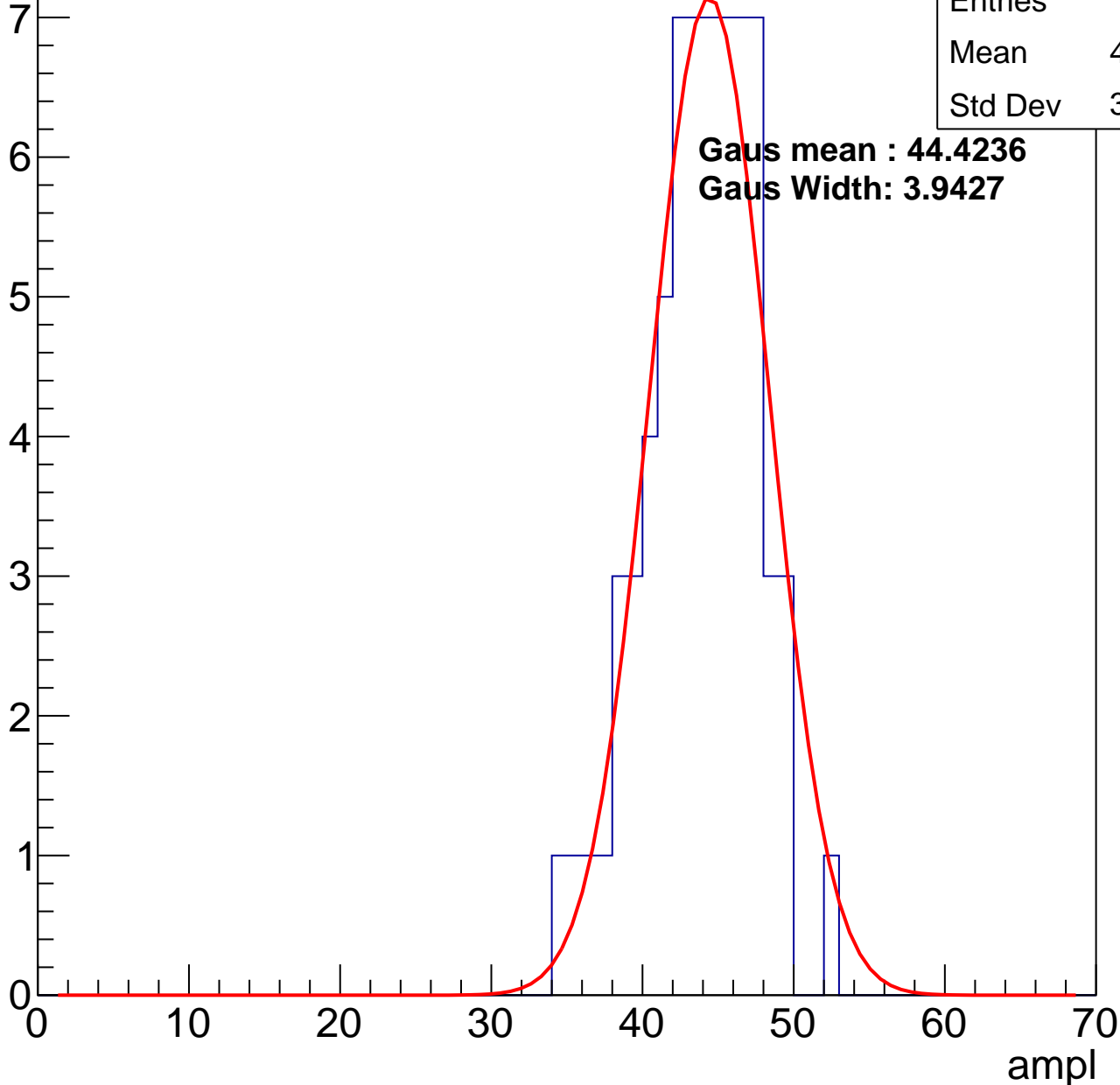
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.38
Std Dev	3.598

Gaus mean : 44.4236

Gaus Width: 3.9427

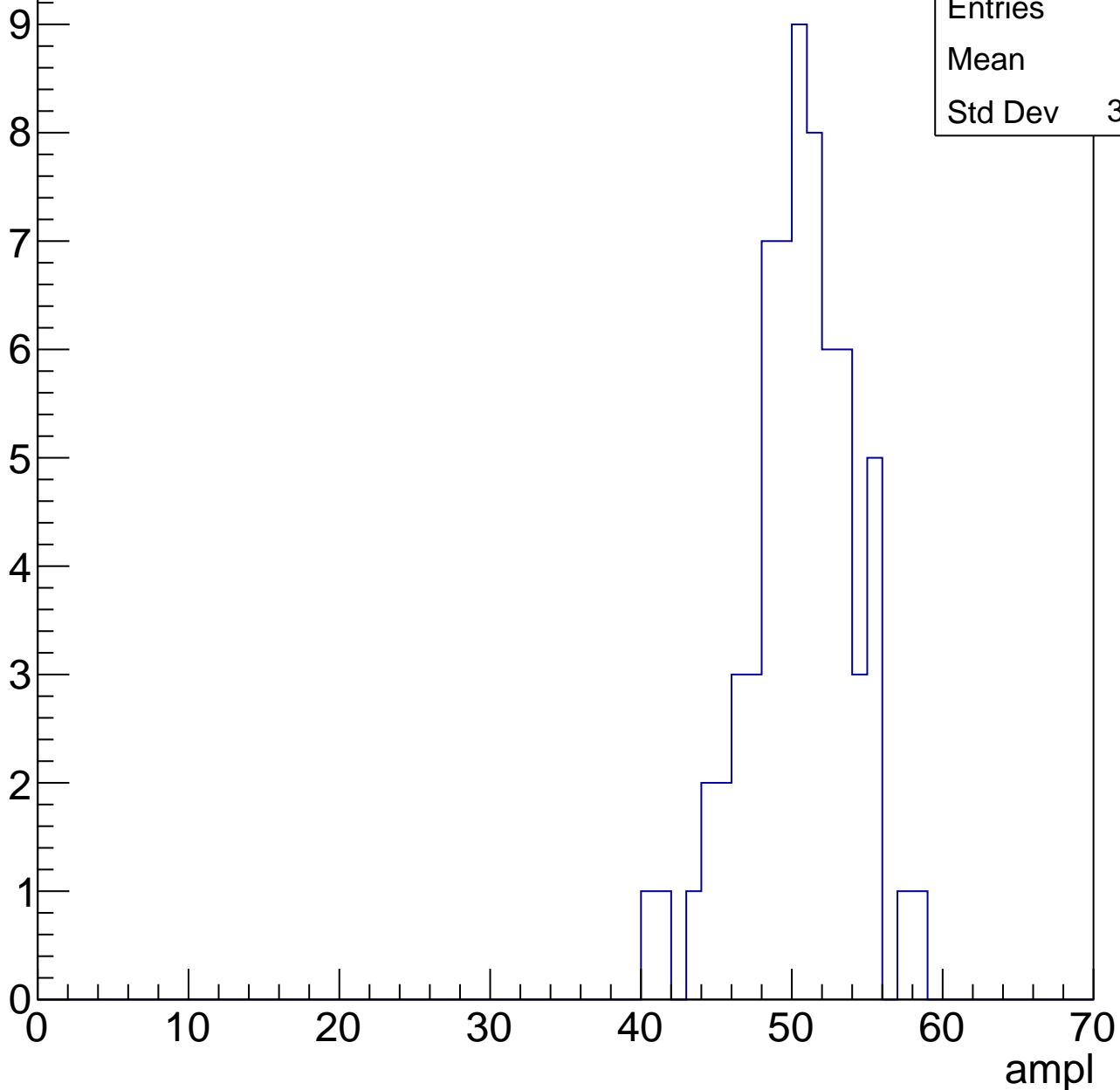


B0L001S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50
Std Dev	3.555

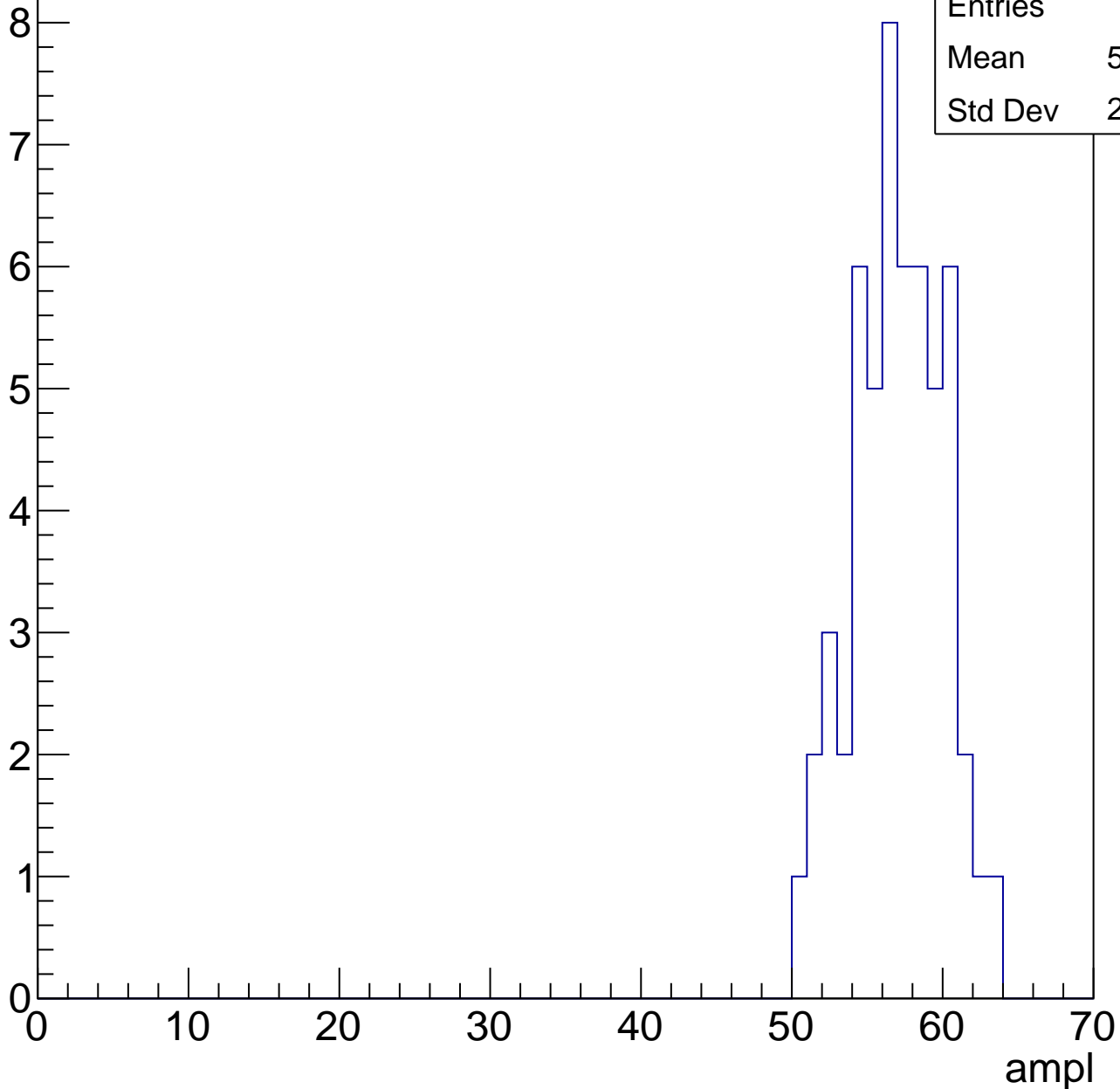


B0L001S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.54
Std Dev	2.955

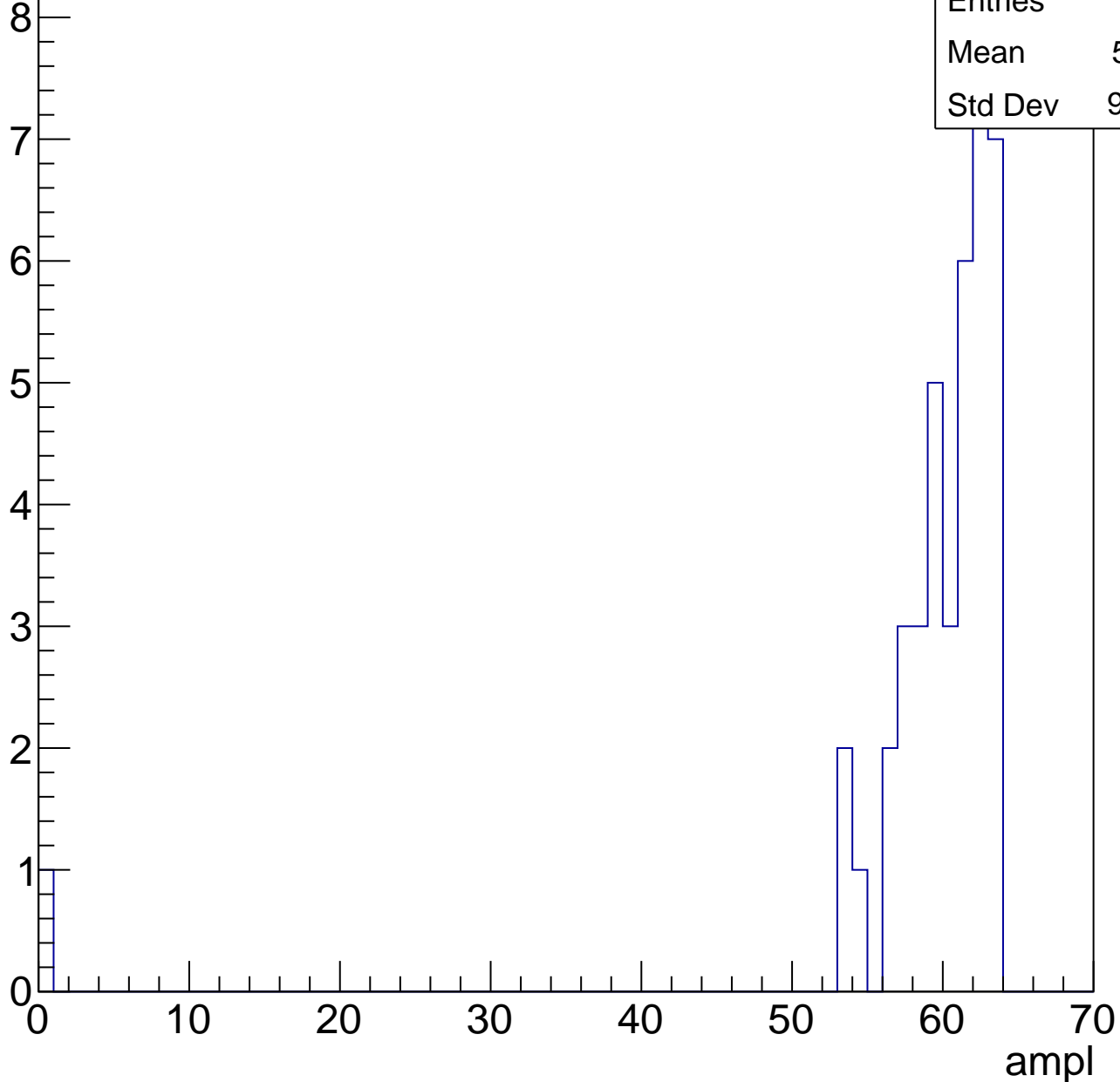


B0L001S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.41
Std Dev	9.637



B0L001S, U15-ch33, adc6

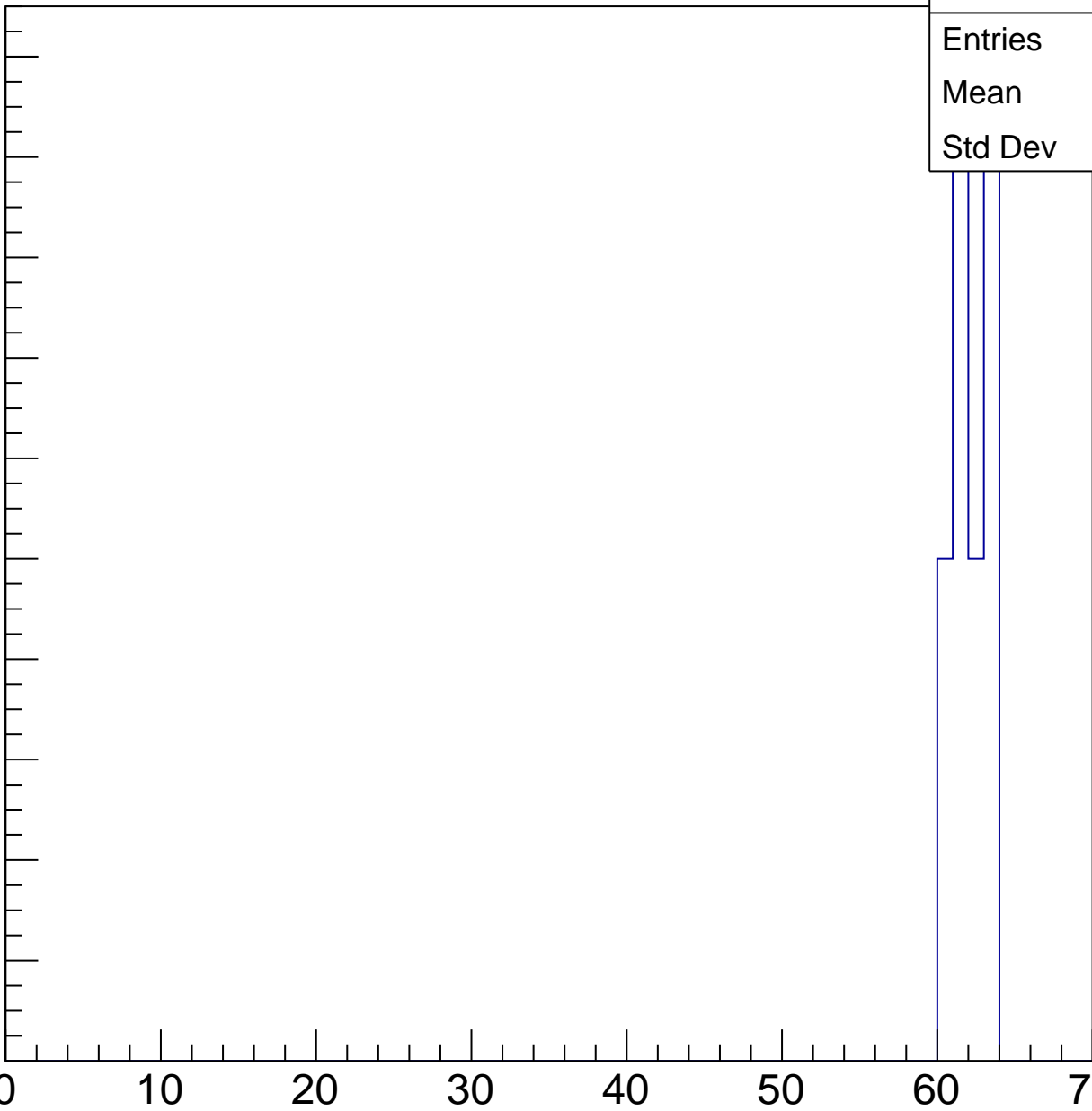
calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl



B0L001S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch34, adc0

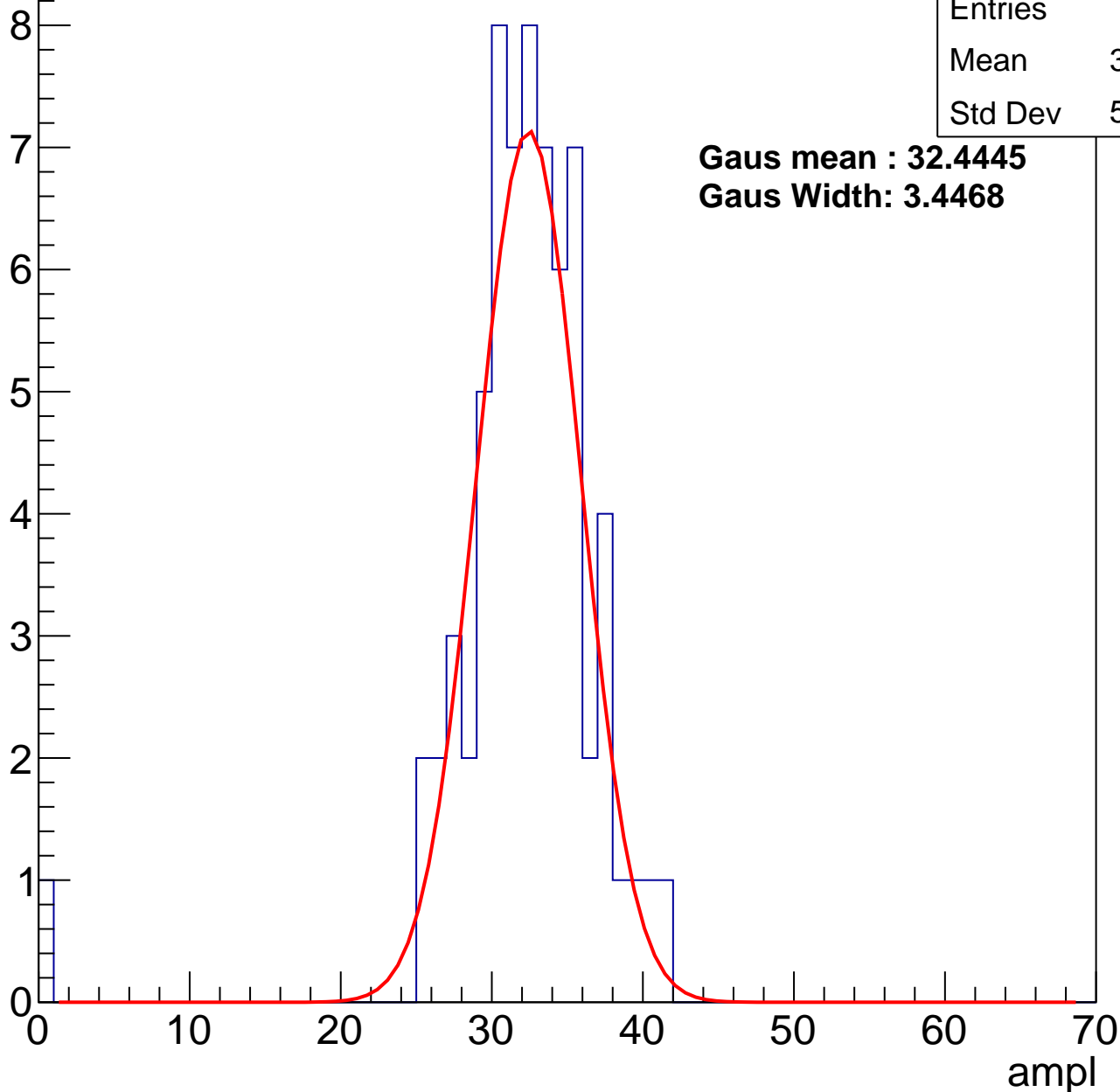
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.69
Std Dev	5.194

Gaus mean : 32.4445

Gaus Width: 3.4468



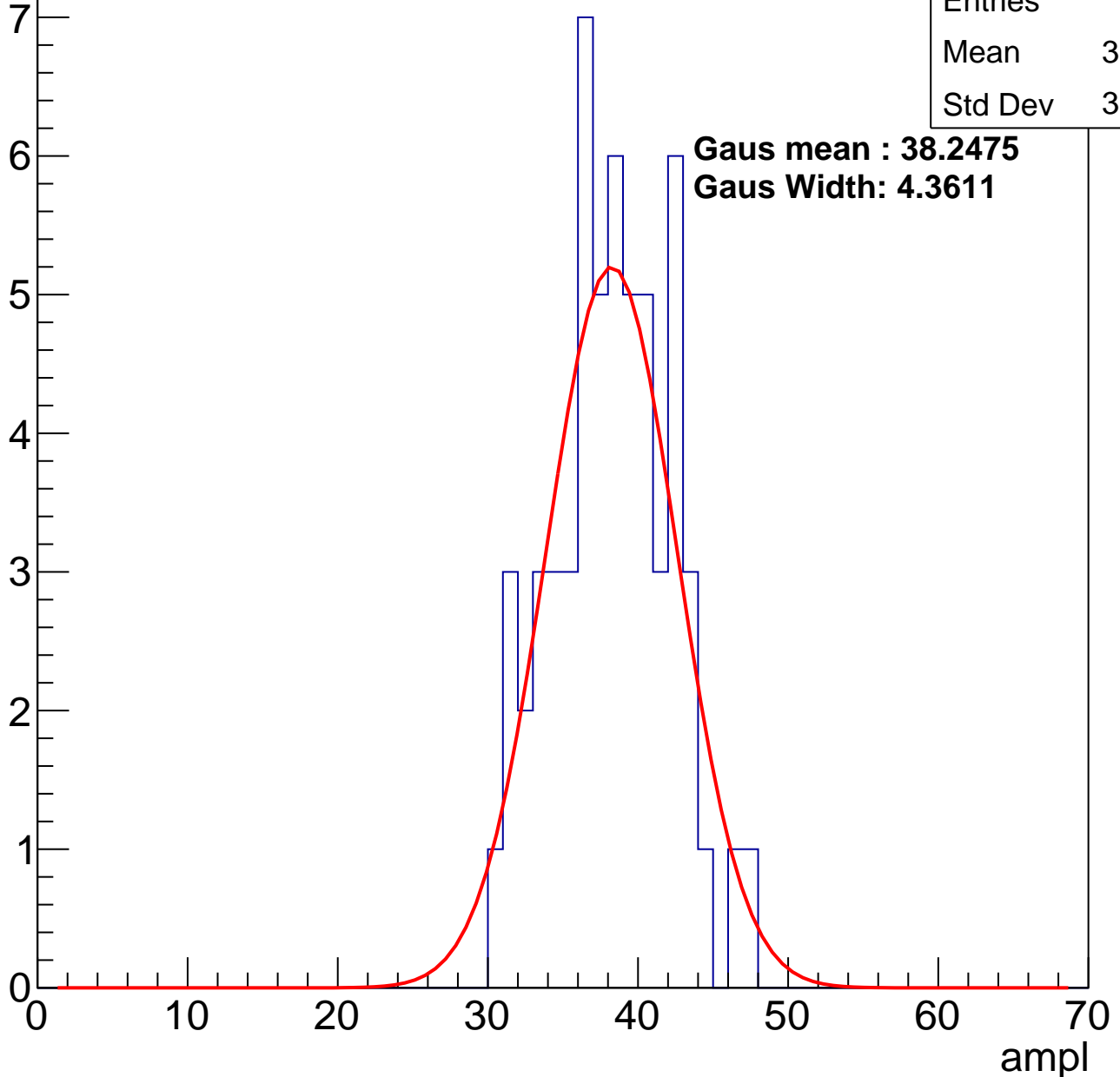
B0L001S, U15-ch34, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.83
Std Dev	3.869

Gaus mean : 38.2475
Gaus Width: 4.3611



B0L001S, U15-ch34, adc2

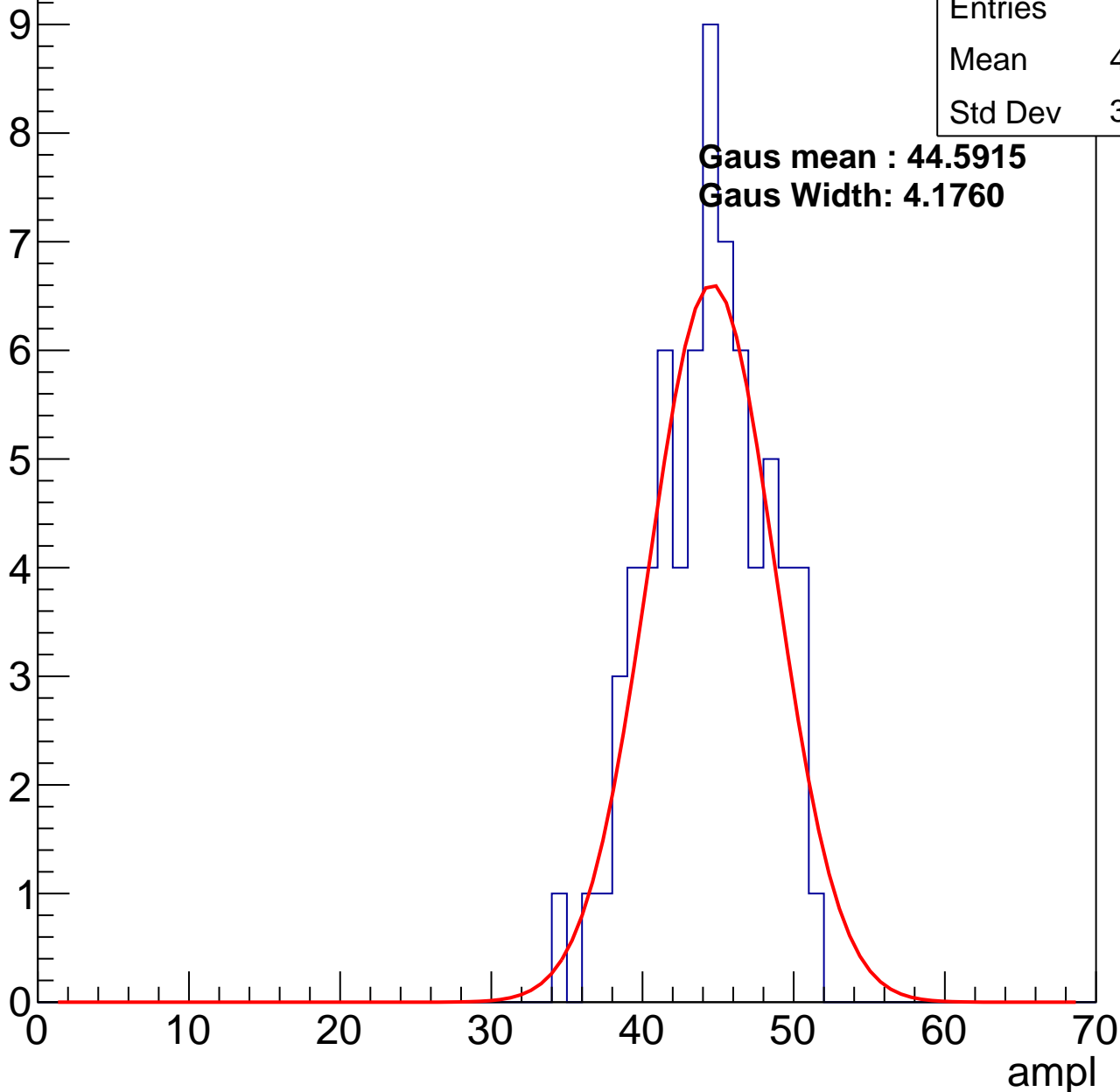
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.87
Std Dev	3.779

Gaus mean : 44.5915

Gaus Width: 4.1760

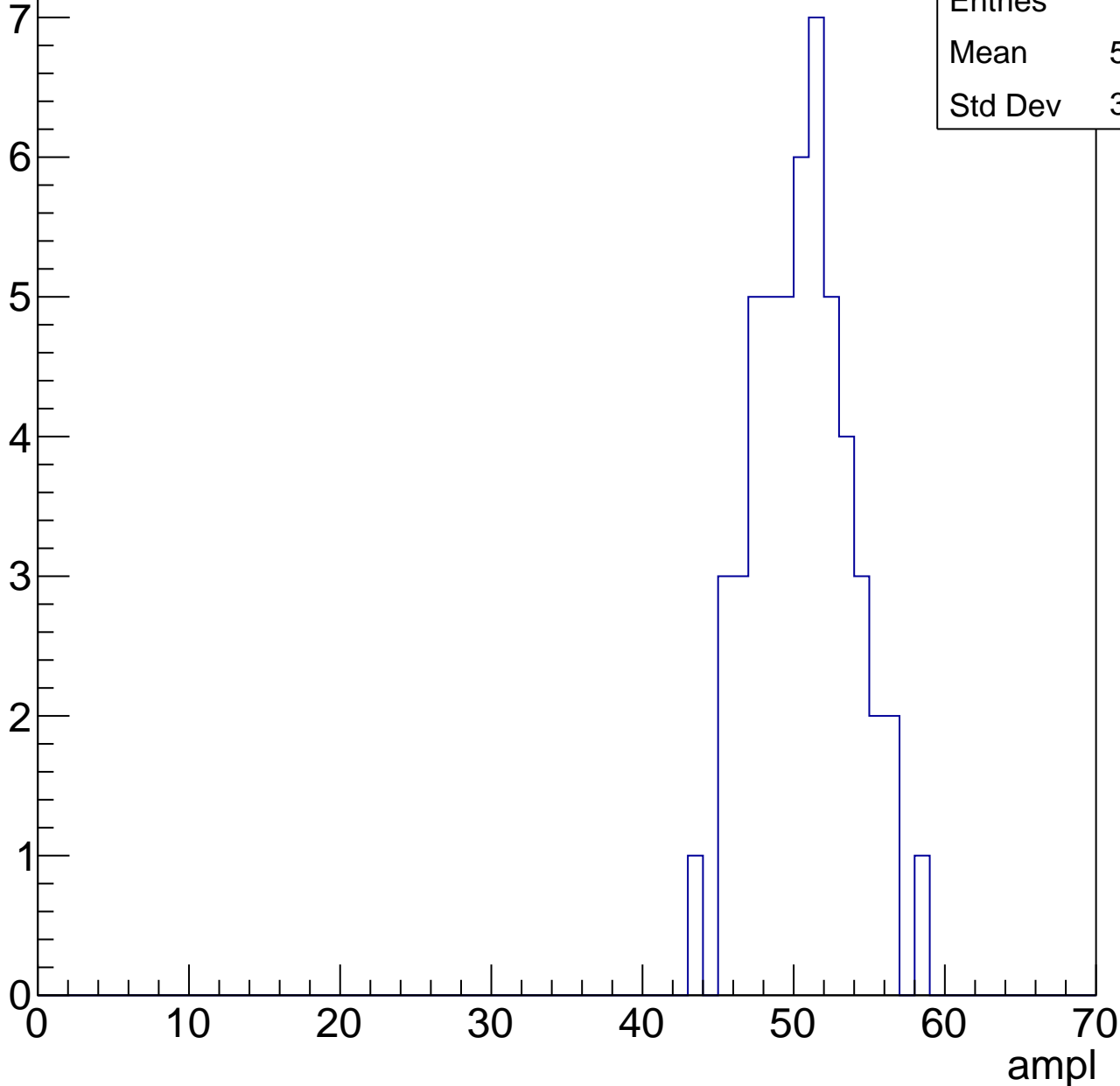


B0L001S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	50.13
Std Dev	3.217

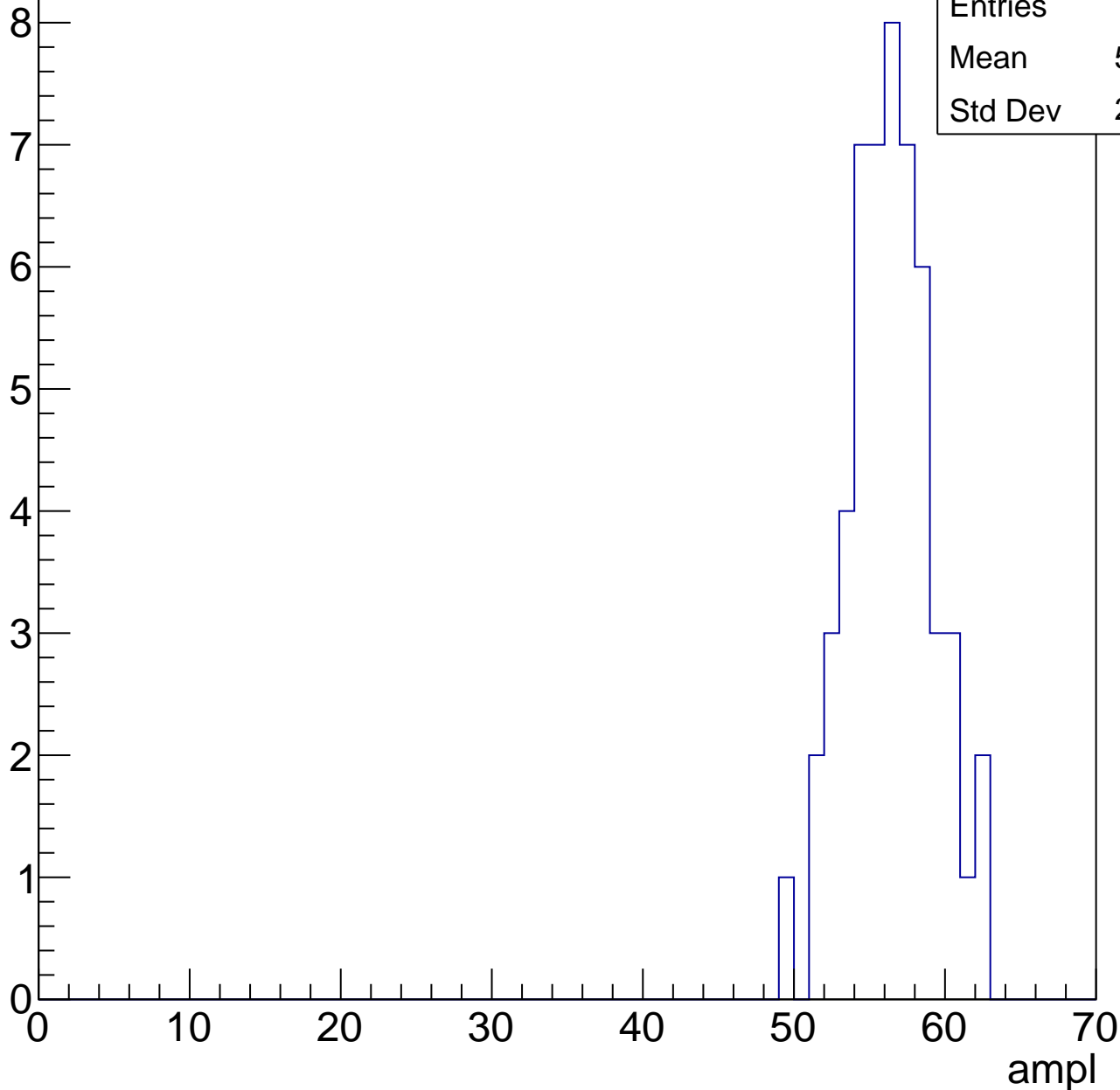


B0L001S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.91
Std Dev	2.791

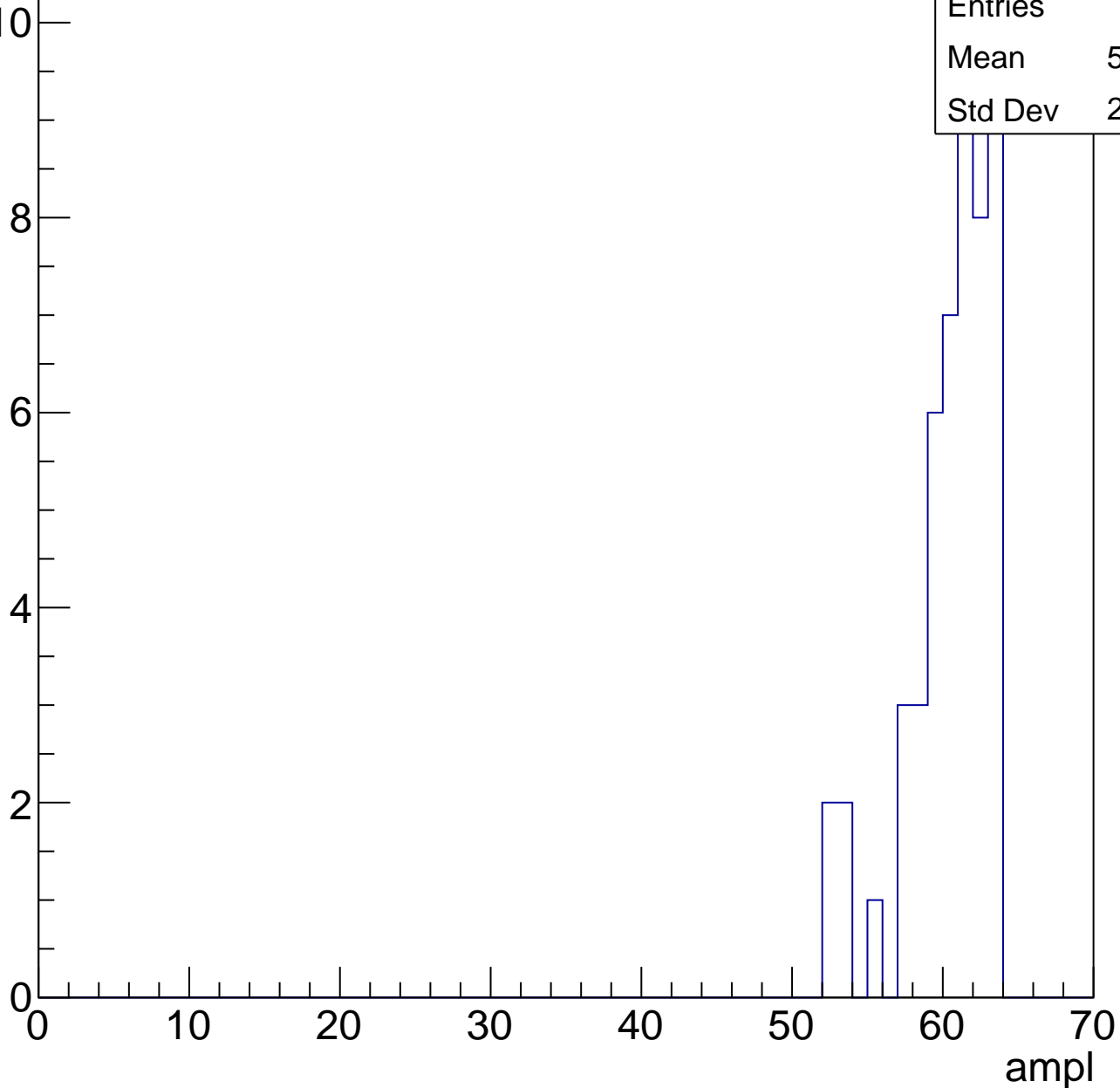


B0L001S, U15-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

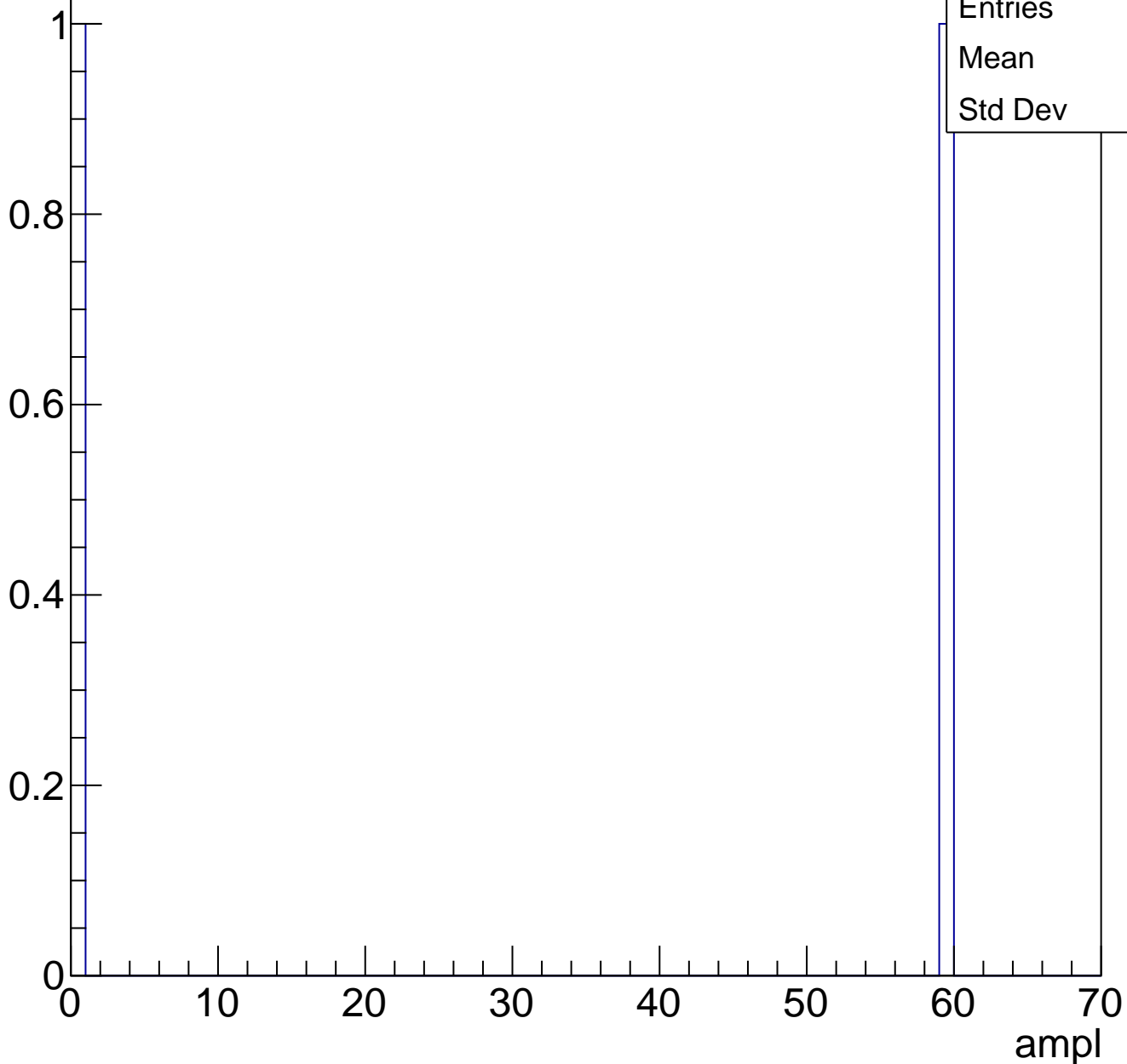
Entries	51
Mean	59.98
Std Dev	2.893



B0L001S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L001S, U15-ch35, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	30.29
Std Dev	5.139

Gaus mean : 31.6490

Gaus Width: 3.5437

Entry

10
8
6
4
2
0

ampl

0

10

20

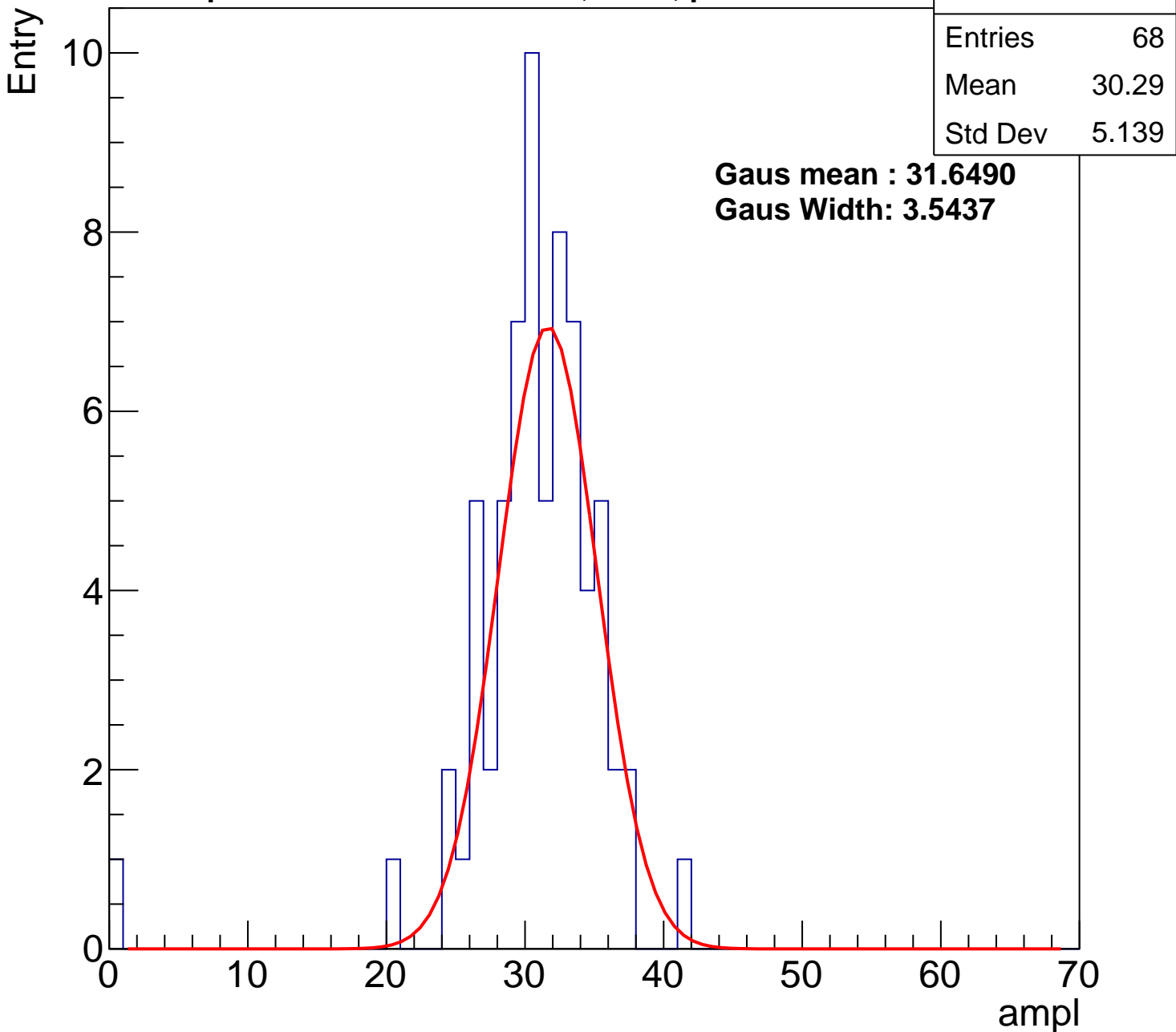
30

40

50

60

70



B0L001S, U15-ch35, adc1

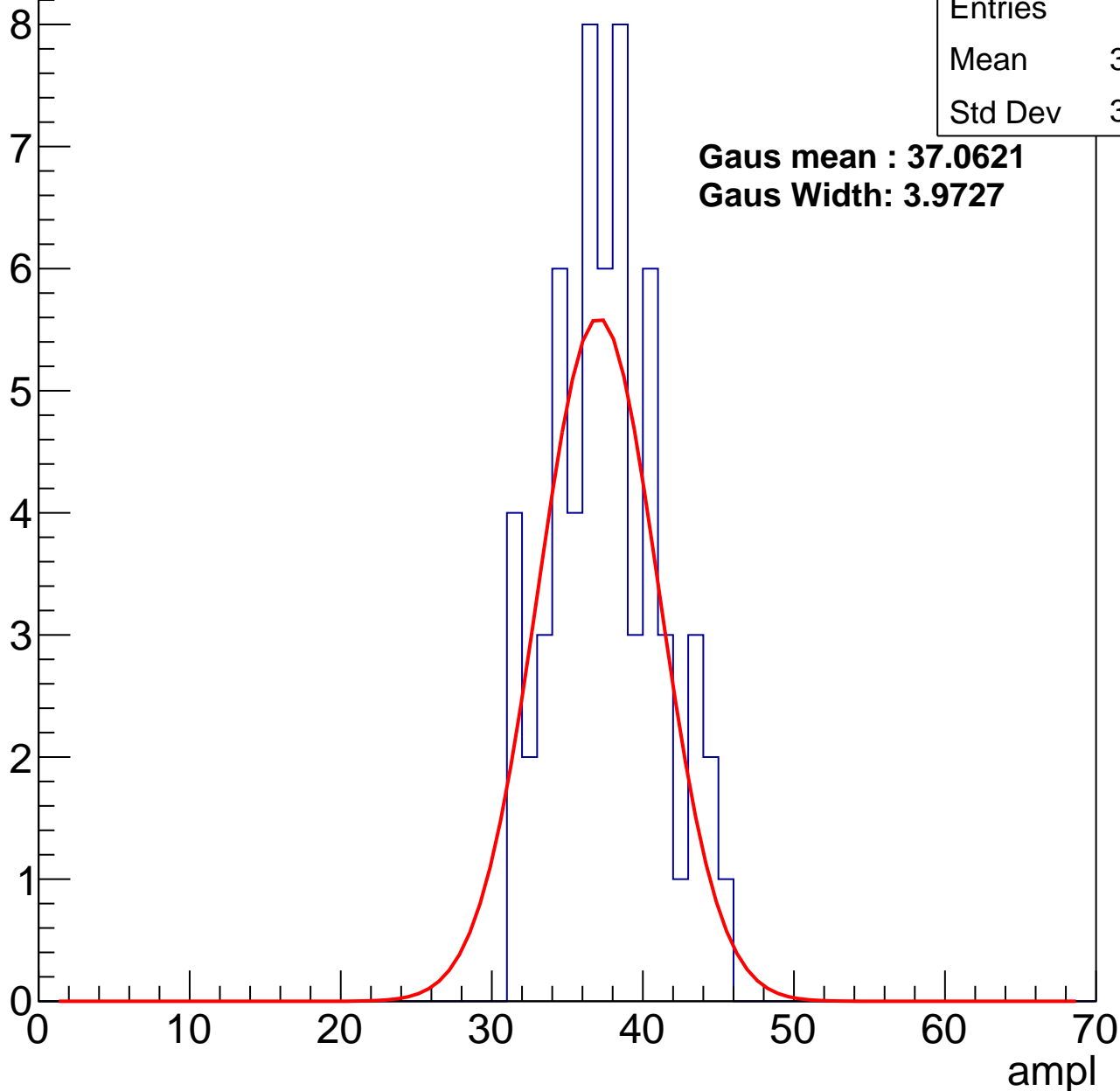
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.15
Std Dev	3.502

Gaus mean : 37.0621

Gaus Width: 3.9727



B0L001S, U15-ch35, adc2

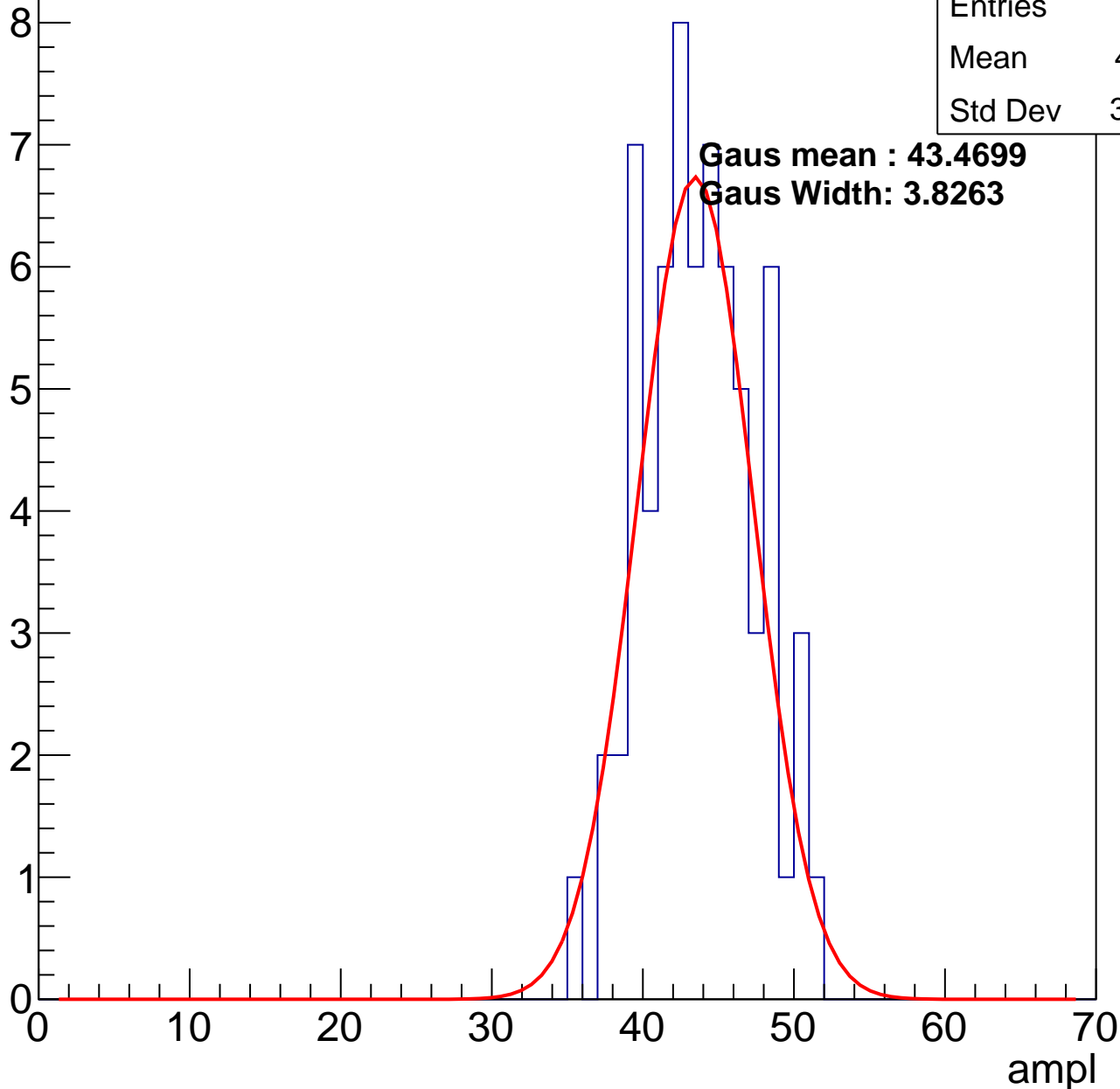
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.31
Std Dev	3.598

Gaus mean : 43.4699

Gaus Width: 3.8263

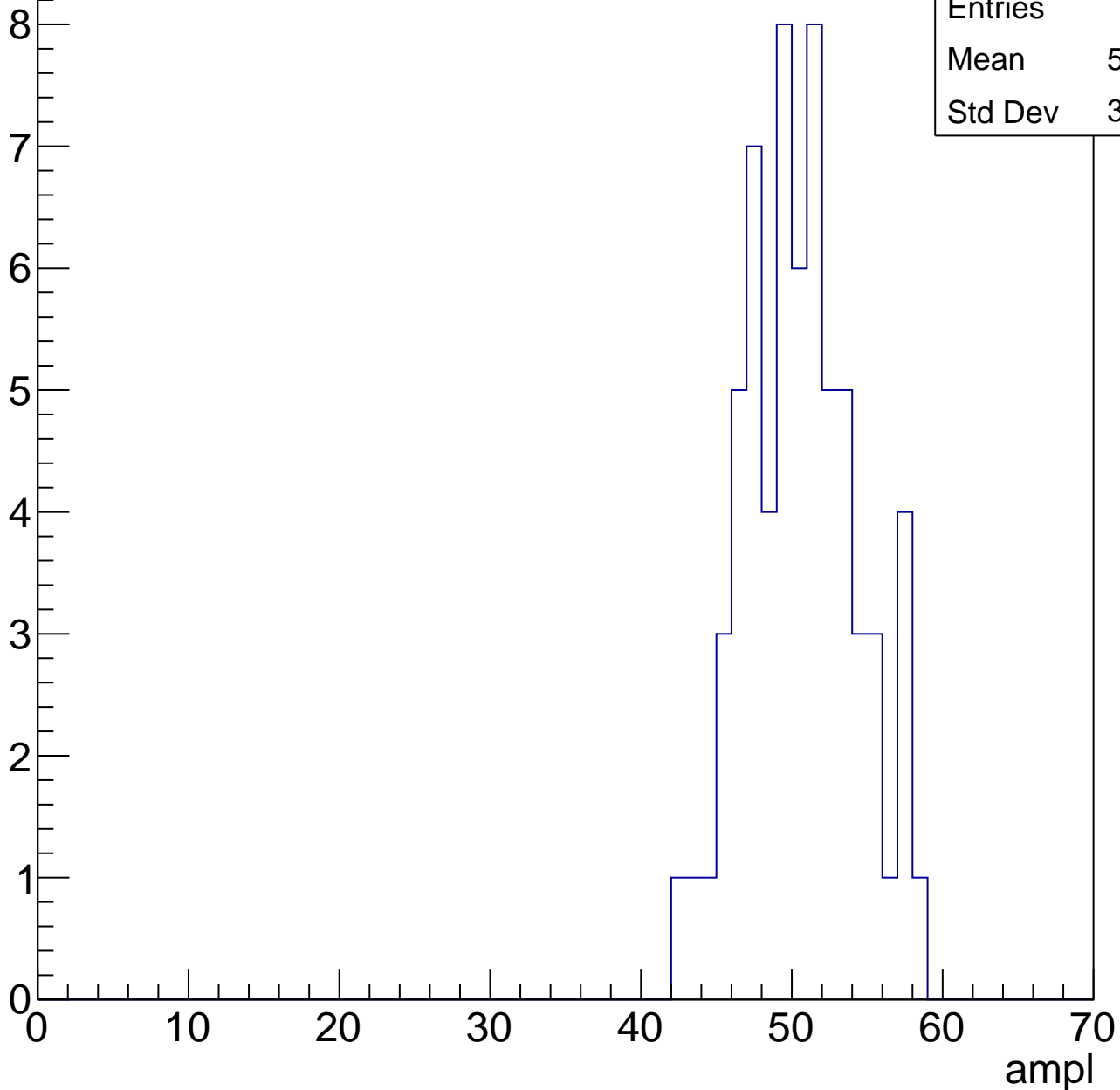


B0L001S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

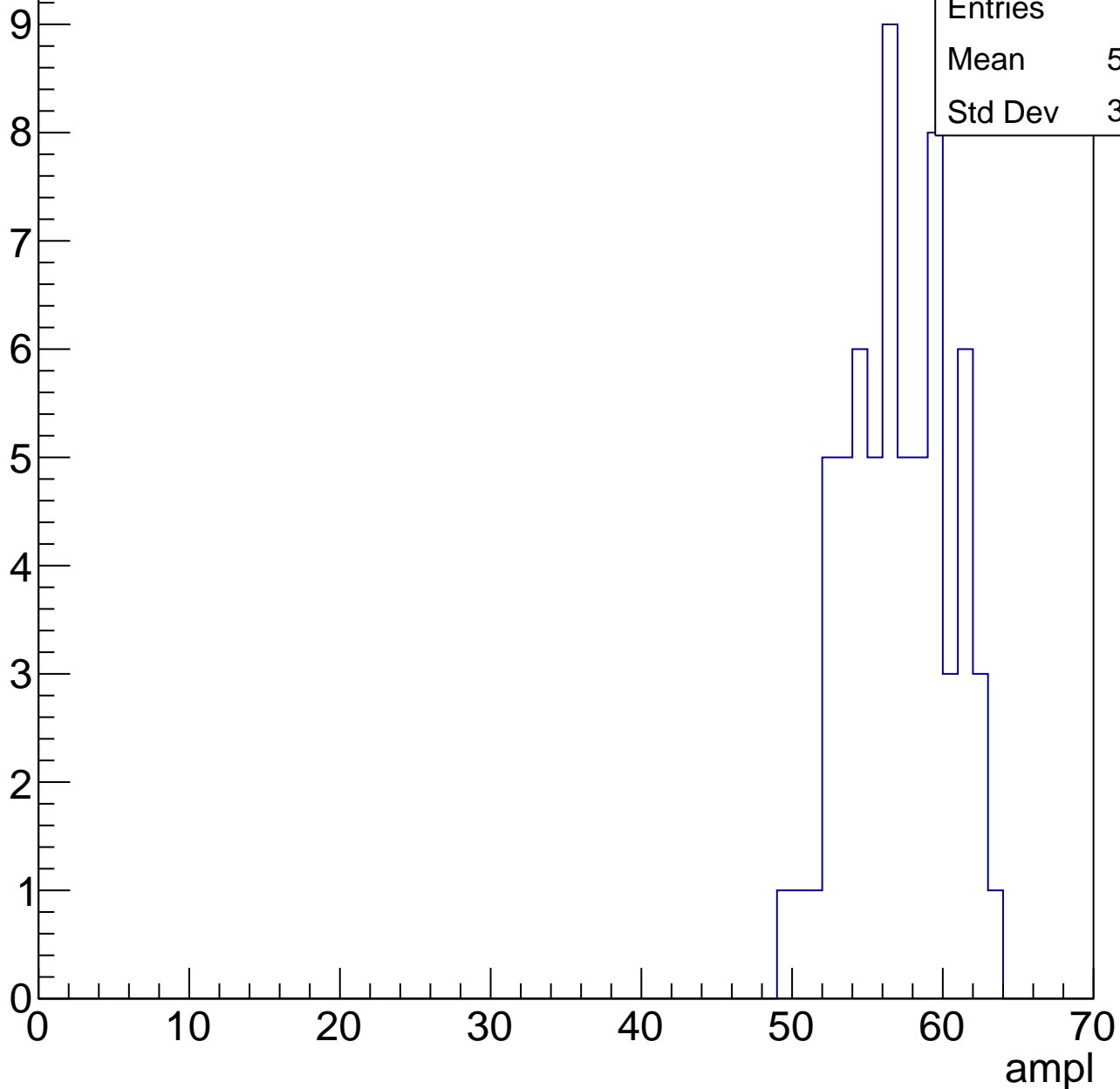
Entries	66
Mean	50.14
Std Dev	3.655



B0L001S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



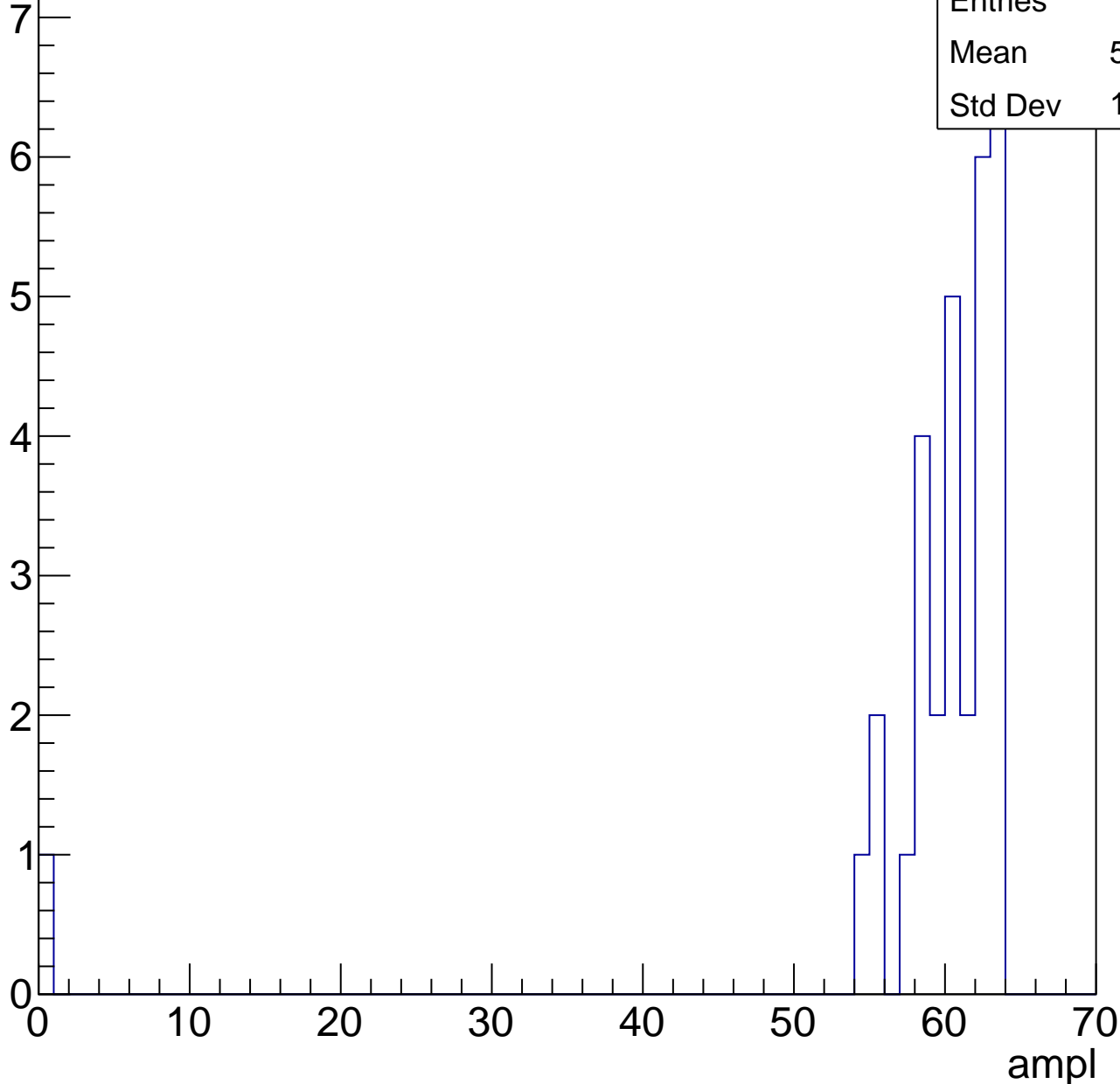
Entries	64
Mean	56.56
Std Dev	3.297

B0L001S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

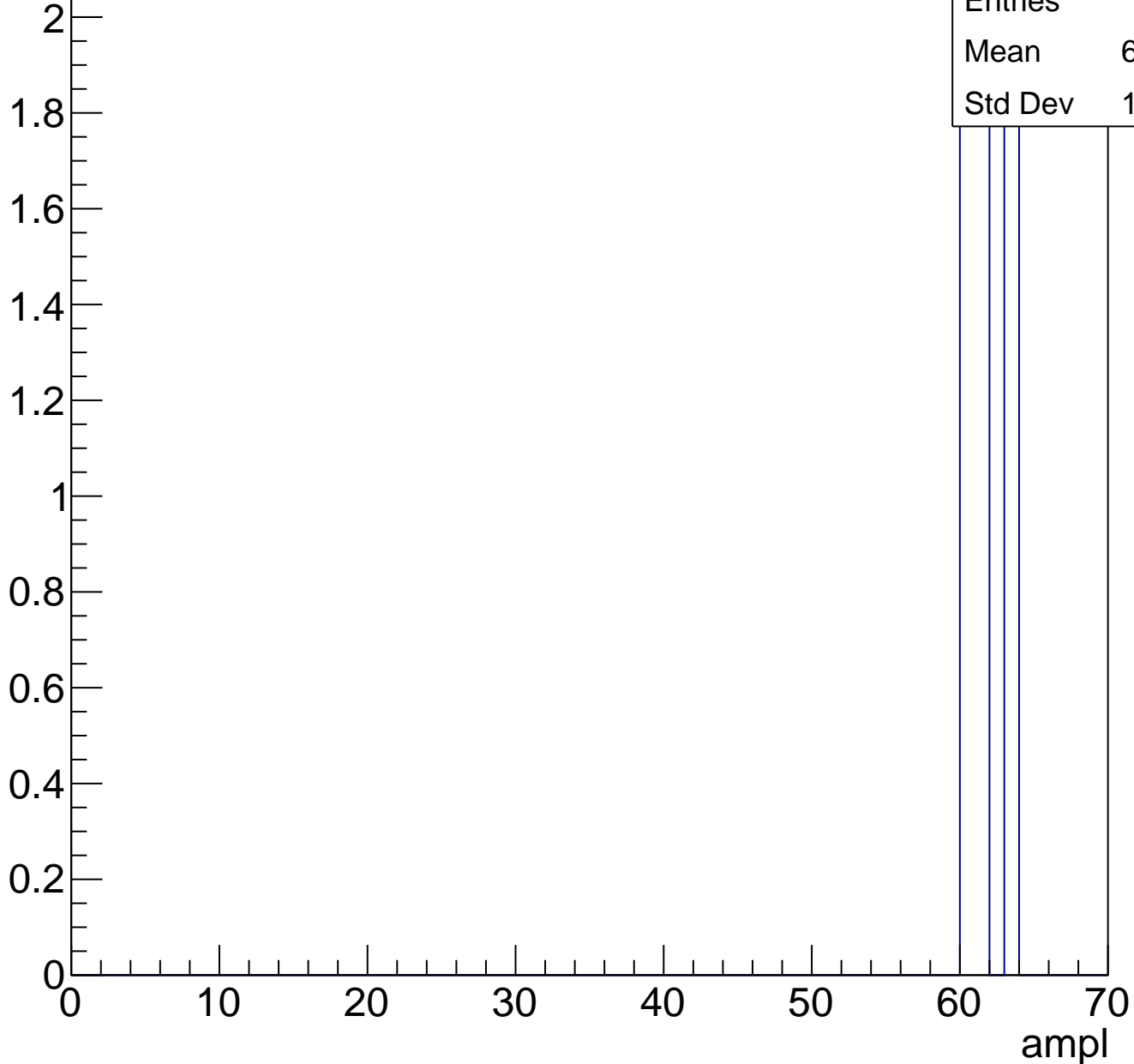
Entries	31
Mean	58.26
Std Dev	10.94



B0L001S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch36, adc0

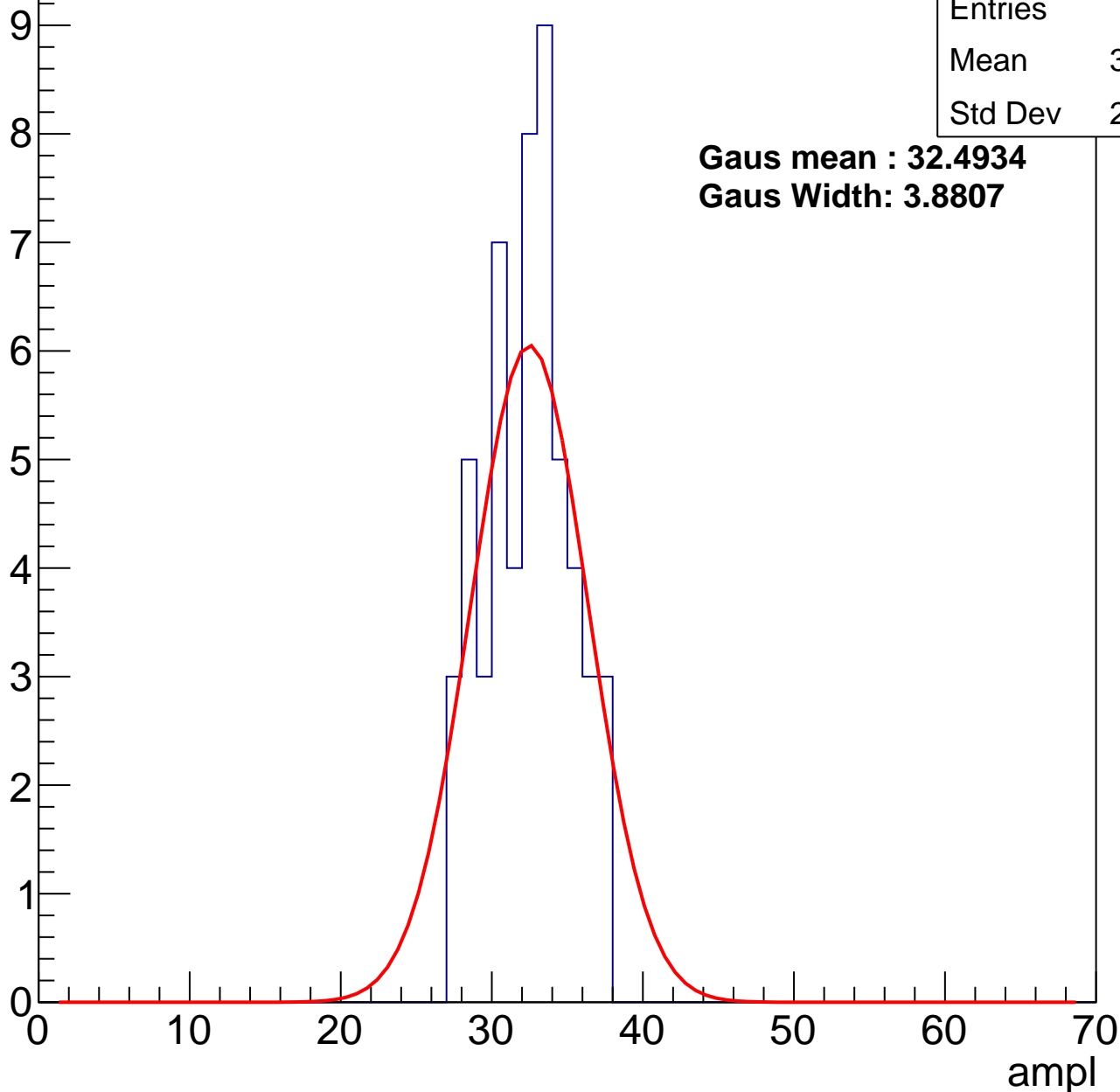
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	31.93
Std Dev	2.727

Gaus mean : 32.4934

Gaus Width: 3.8807



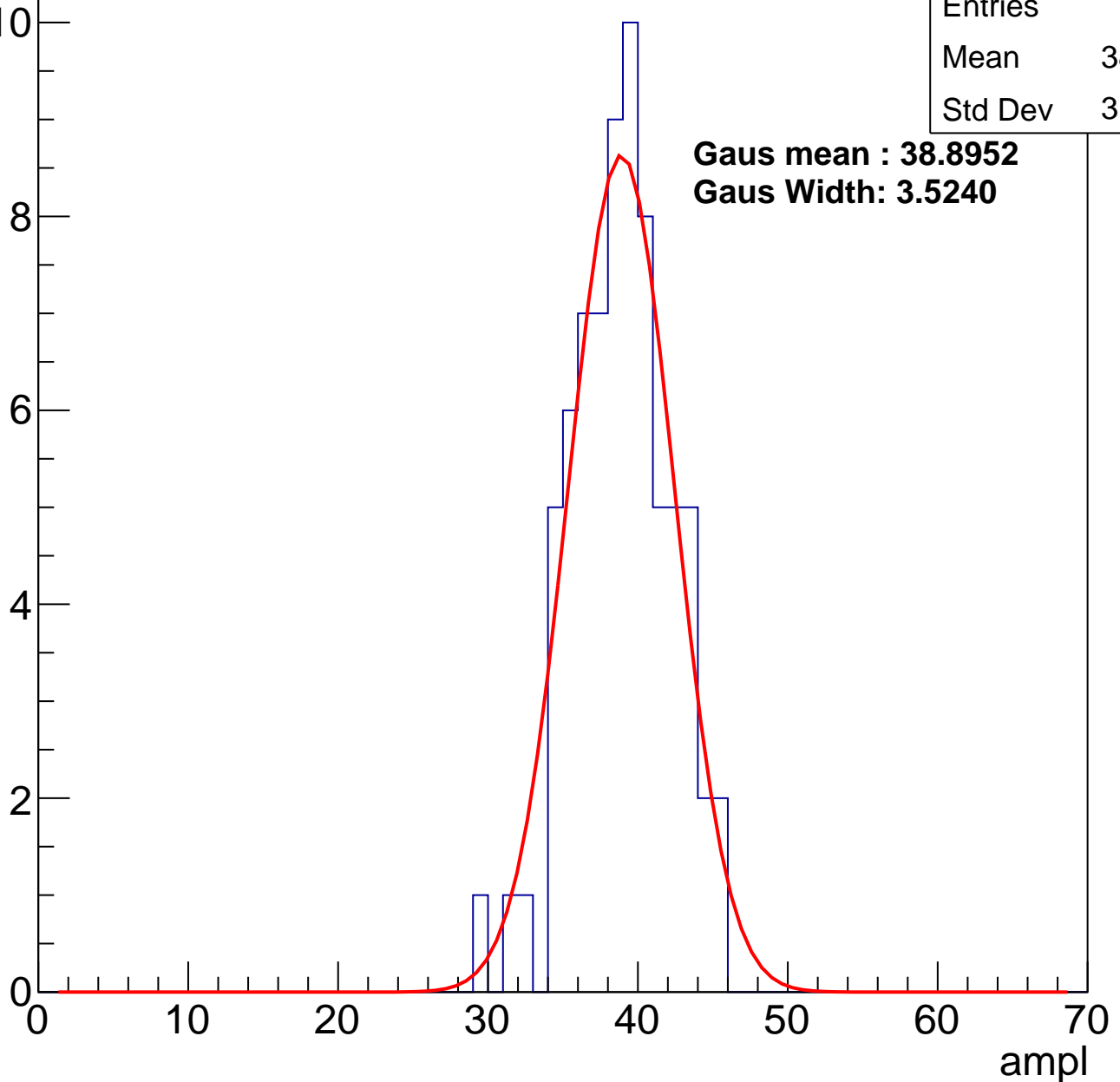
B0L001S, U15-ch36, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.42
Std Dev	3.255

Gaus mean : 38.8952
Gaus Width: 3.5240



B0L001S, U15-ch36, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	45.78
Std Dev	3.231

Gaus mean : 46.1618

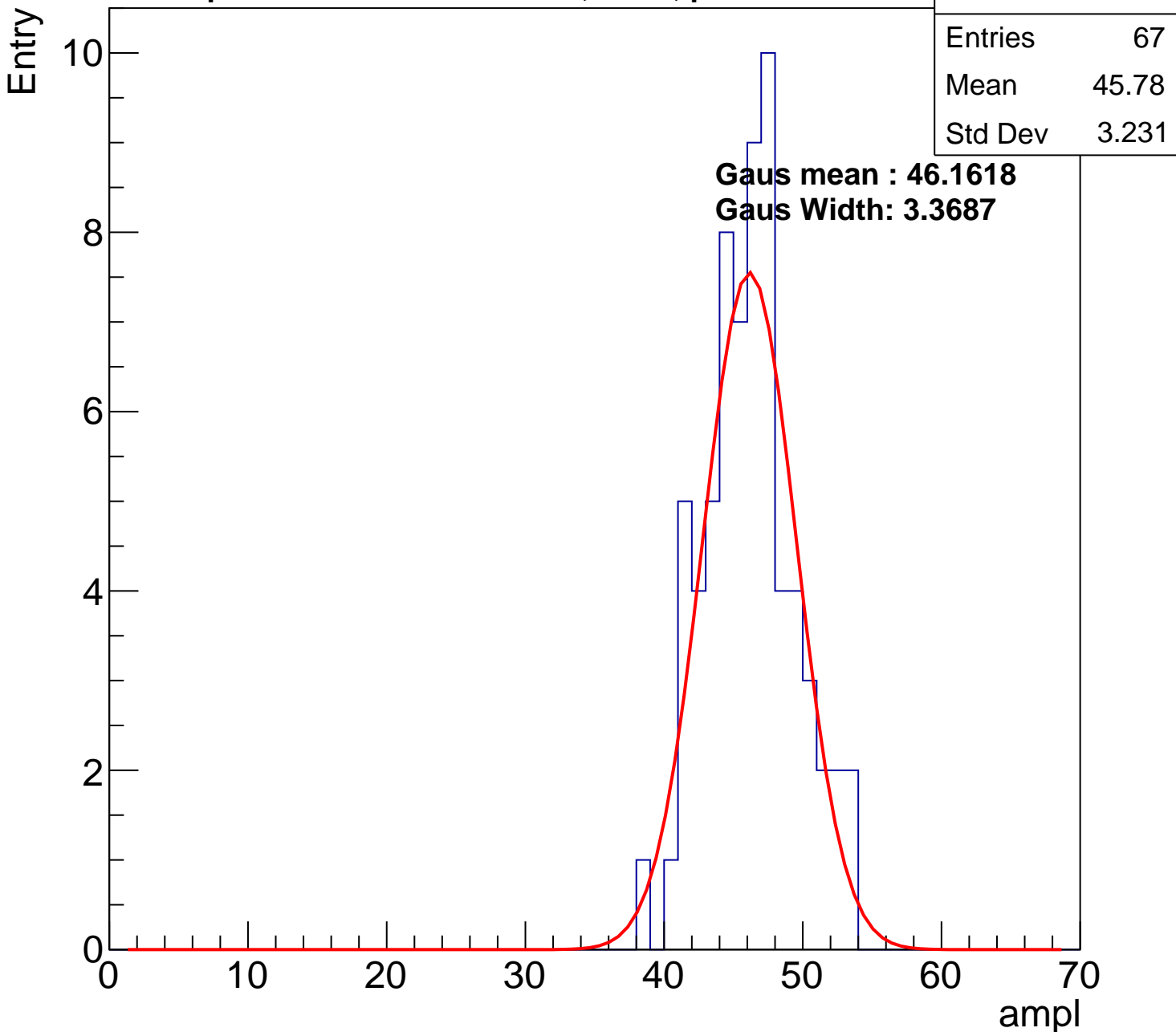
Gaus Width: 3.3687

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

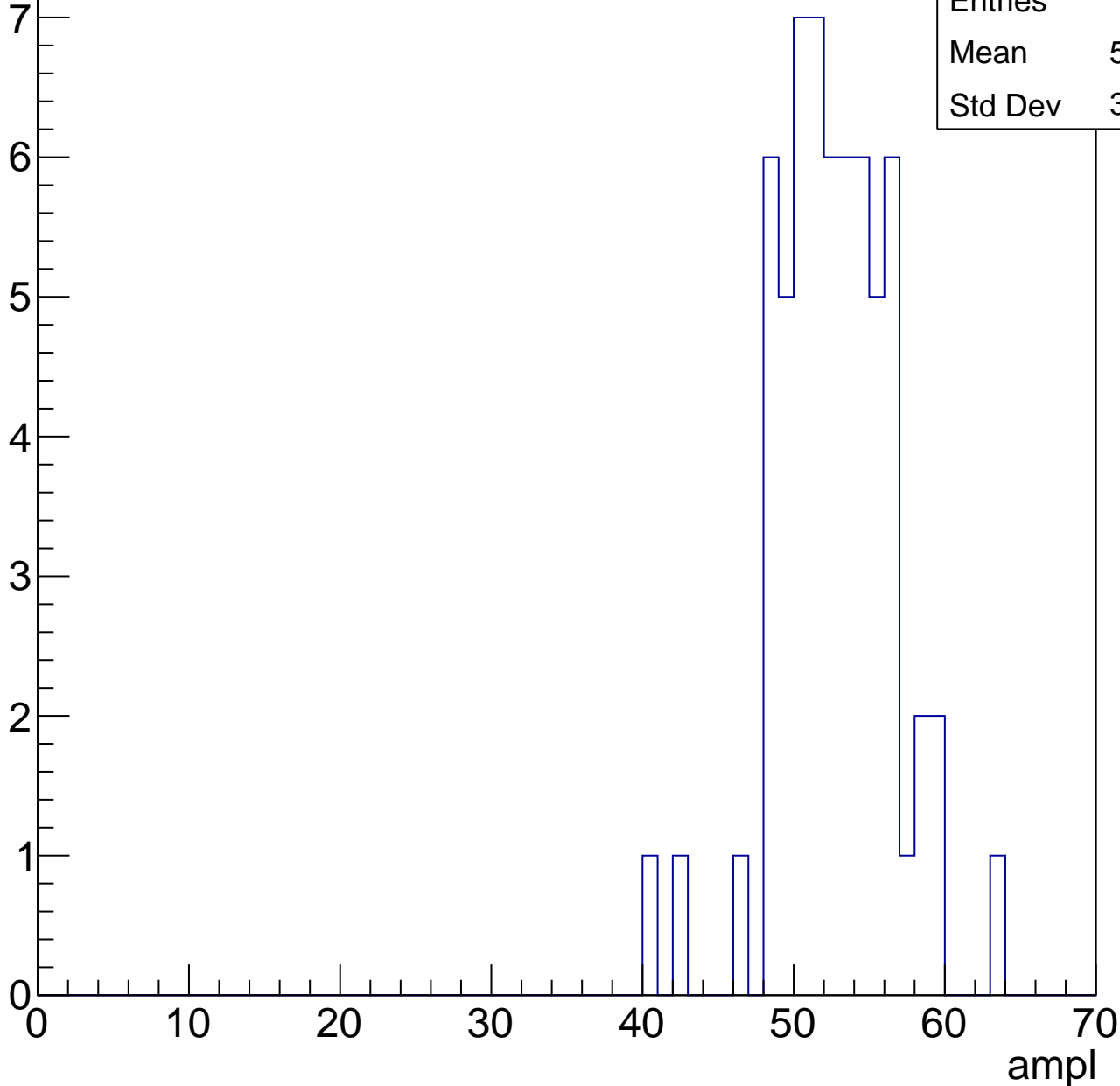


B0L001S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.17
Std Dev	3.865

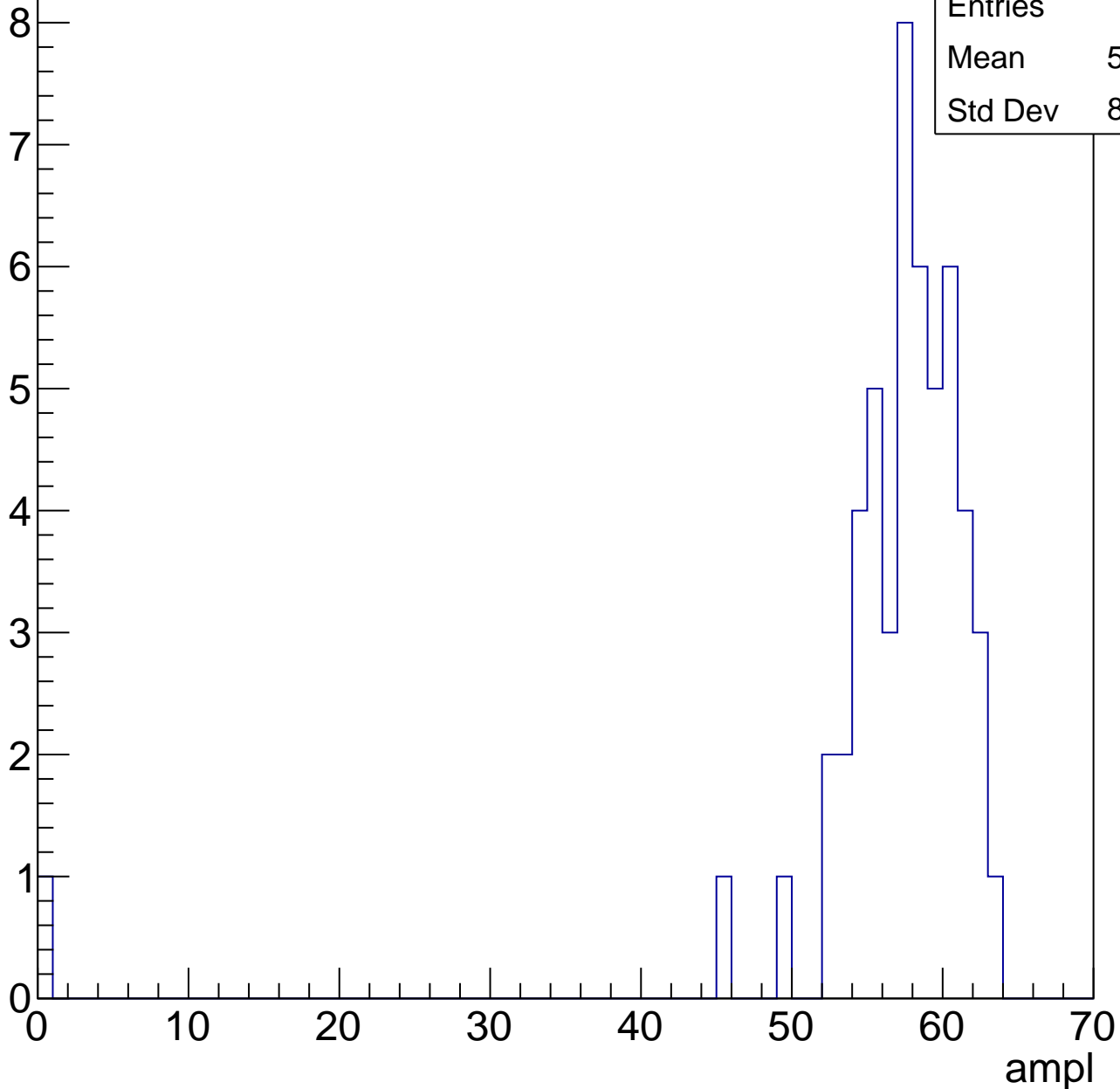


B0L001S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.06
Std Dev	8.556

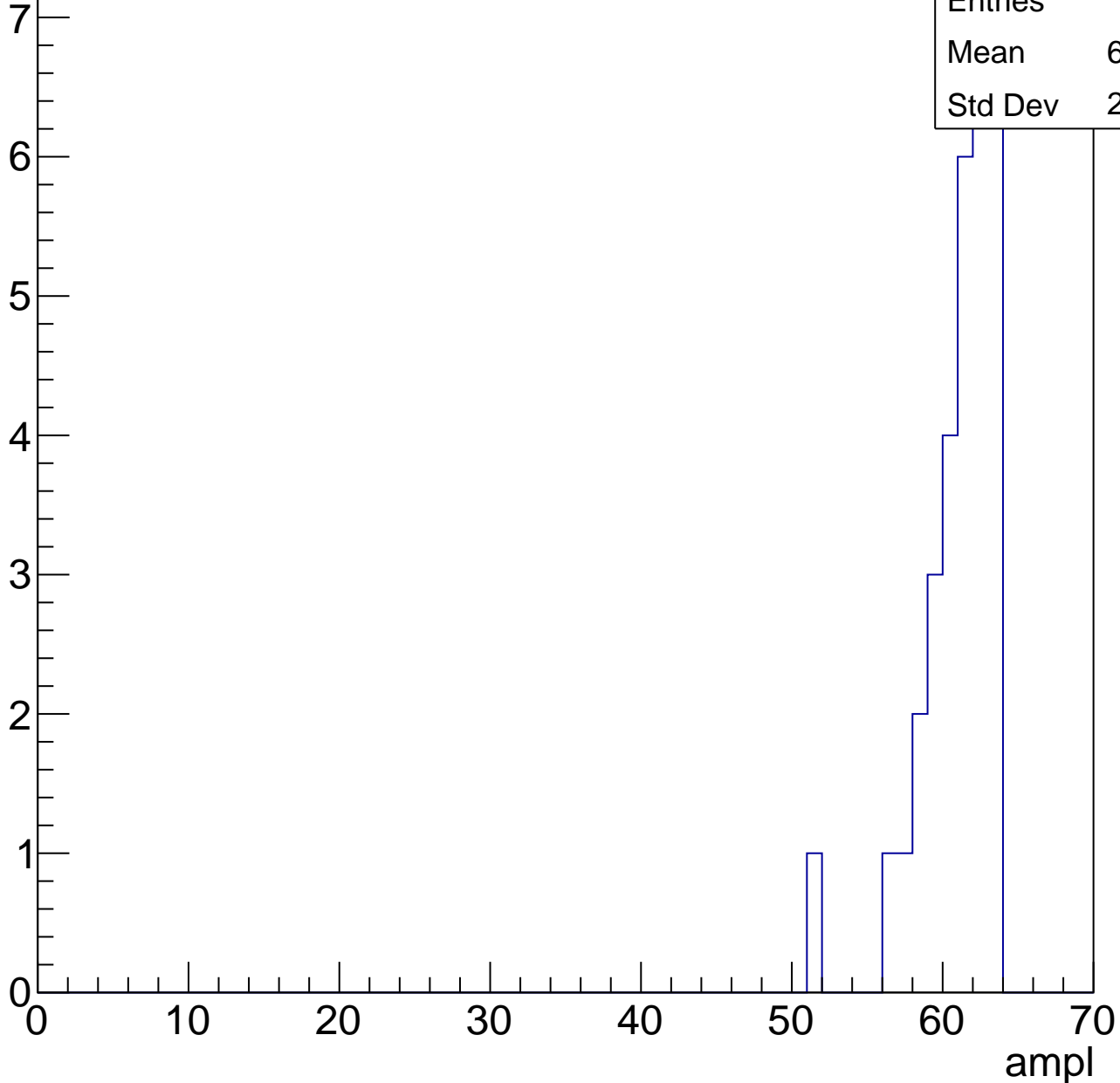


B0L001S, U15-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.56
Std Dev	2.524



B0L001S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

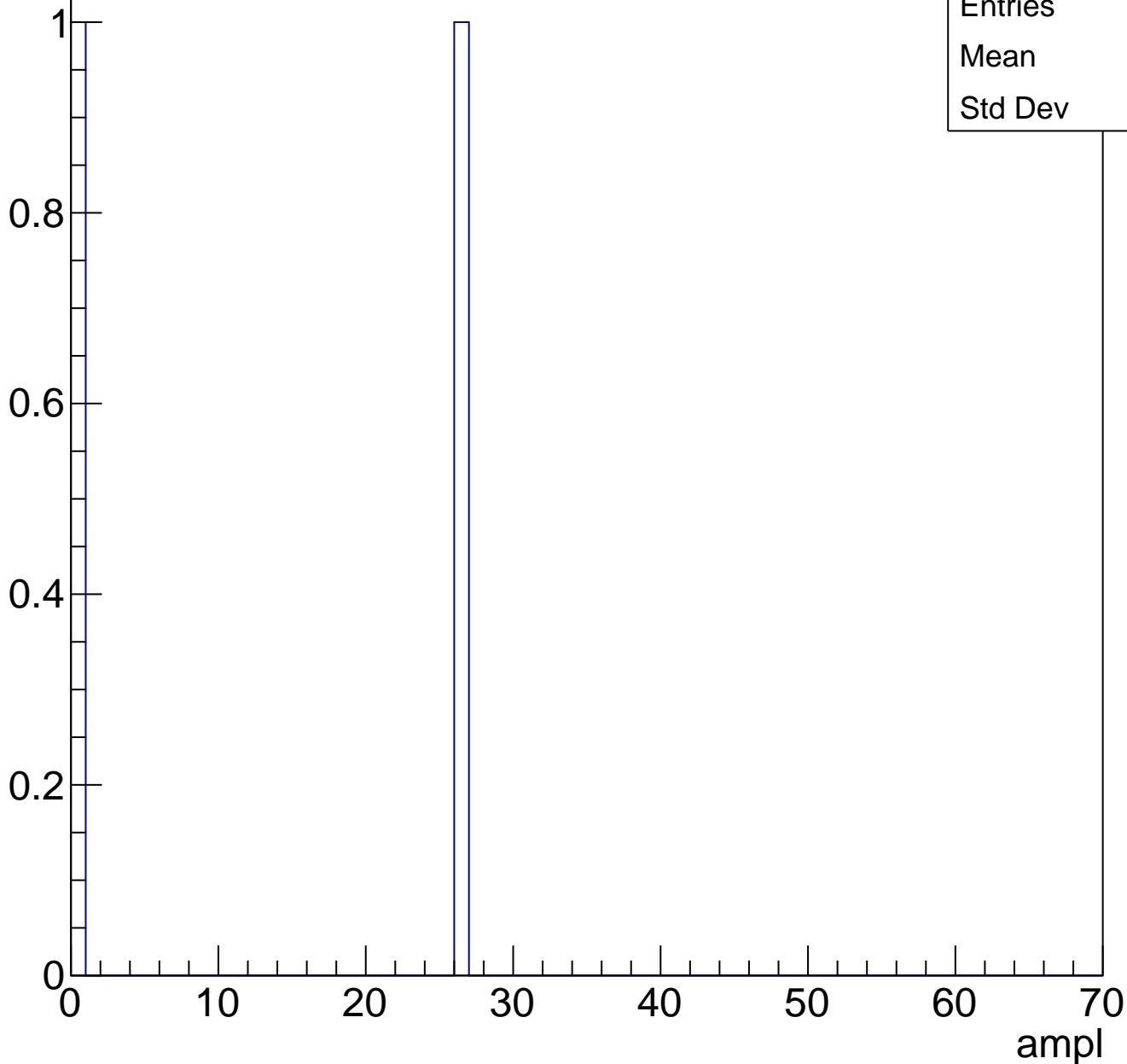
Entry



B0L001S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch37, adc0

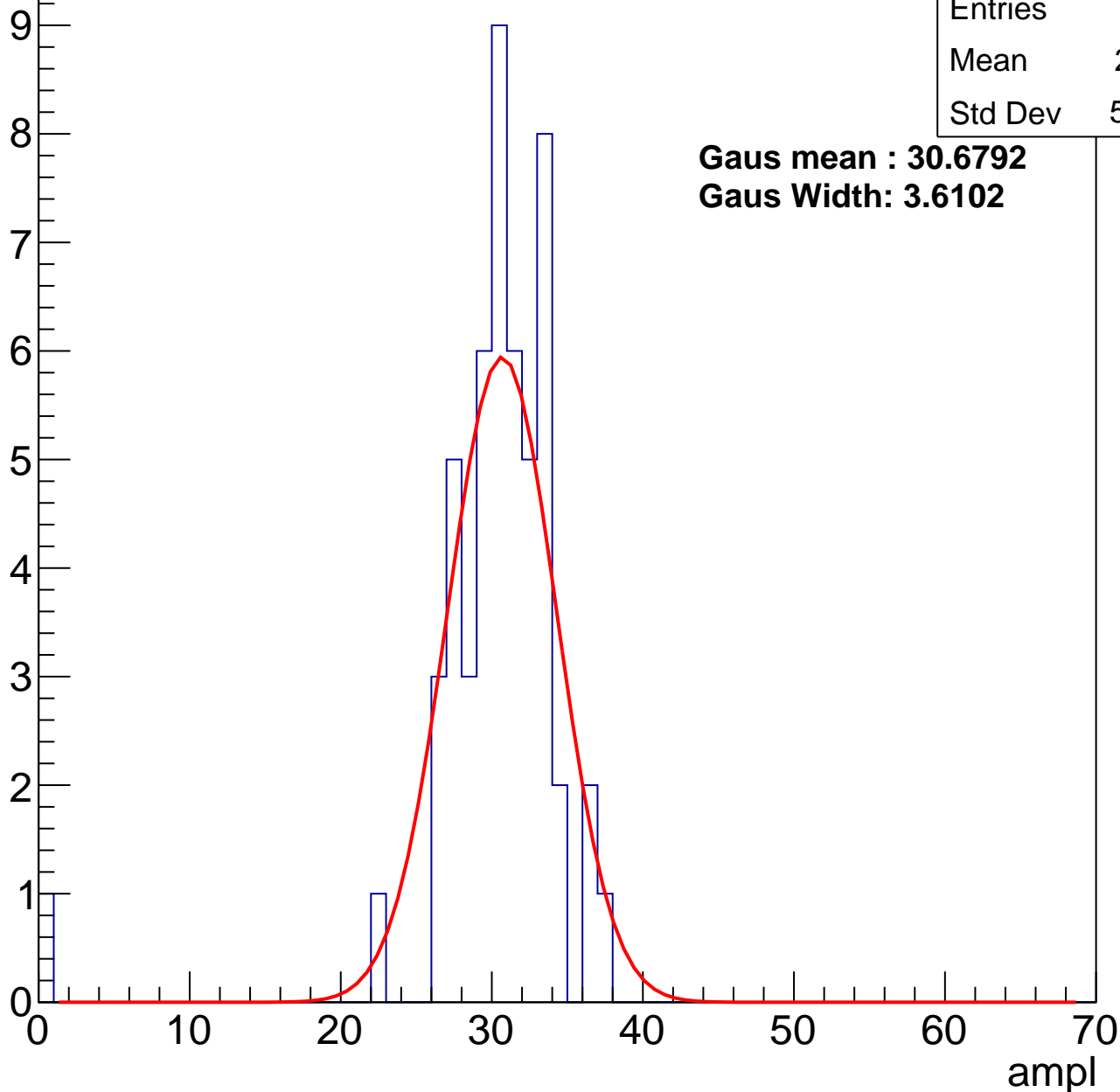
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.81
Std Dev	5.046

Gaus mean : 30.6792

Gaus Width: 3.6102

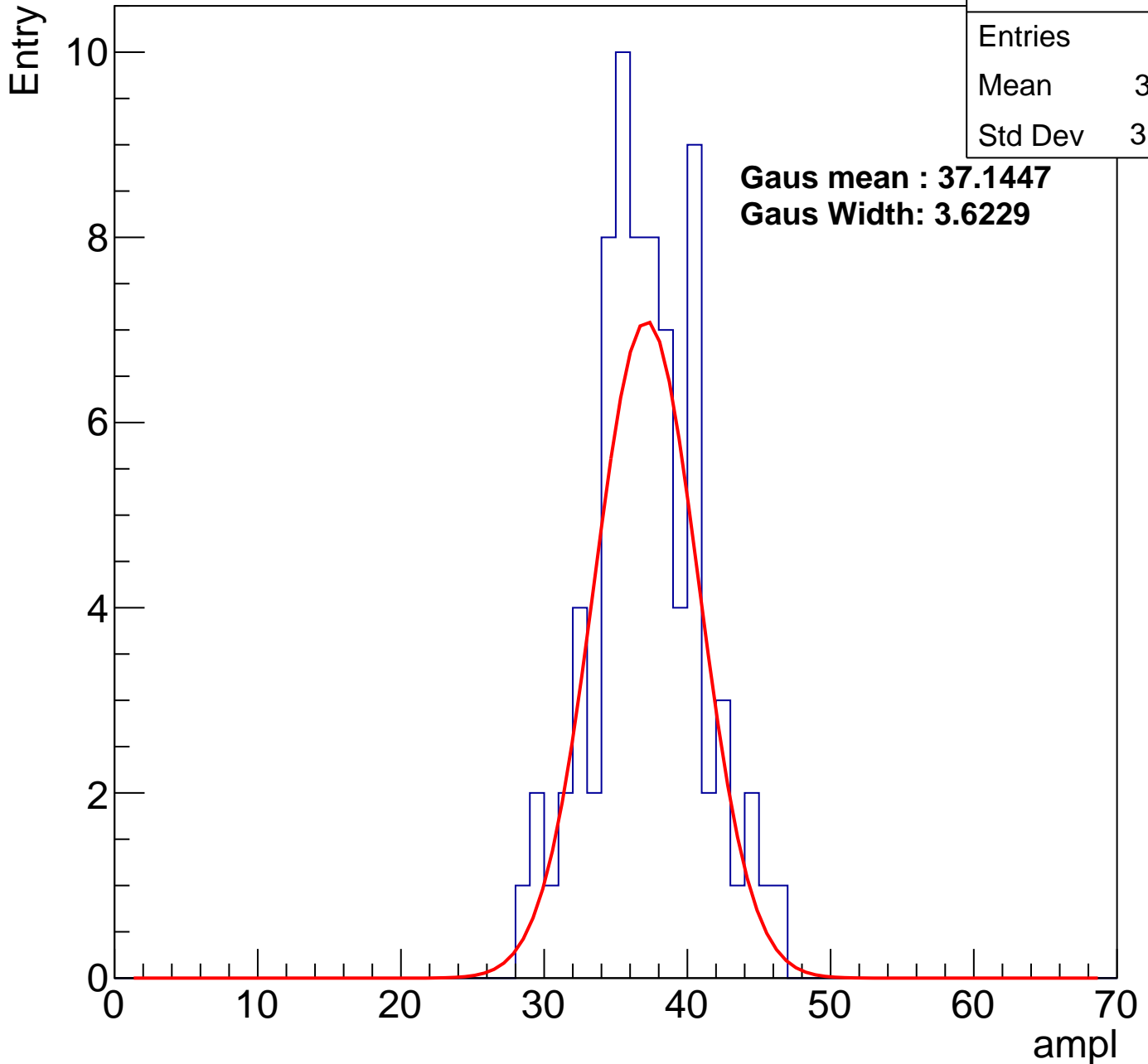


B0L001S, U15-ch37, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	36.71
Std Dev	3.745

Gaus mean : 37.1447
Gaus Width: 3.6229



B0L001S, U15-ch37, adc2

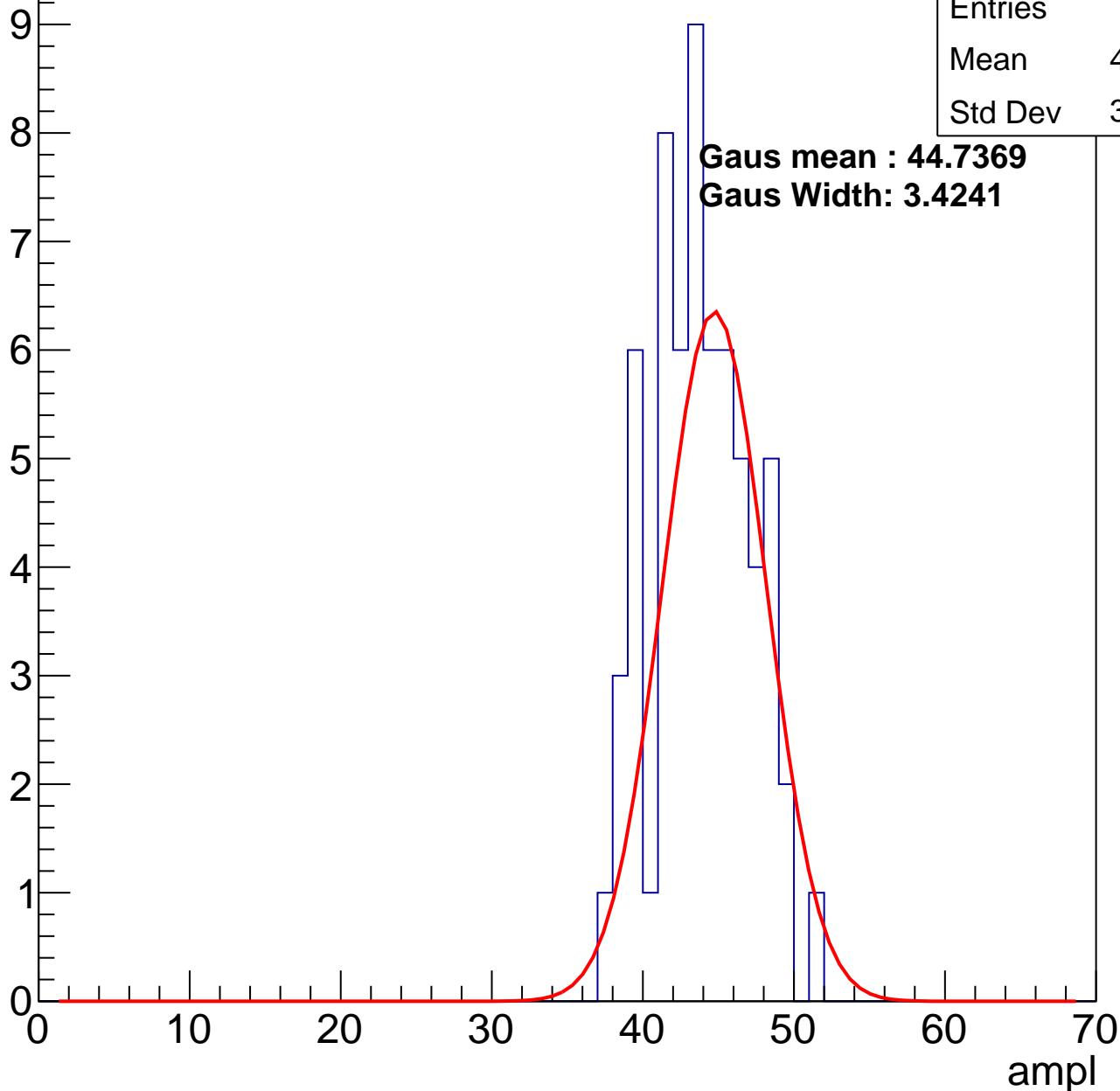
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.38
Std Dev	3.199

Gaus mean : 44.7369

Gaus Width: 3.4241

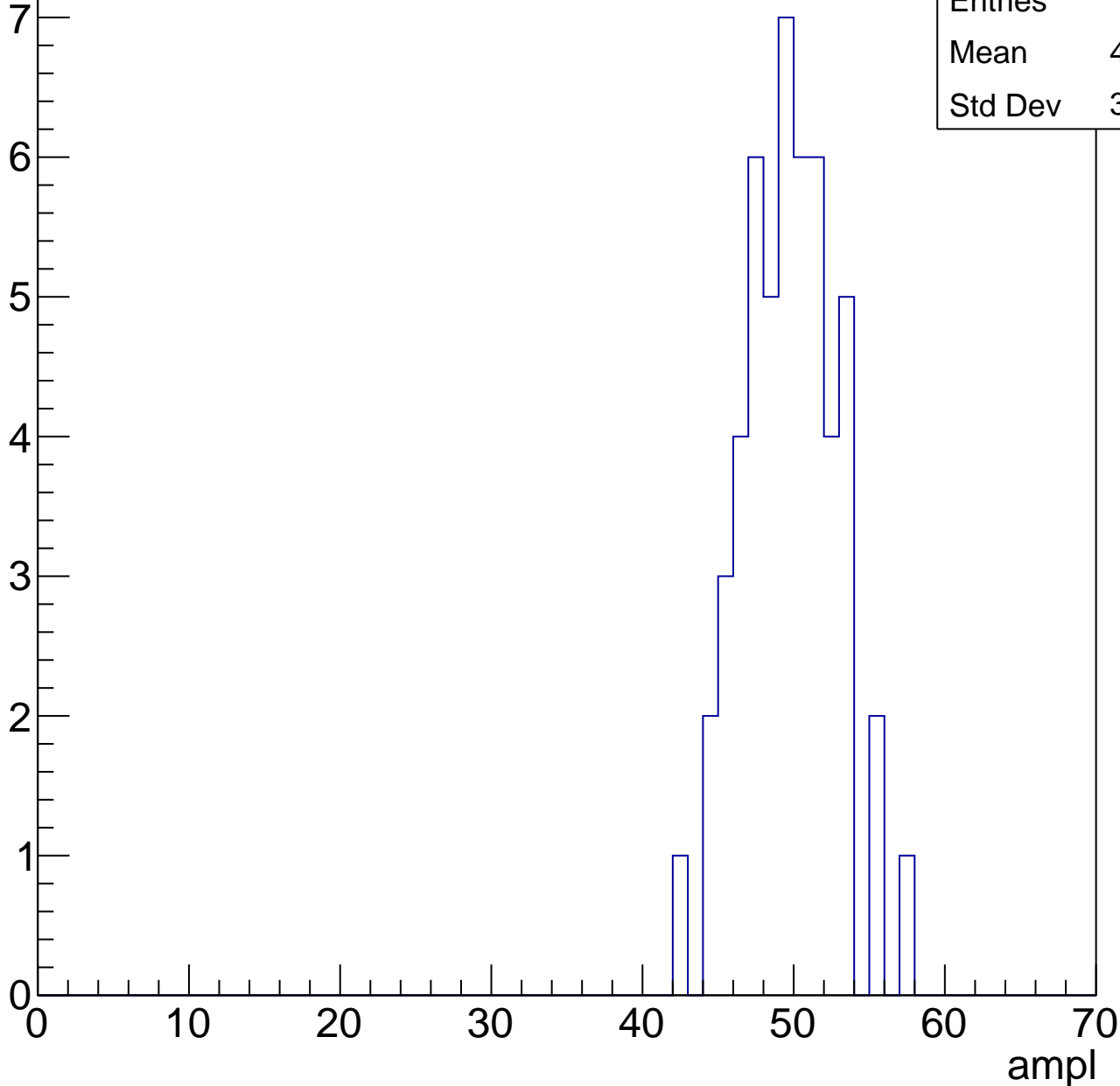


B0L001S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

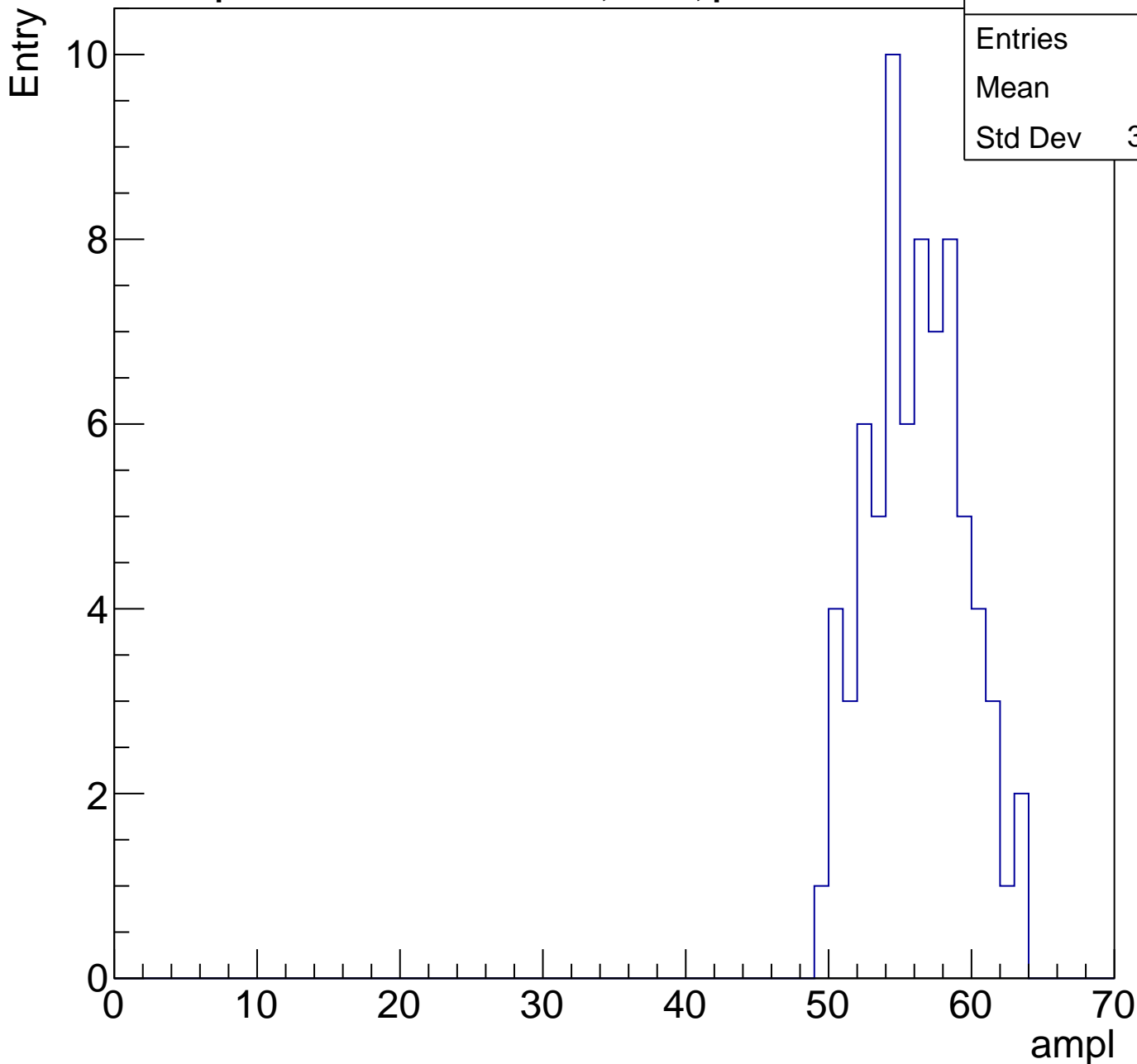
Entries	52
Mean	49.23
Std Dev	3.074



B0L001S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	55.7
Std Dev	3.322

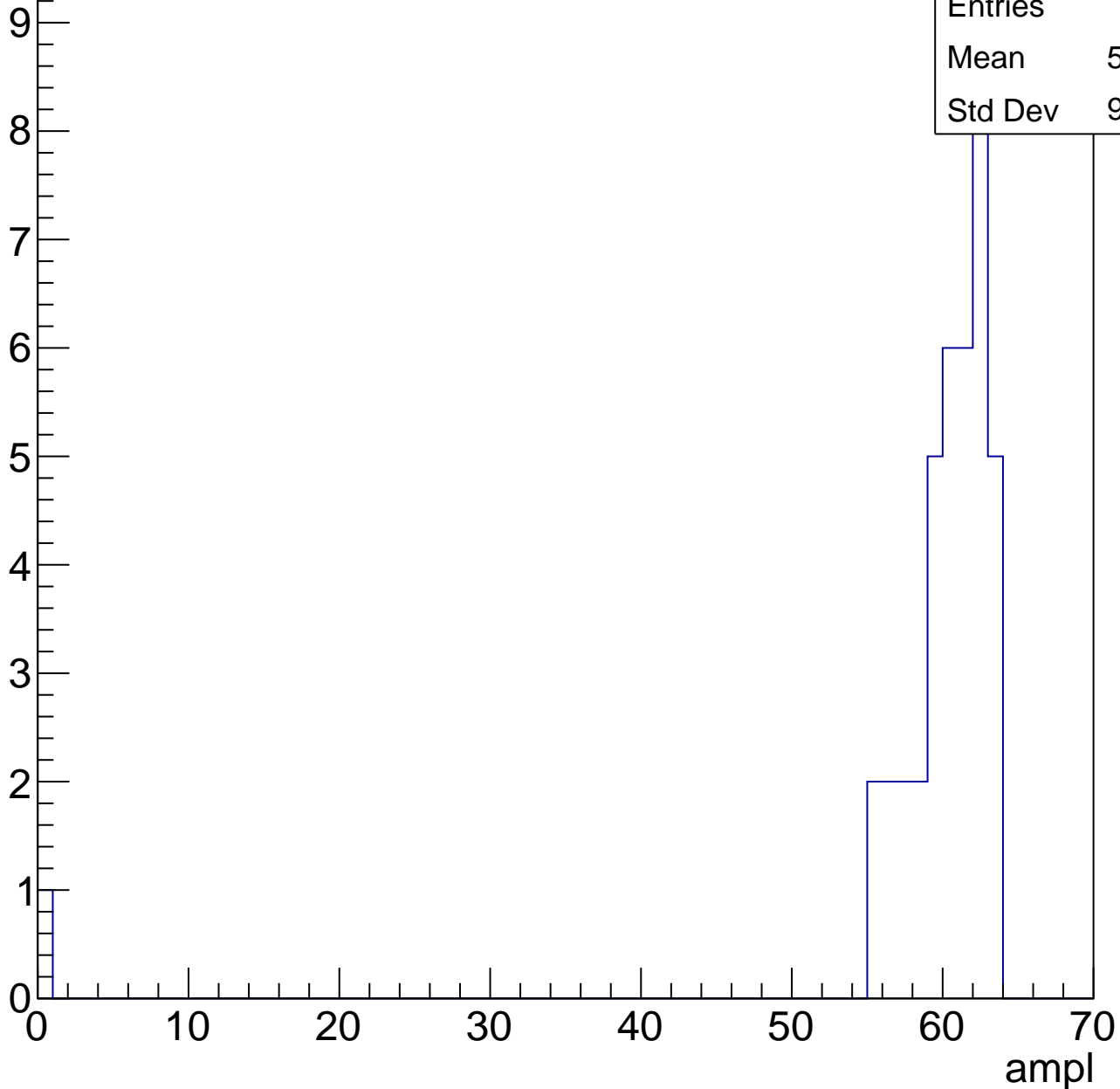


B0L001S, U15-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.65
Std Dev	9.653



B0L001S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U15-ch38, adc0

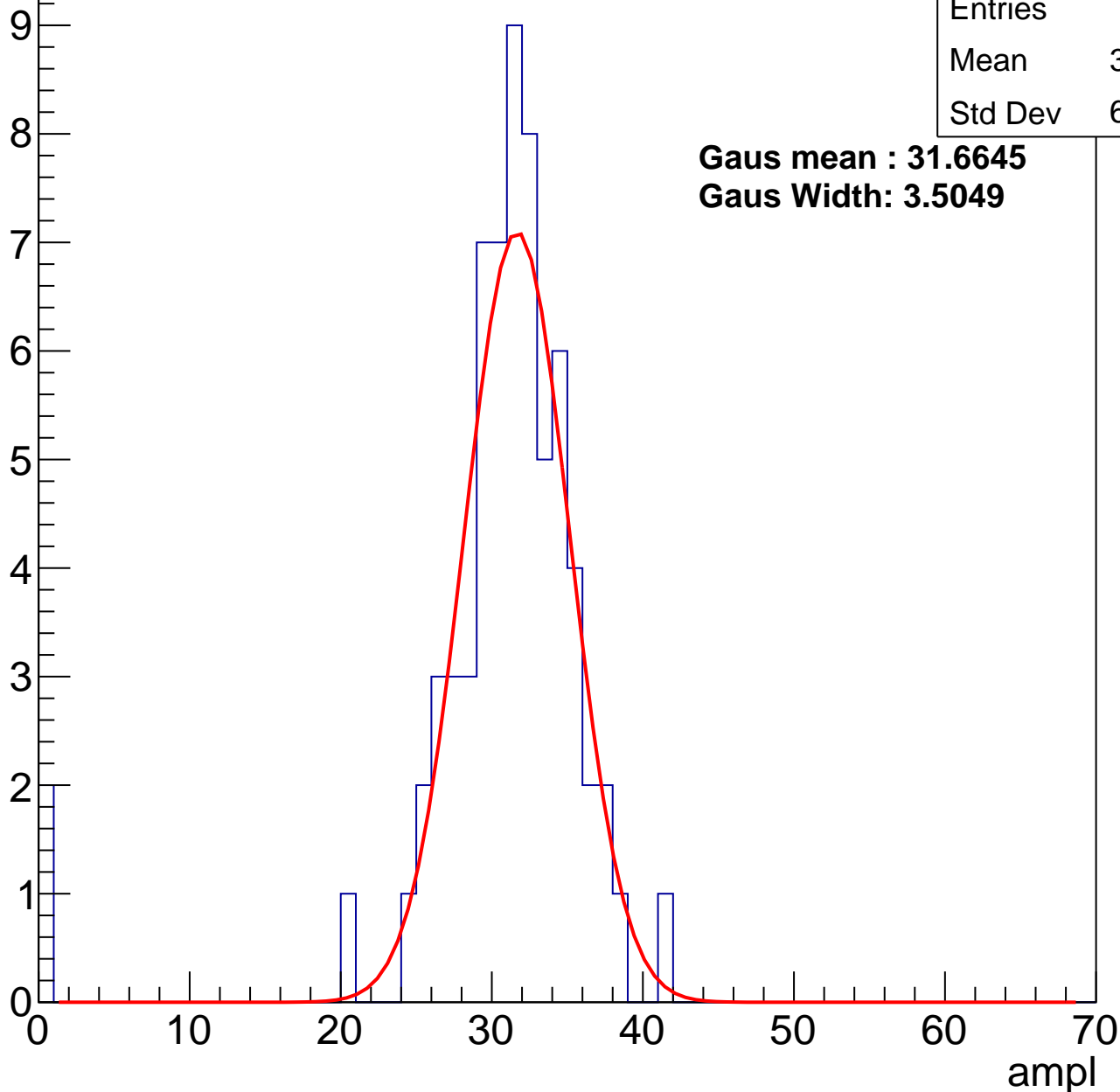
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.13
Std Dev	6.367

Gaus mean : 31.6645

Gaus Width: 3.5049



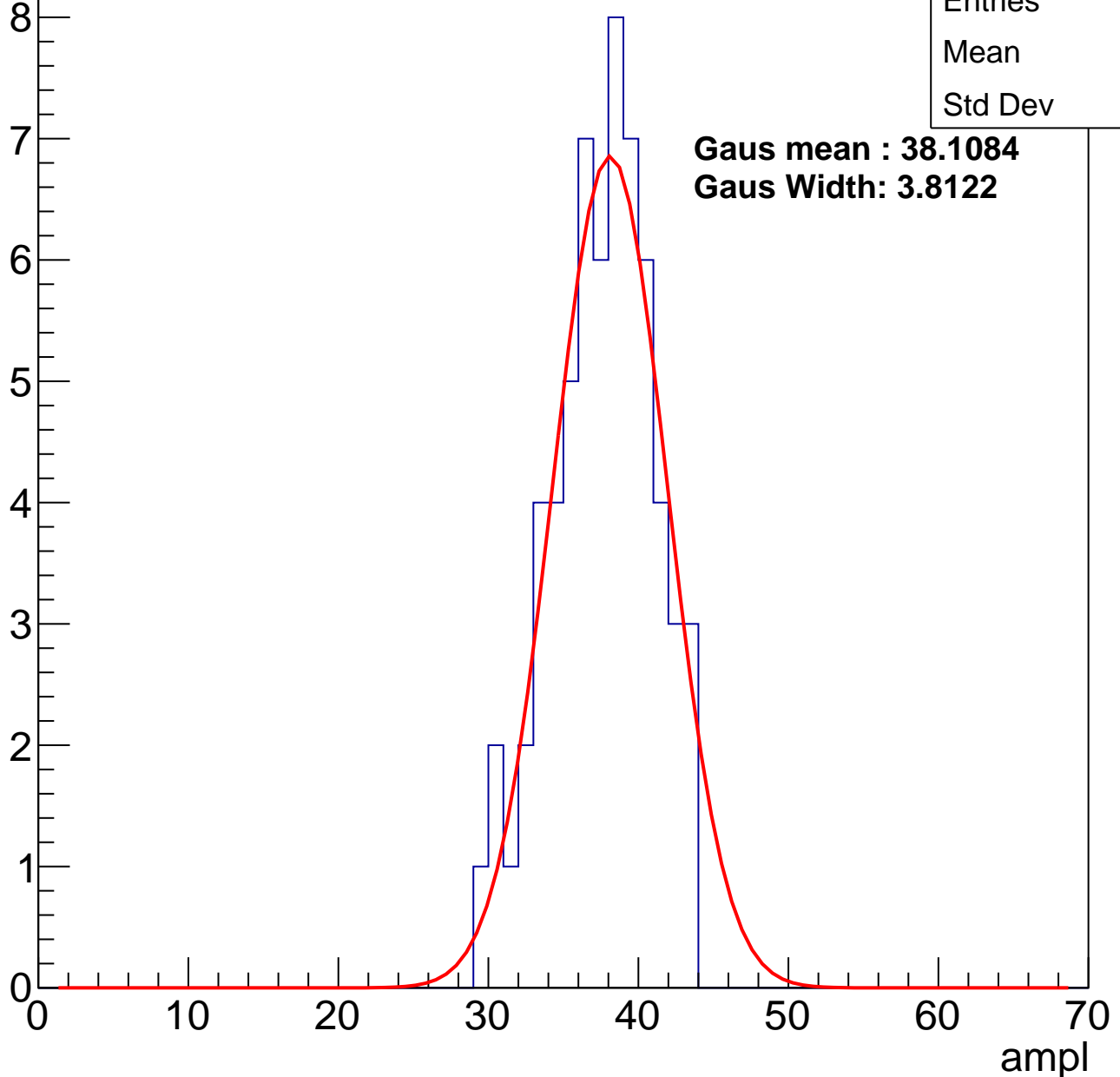
B0L001S, U15-ch38, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.1
Std Dev	3.36

Gaus mean : 38.1084
Gaus Width: 3.8122



B0L001S, U15-ch38, adc2

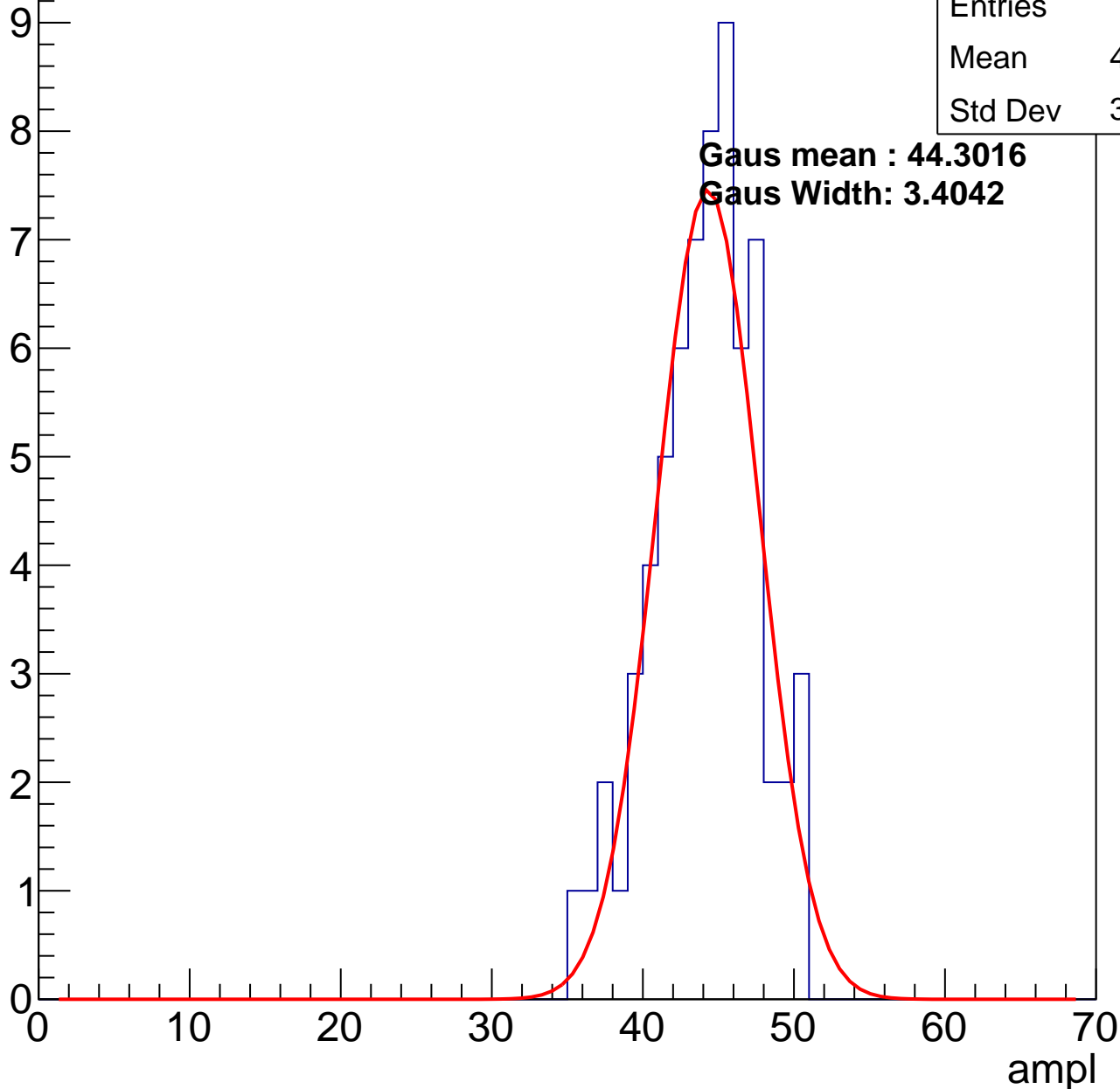
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.64
Std Dev	3.393

Gaus mean : 44.3016

Gaus Width: 3.4042

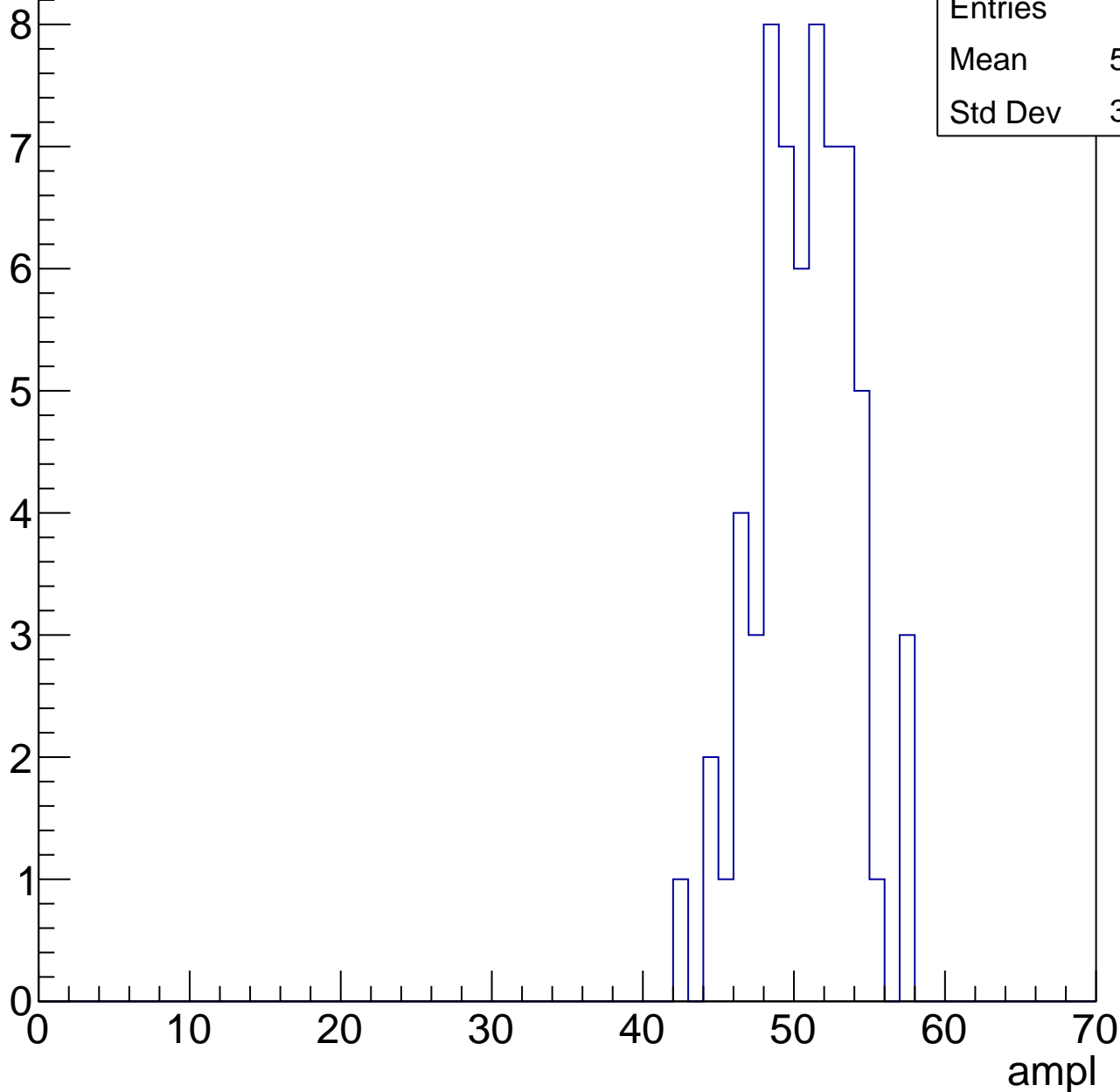


B0L001S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

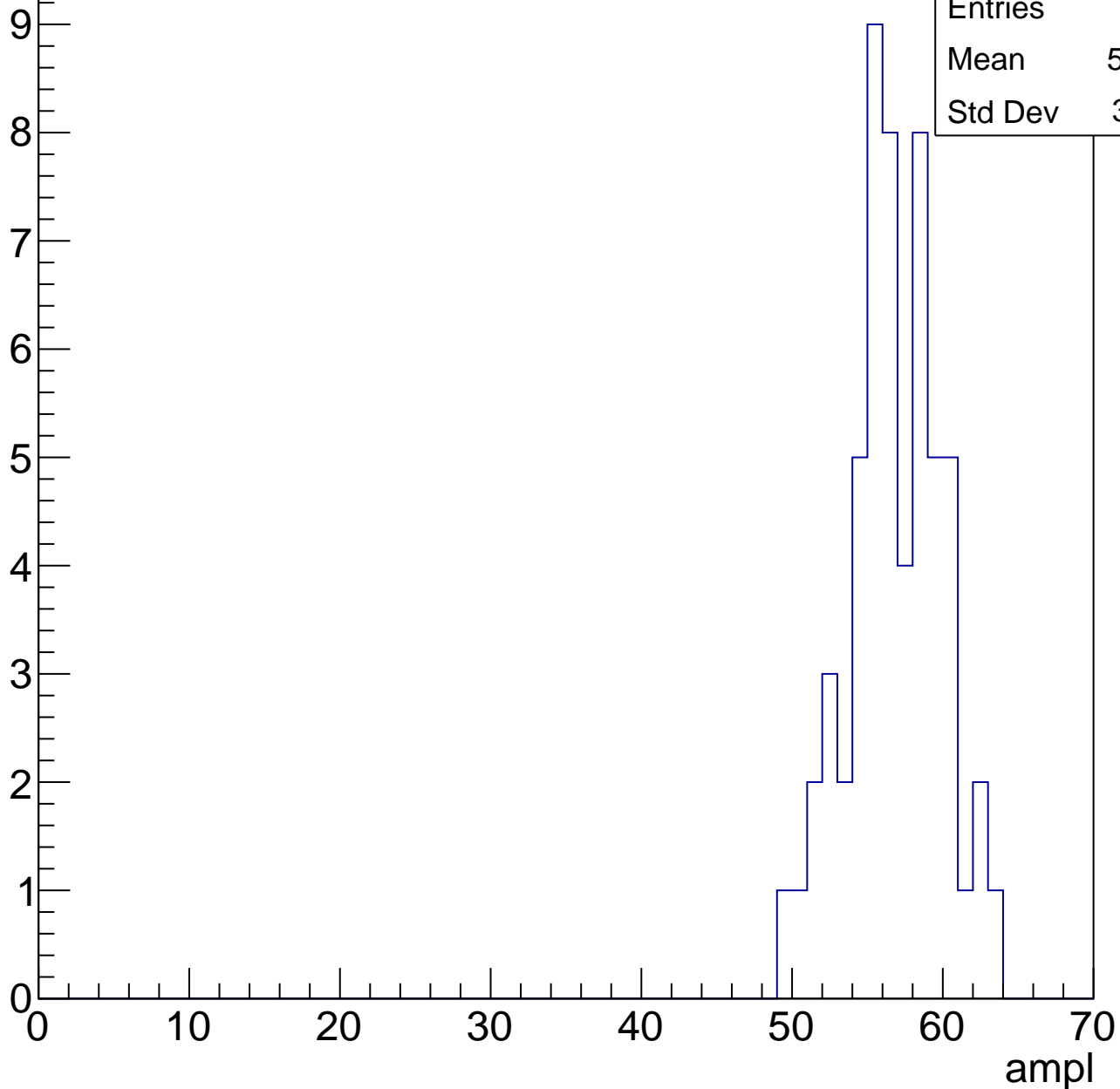
Entries	63
Mean	50.25
Std Dev	3.182



B0L001S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



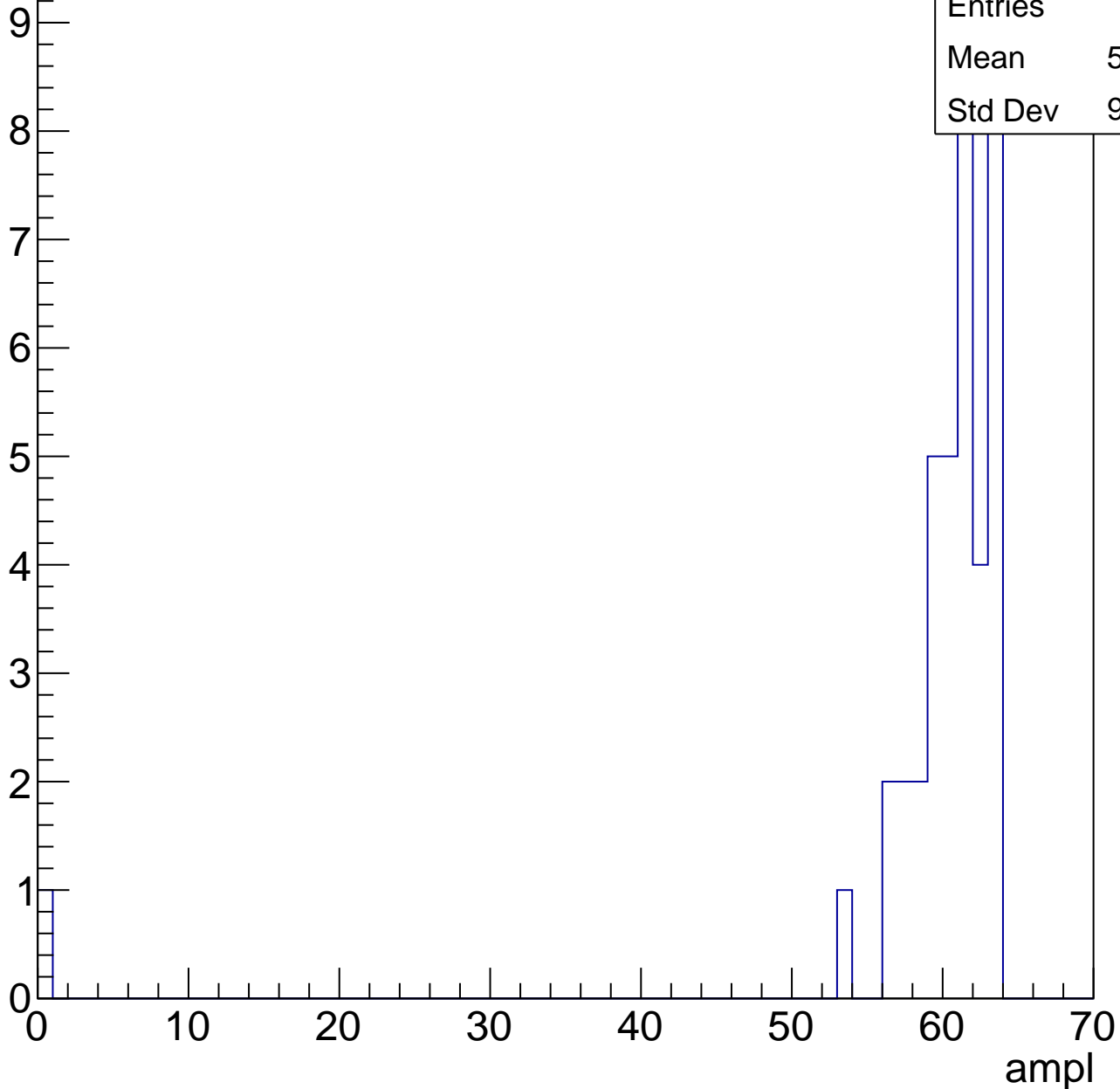
Entries	57
Mean	56.33
Std Dev	3.051

B0L001S, U15-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

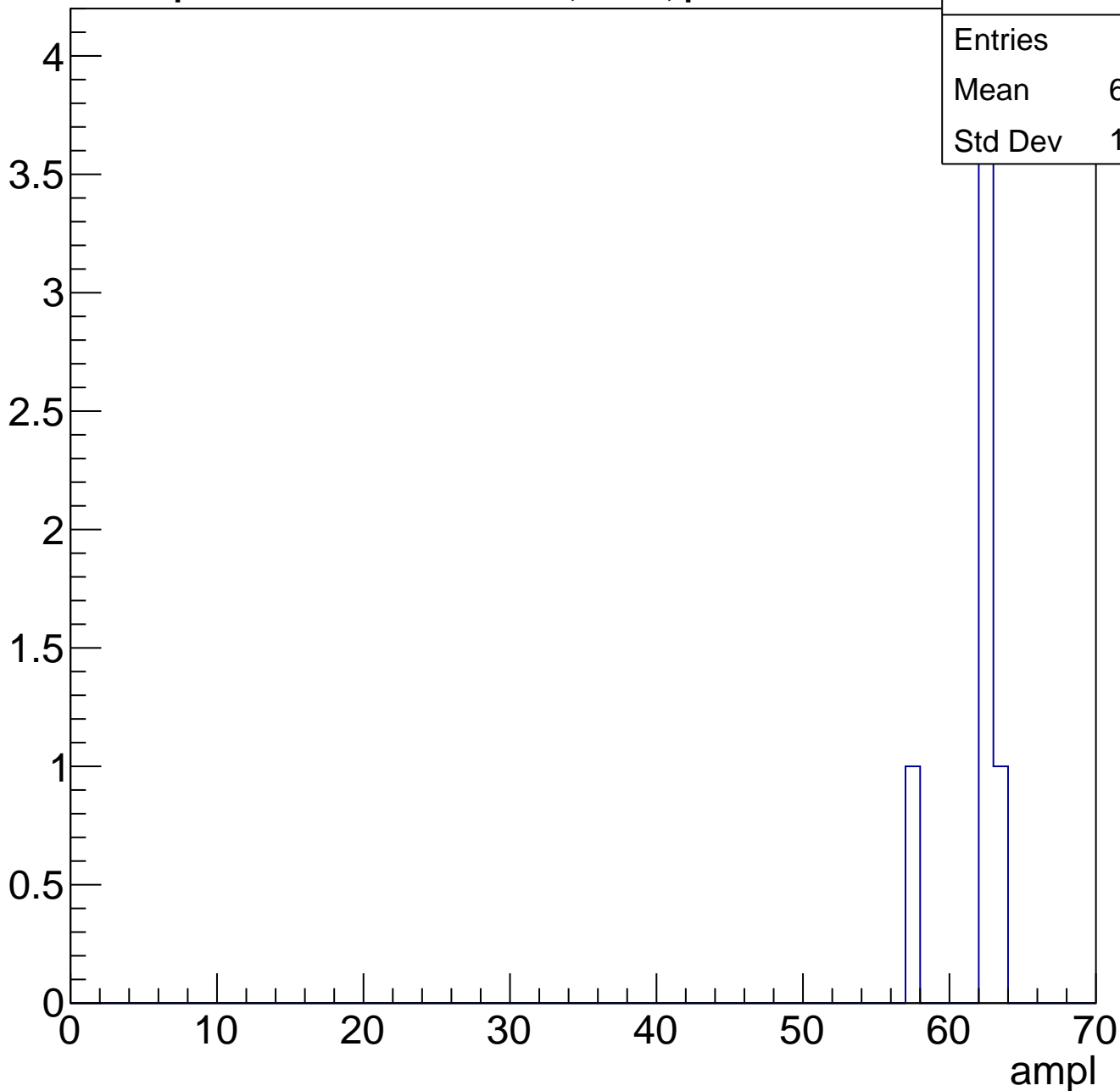
Entries	39
Mean	58.74
Std Dev	9.802



B0L001S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch39, adc0

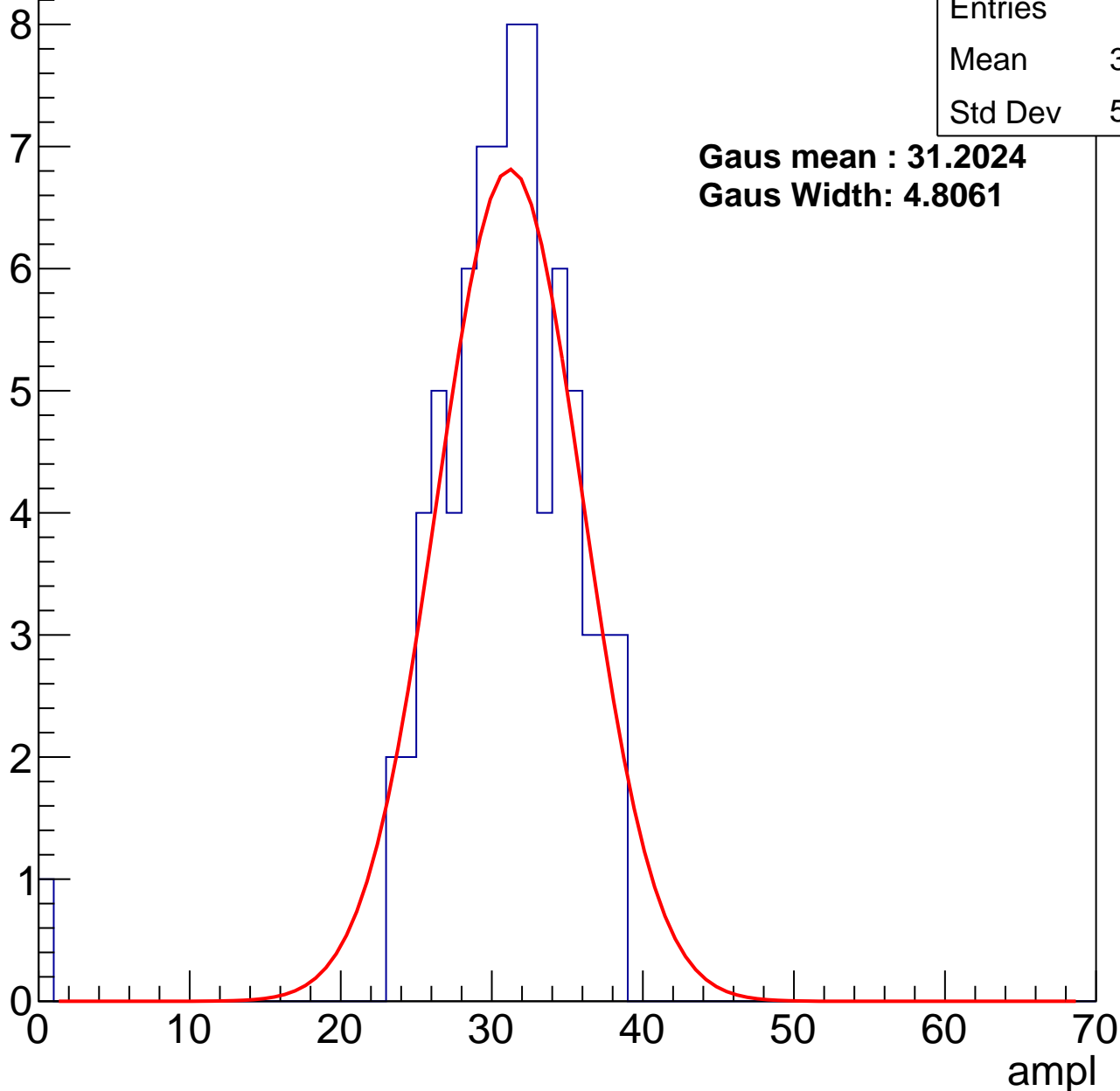
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.27
Std Dev	5.133

Gaus mean : 31.2024

Gaus Width: 4.8061



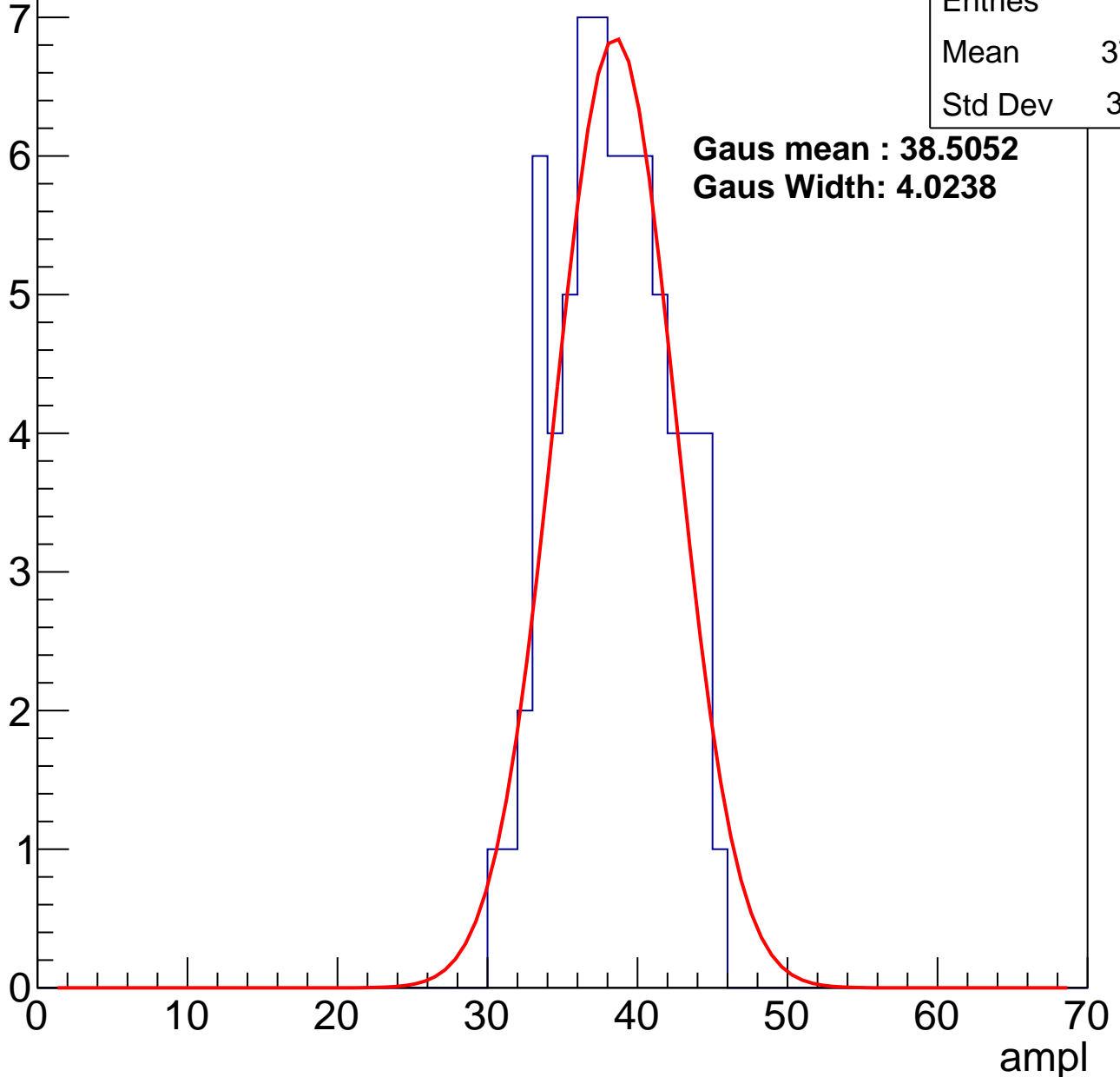
B0L001S, U15-ch39, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.87
Std Dev	3.631

Gaus mean : 38.5052
Gaus Width: 4.0238



B0L001S, U15-ch39, adc2

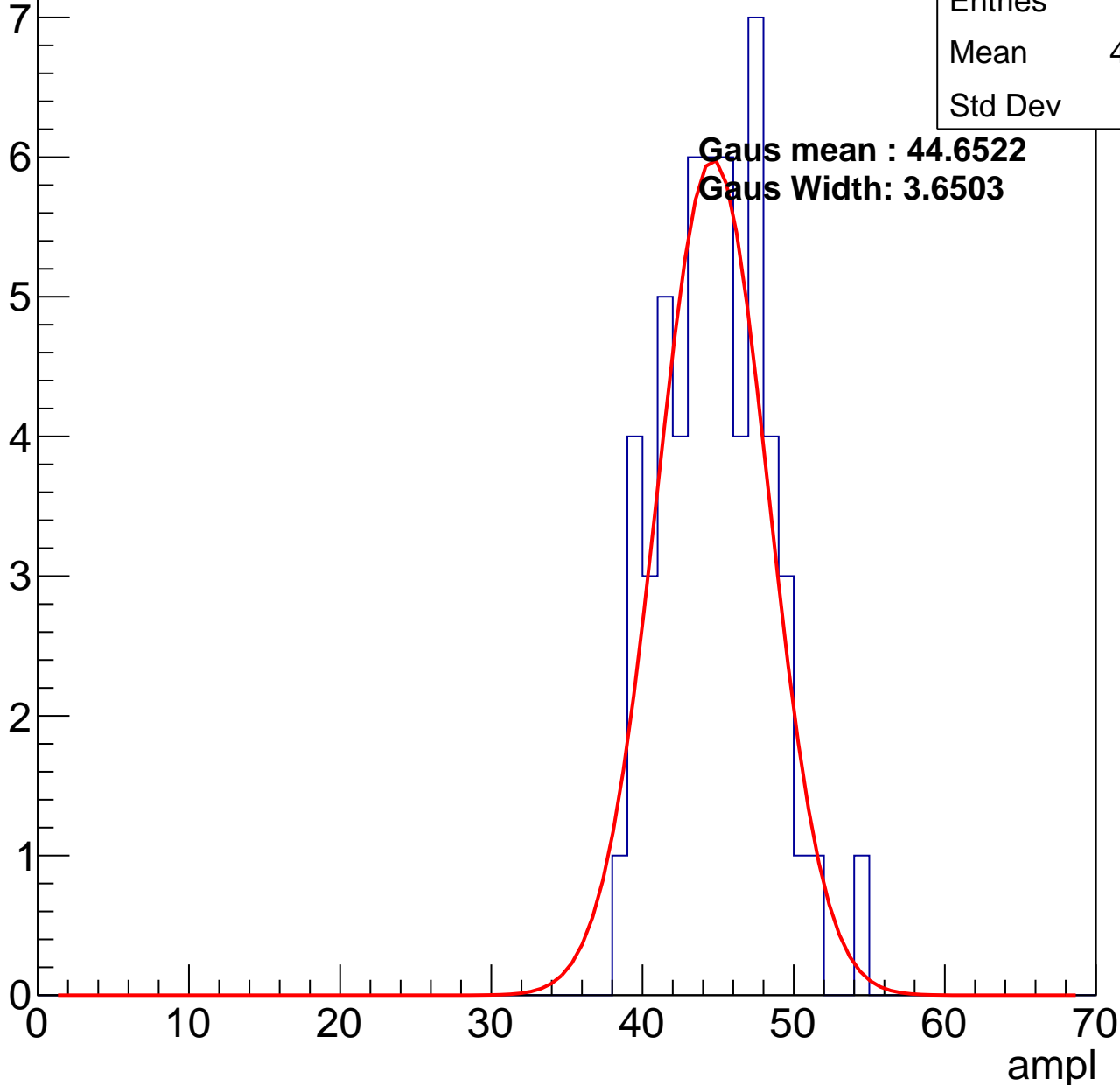
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.39
Std Dev	3.41

Gaus mean : 44.6522

Gaus Width: 3.6503

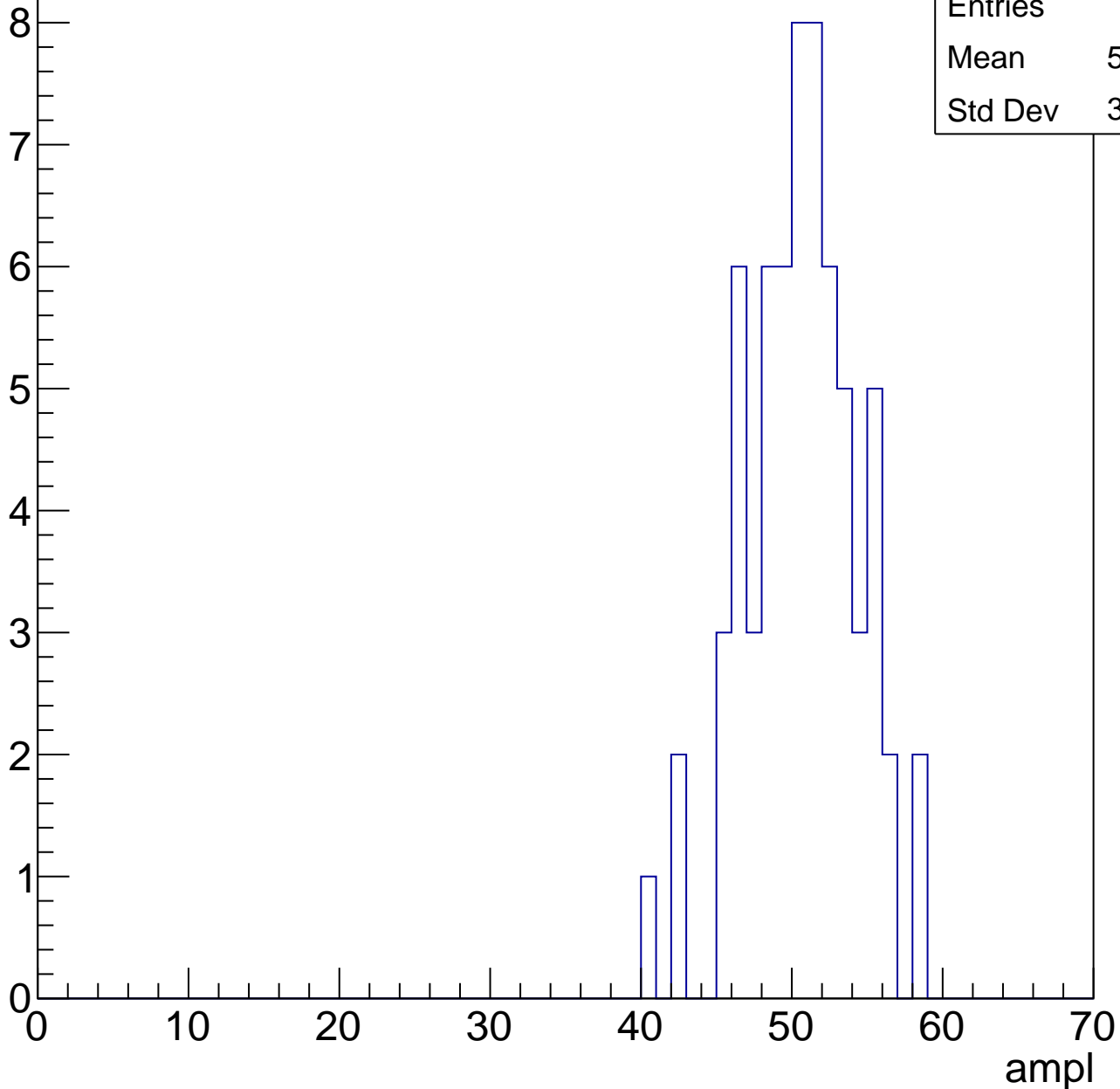


B0L001S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.12
Std Dev	3.703

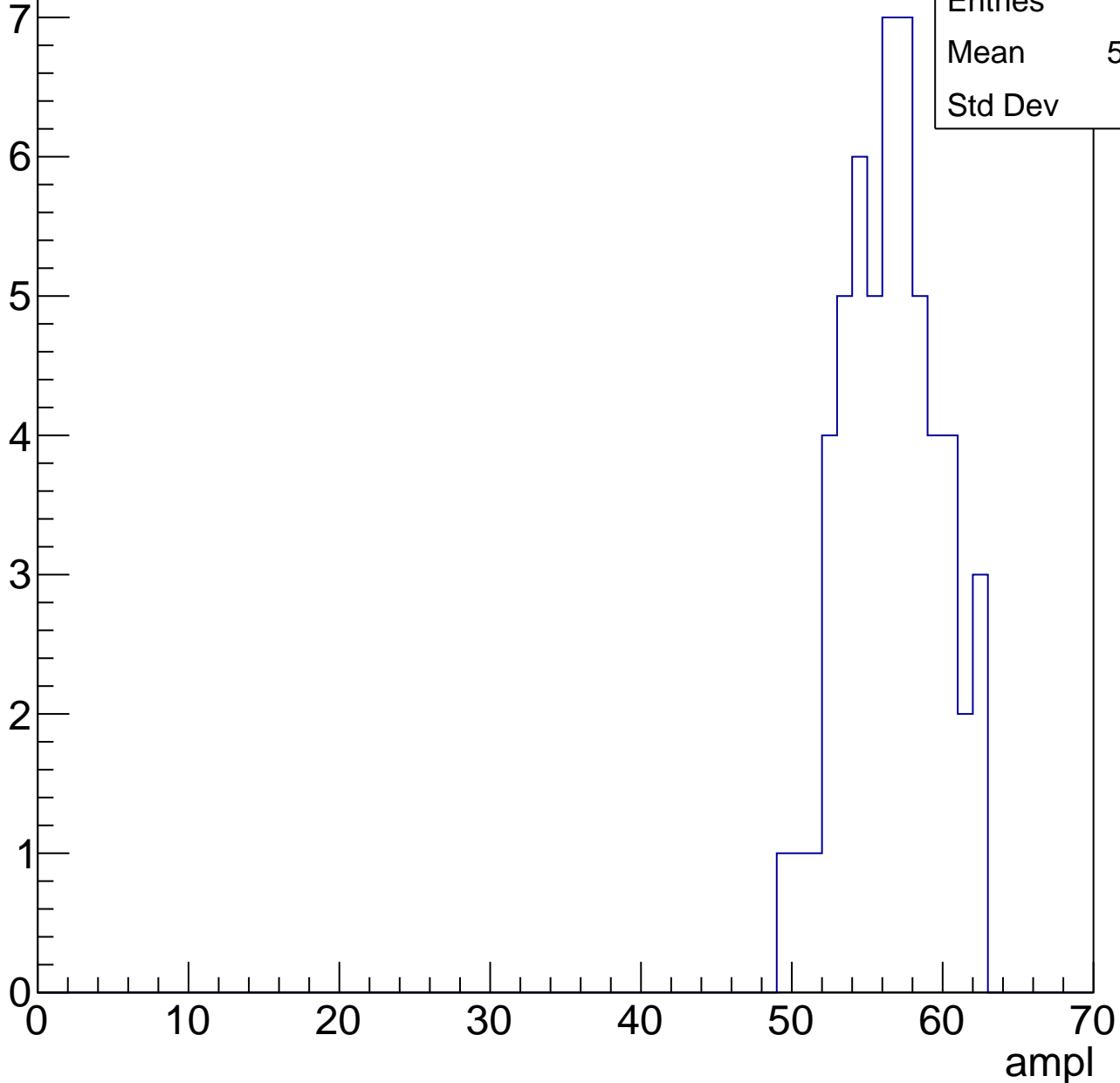


B0L001S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.13
Std Dev	3.11

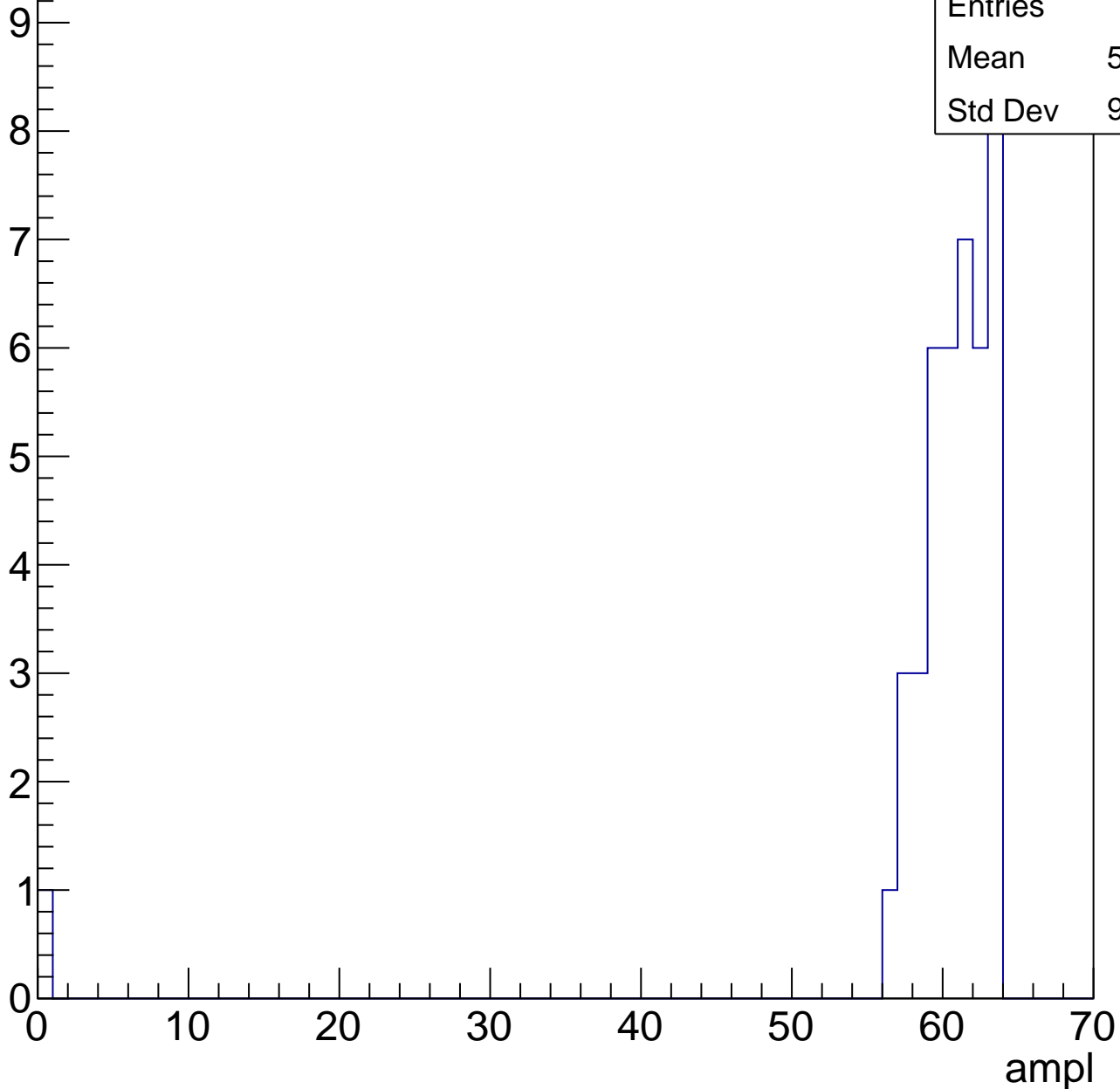


B0L001S, U15-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.07
Std Dev	9.432



B0L001S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch40, adc0

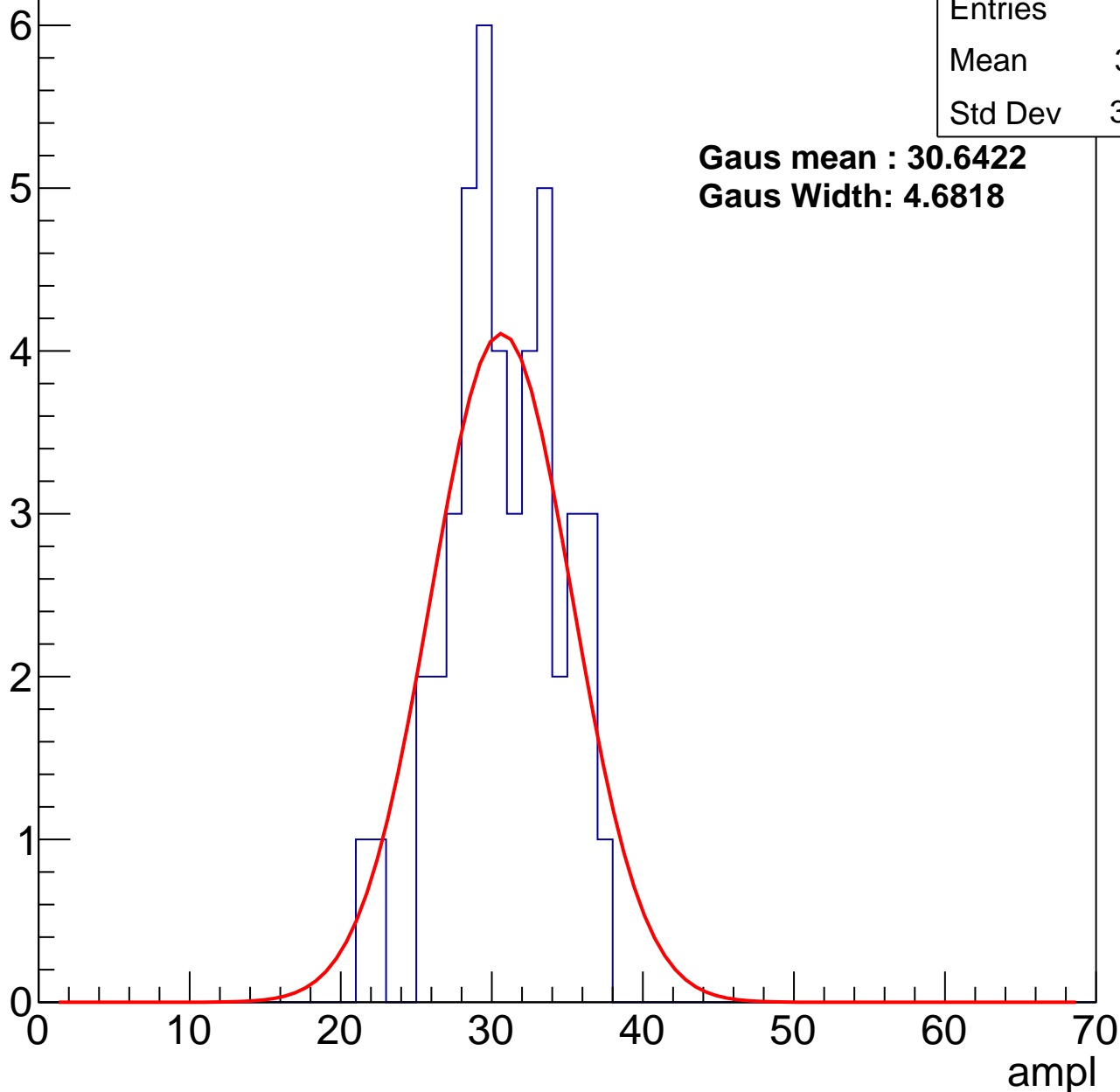
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	30.31
Std Dev	3.663

Gaus mean : 30.6422

Gaus Width: 4.6818



B0L001S, U15-ch40, adc1

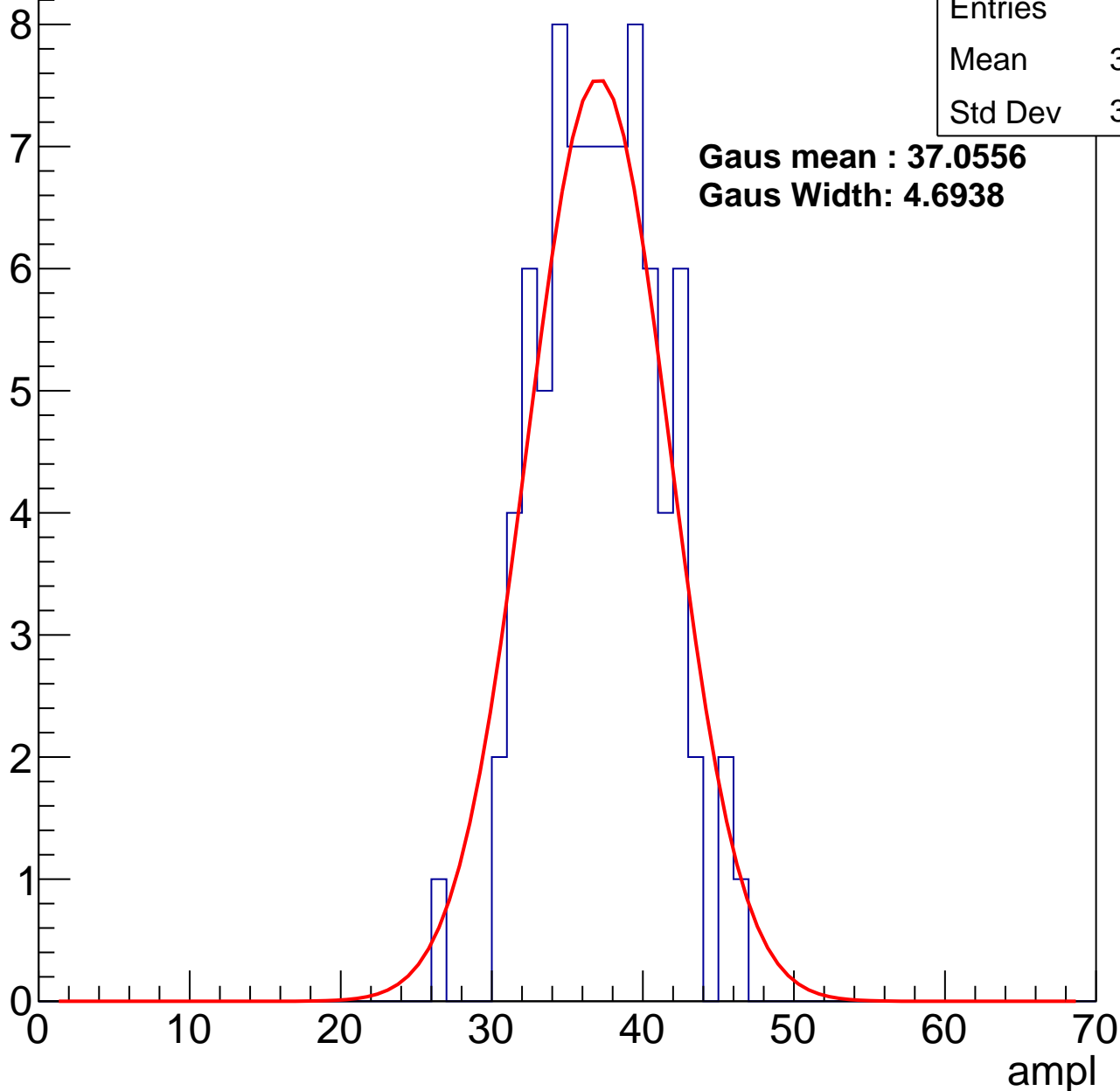
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	36.76
Std Dev	3.932

Gaus mean : 37.0556

Gaus Width: 4.6938



B0L001S, U15-ch40, adc2

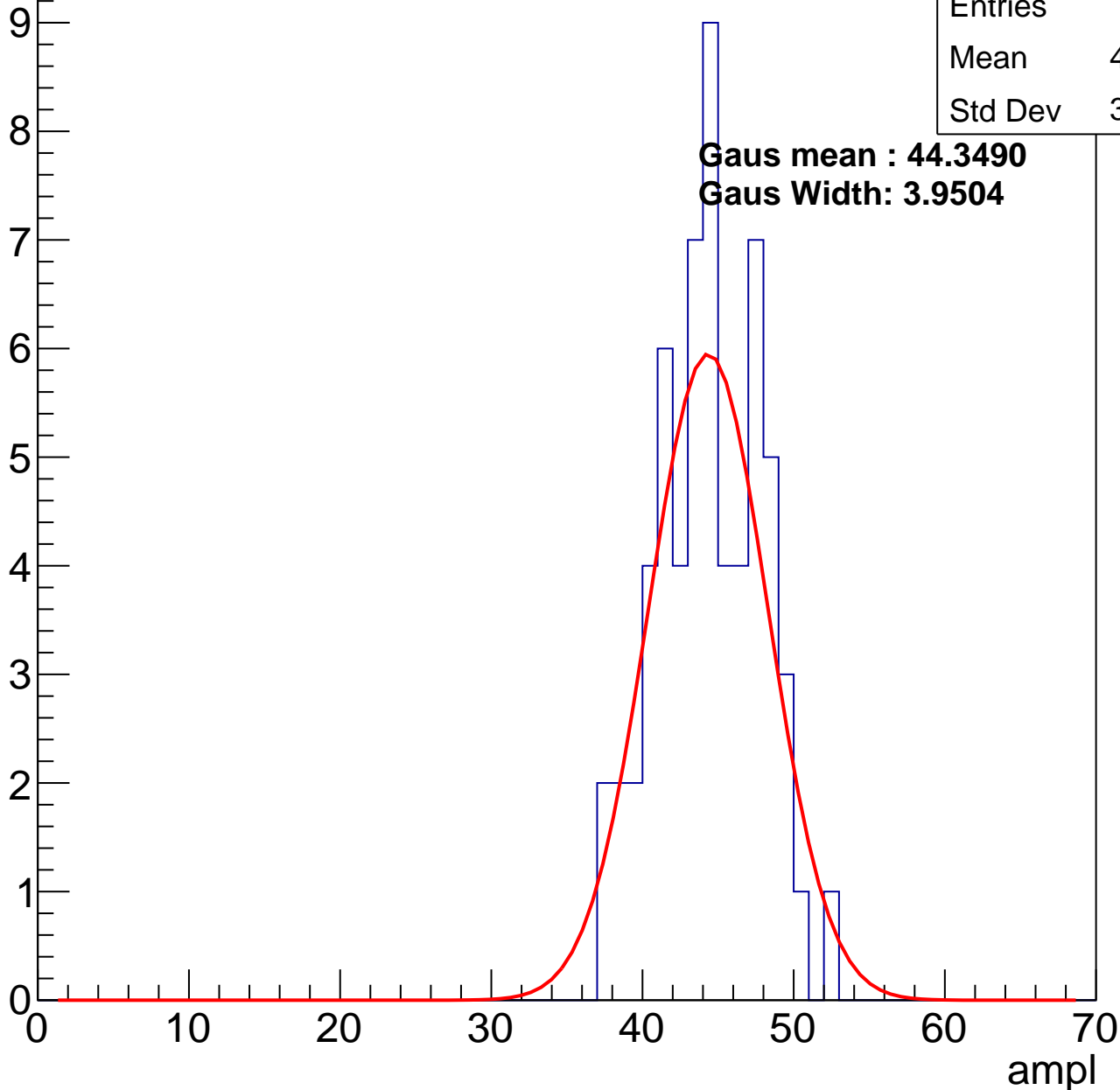
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.95
Std Dev	3.385

Gaus mean : 44.3490

Gaus Width: 3.9504

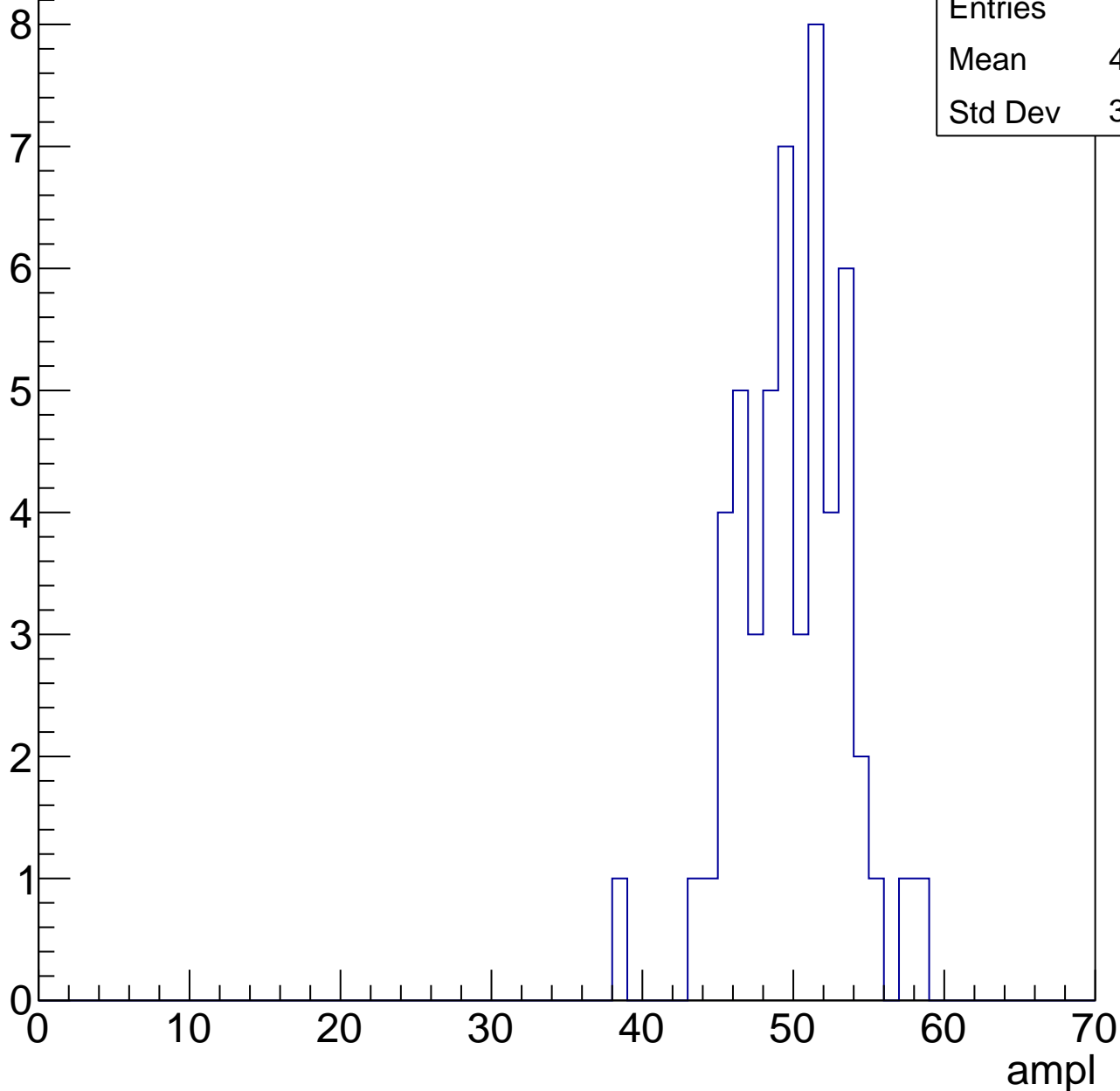


B0L001S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	49.45
Std Dev	3.622

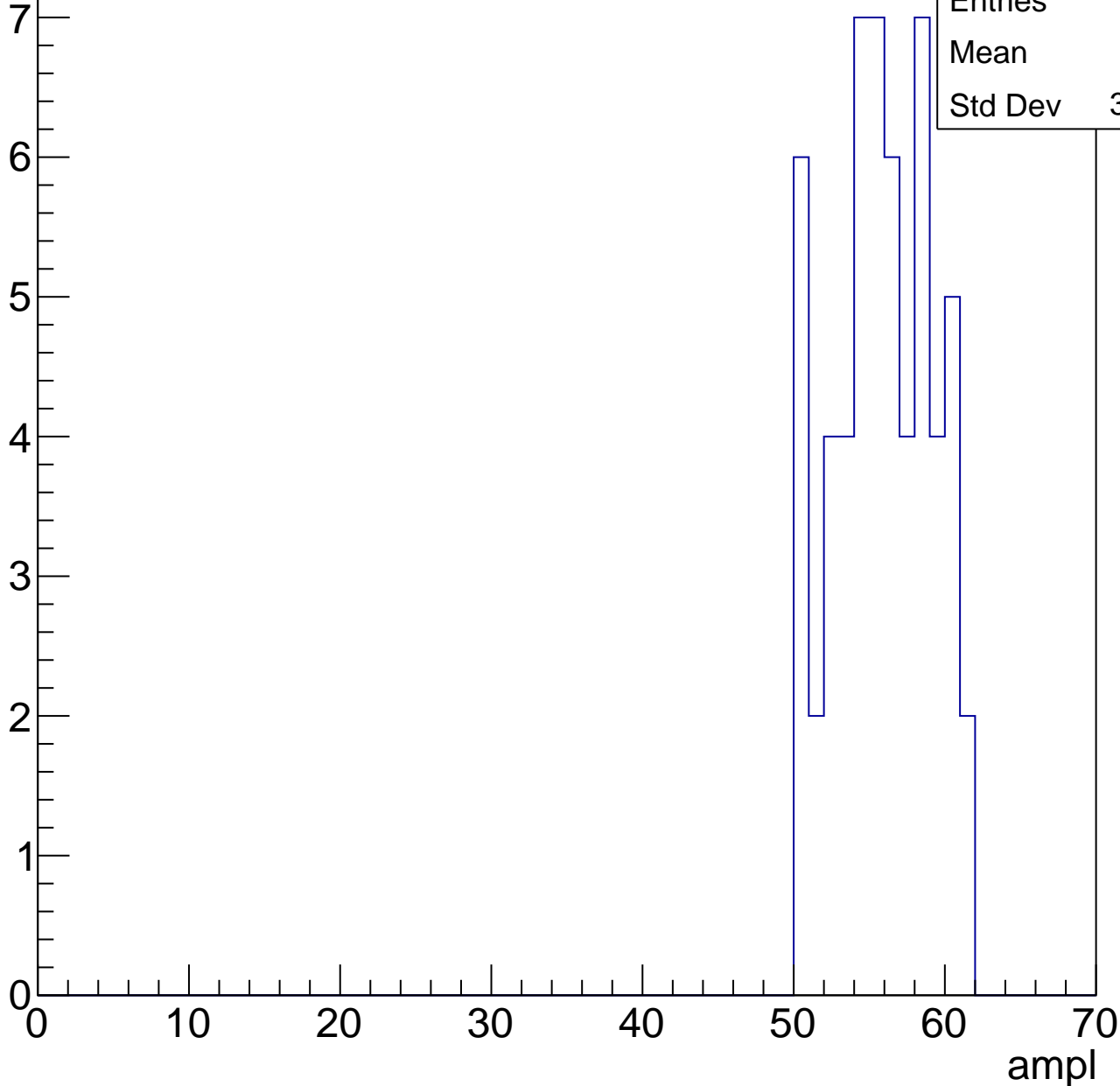


B0L001S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	55.4
Std Dev	3.156

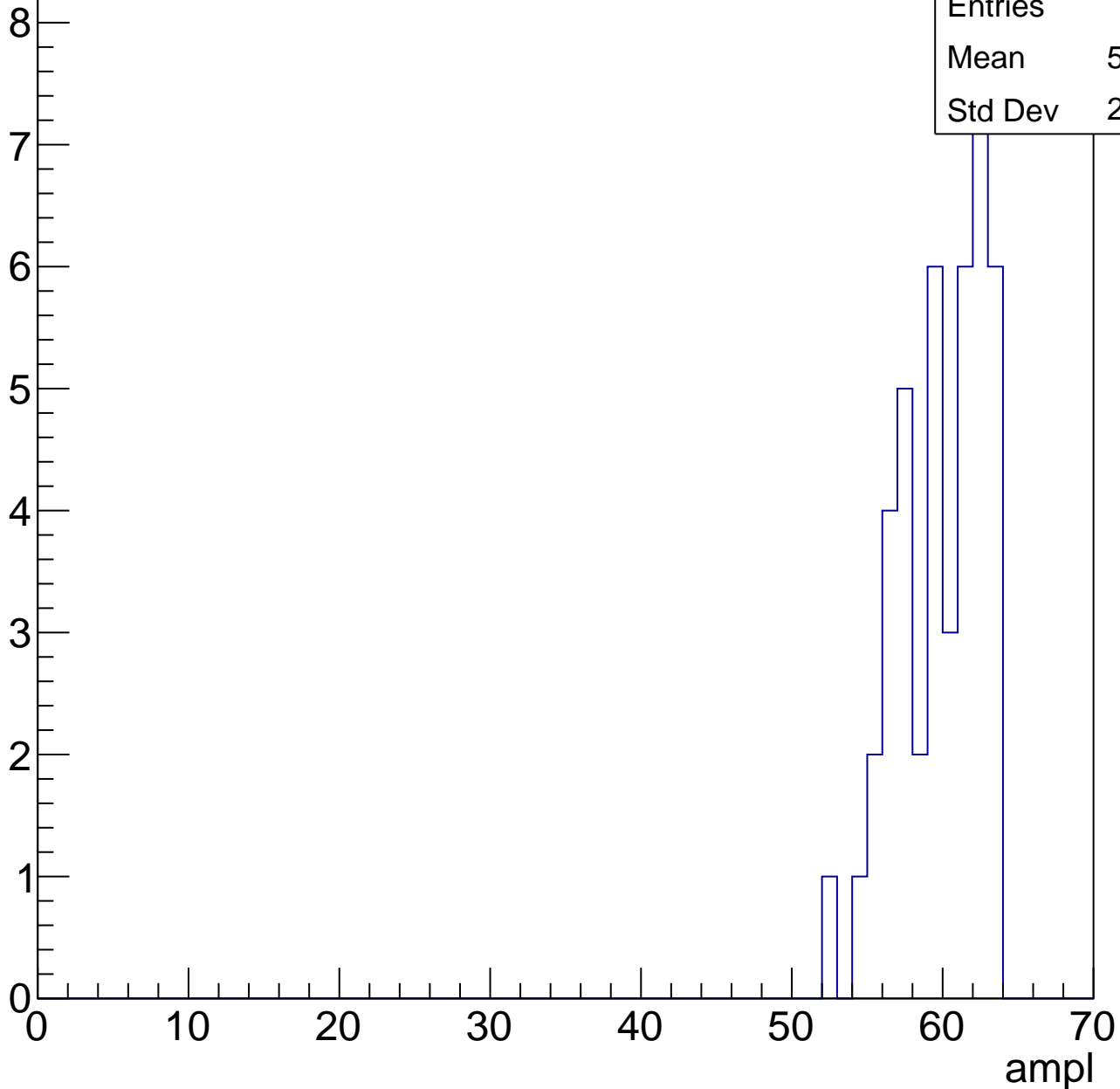


B0L001S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

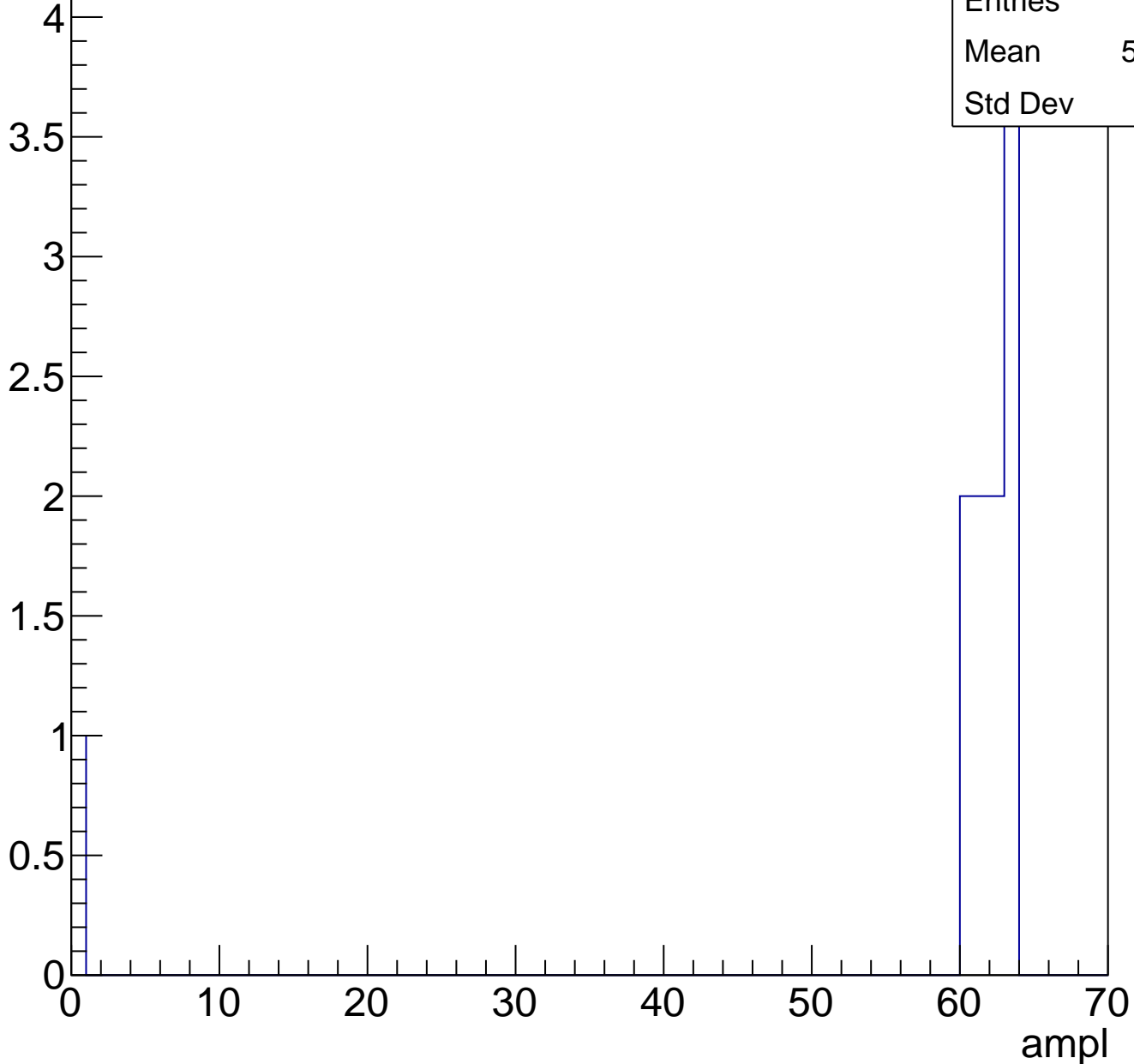
Entries	44
Mean	59.43
Std Dev	2.824



B0L001S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch41, adc0

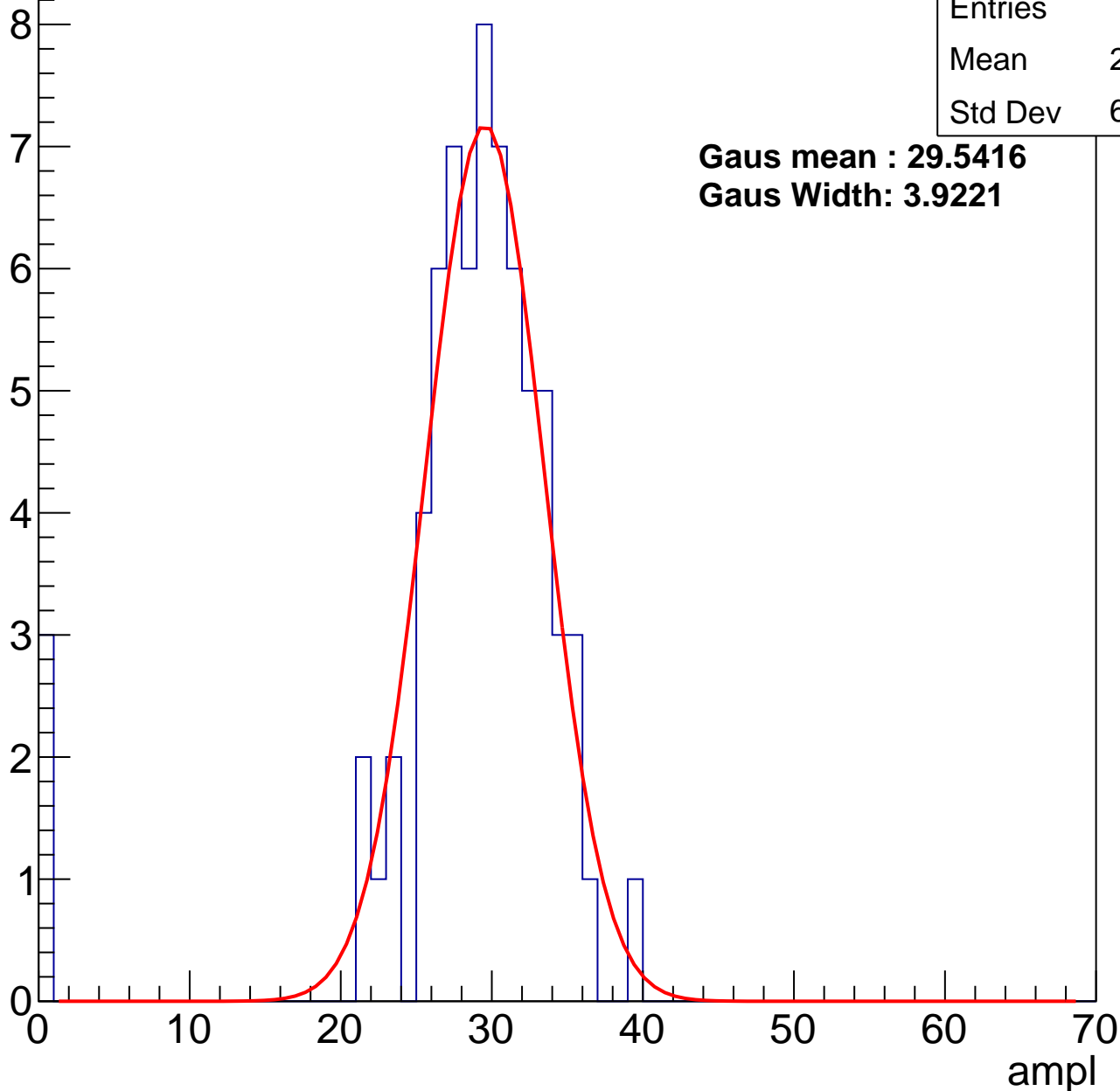
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	27.97
Std Dev	6.905

Gaus mean : 29.5416

Gaus Width: 3.9221



B0L001S, U15-ch41, adc1

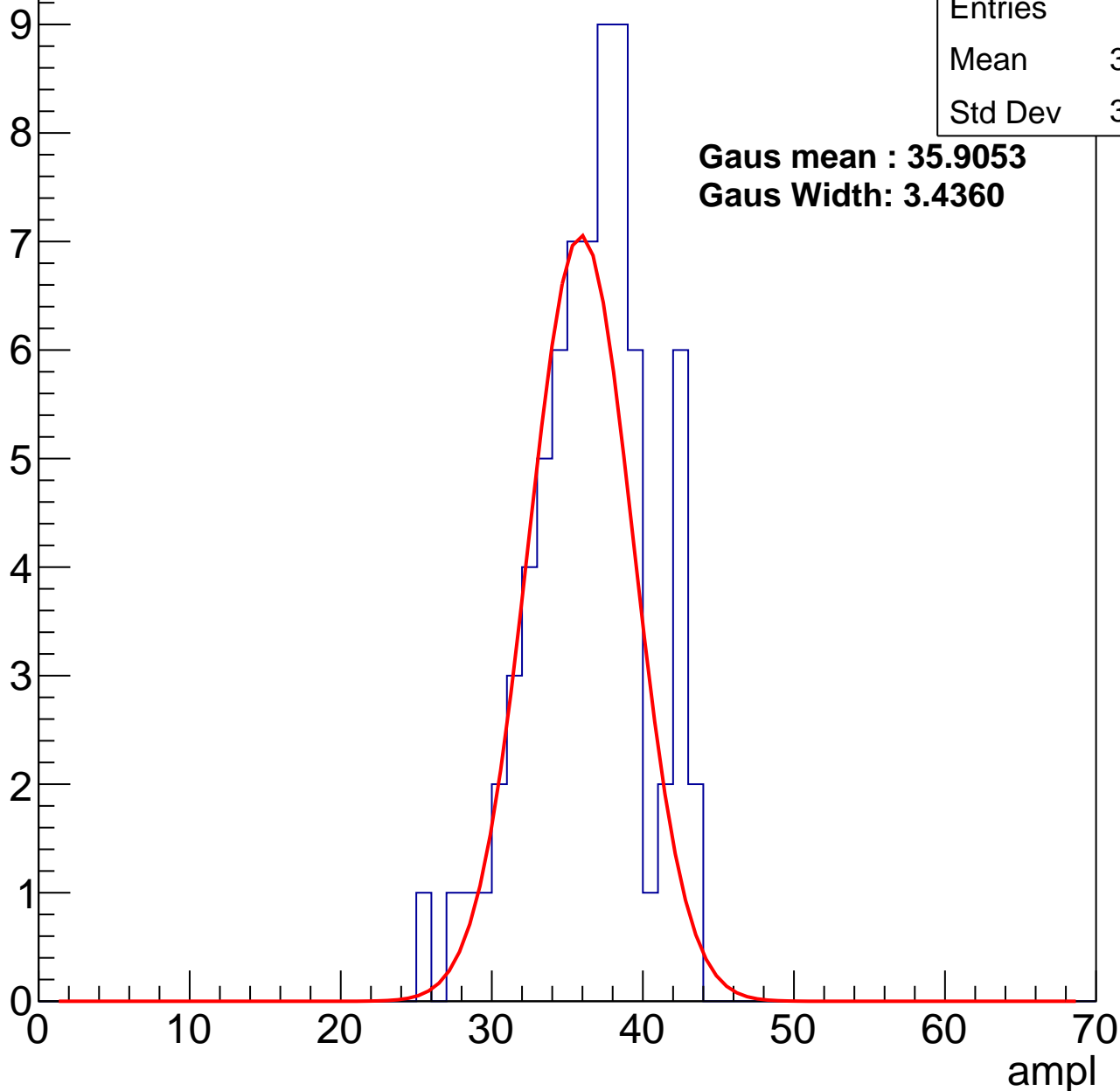
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	35.96
Std Dev	3.848

Gaus mean : 35.9053

Gaus Width: 3.4360



B0L001S, U15-ch41, adc2

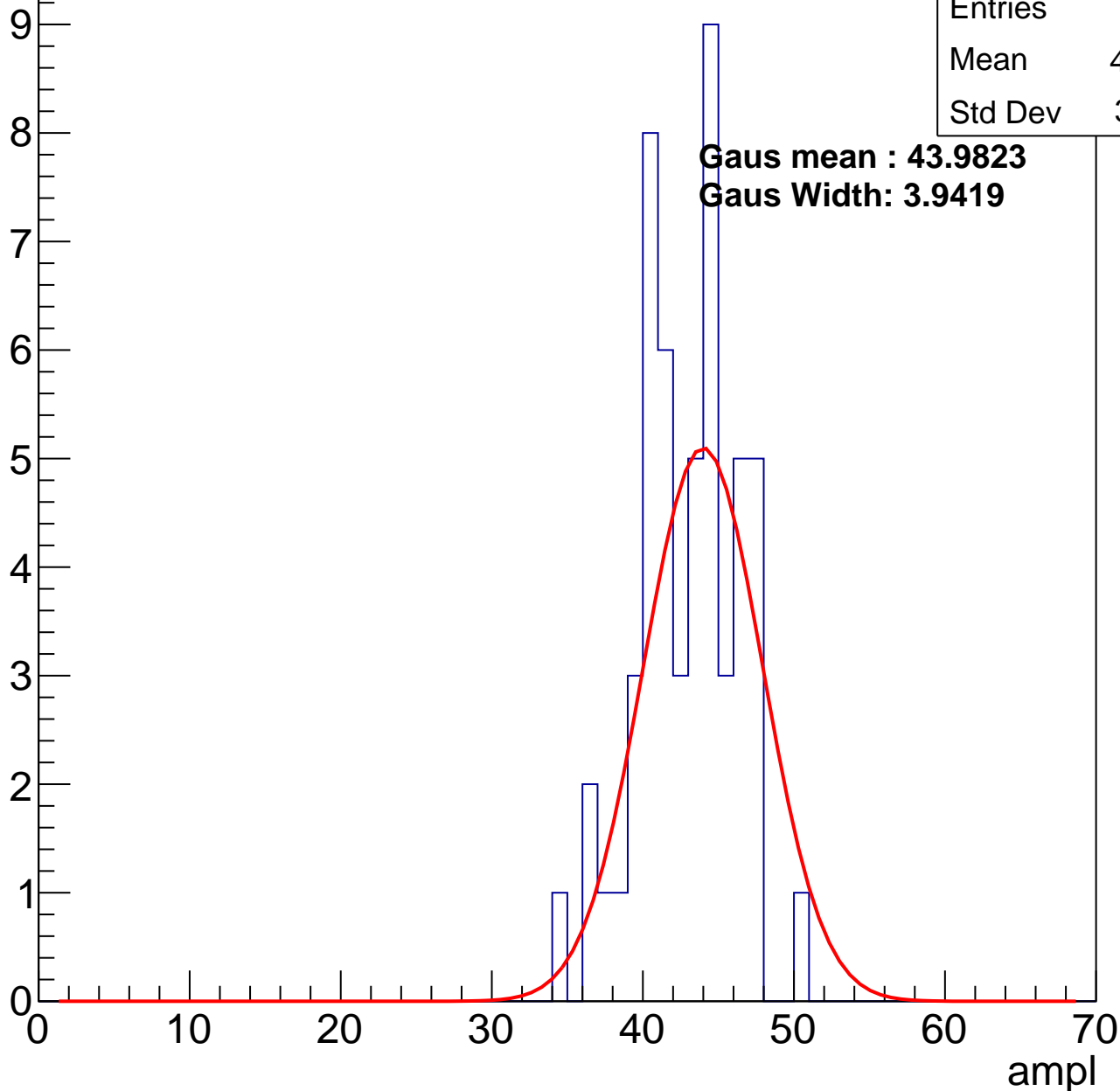
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.47
Std Dev	3.271

Gaus mean : 43.9823

Gaus Width: 3.9419

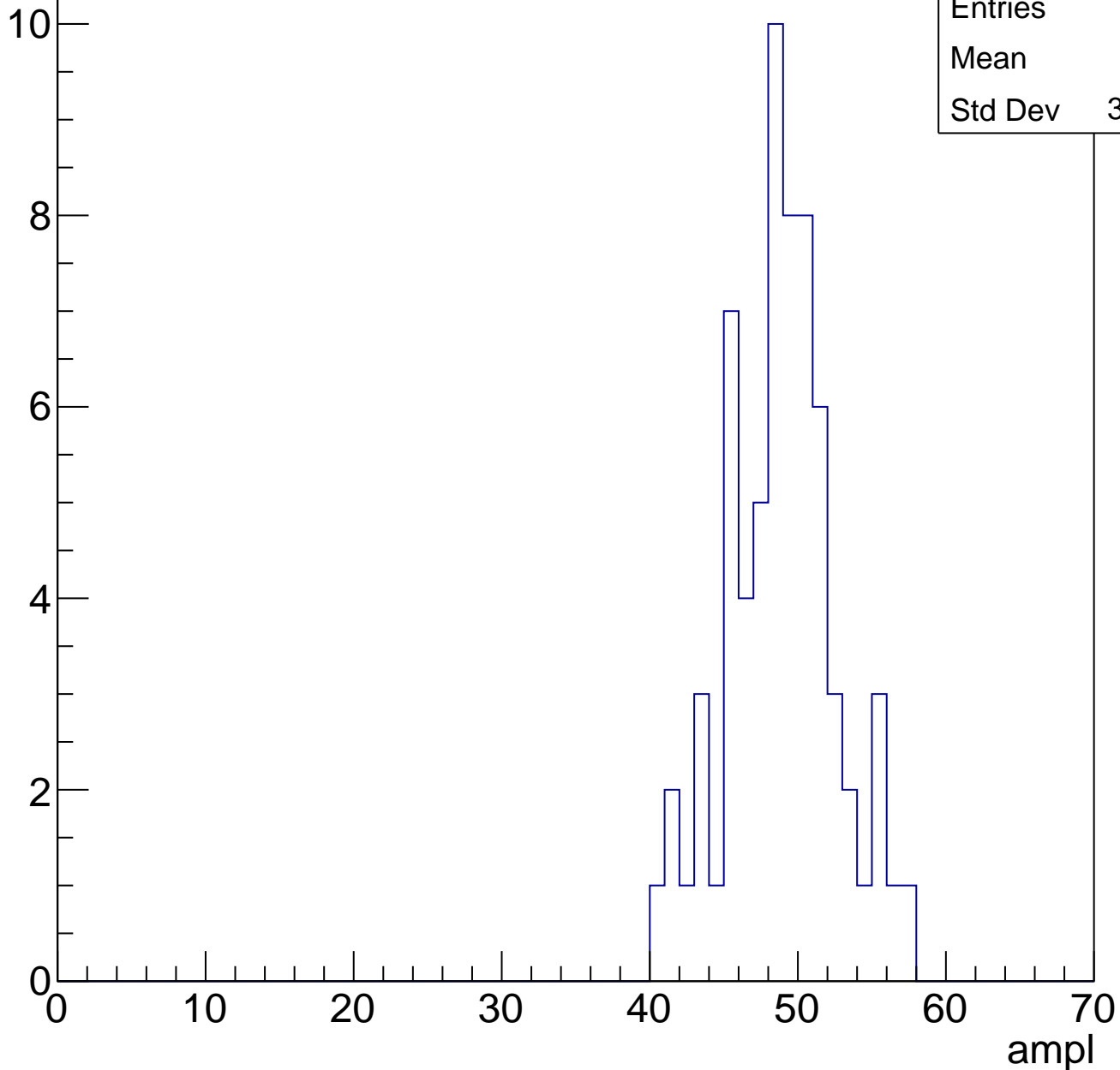


B0L001S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	48.4
Std Dev	3.628

Entry

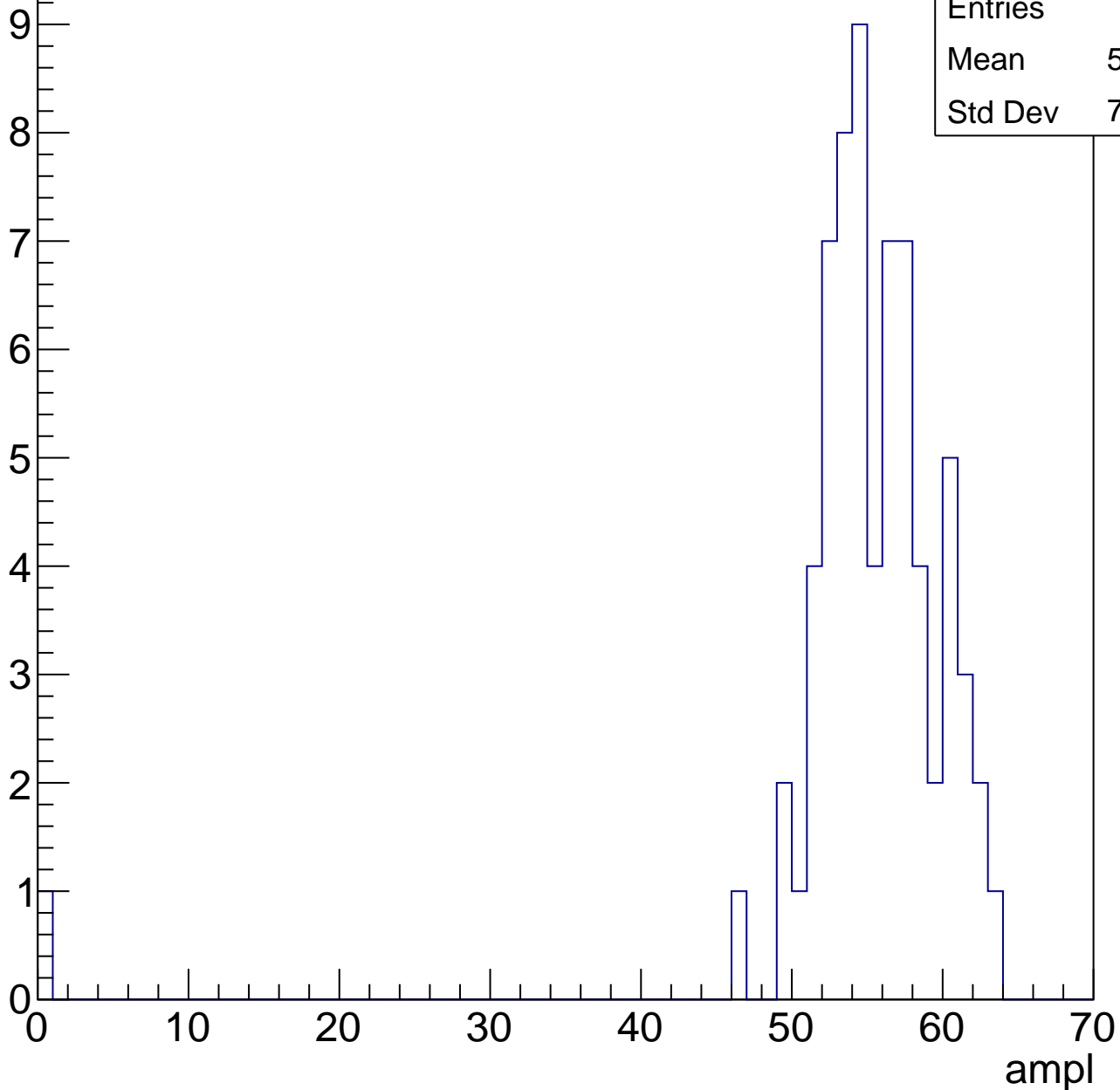


B0L001S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

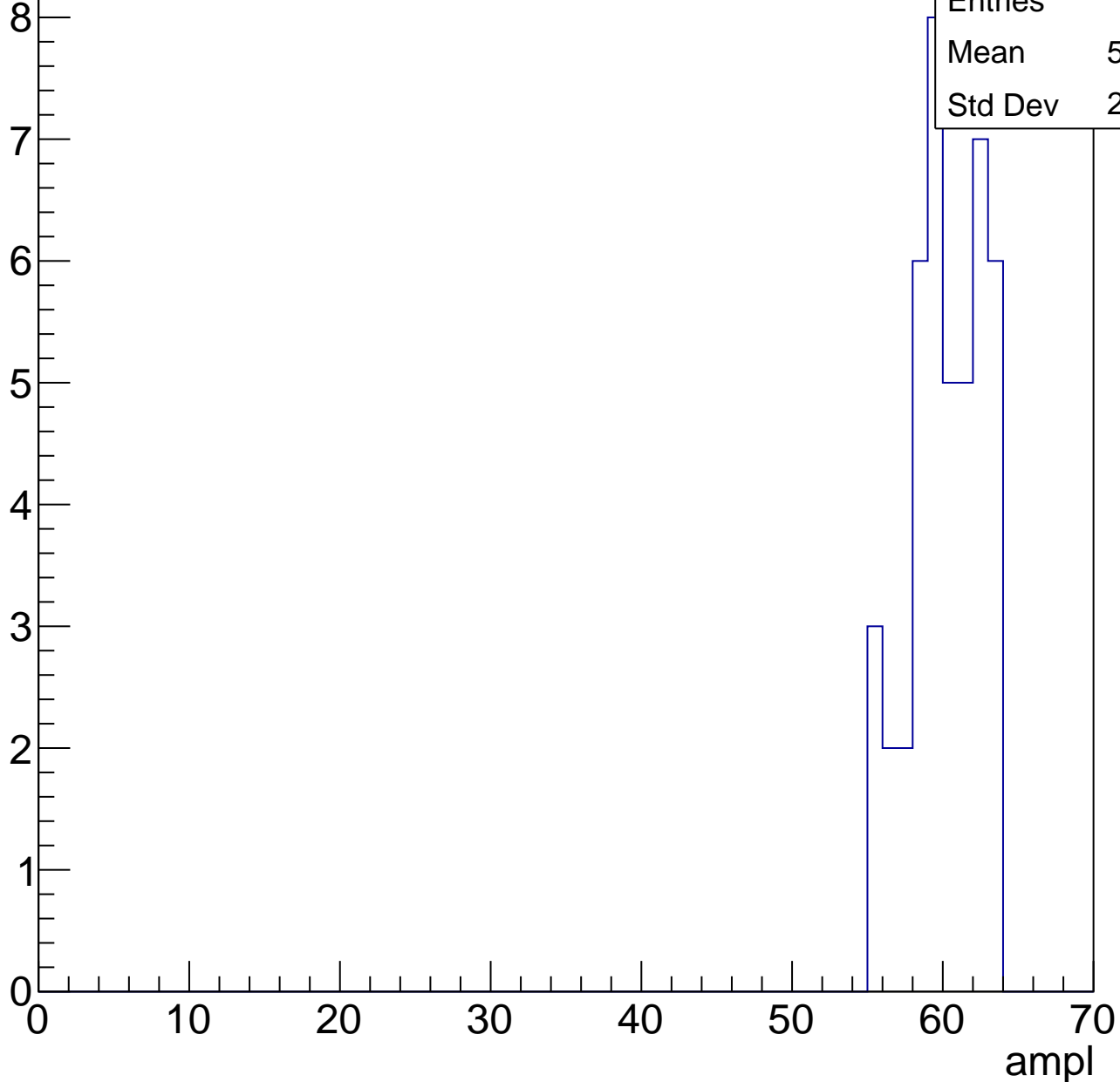
Entries	68
Mean	54.46
Std Dev	7.523



B0L001S, U15-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	9
Std Dev	9

B0L001S, U15-ch42, adc0

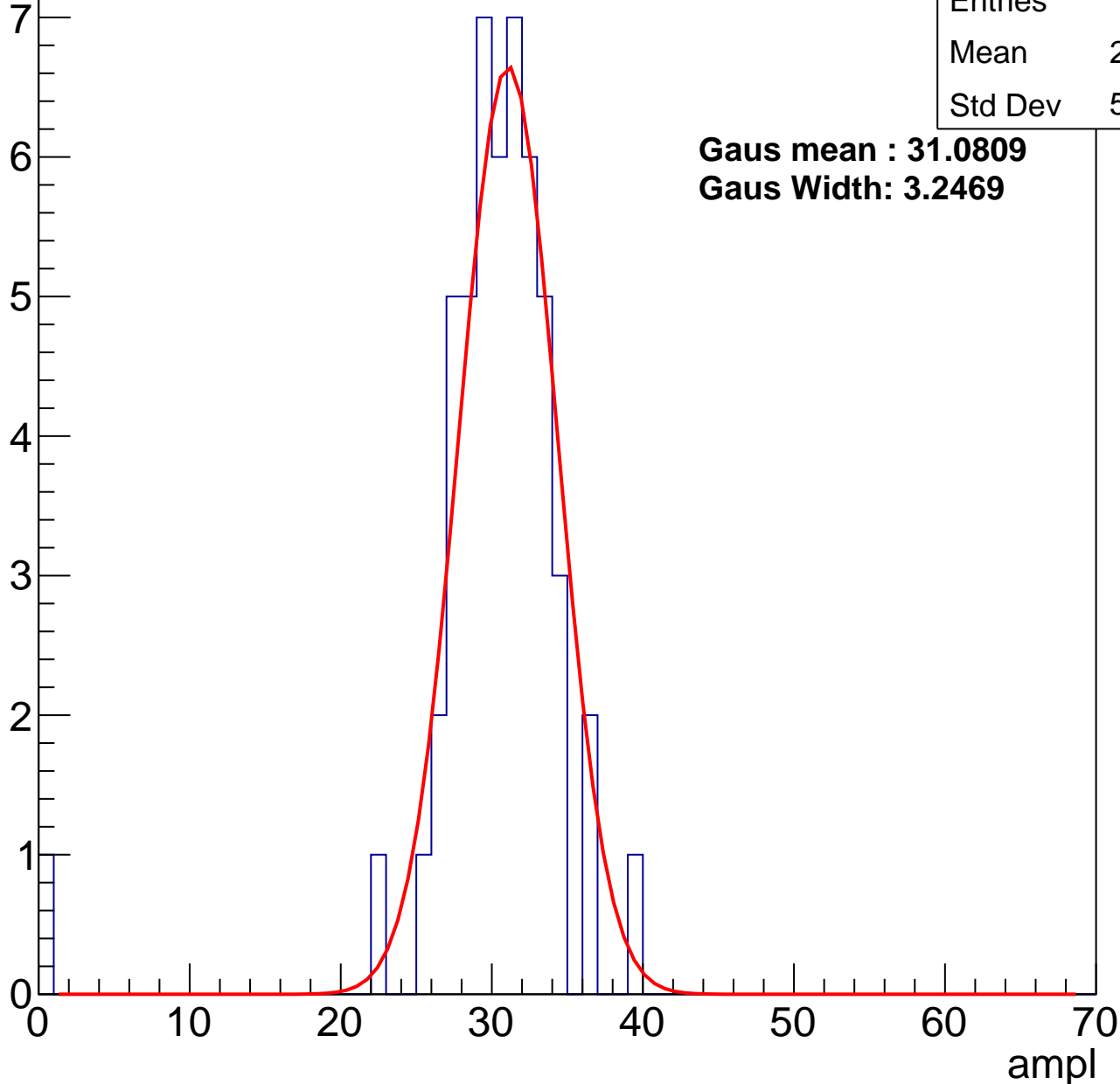
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.69
Std Dev	5.124

Gaus mean : 31.0809

Gaus Width: 3.2469

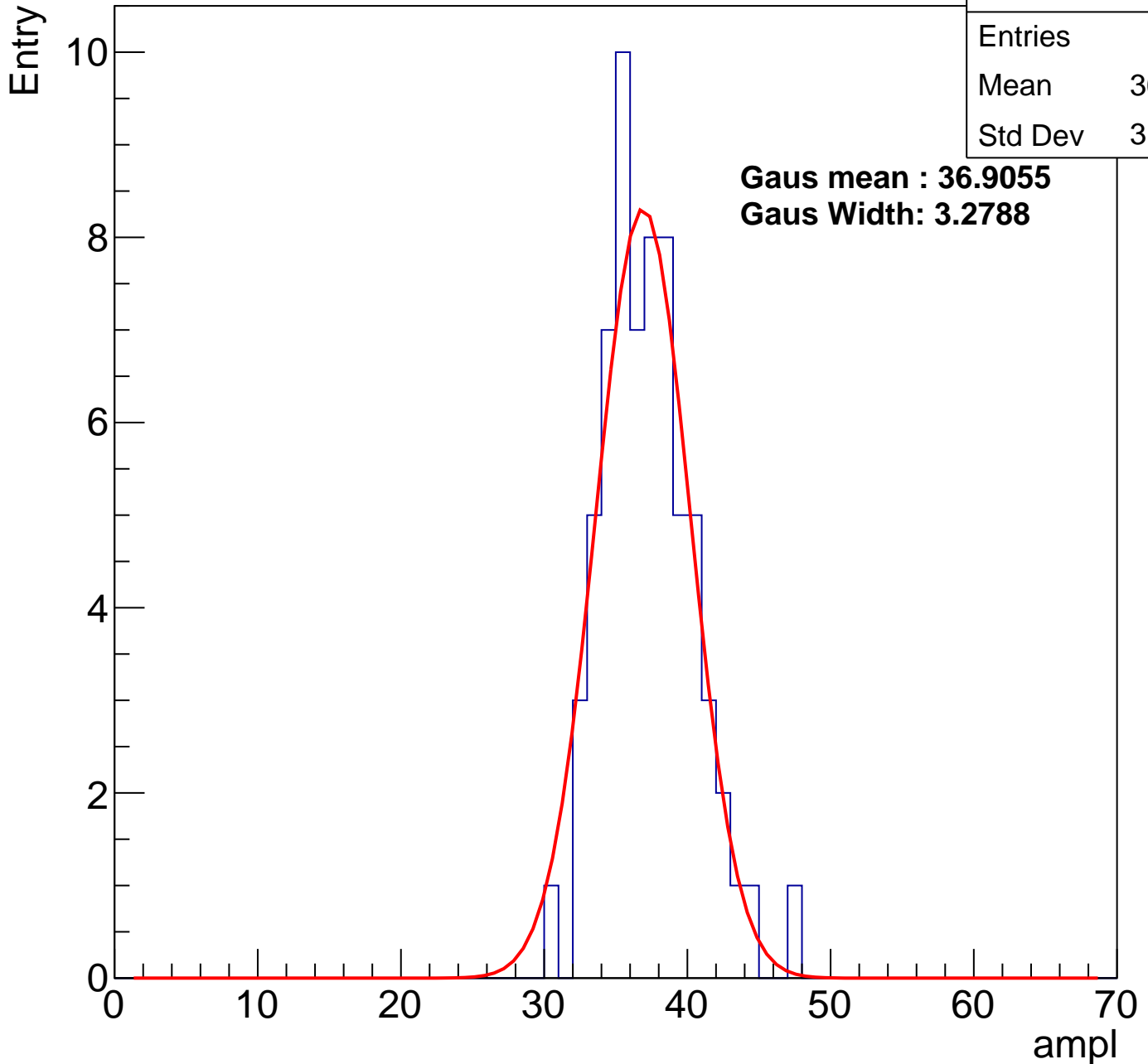


B0L001S, U15-ch42, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	36.82
Std Dev	3.157

Gaus mean : 36.9055
Gaus Width: 3.2788



B0L001S, U15-ch42, adc2

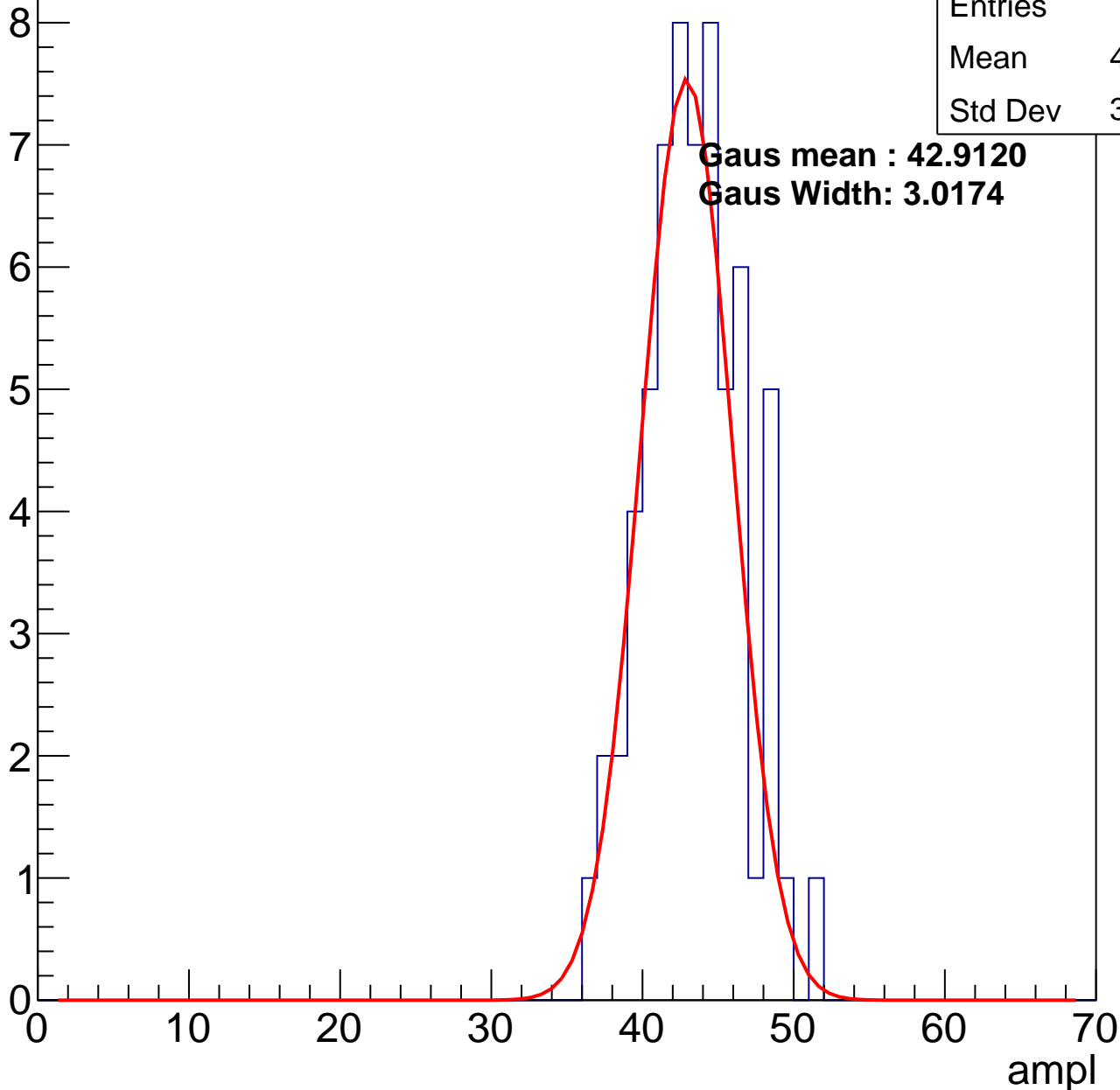
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.95
Std Dev	3.184

Gaus mean : 42.9120

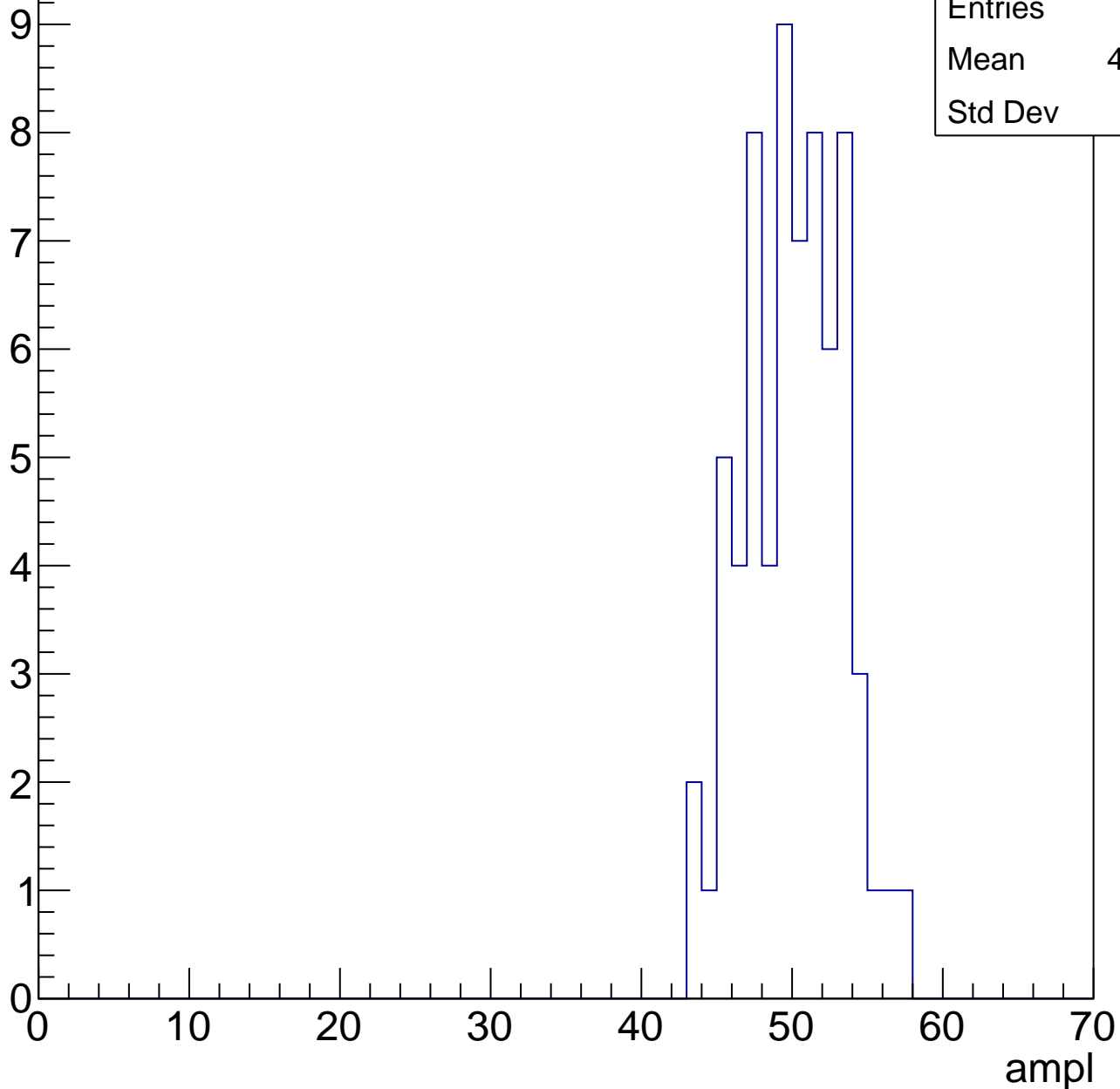
Gaus Width: 3.0174



B0L001S, U15-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



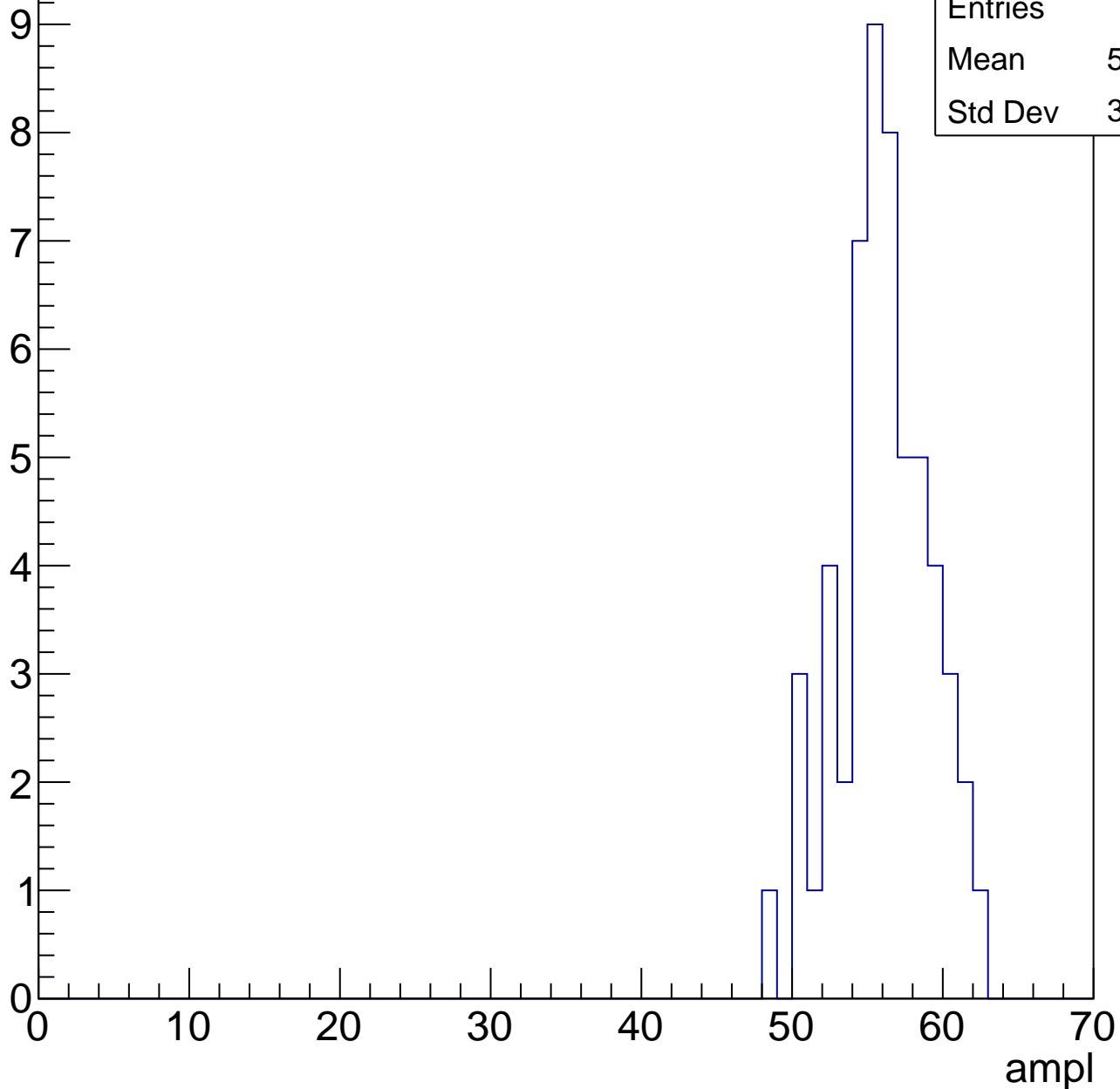
Entries	68
Mean	49.59
Std Dev	3.14

B0L001S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	55.62
Std Dev	3.024

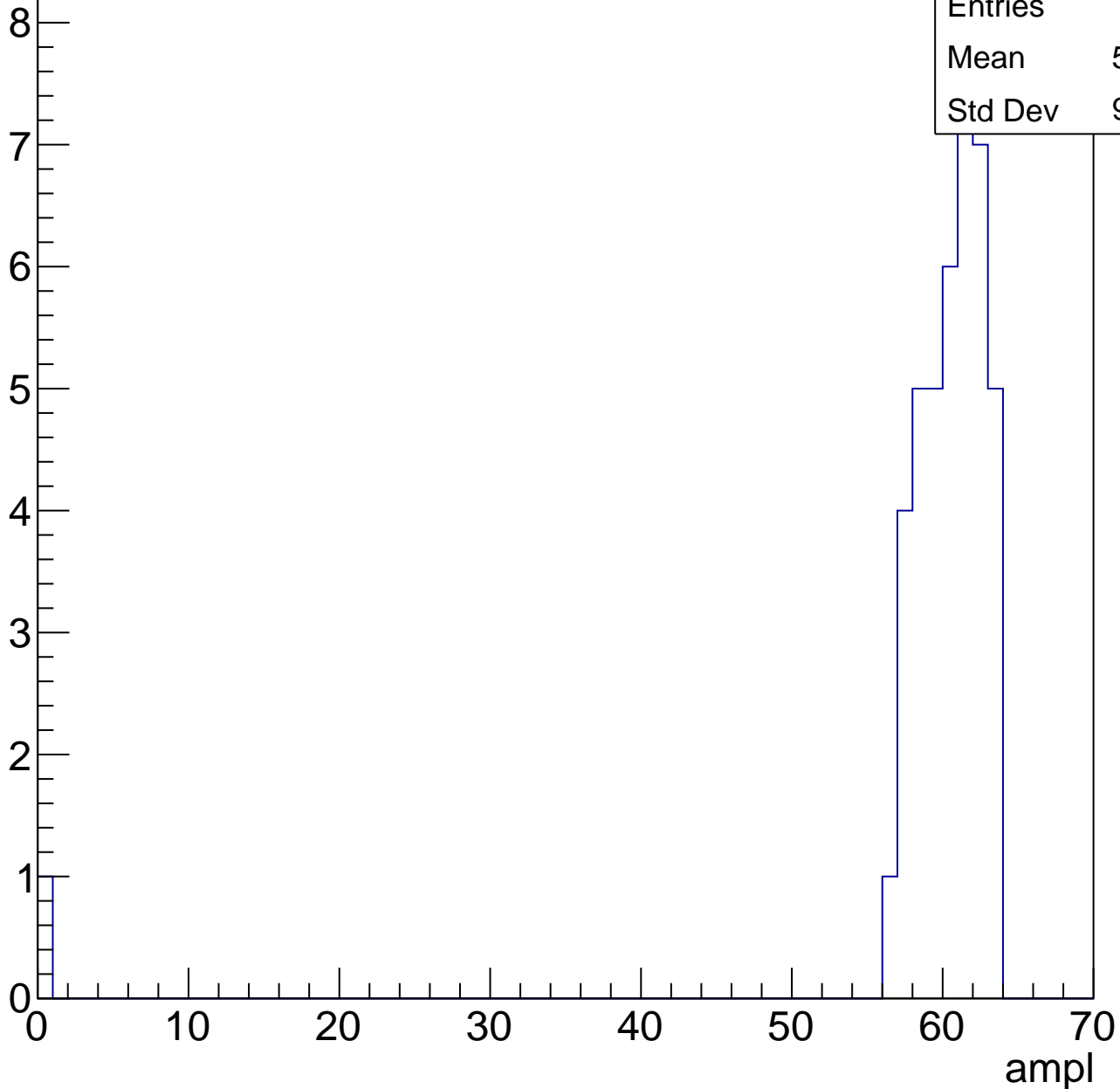


B0L001S, U15-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

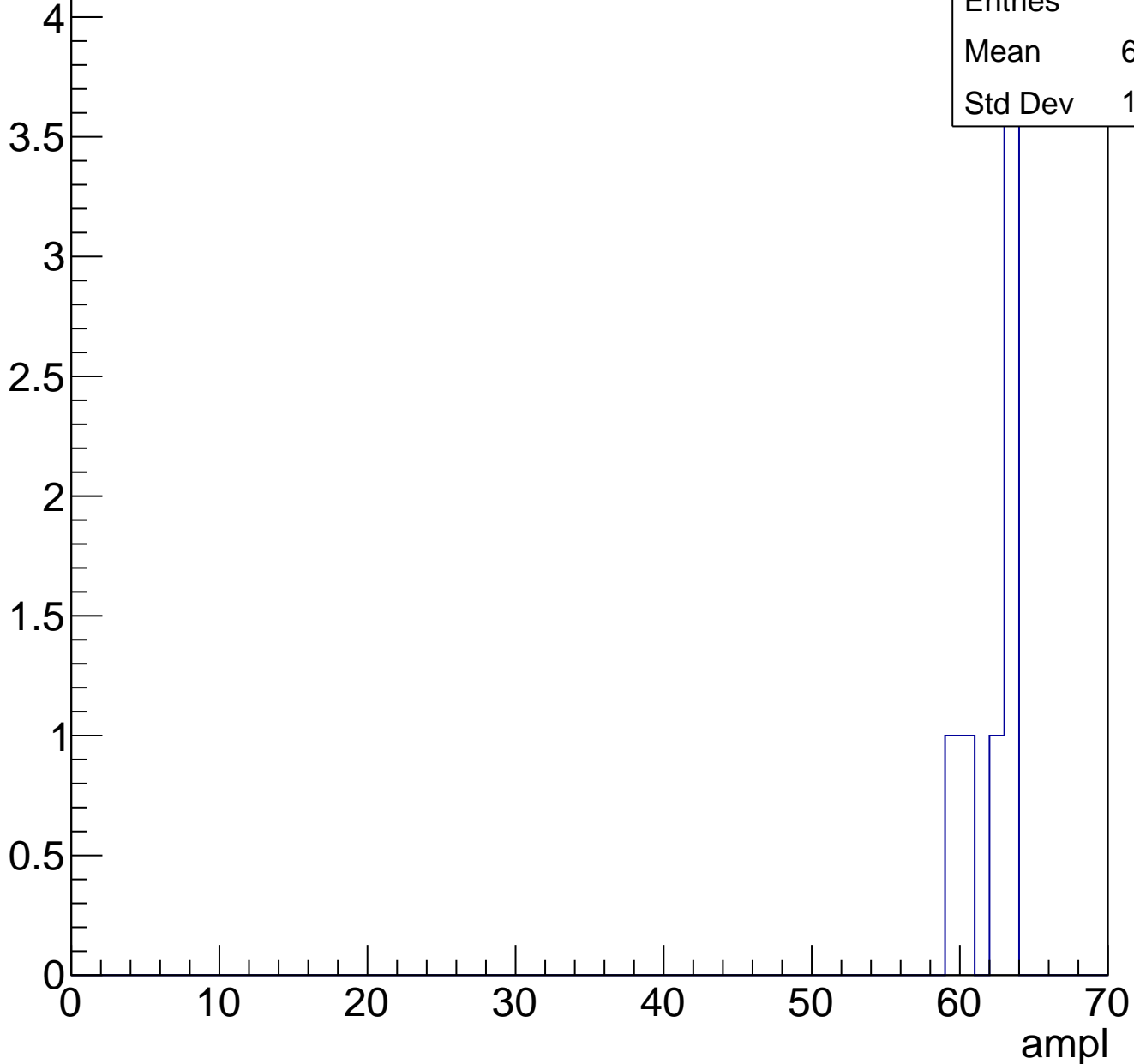
Entries	42
Mean	58.71
Std Dev	9.371



B0L001S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch43, adc0

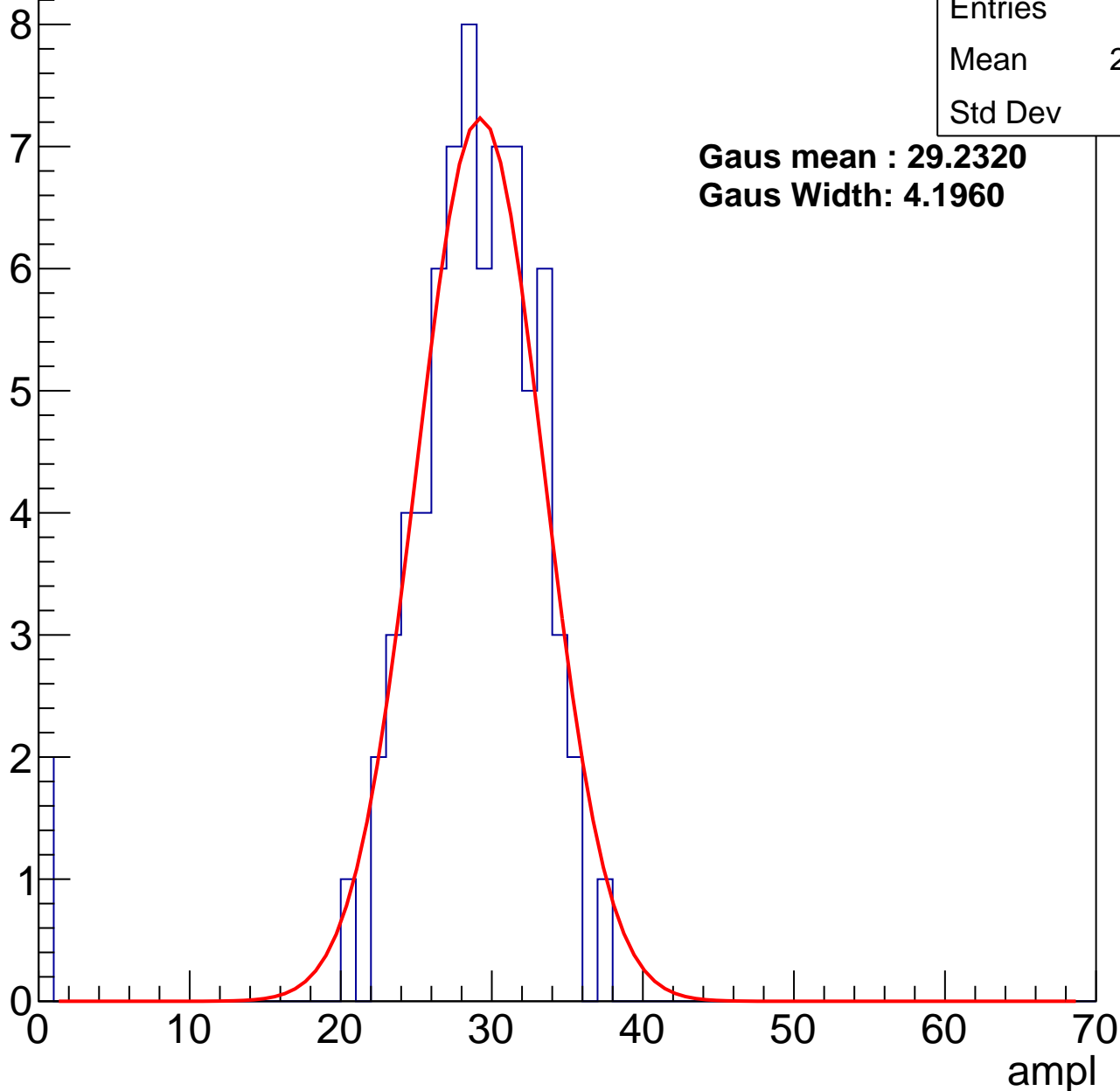
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	27.92
Std Dev	5.84

Gaus mean : 29.2320

Gaus Width: 4.1960



B0L001S, U15-ch43, adc1

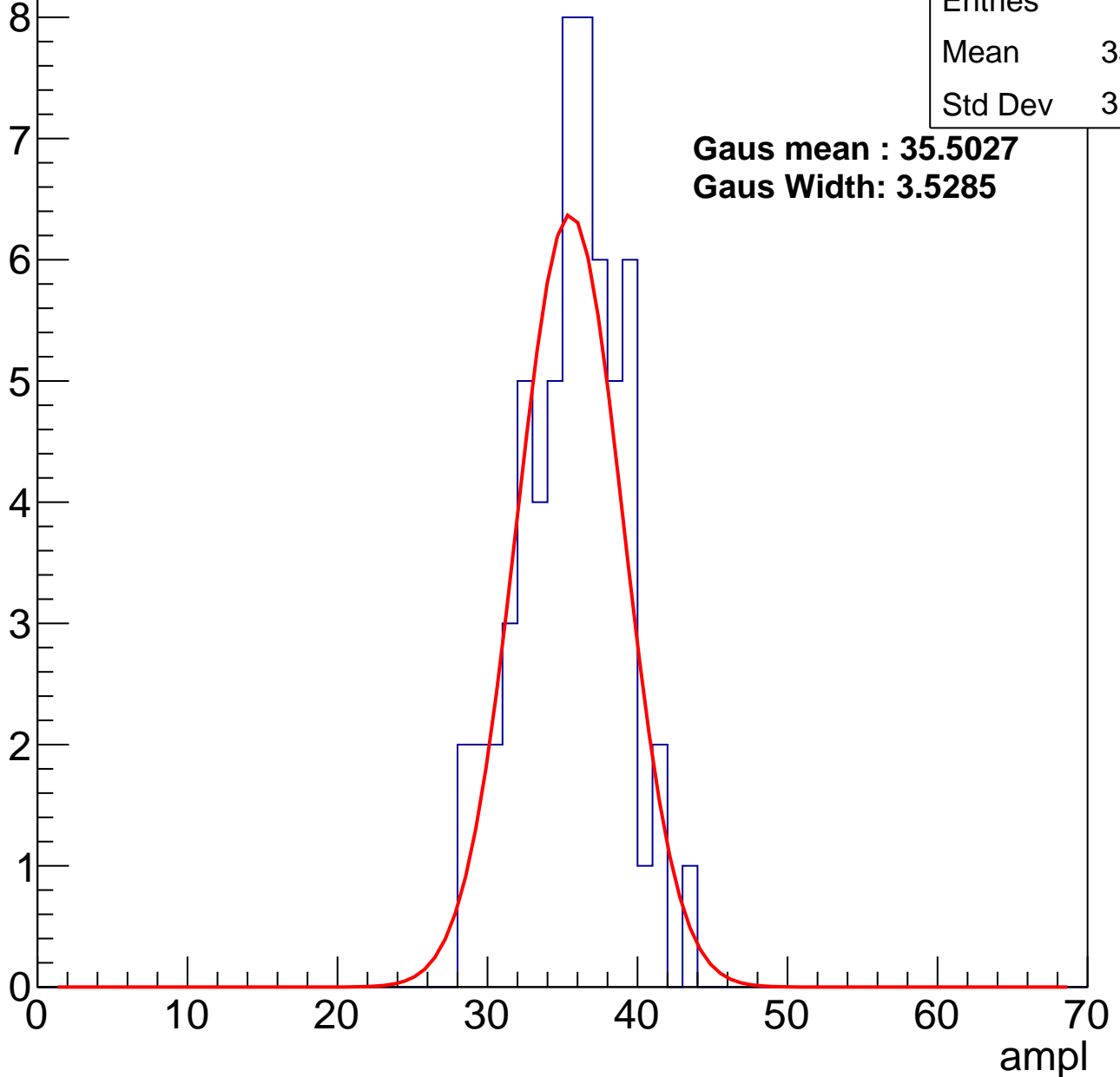
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	35.13
Std Dev	3.334

Gaus mean : 35.5027

Gaus Width: 3.5285



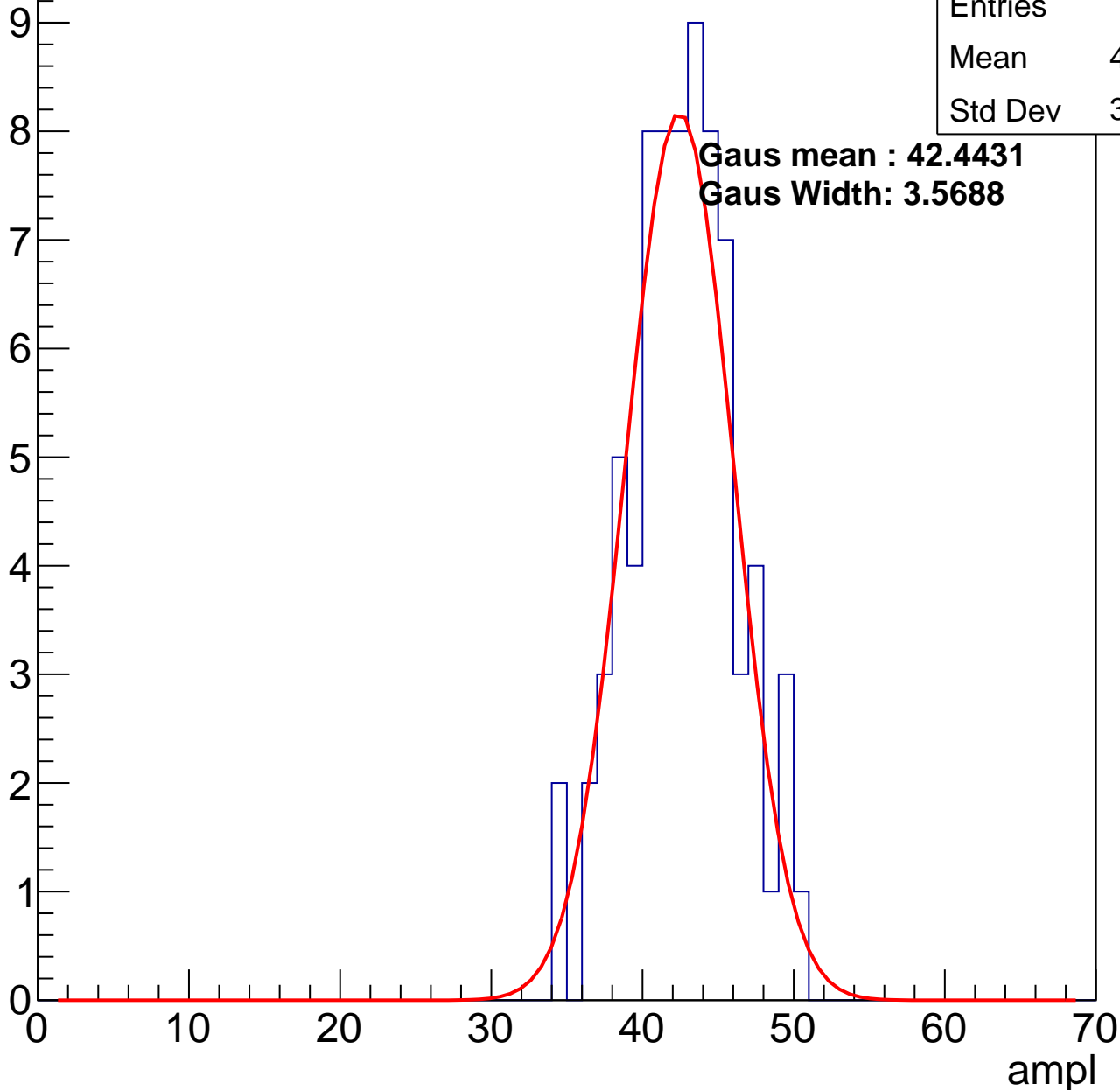
B0L001S, U15-ch43, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	42.18
Std Dev	3.493

Gaus mean : 42.4431
Gaus Width: 3.5688

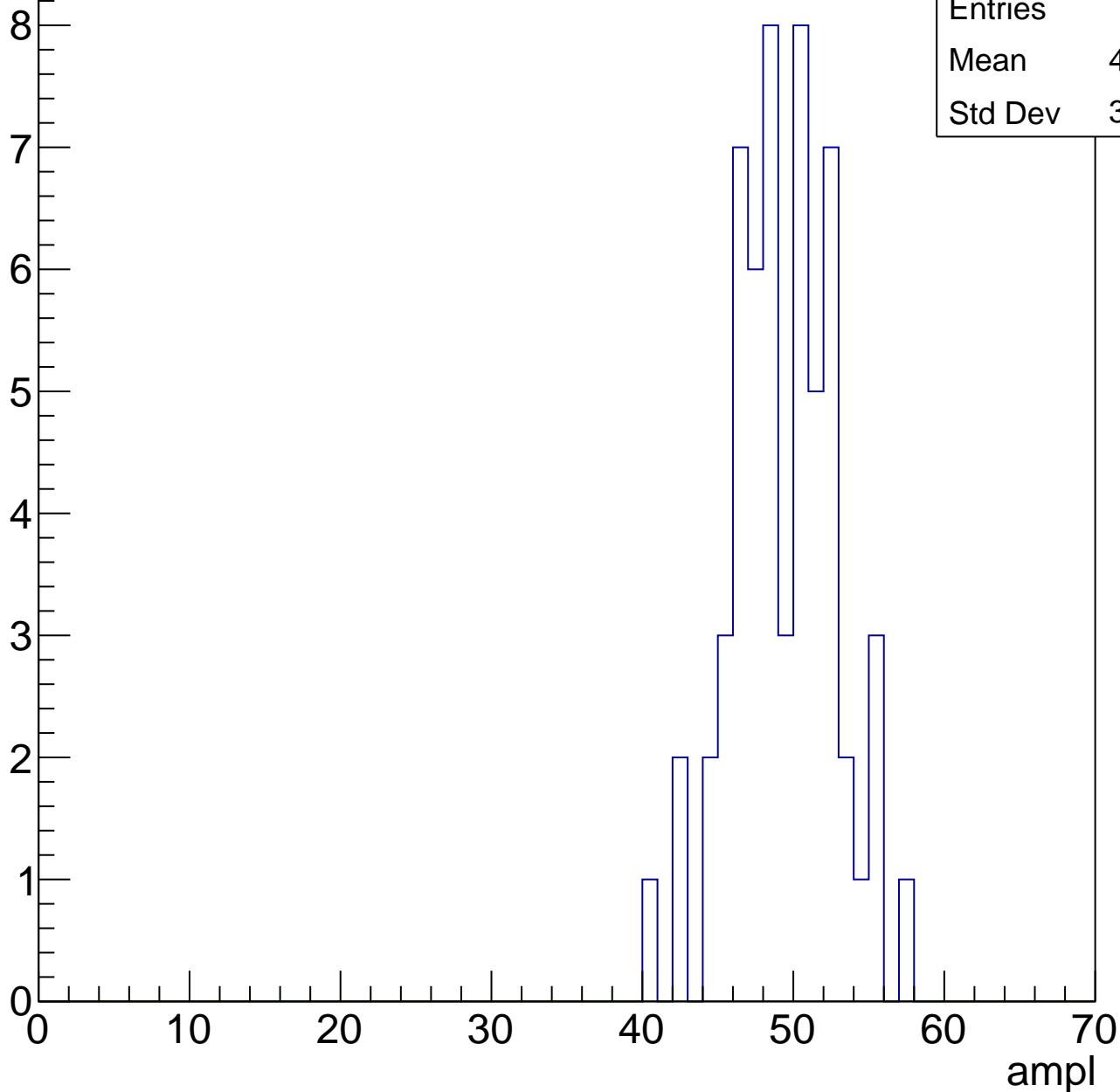


B0L001S, U15-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

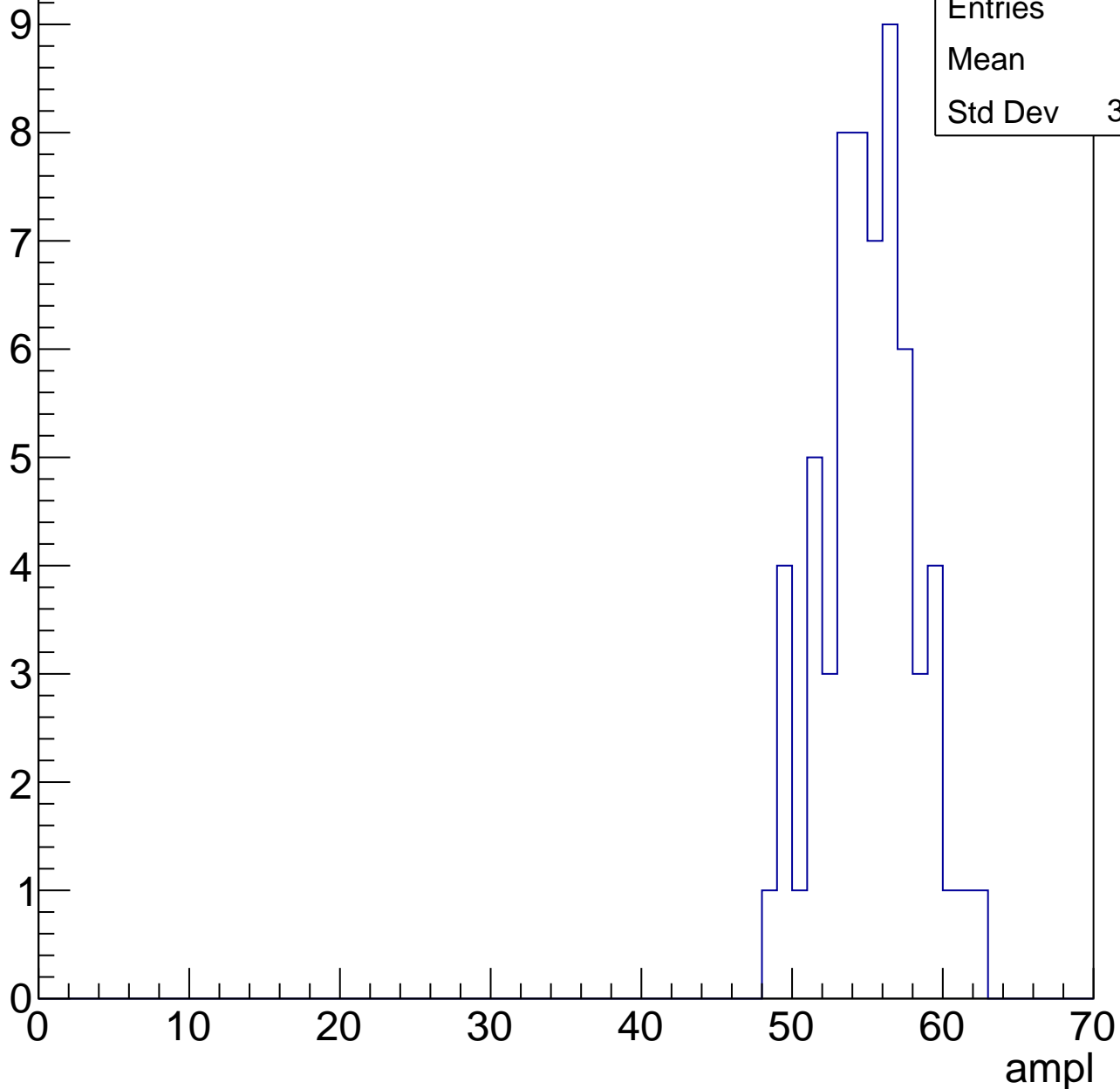
Entries	59
Mean	48.86
Std Dev	3.422



B0L001S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

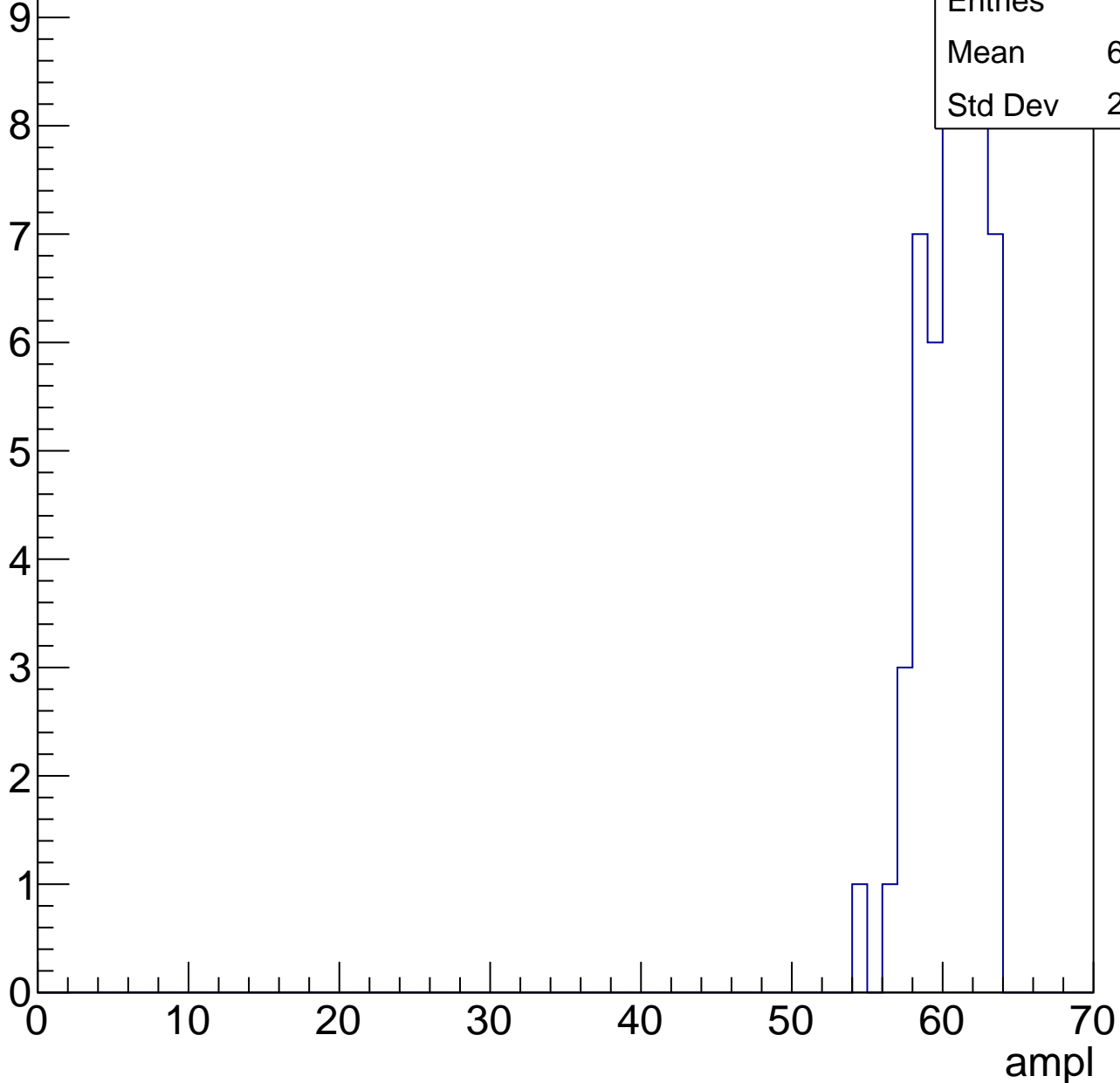


B0L001S, U15-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	60.12
Std Dev	2.075



B0L001S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	6
Mean	52
Std Dev	23.27

B0L001S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch44, adc0

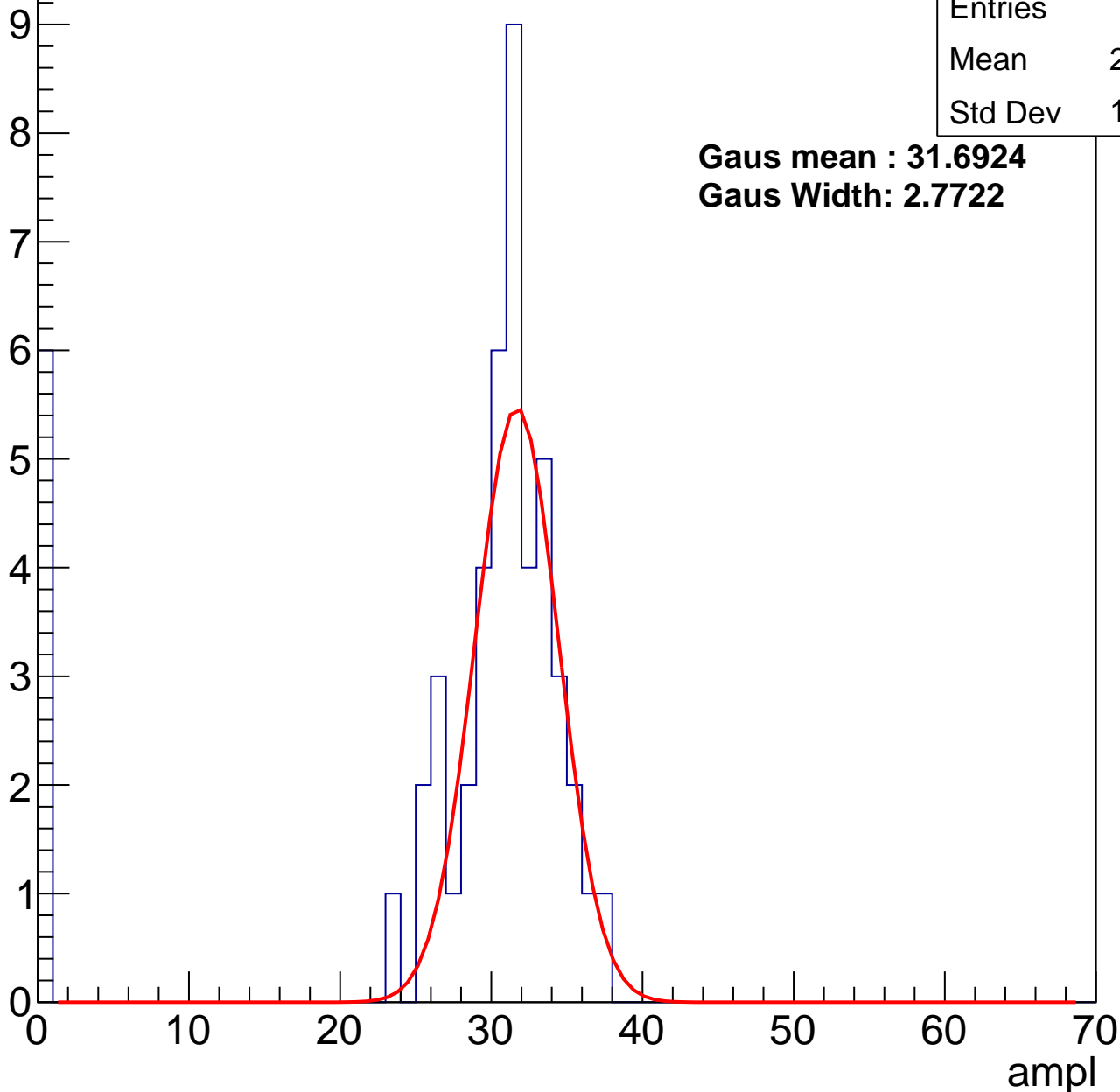
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	26.94
Std Dev	10.35

Gaus mean : 31.6924

Gaus Width: 2.7722



B0L001S, U15-ch44, adc1

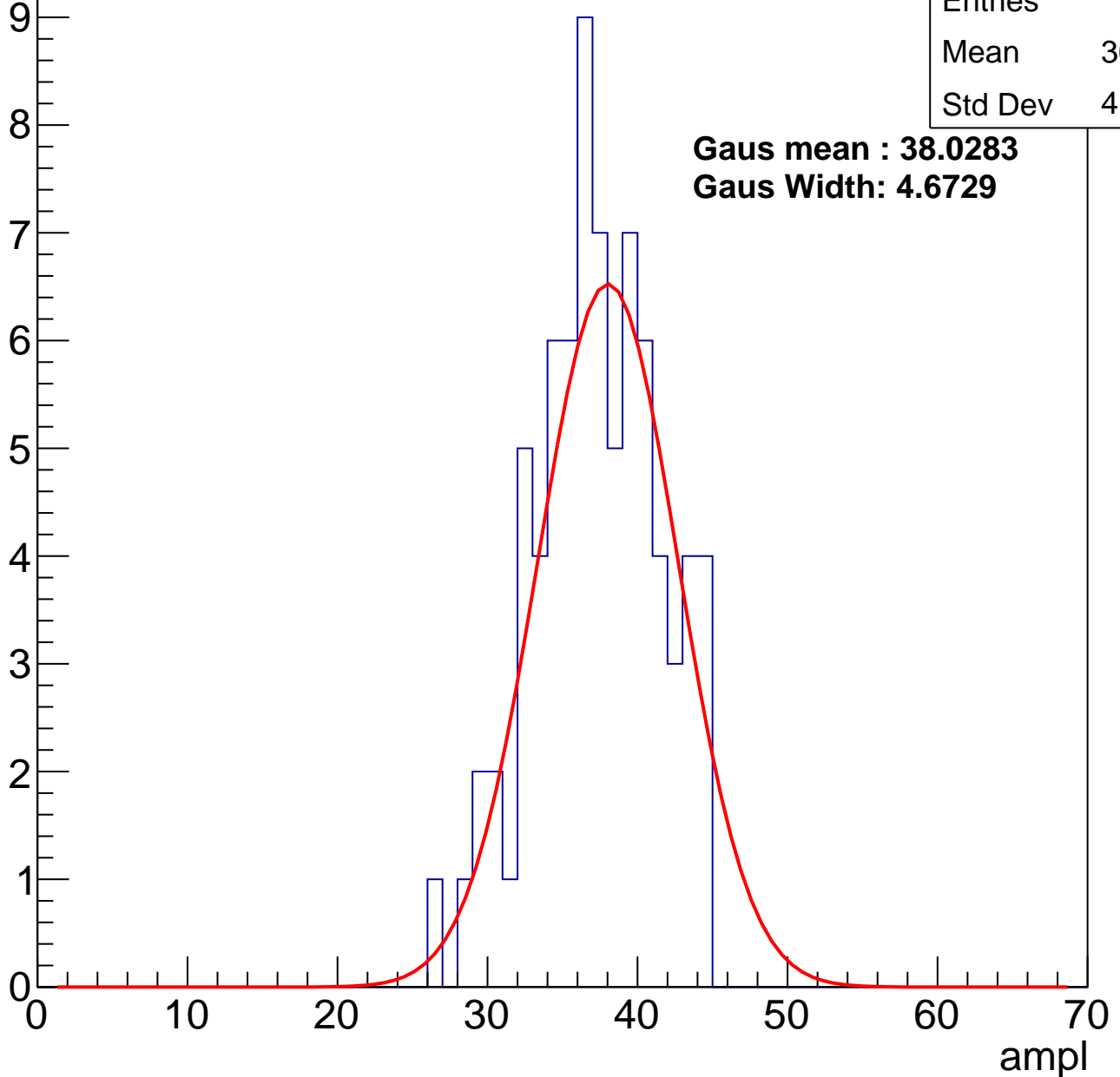
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.79
Std Dev	4.113

Gaus mean : 38.0283

Gaus Width: 4.6729



B0L001S, U15-ch44, adc2

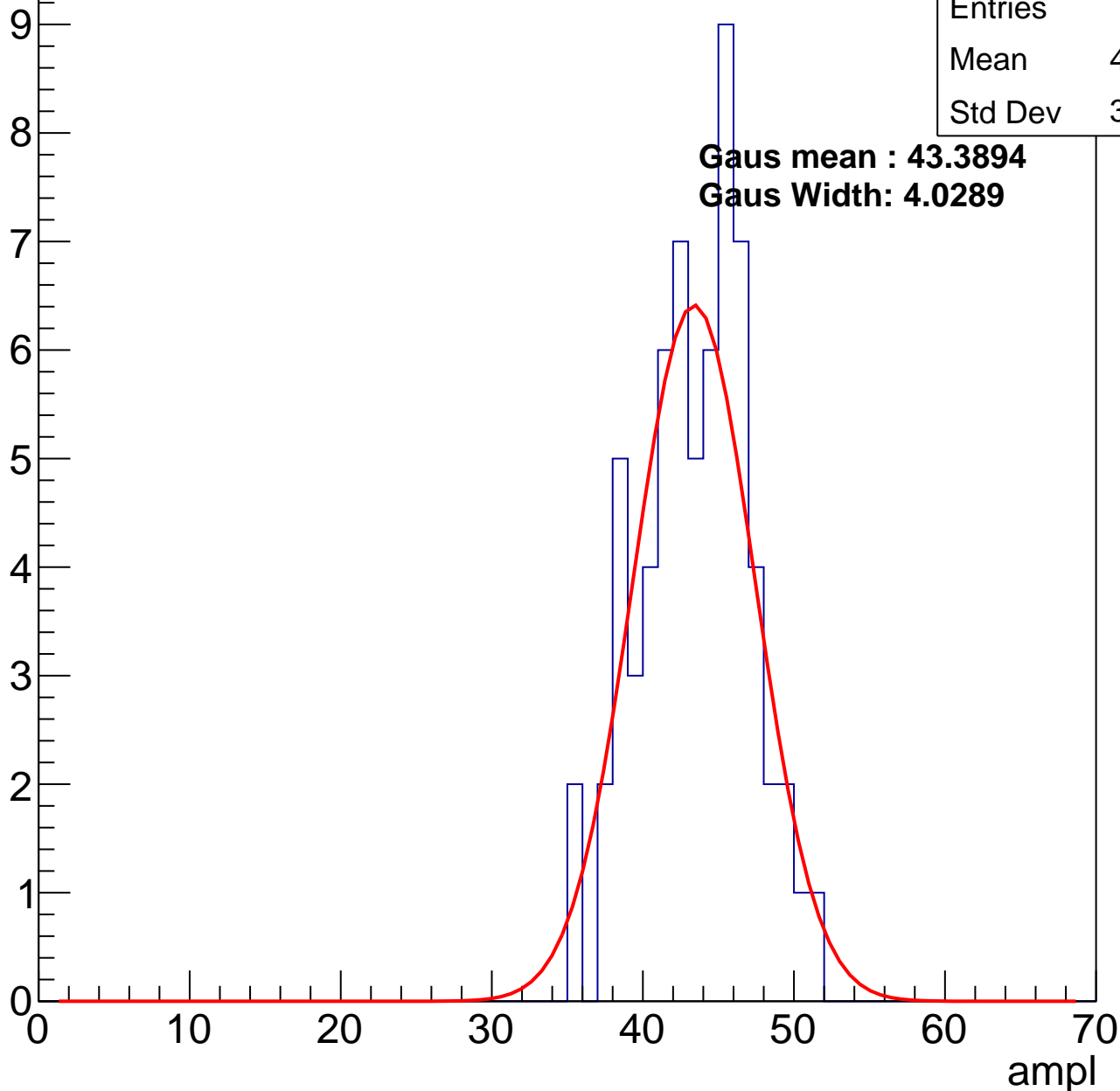
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.03
Std Dev	3.576

Gaus mean : 43.3894

Gaus Width: 4.0289

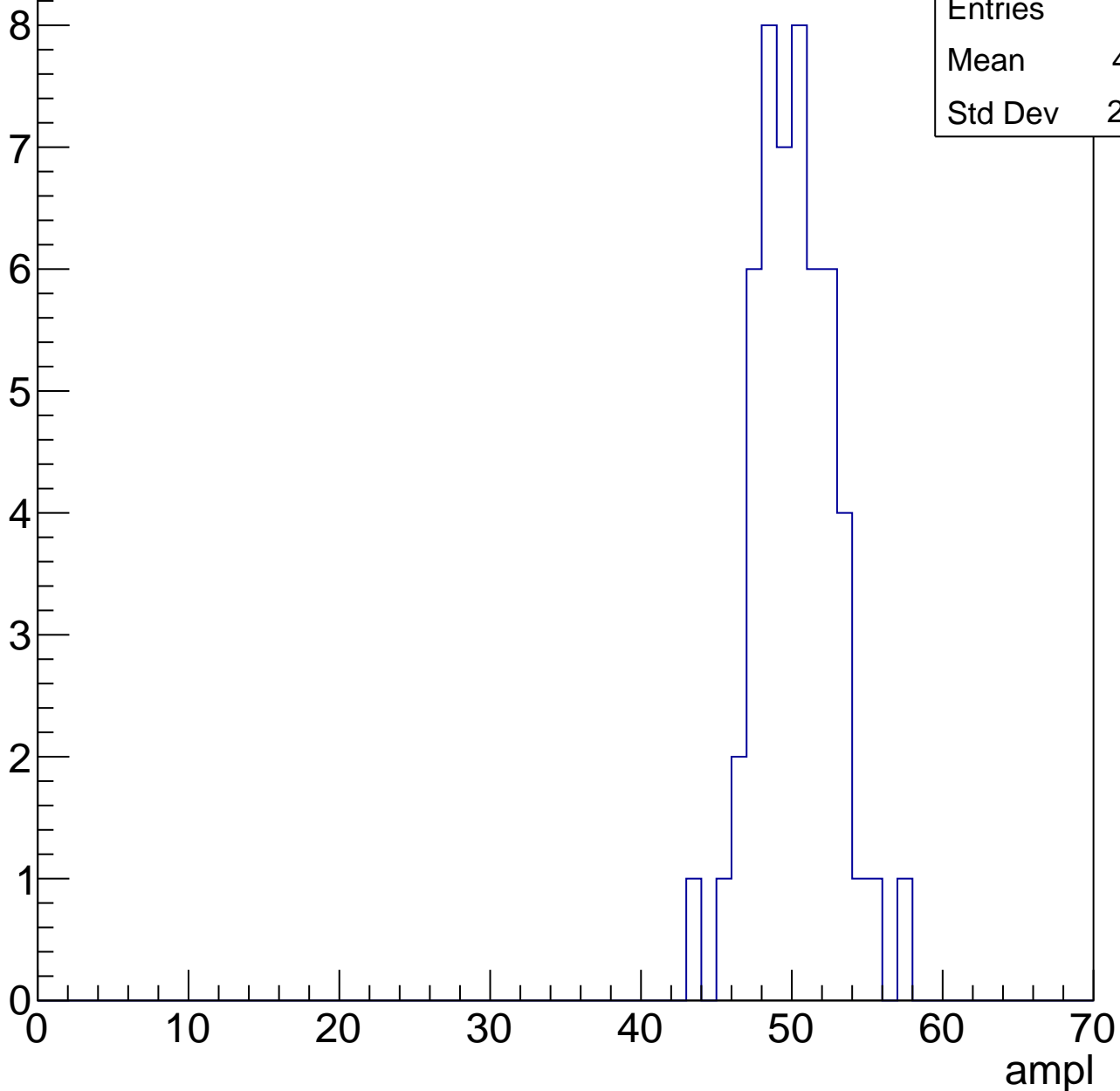


B0L001S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	49.71
Std Dev	2.597

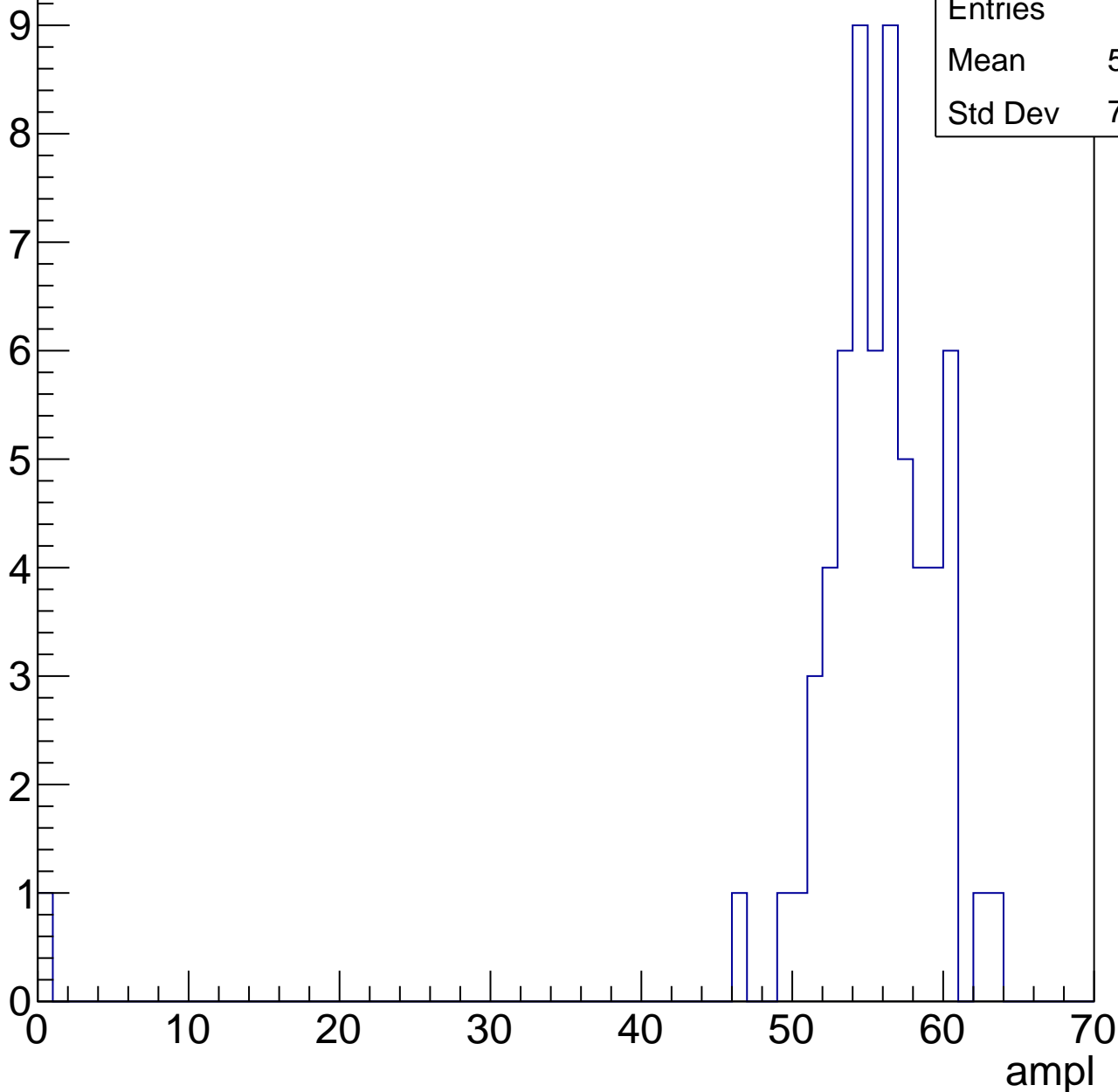


B0L001S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	54.55
Std Dev	7.687

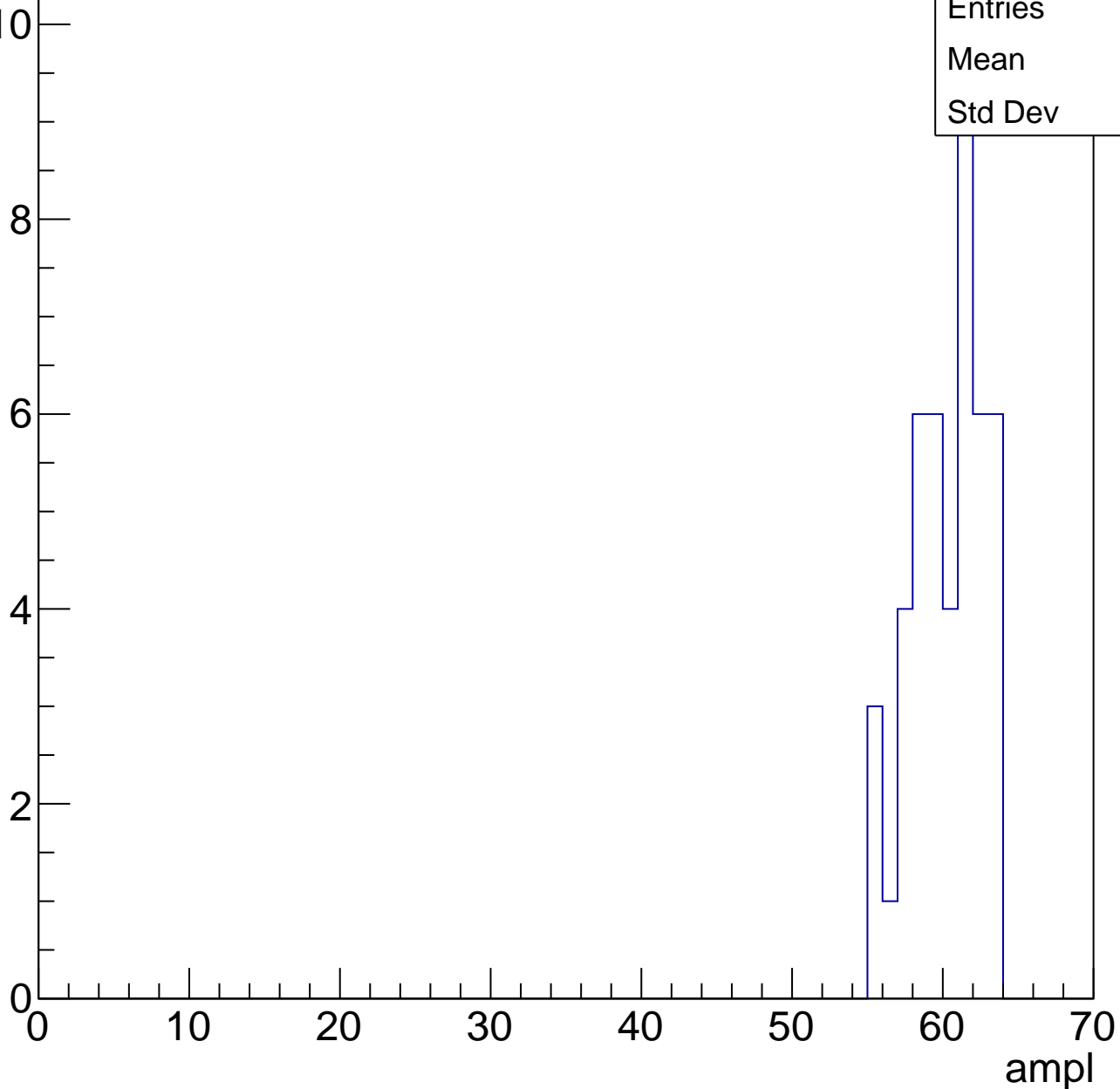


B0L001S, U15-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

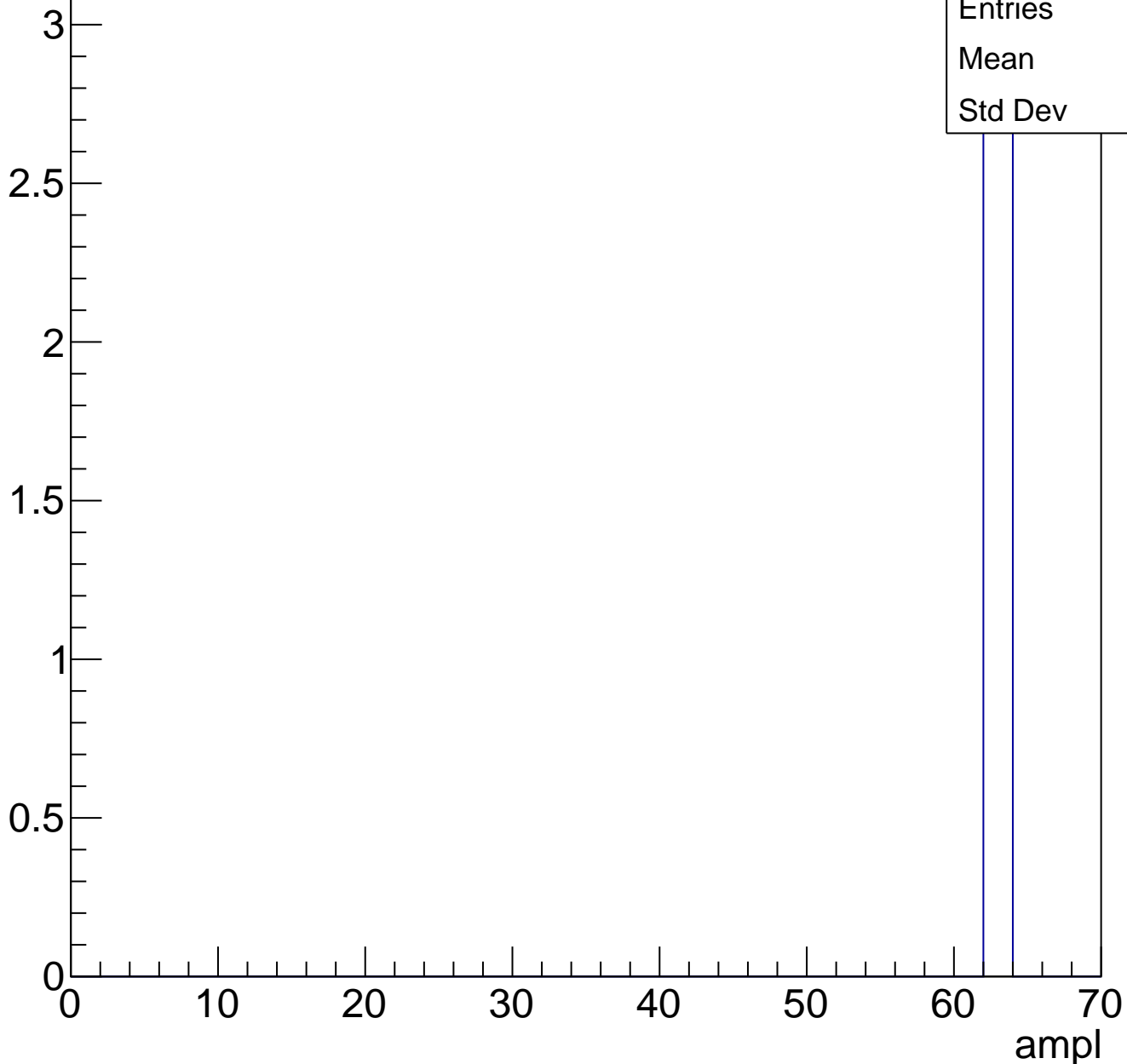
Entries	46
Mean	59.8
Std Dev	2.3



B0L001S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch45, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	30.74
Std Dev	6.186

Gaus mean : 32.0032

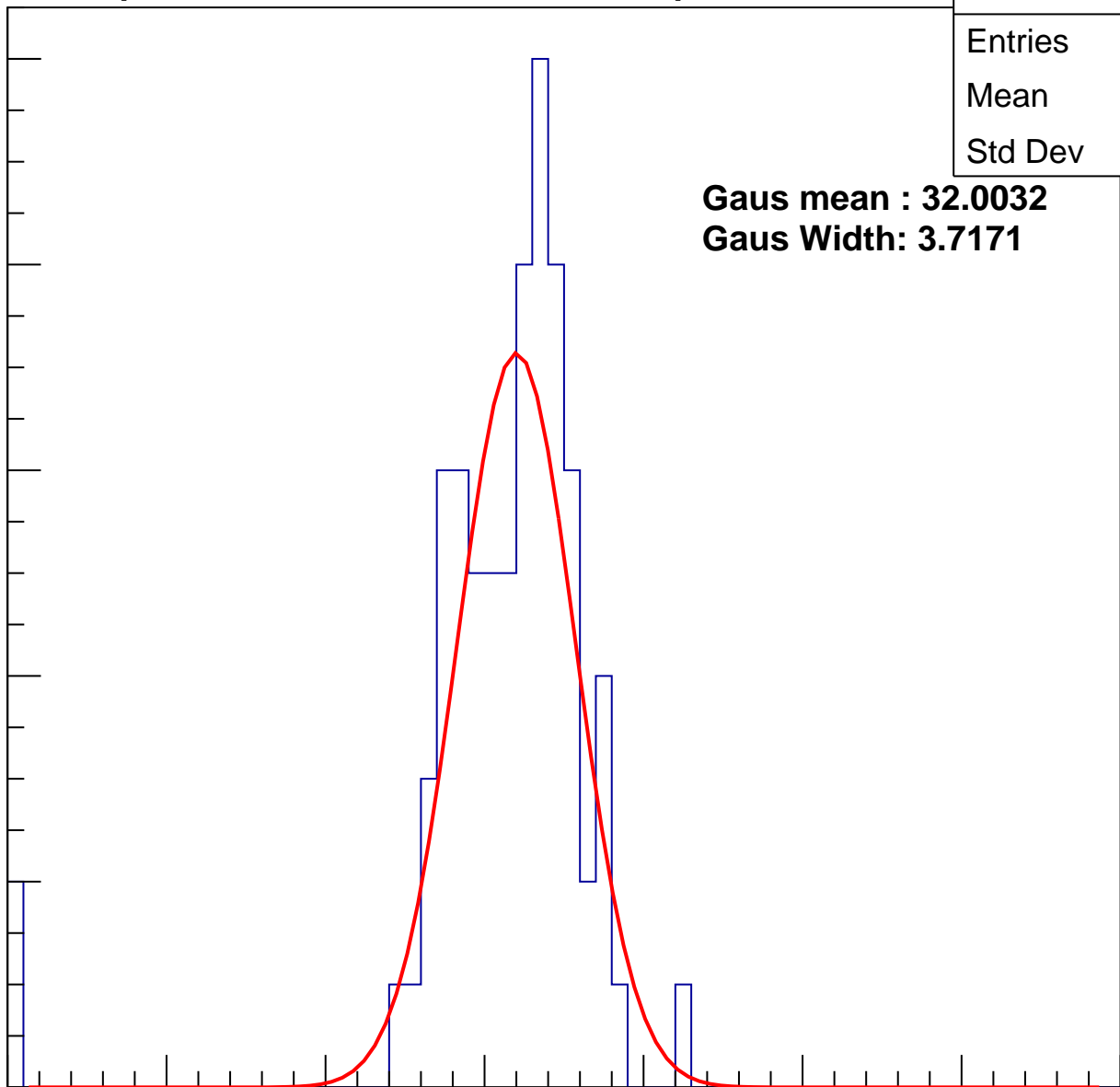
Gaus Width: 3.7171

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch45, adc1

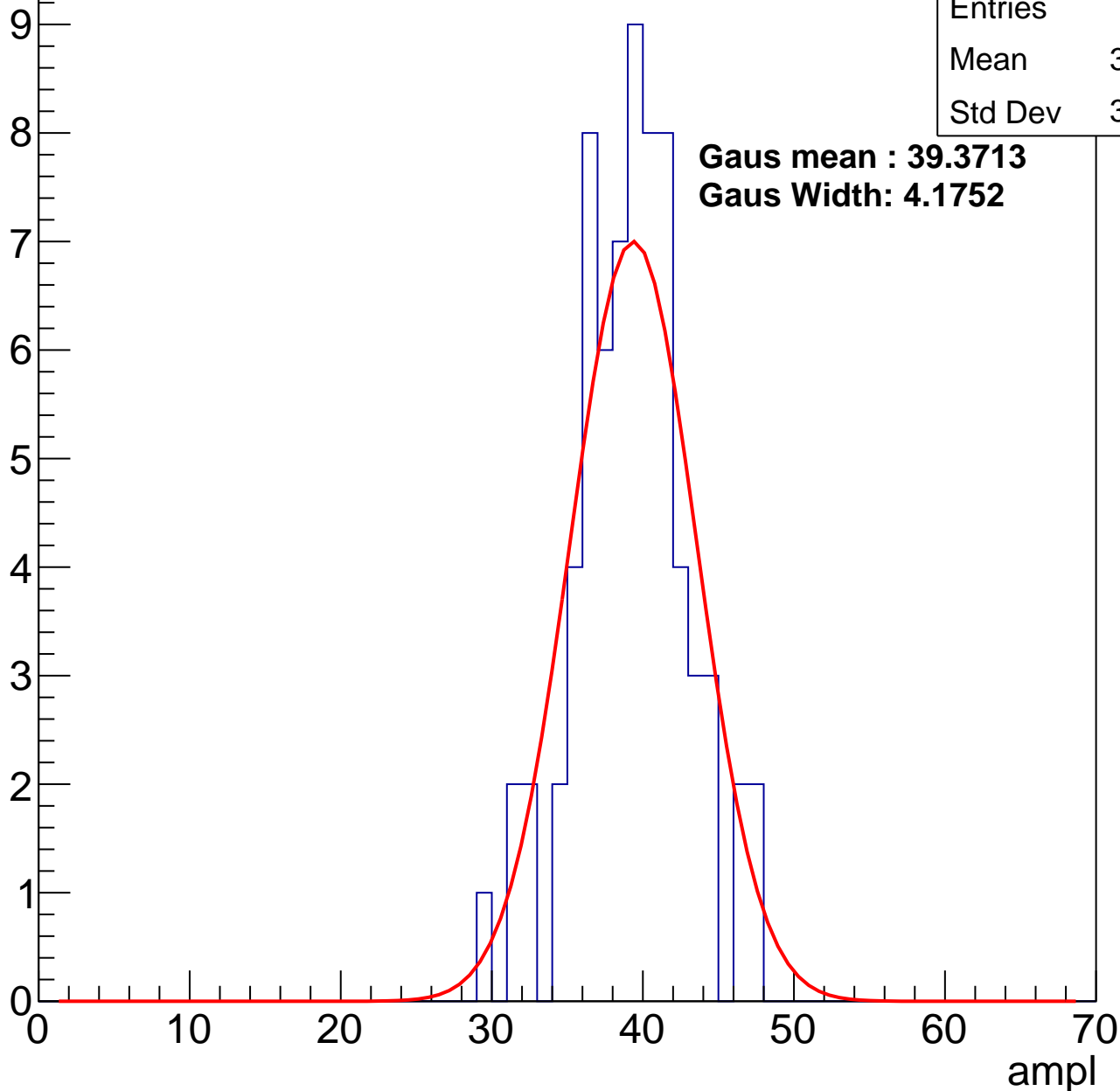
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.77
Std Dev	3.686

Gaus mean : 39.3713

Gaus Width: 4.1752



B0L001S, U15-ch45, adc2

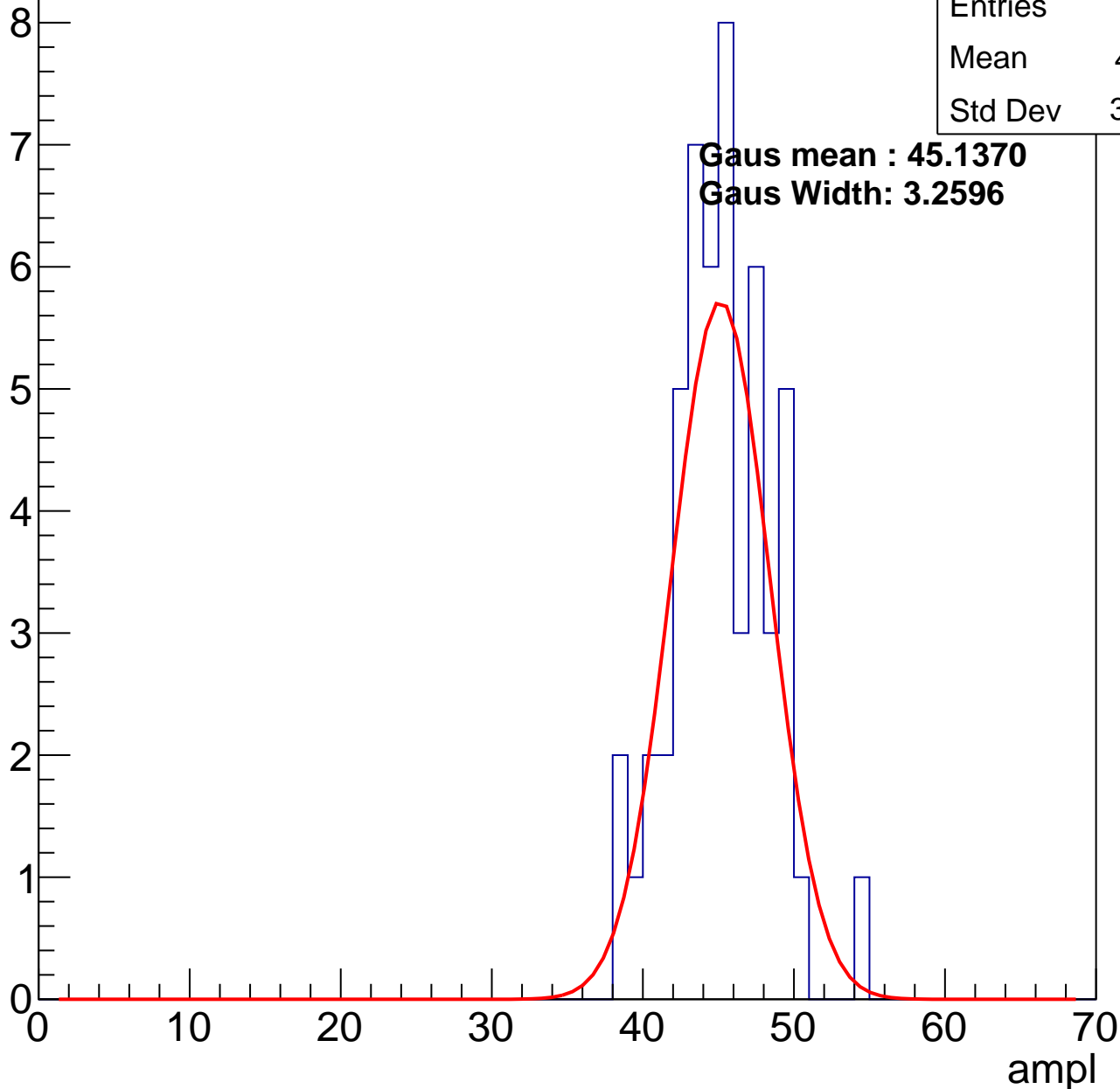
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.71
Std Dev	3.195

Gaus mean : 45.1370

Gaus Width: 3.2596

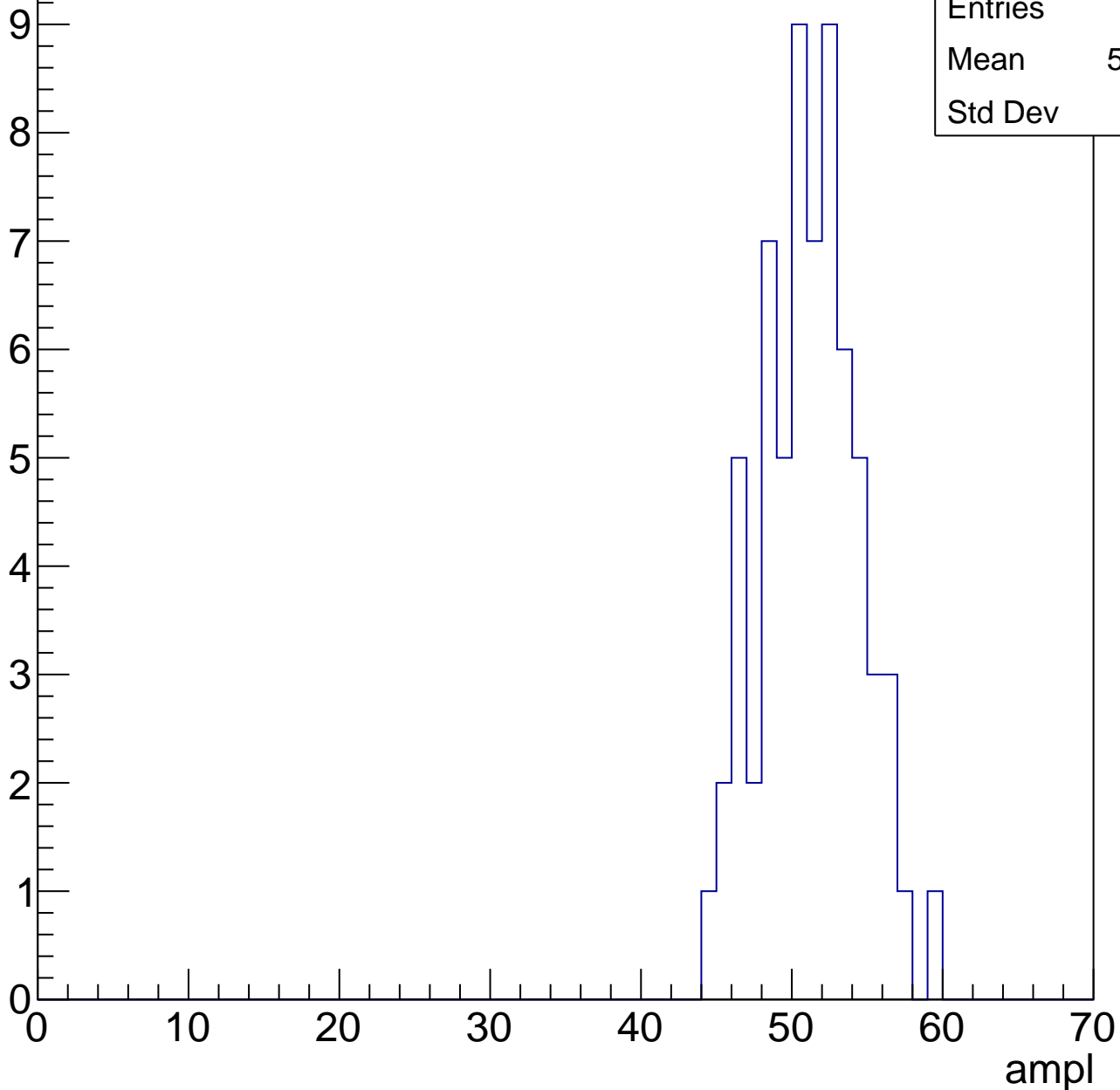


B0L001S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

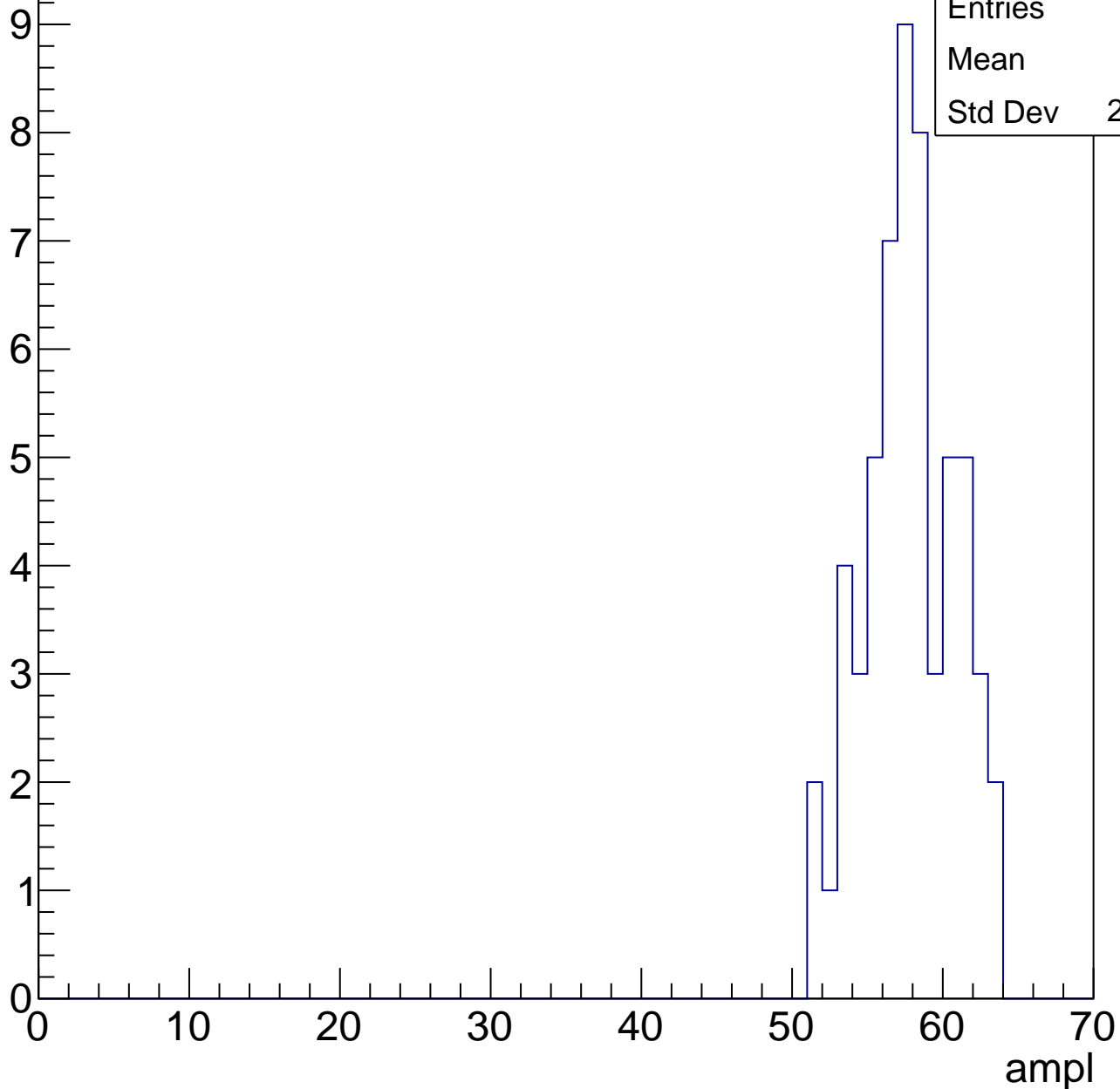
Entries	66
Mean	50.77
Std Dev	3.18



B0L001S, U15-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



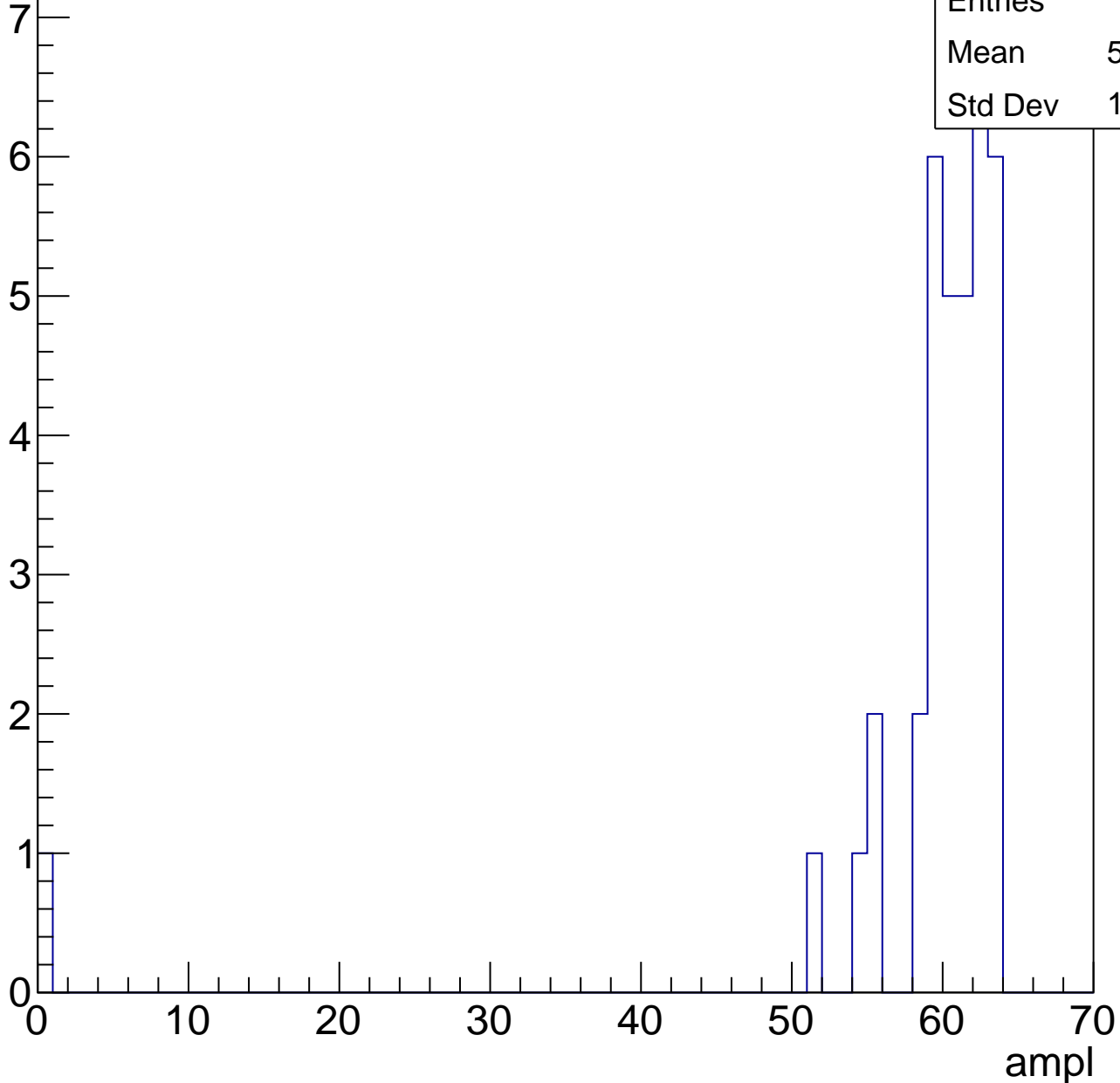
Entries	57
Mean	57.3
Std Dev	2.967

B0L001S, U15-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.39
Std Dev	10.24



B0L001S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch46, adc0

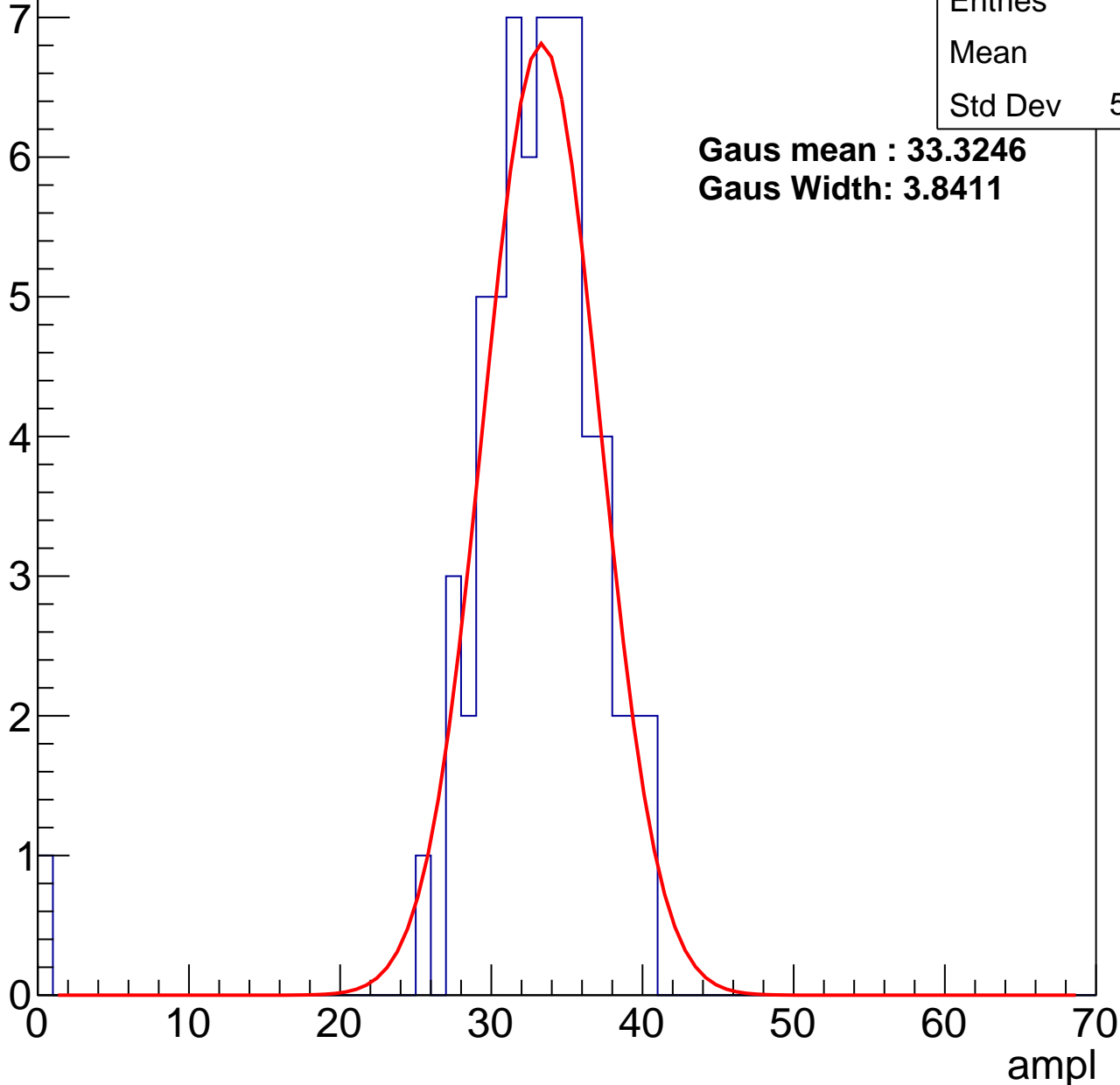
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.4
Std Dev	5.265

Gaus mean : 33.3246

Gaus Width: 3.8411



B0L001S, U15-ch46, adc1

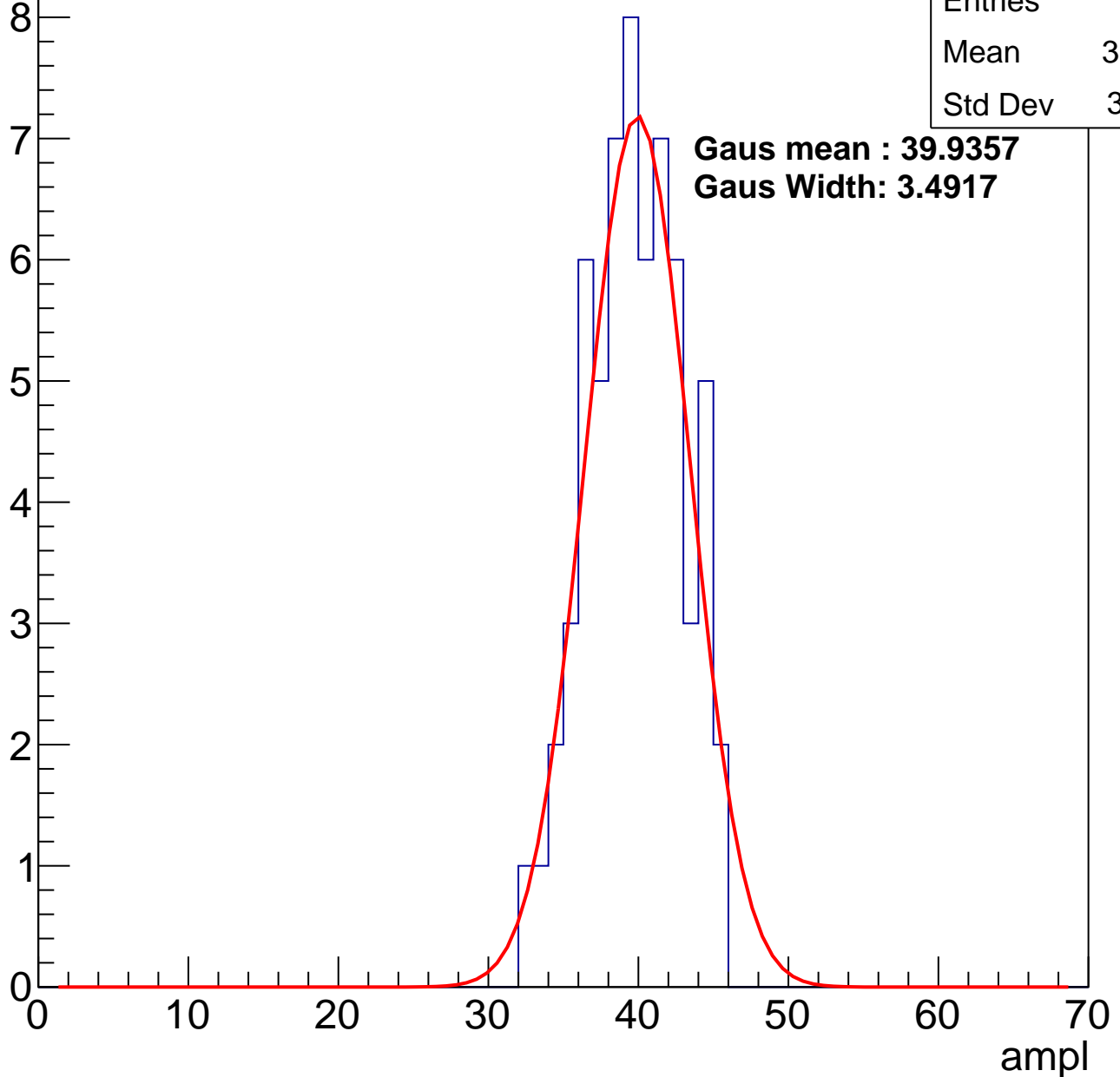
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	39.27
Std Dev	3.091

Gaus mean : 39.9357

Gaus Width: 3.4917



B0L001S, U15-ch46, adc2

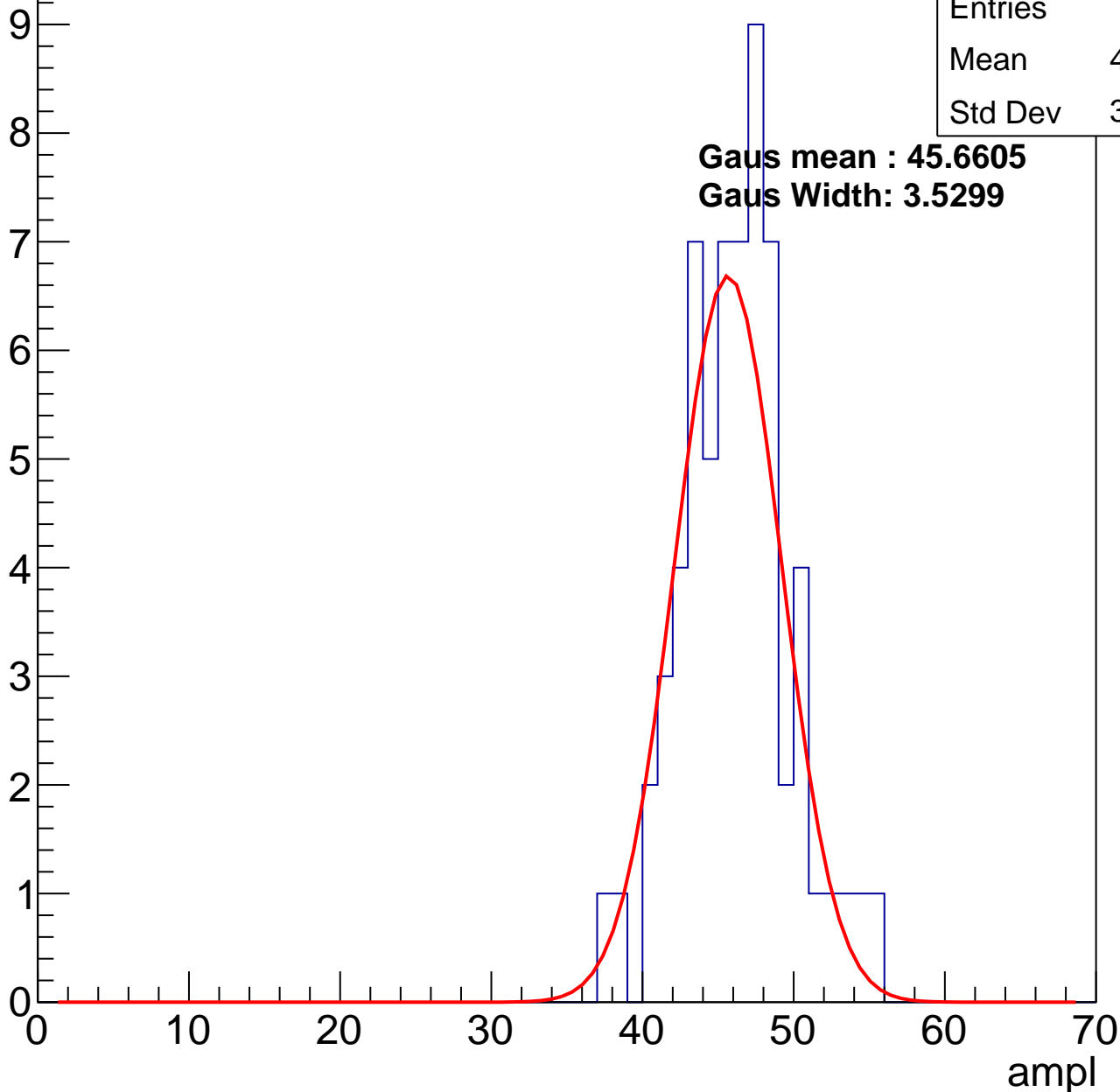
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.72
Std Dev	3.568

Gaus mean : 45.6605

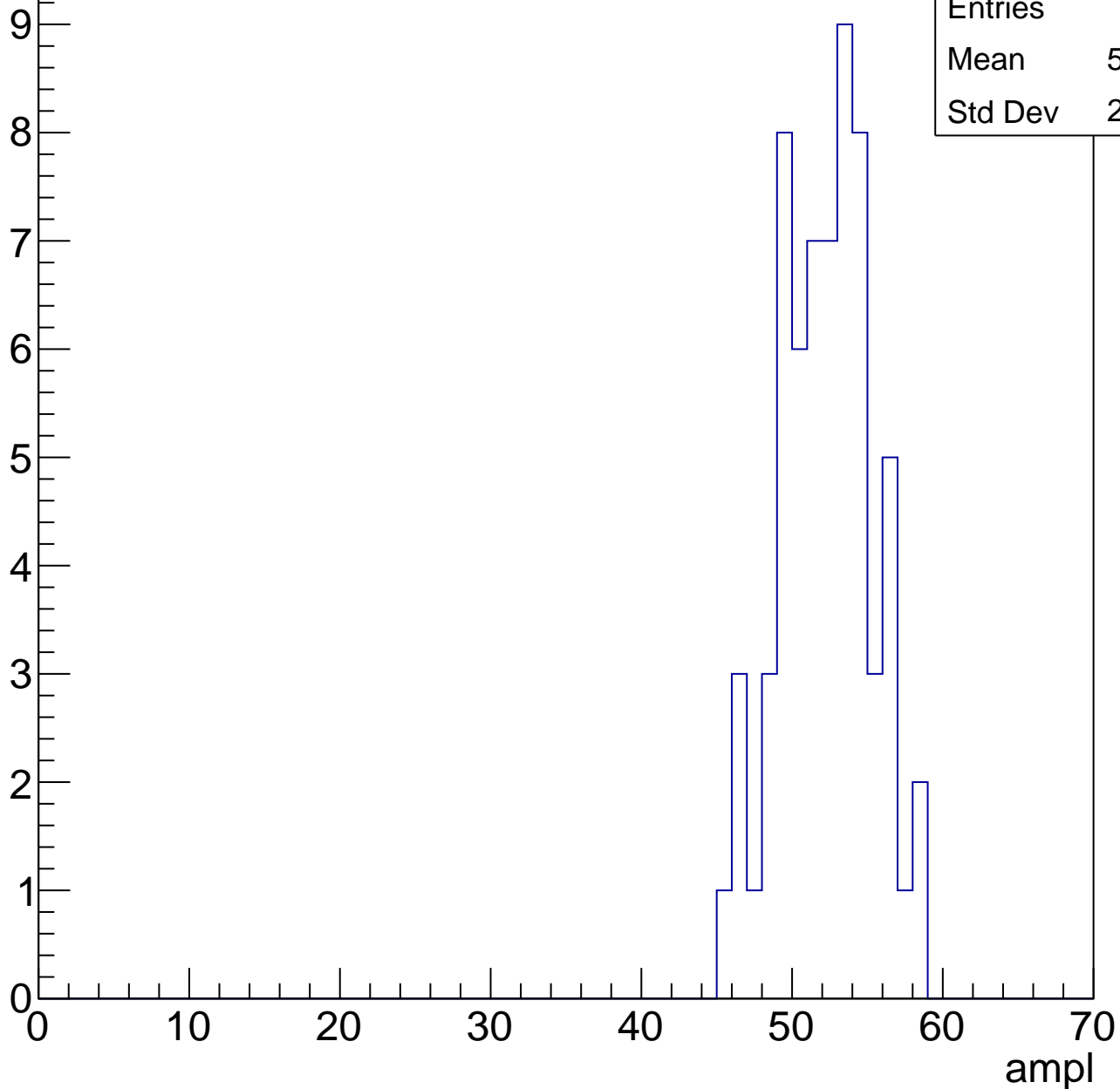
Gaus Width: 3.5299



B0L001S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

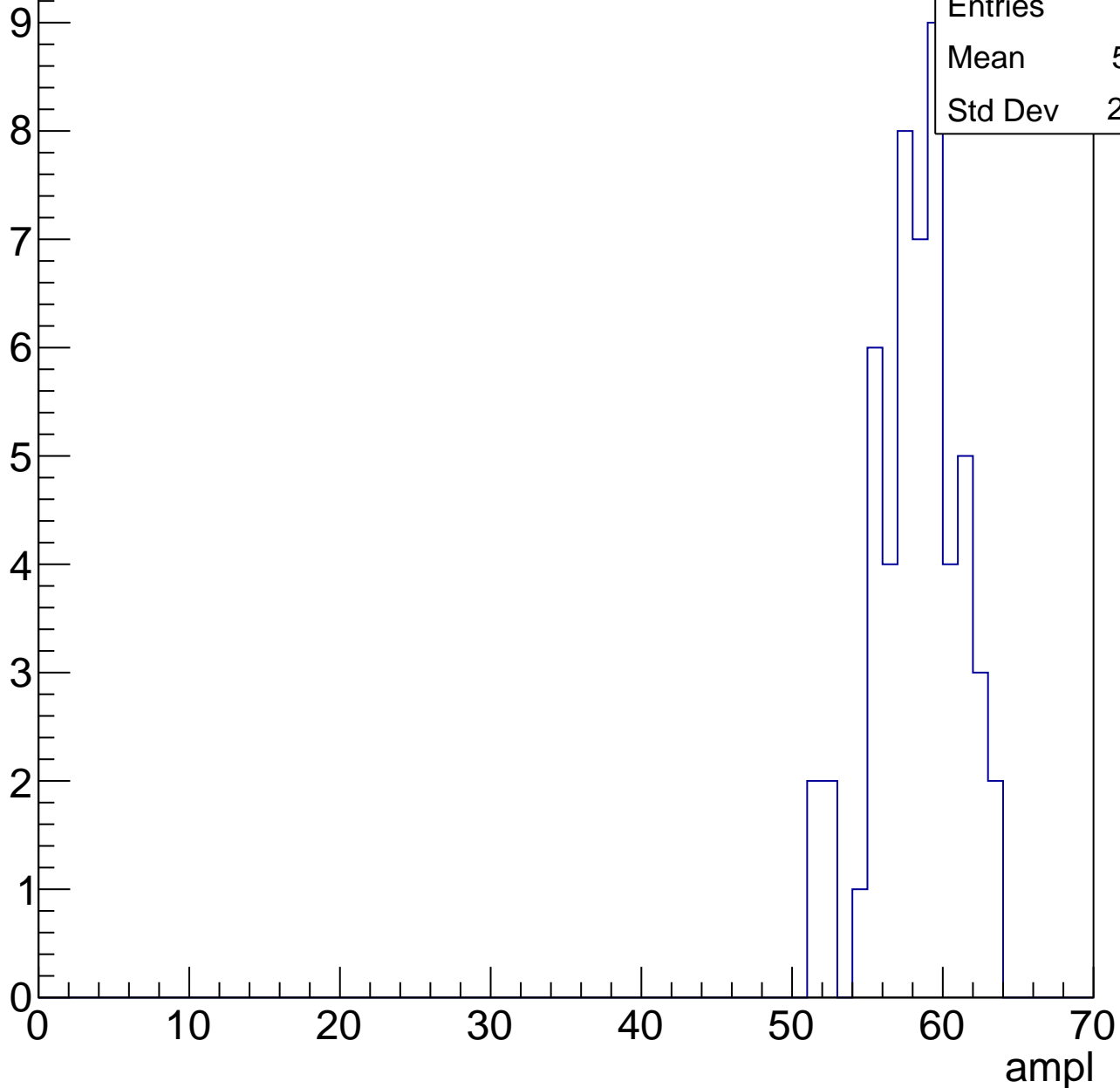
Entry



B0L001S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



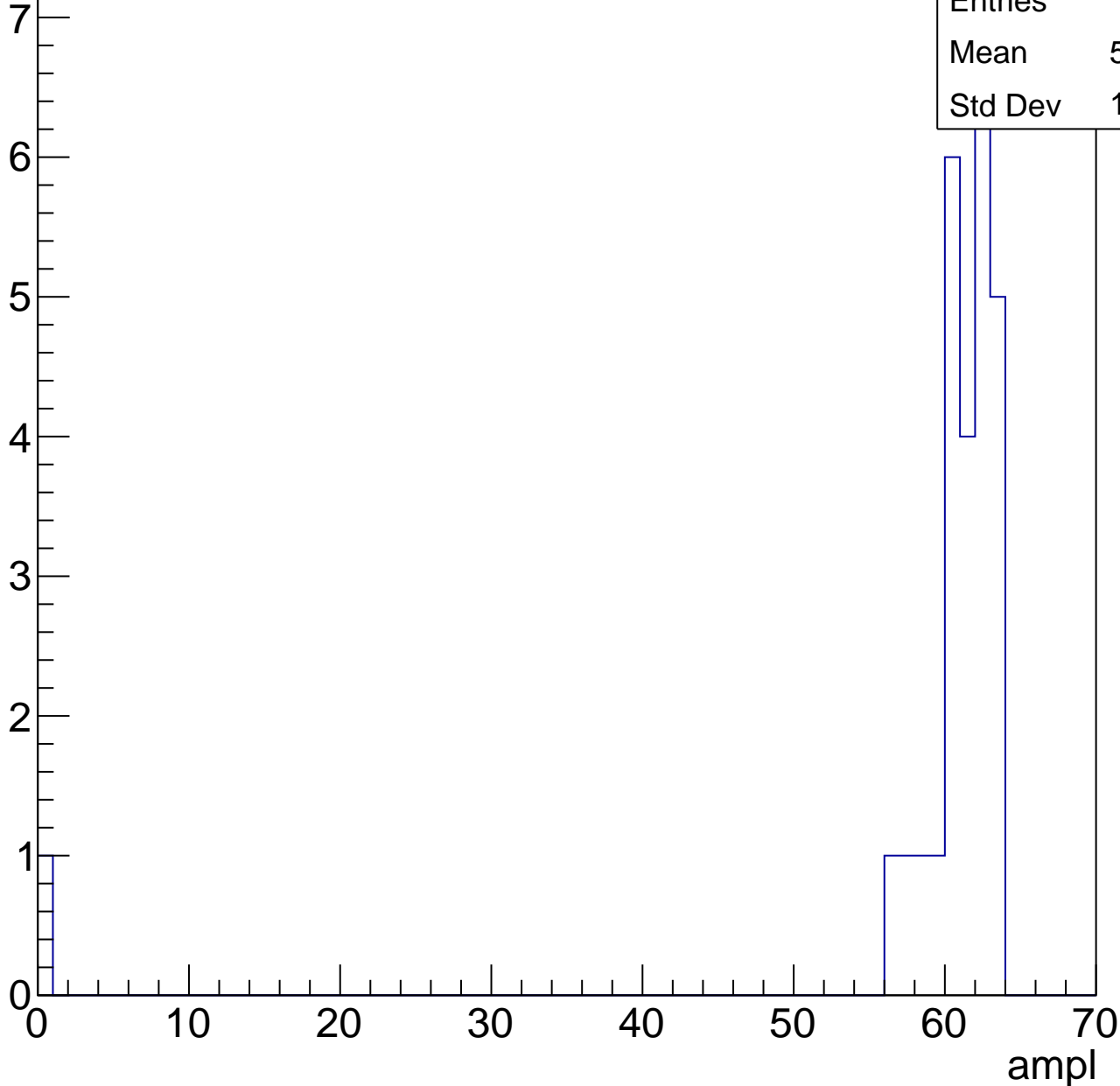
Entries	53
Mean	57.81
Std Dev	2.849

B0L001S, U15-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.63
Std Dev	11.64



B0L001S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch47, adc0

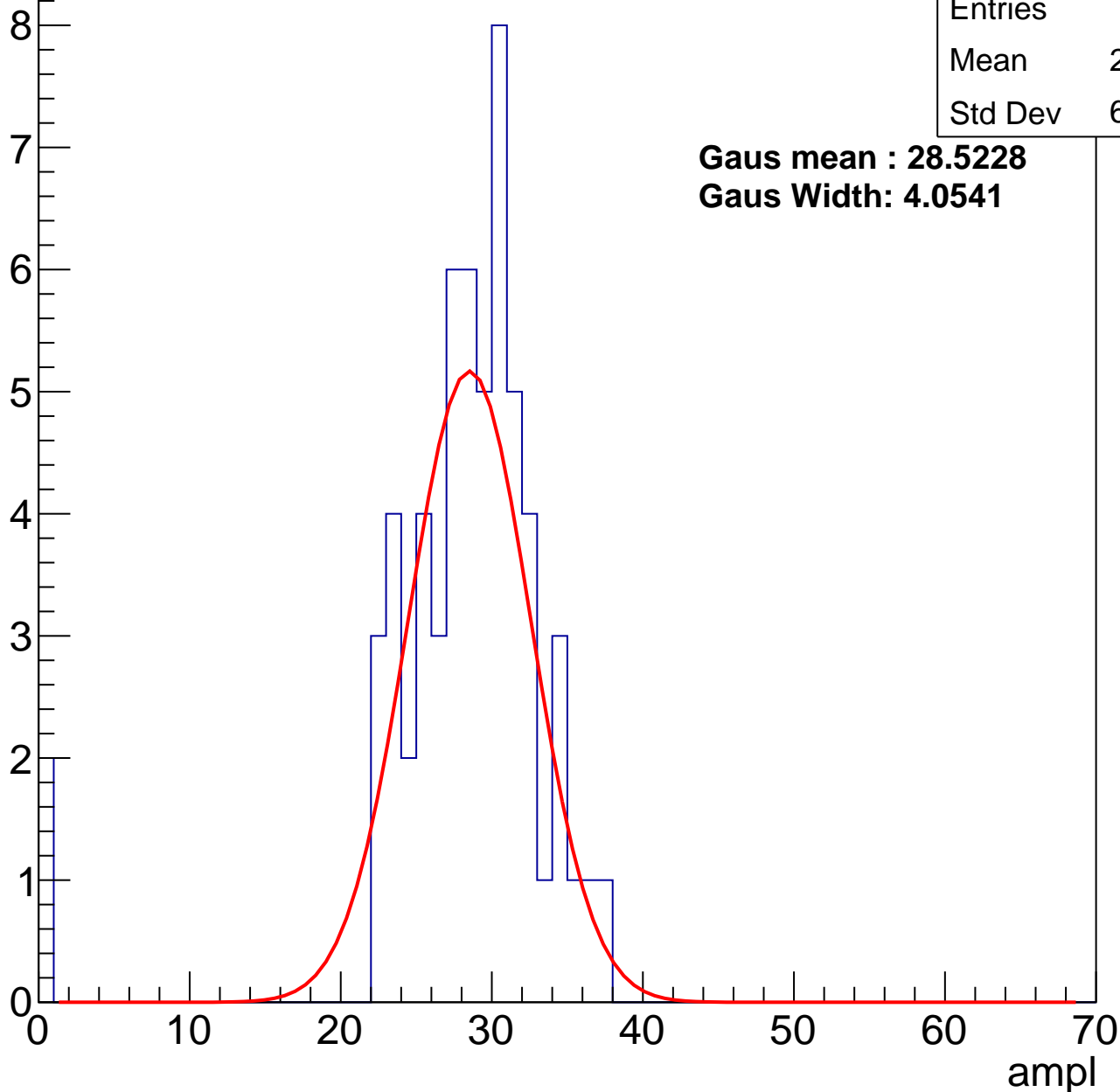
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	27.54
Std Dev	6.274

Gaus mean : 28.5228

Gaus Width: 4.0541



B0L001S, U15-ch47, adc1

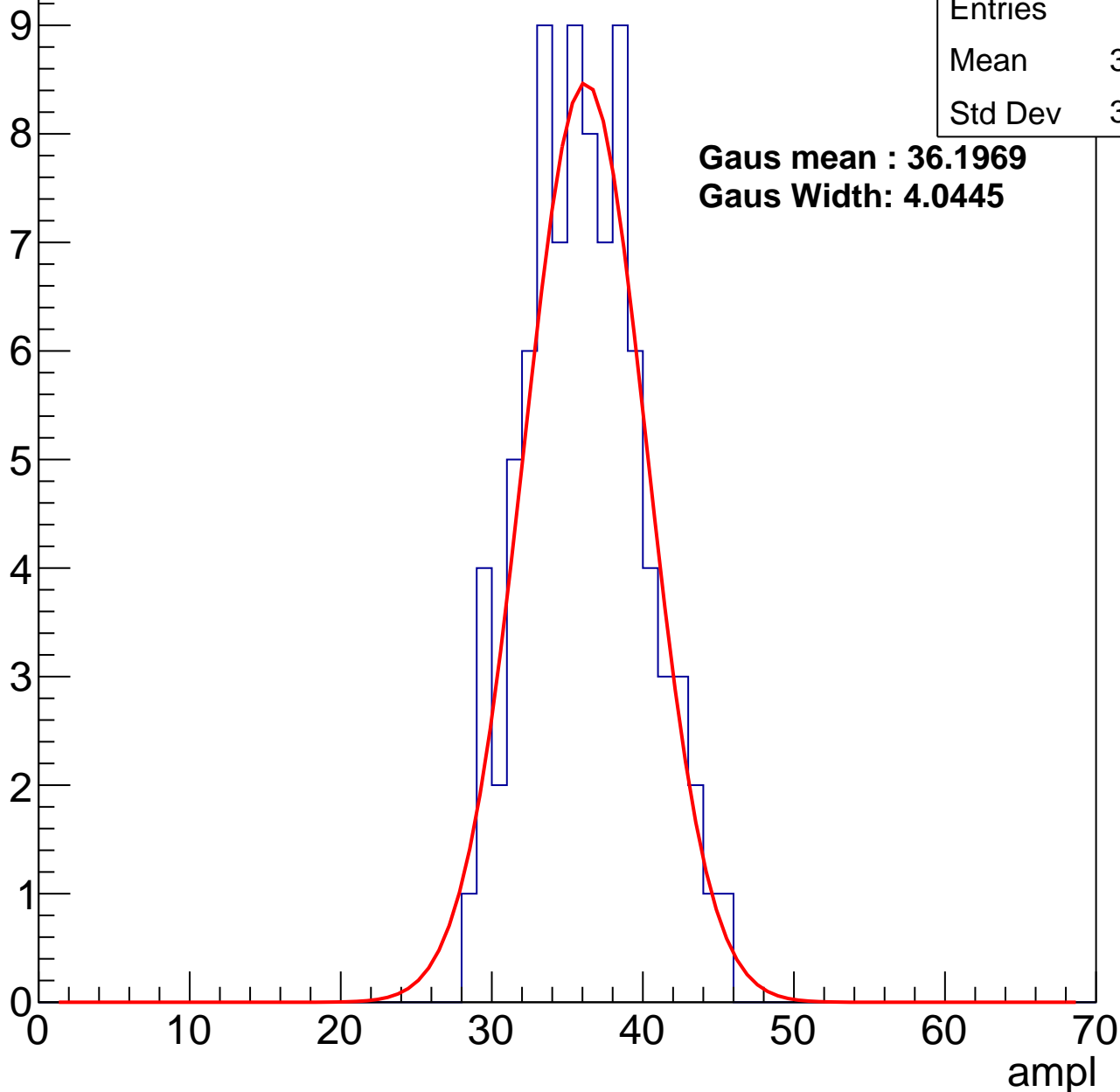
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	35.72
Std Dev	3.793

Gaus mean : 36.1669

Gaus Width: 4.0445



B0L001S, U15-ch47, adc2

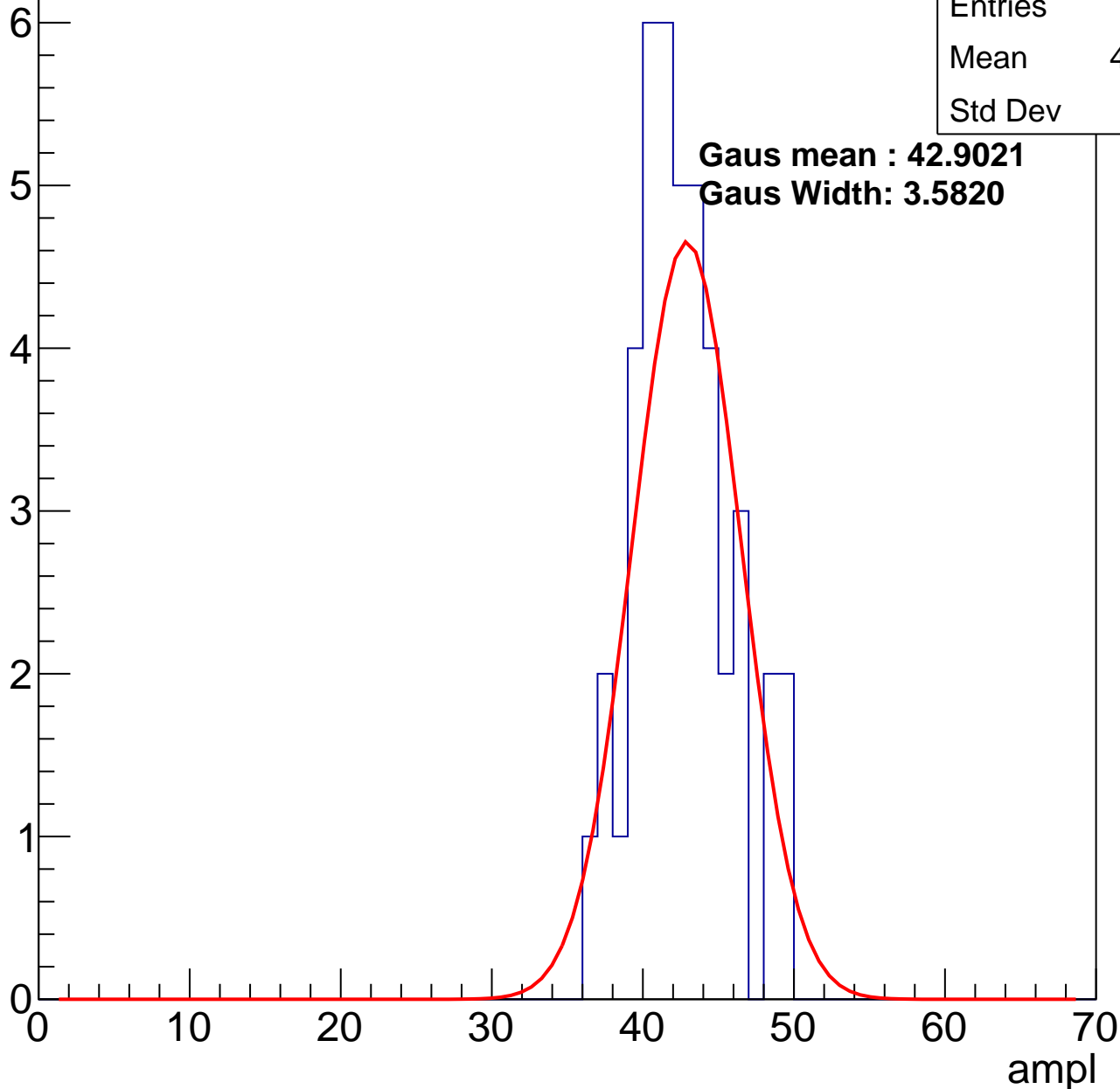
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	42.16
Std Dev	3.14

Gaus mean : 42.9021

Gaus Width: 3.5820

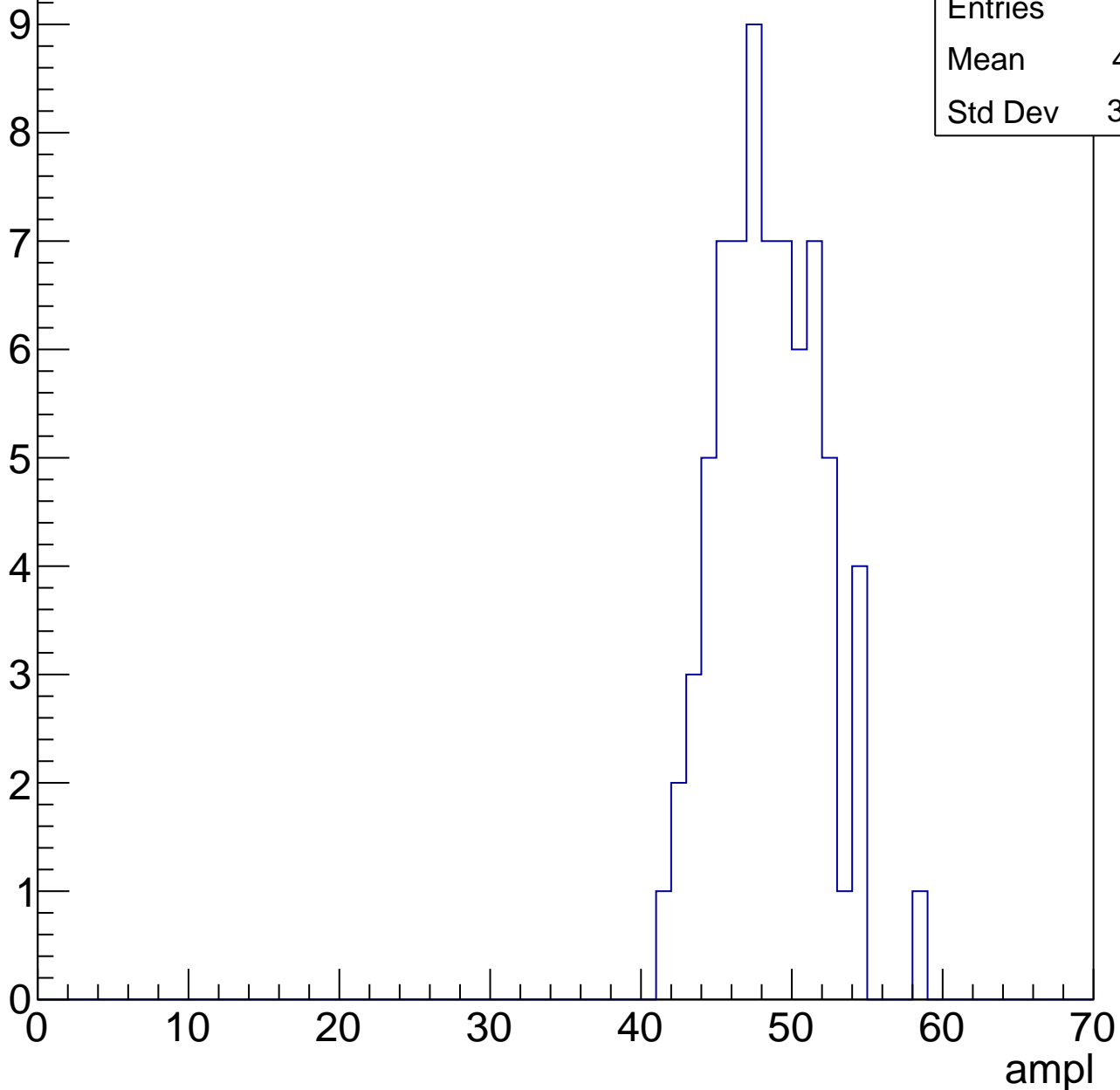


B0L001S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	48.01
Std Dev	3.373

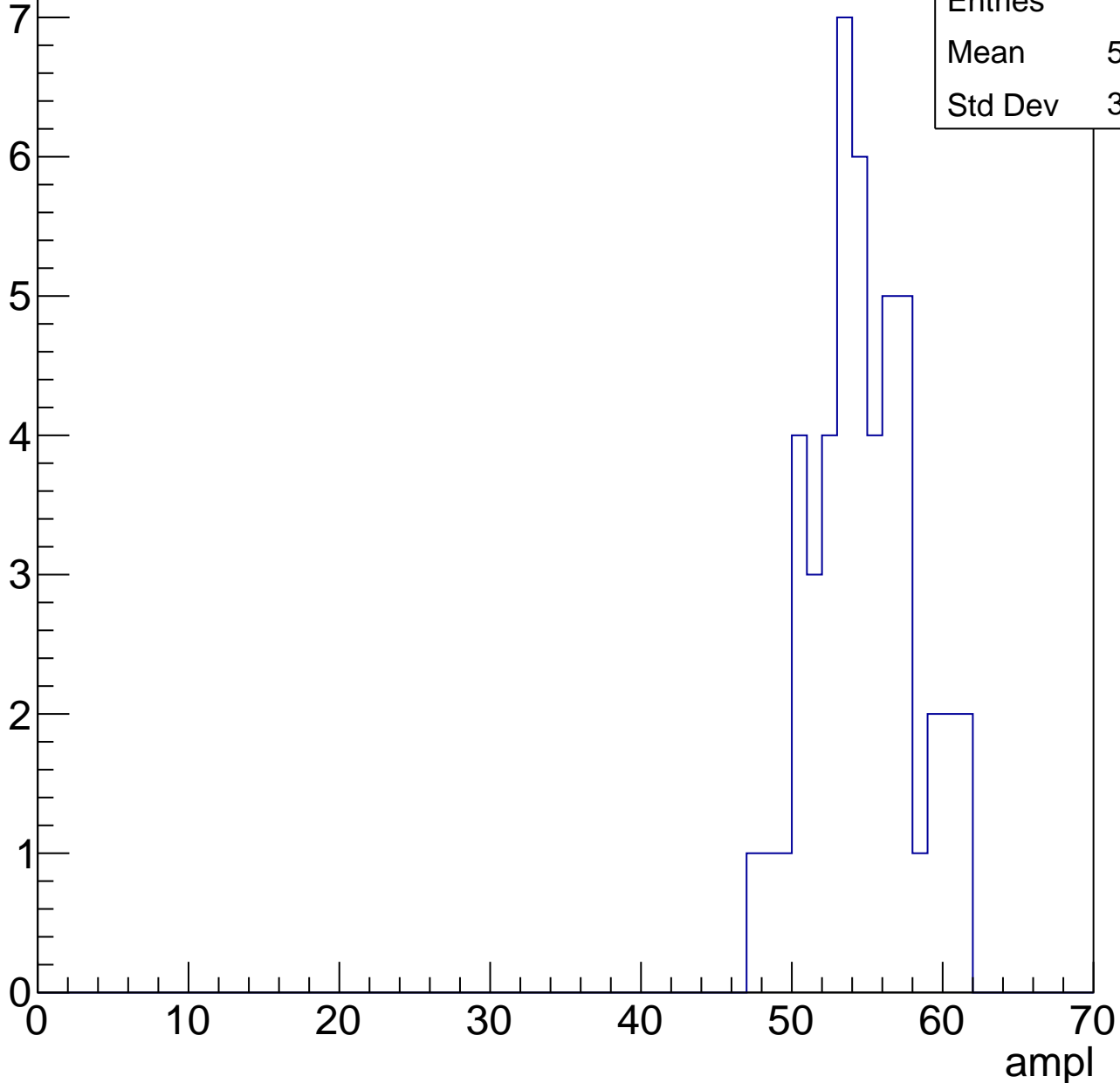


B0L001S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	54.23
Std Dev	3.312

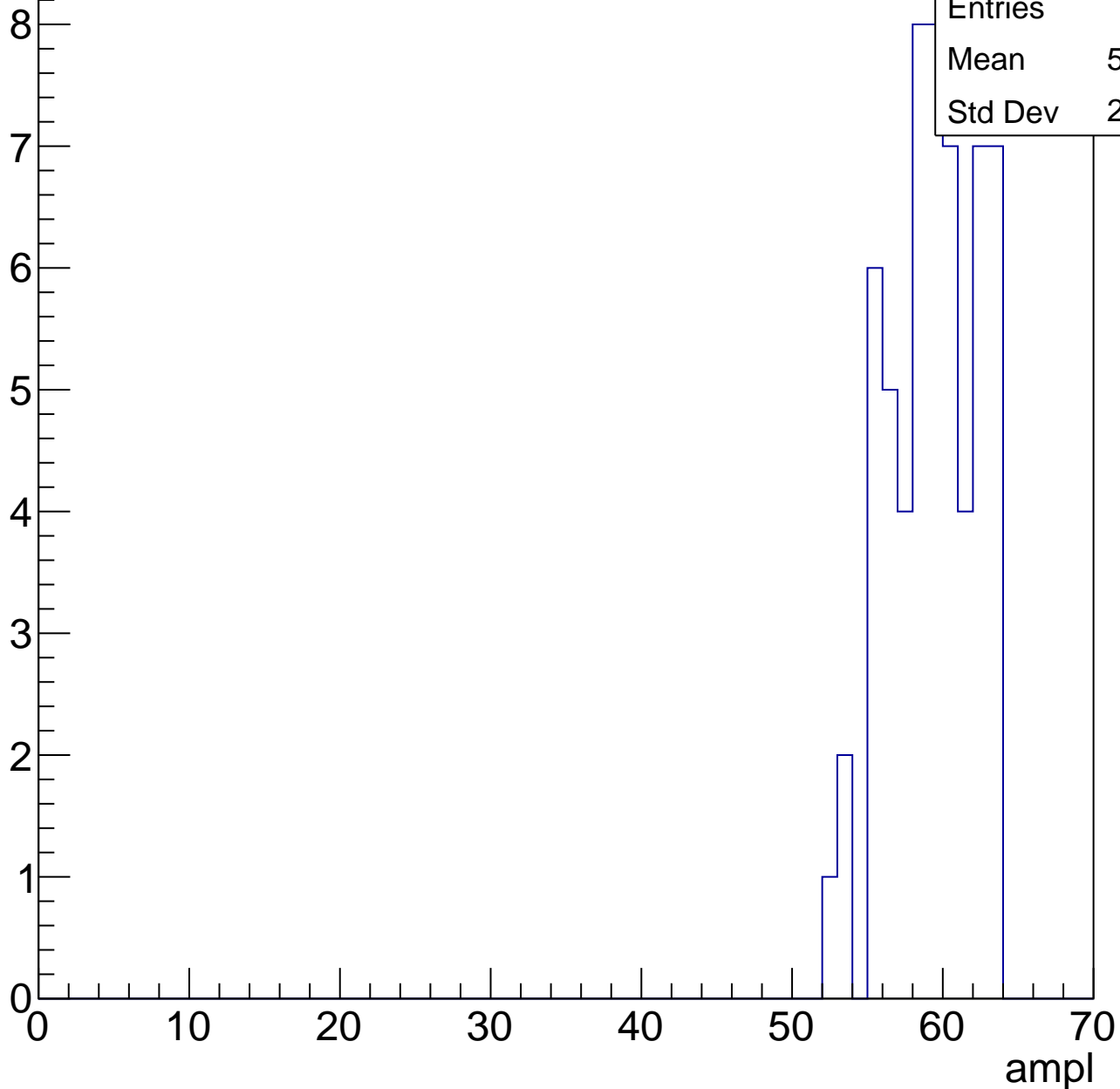


B0L001S, U15-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

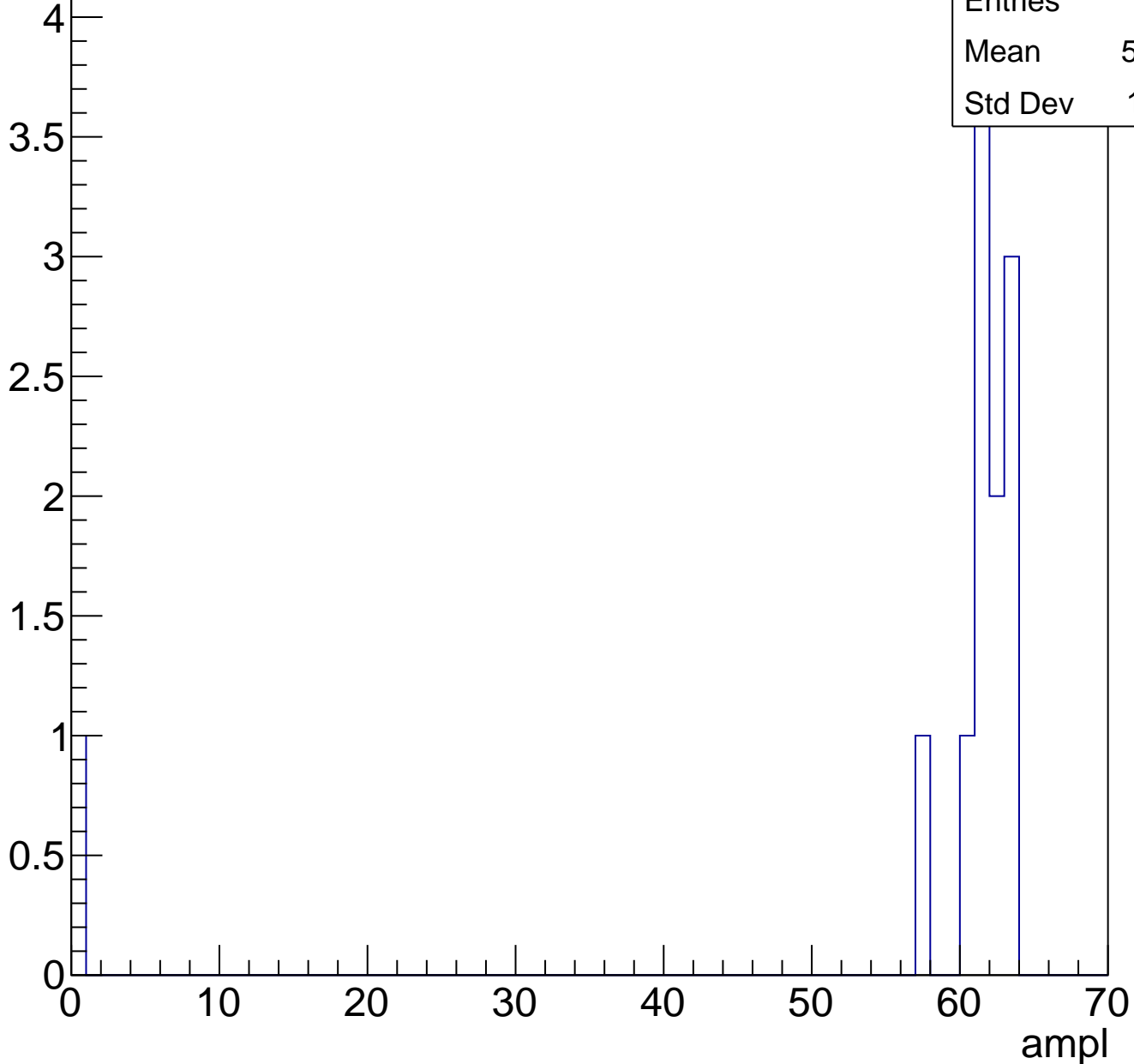
Entries	59
Mean	58.83
Std Dev	2.859



B0L001S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L001S, U15-ch48, adc0

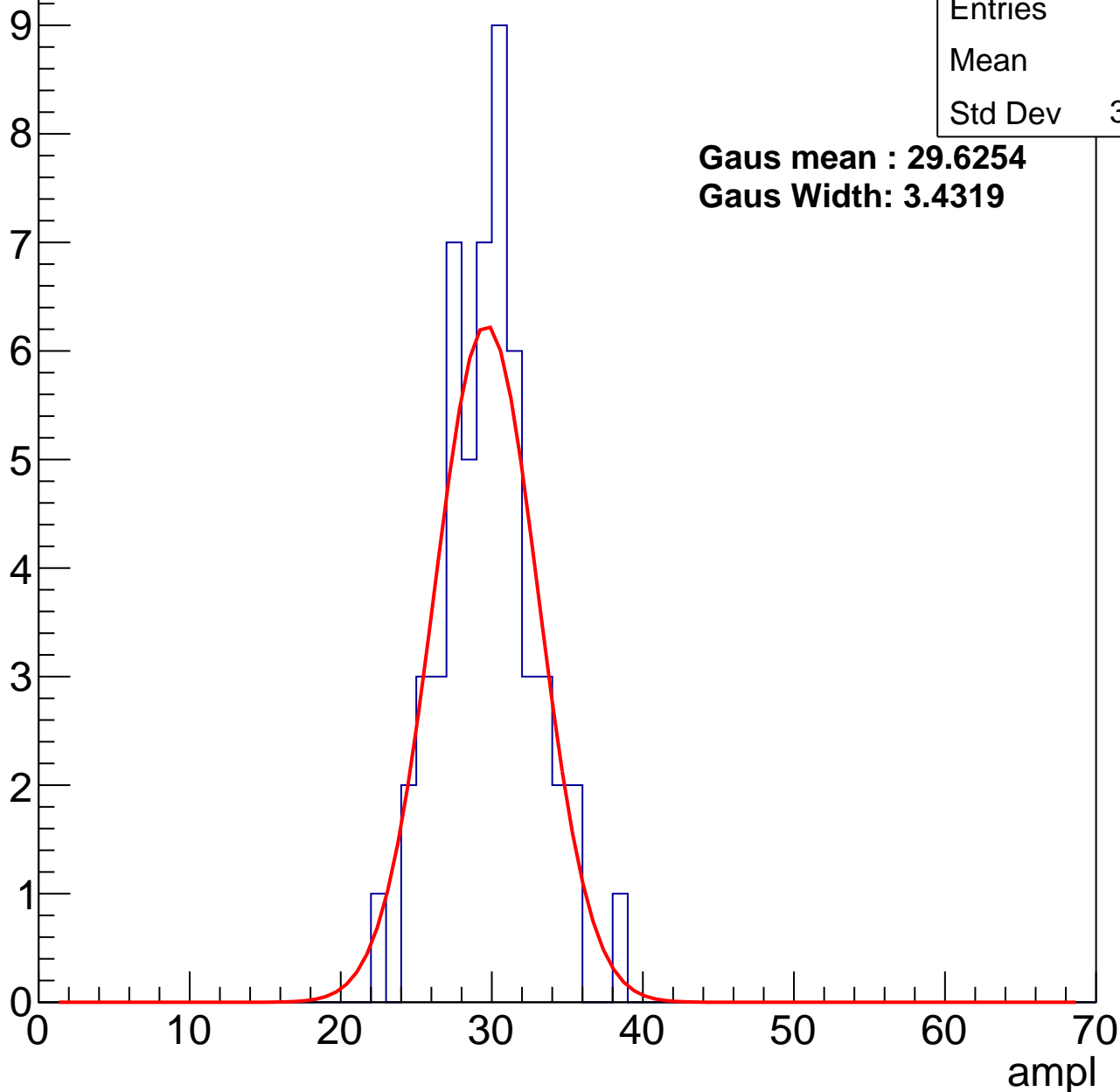
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.3
Std Dev	3.083

Gaus mean : 29.6254

Gaus Width: 3.4319



B0L001S, U15-ch48, adc1

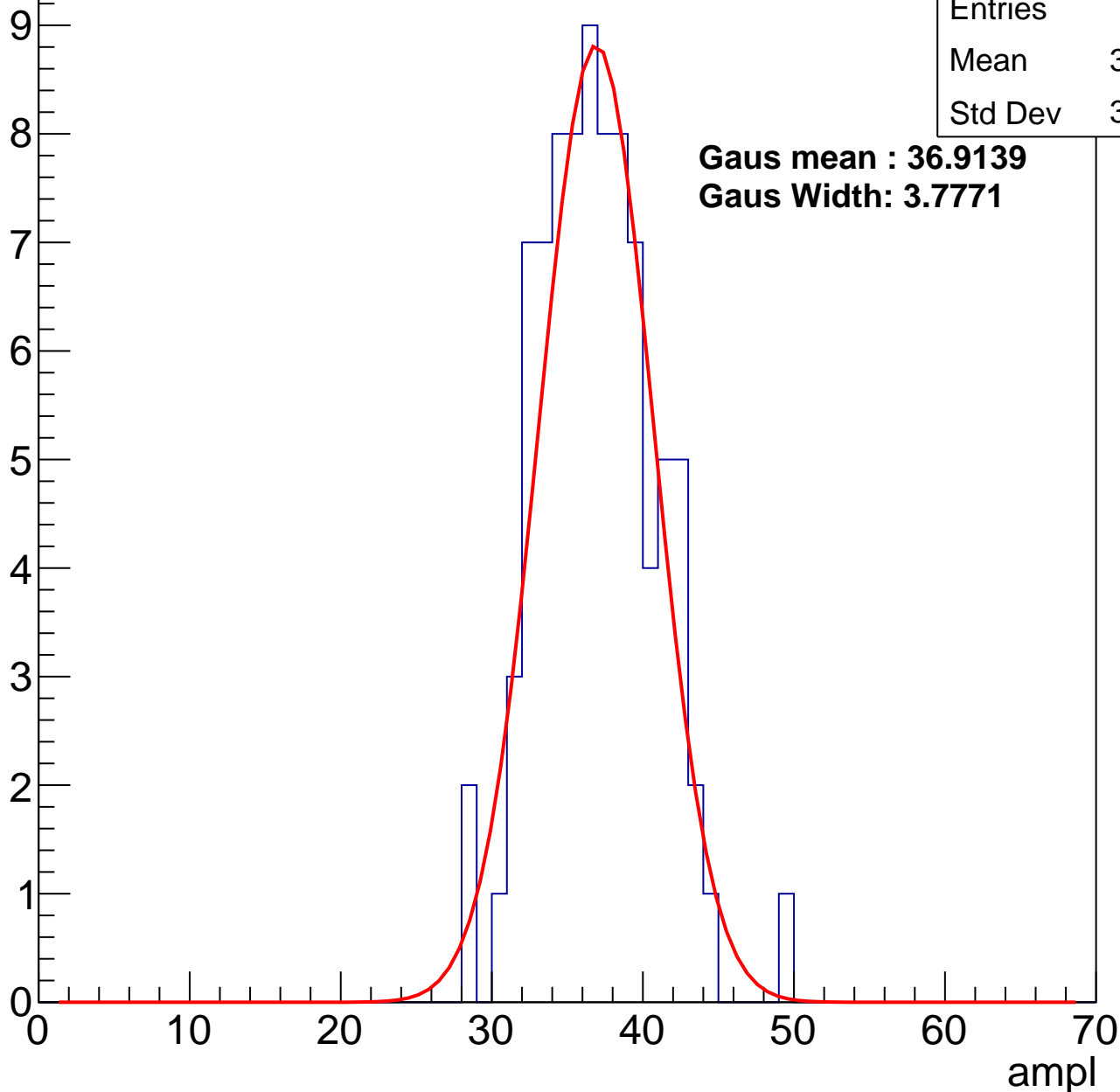
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	36.48
Std Dev	3.793

Gaus mean : 36.9139

Gaus Width: 3.7771

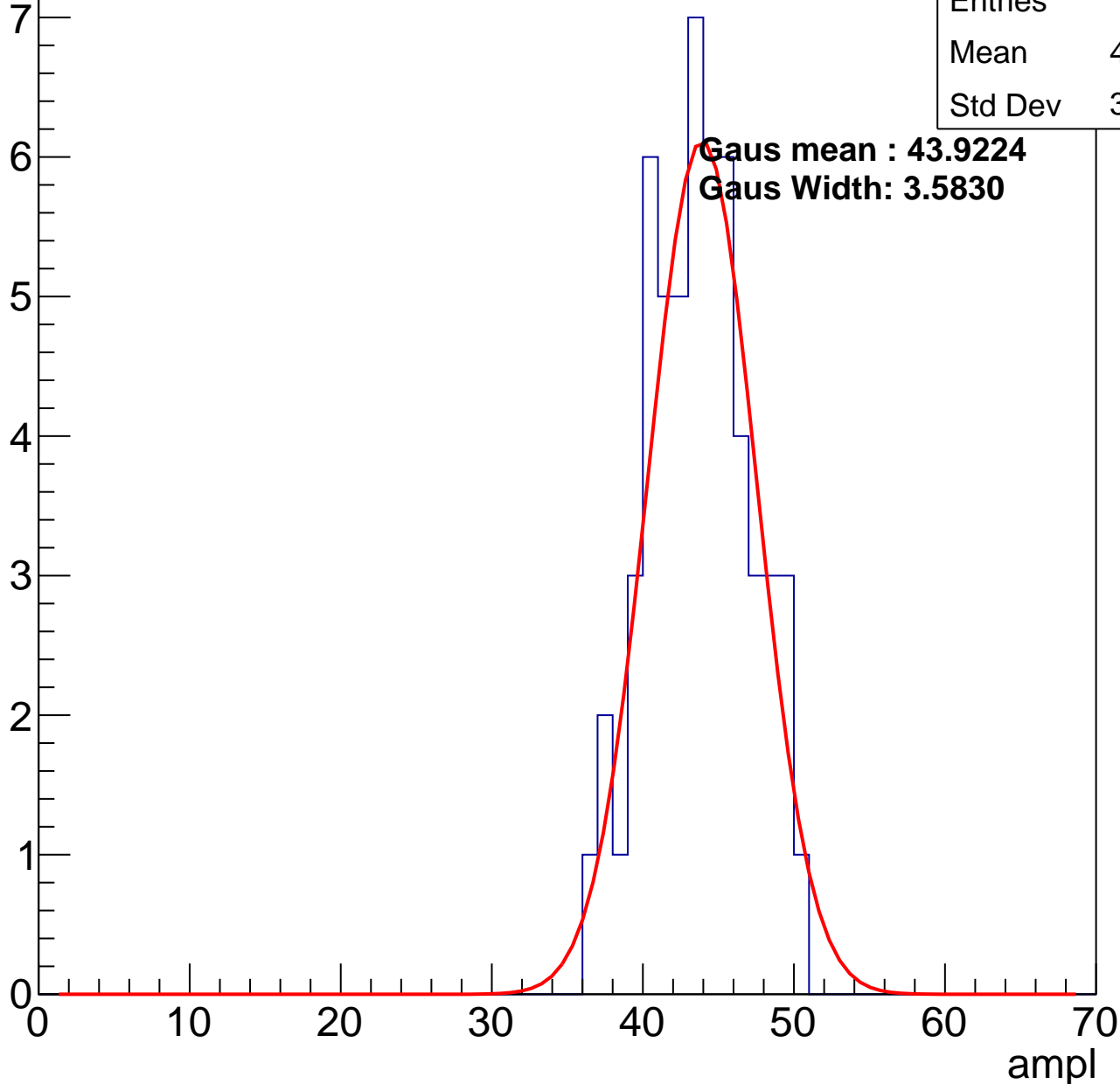


B0L001S, U15-ch48, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

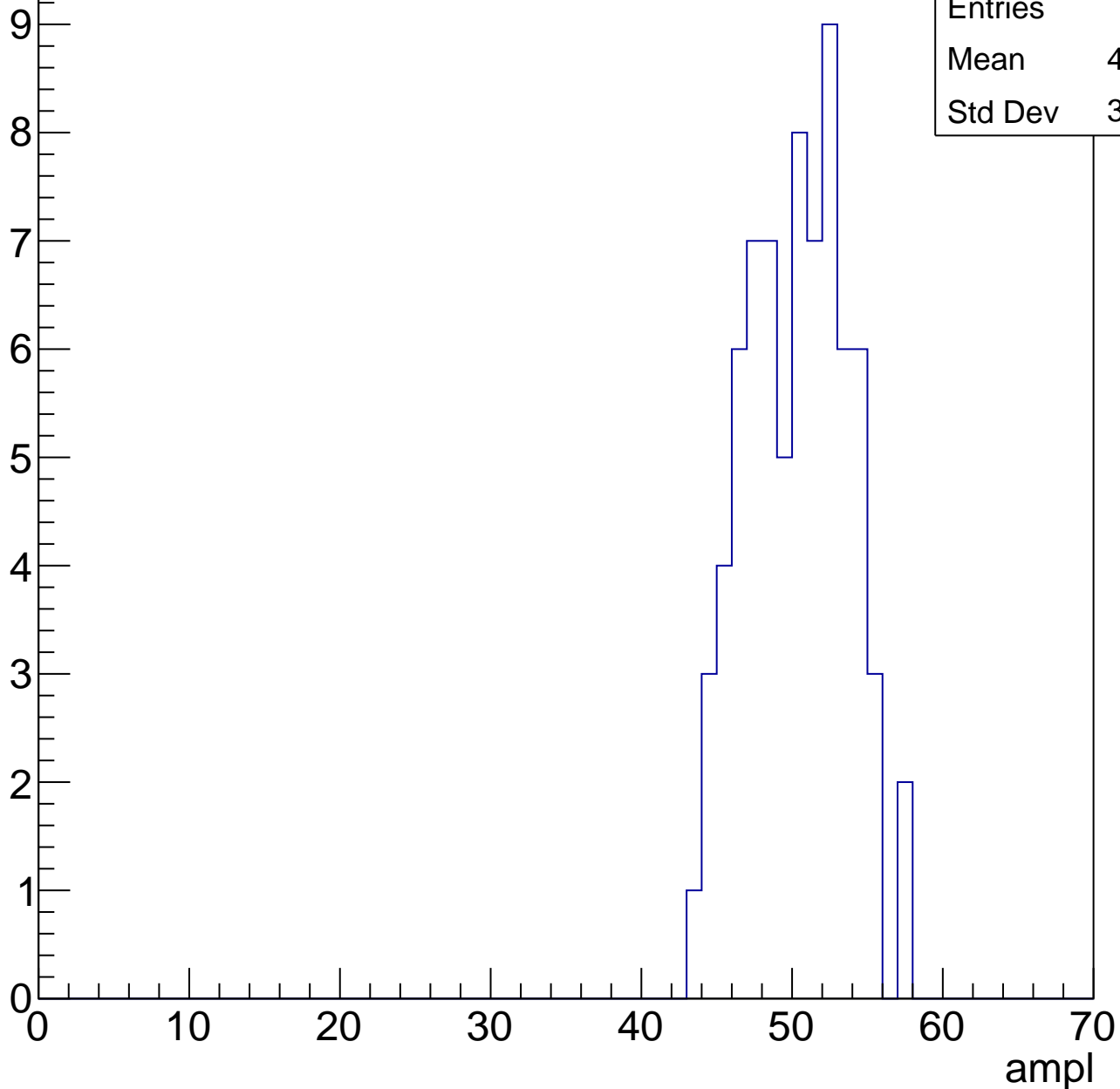
Entries	56
Mean	43.23
Std Dev	3.317



B0L001S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

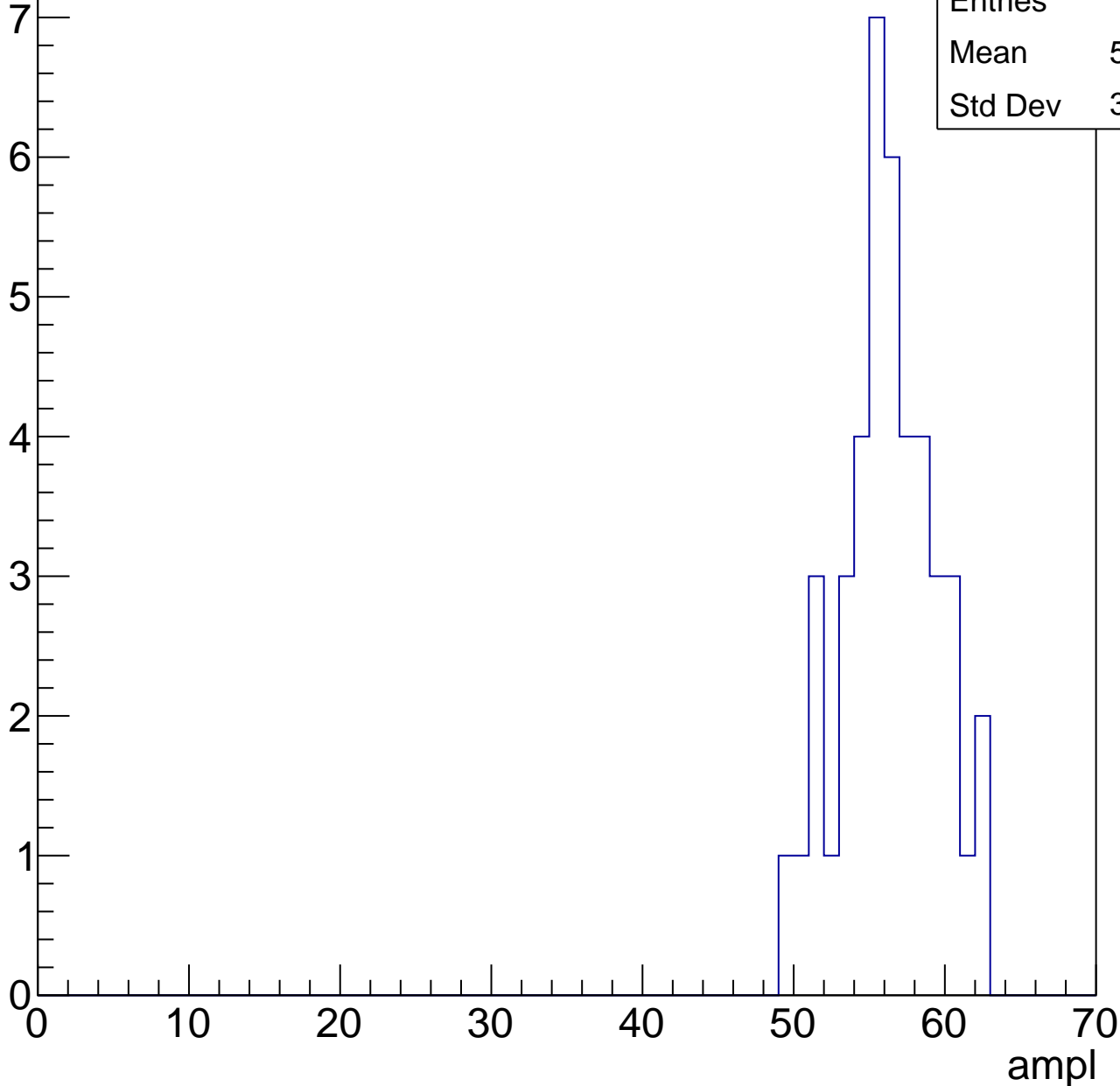


B0L001S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	55.86
Std Dev	3.115

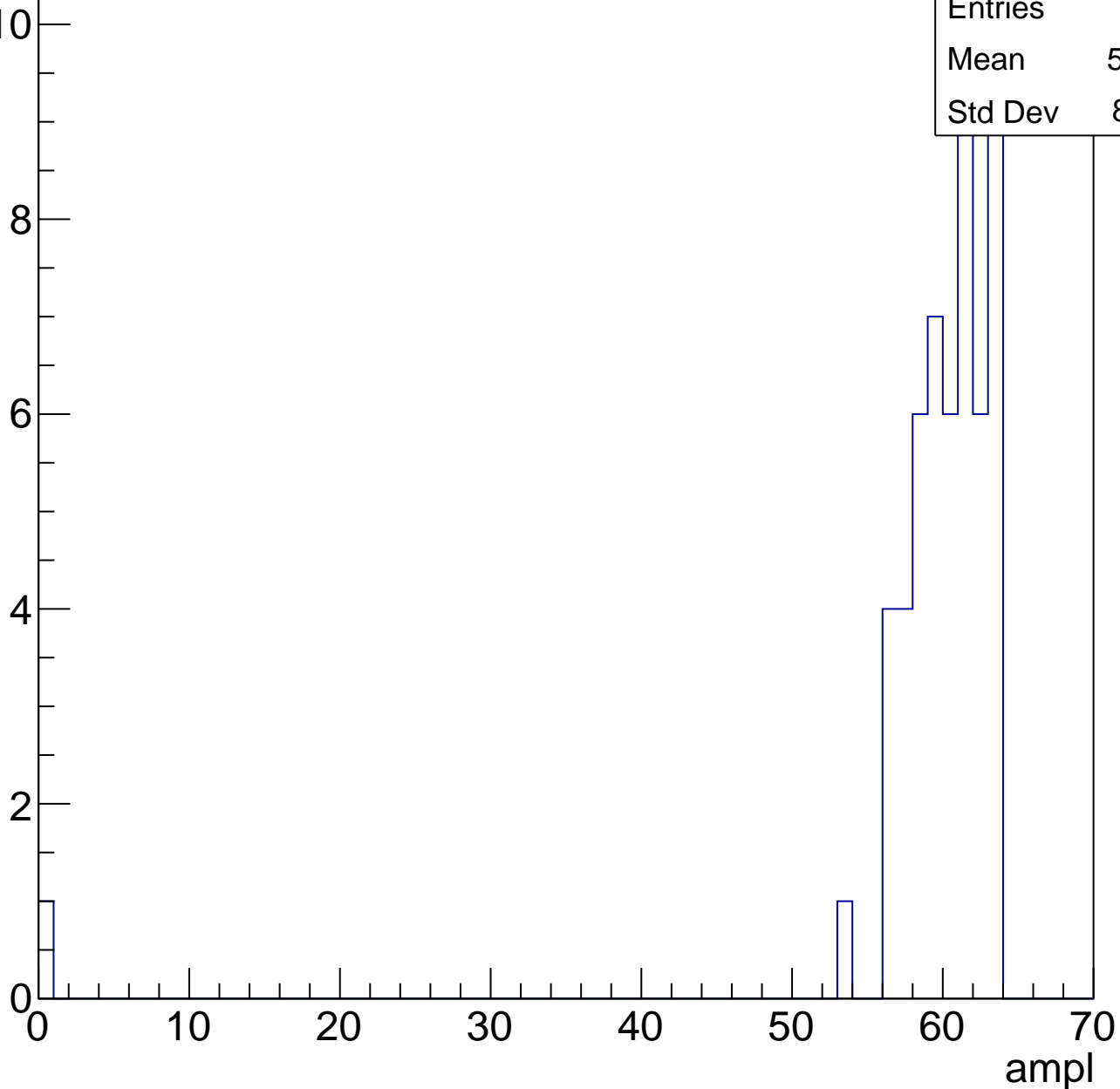


B0L001S, U15-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	58.83
Std Dev	8.421



B0L001S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

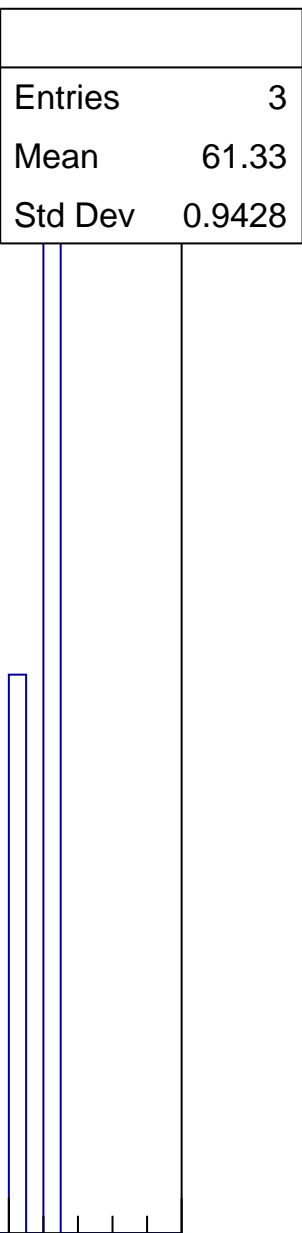
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch49, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	28.95
Std Dev	7.405

Gaus mean : 31.2756

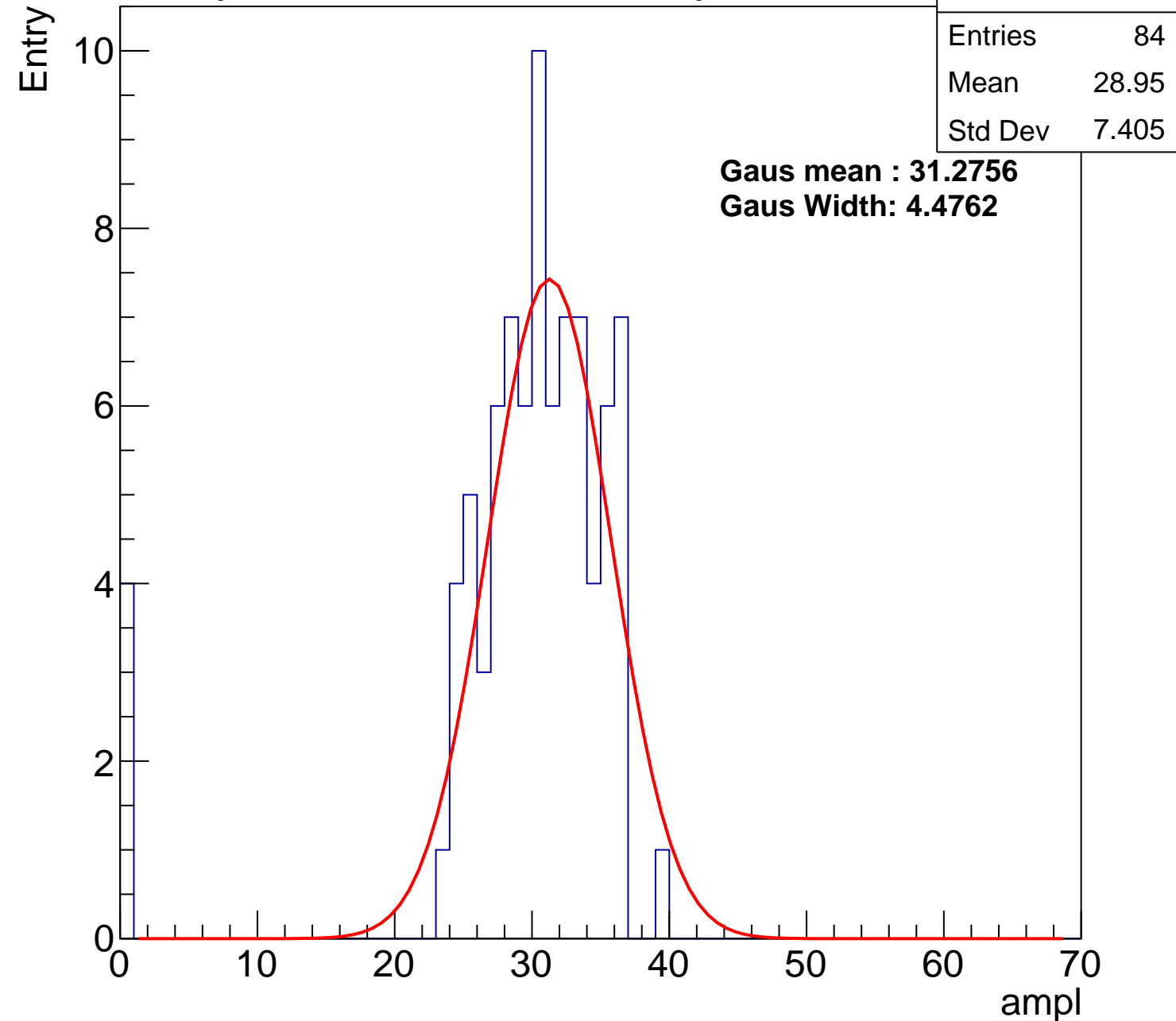
Gaus Width: 4.4762

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch49, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	56
Mean	36.54
Std Dev	3.235

Gaus mean : 37.1431

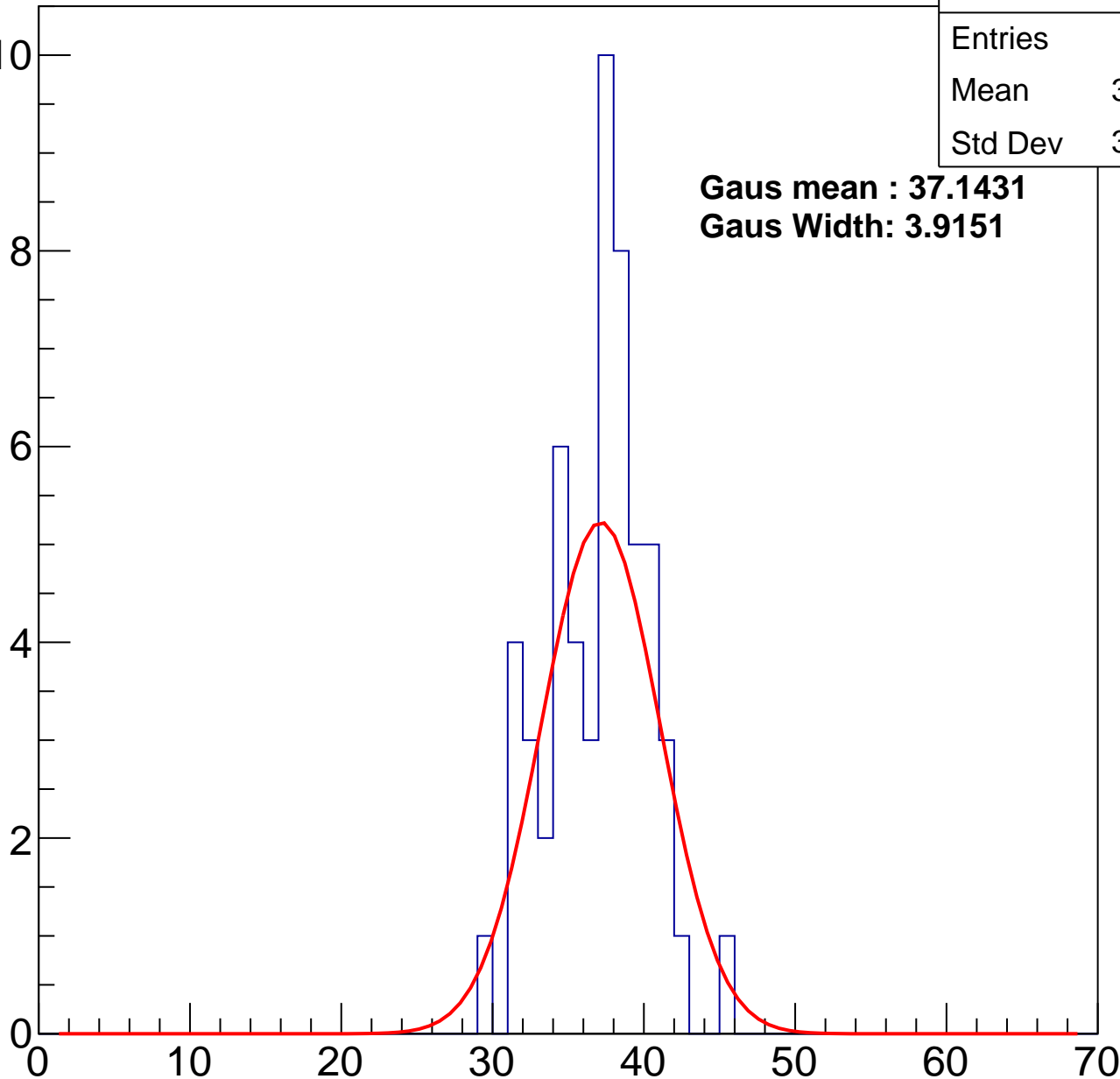
Gaus Width: 3.9151

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch49, adc2

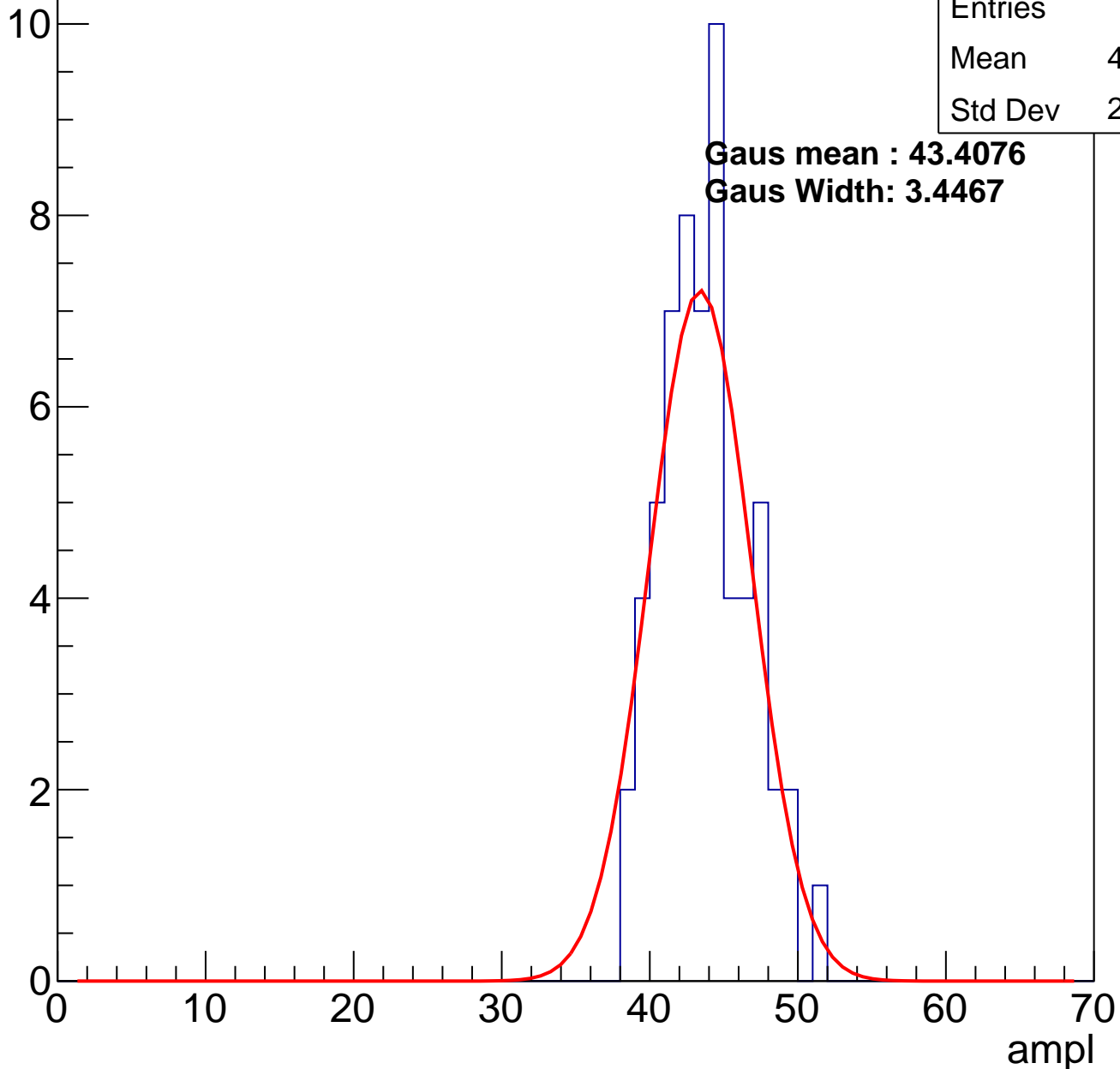
calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	43.28
Std Dev	2.915

Gaus mean : 43.4076

Gaus Width: 3.4467

Entry



B0L001S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

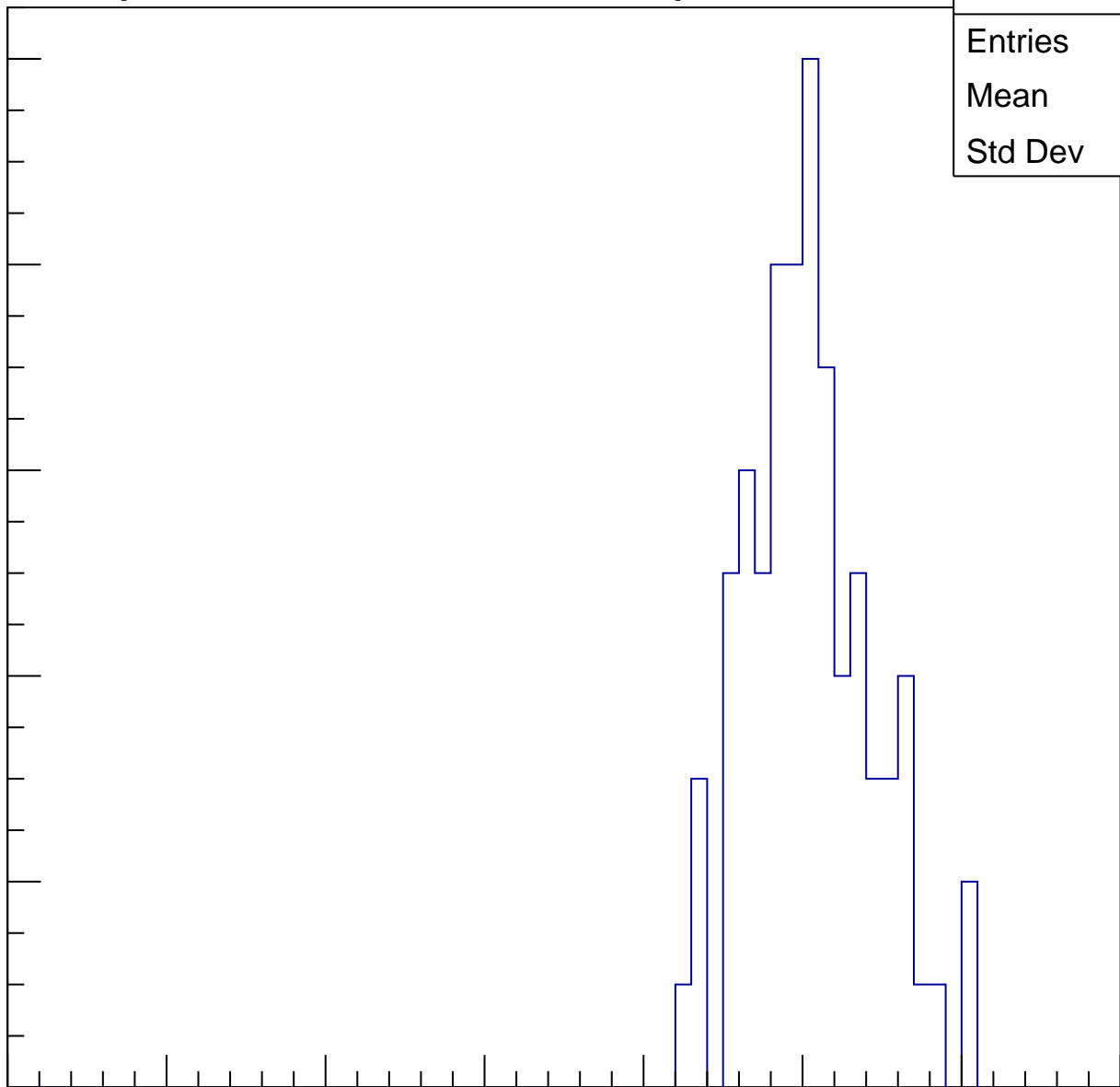
Entries	76
Mean	49.99
Std Dev	3.915

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

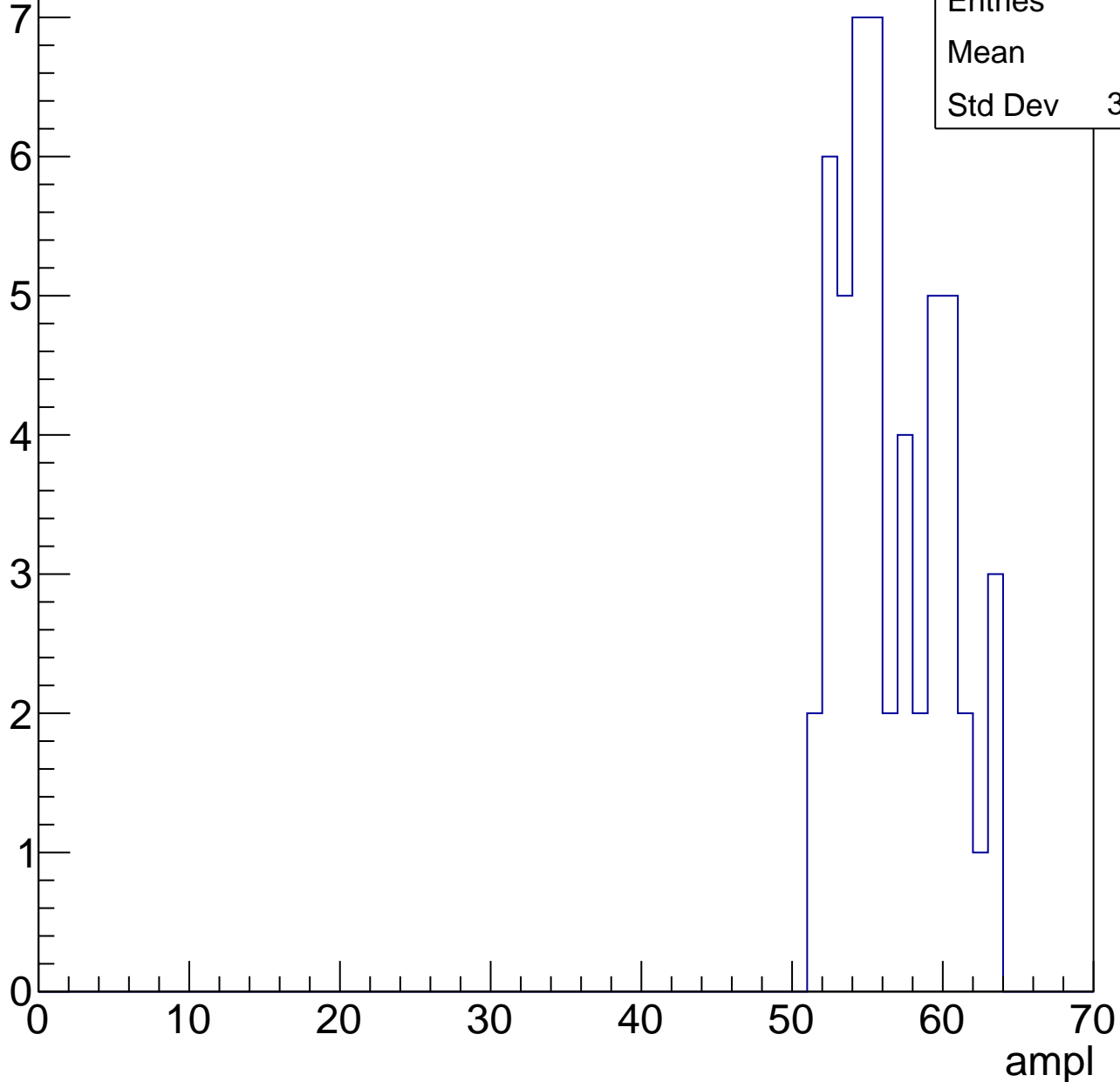


B0L001S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.2
Std Dev	3.413

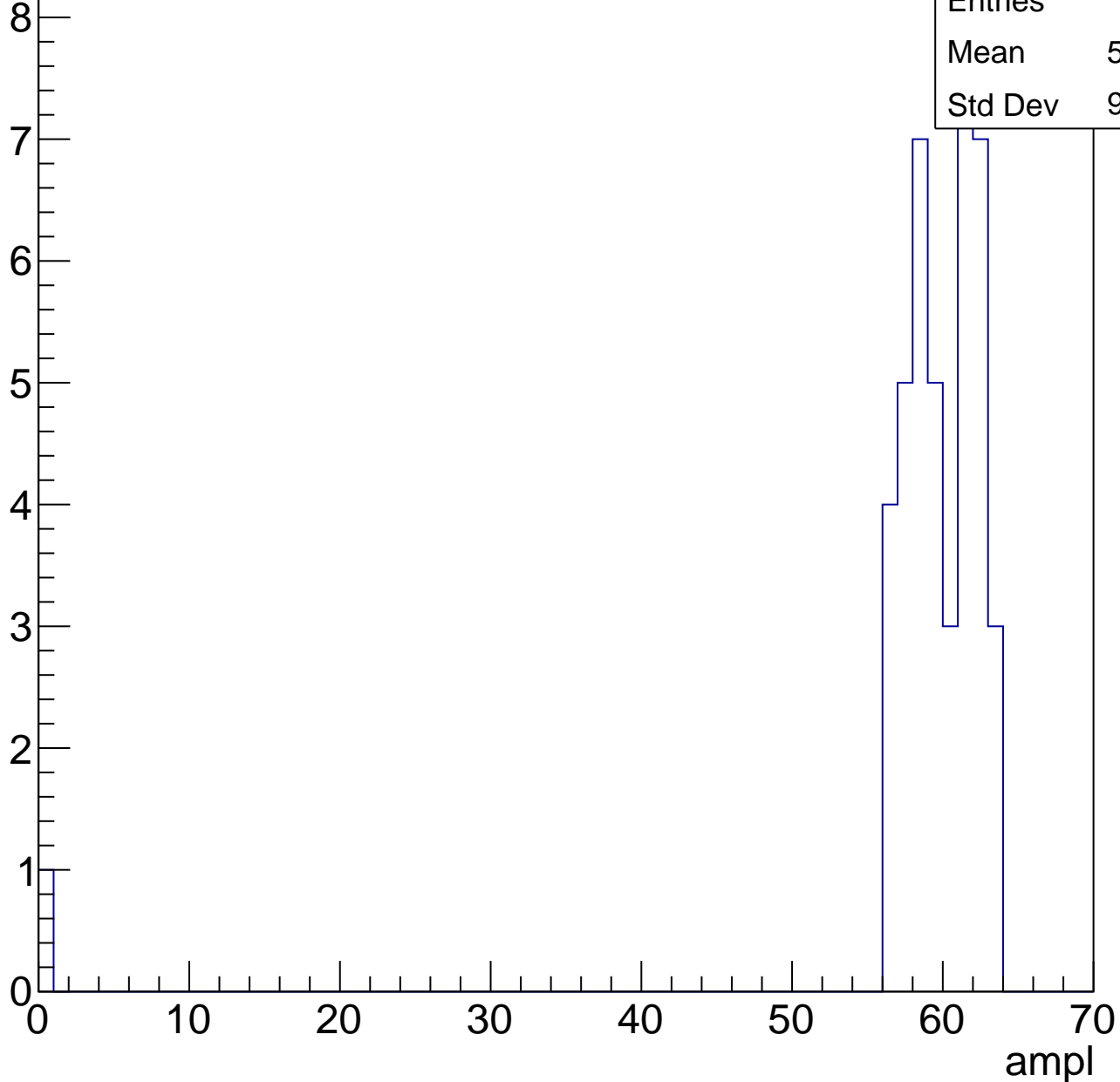


B0L001S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.16
Std Dev	9.226



B0L001S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch50, adc0

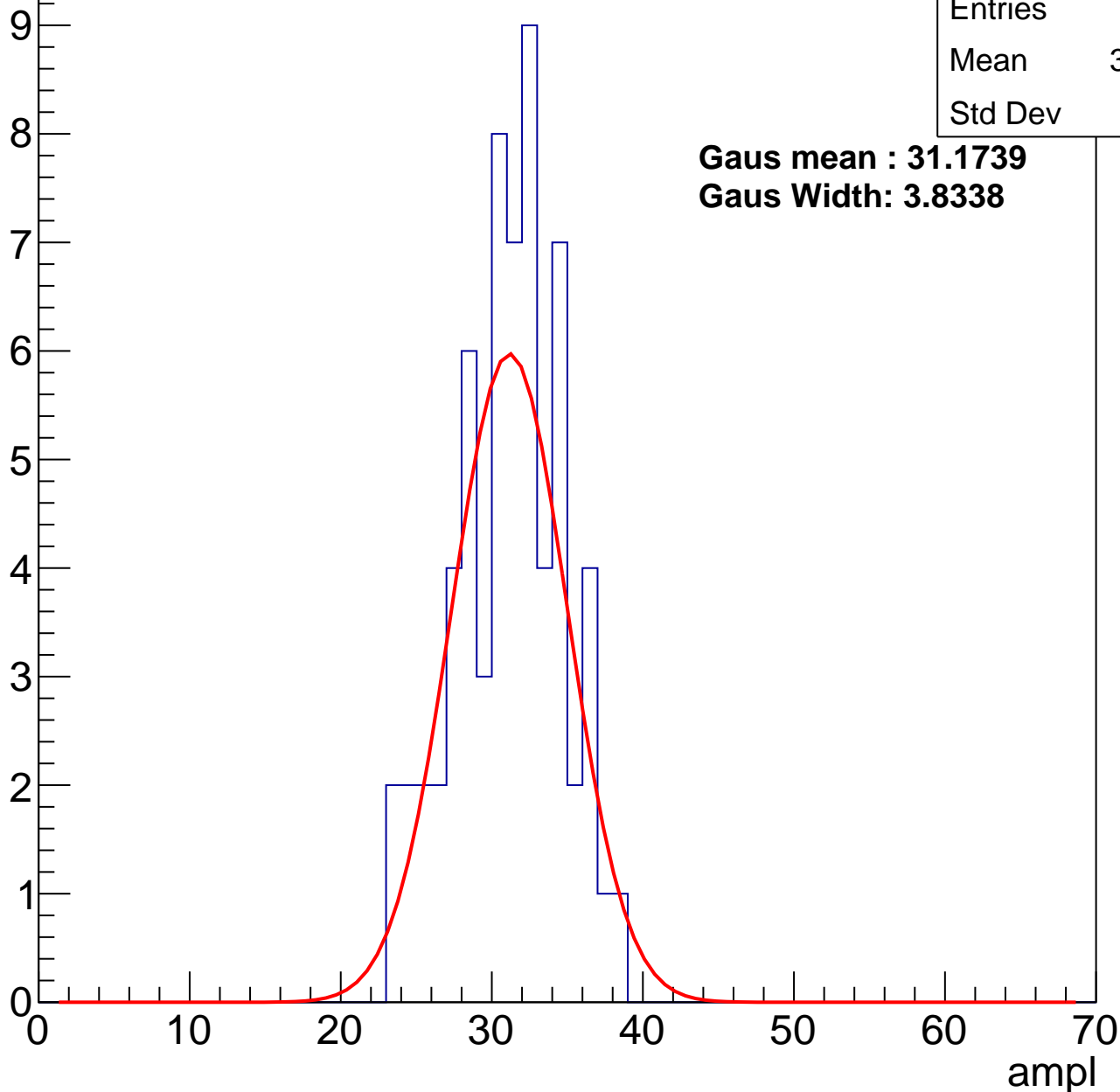
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.67
Std Dev	3.5

Gaus mean : 31.1739

Gaus Width: 3.8338



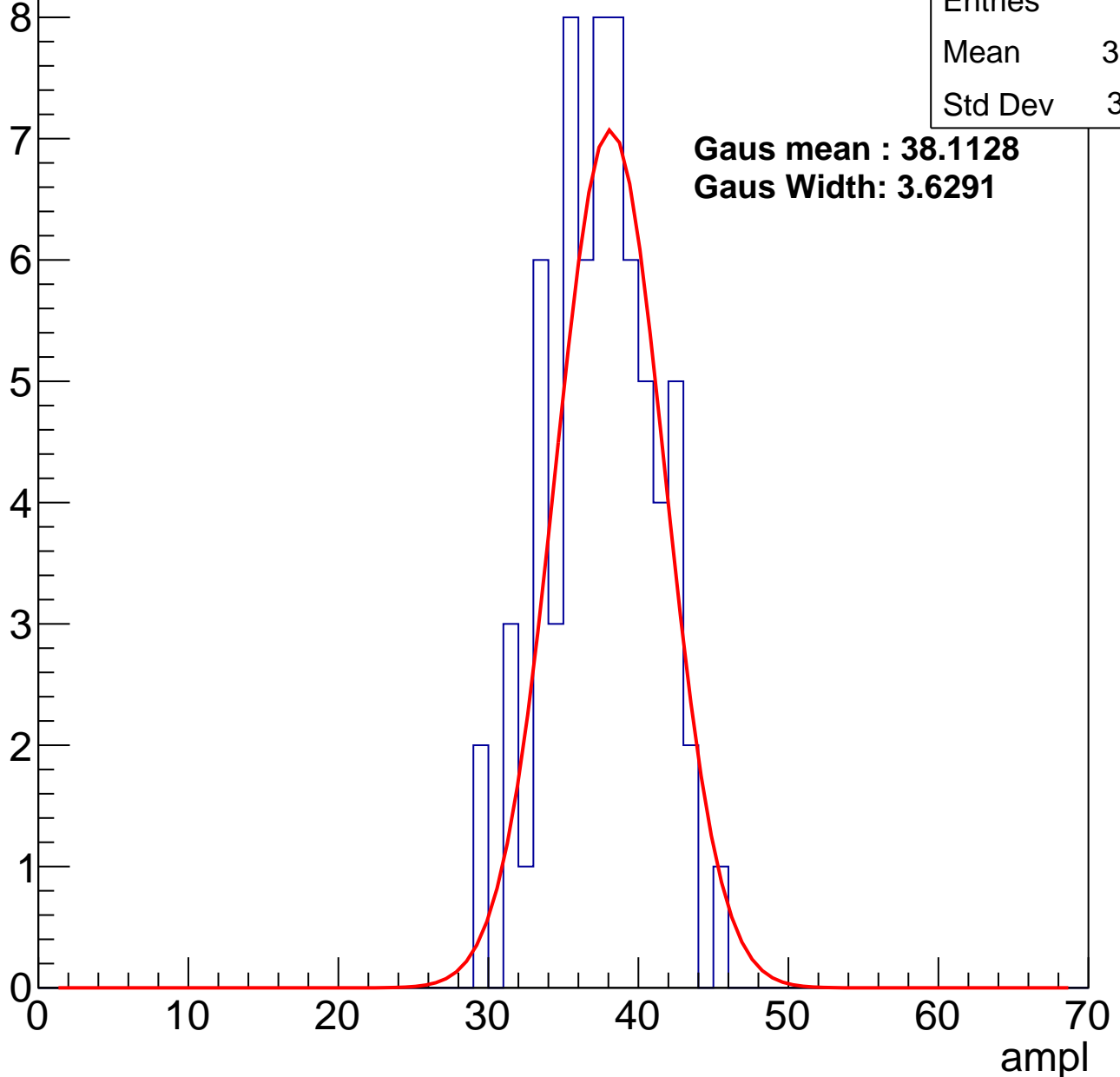
B0L001S, U15-ch50, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.03
Std Dev	3.481

Gaus mean : 38.1128
Gaus Width: 3.6291



B0L001S, U15-ch50, adc2

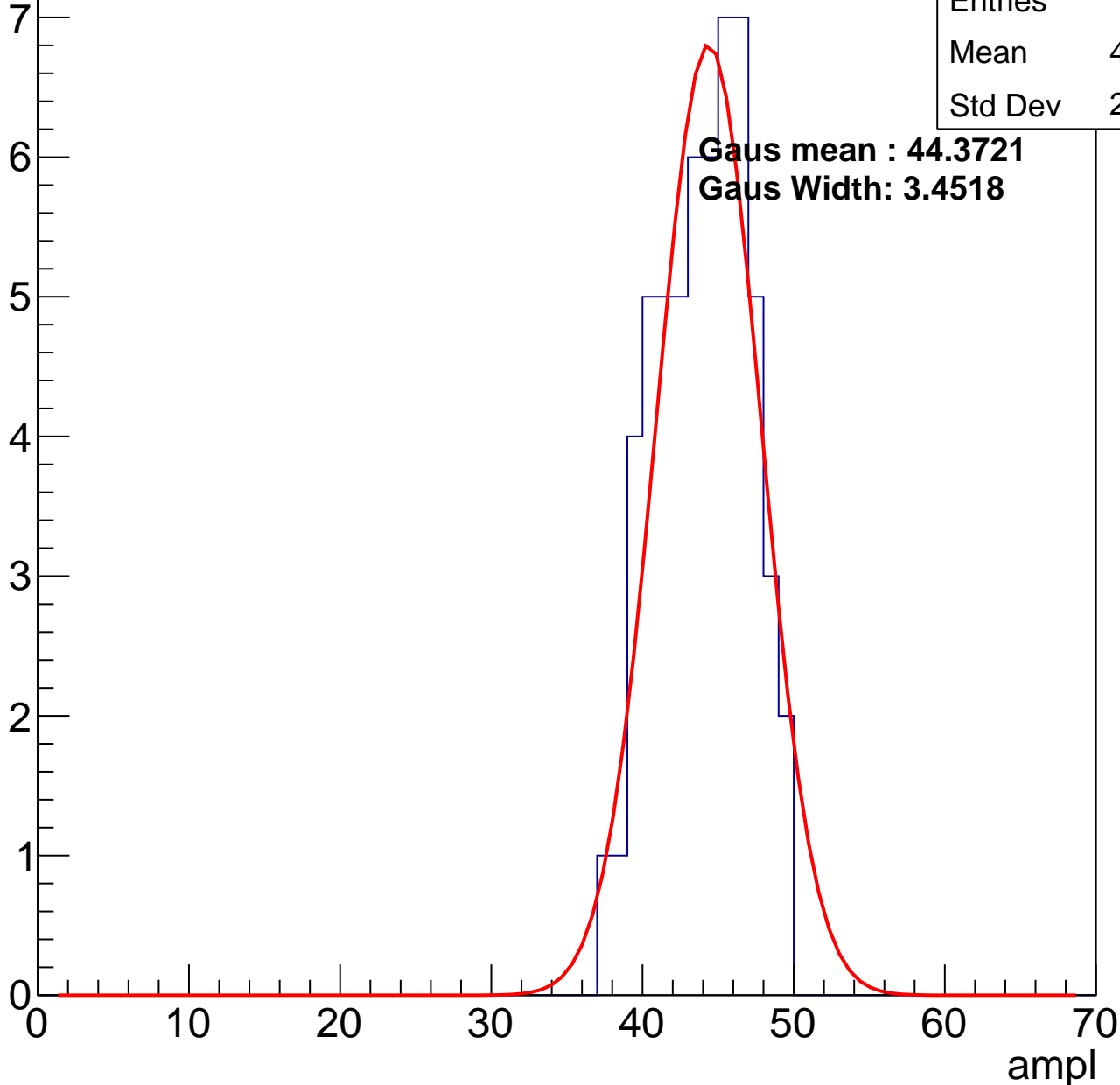
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.54
Std Dev	2.968

Gaus mean : 44.3721

Gaus Width: 3.4518



B0L001S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

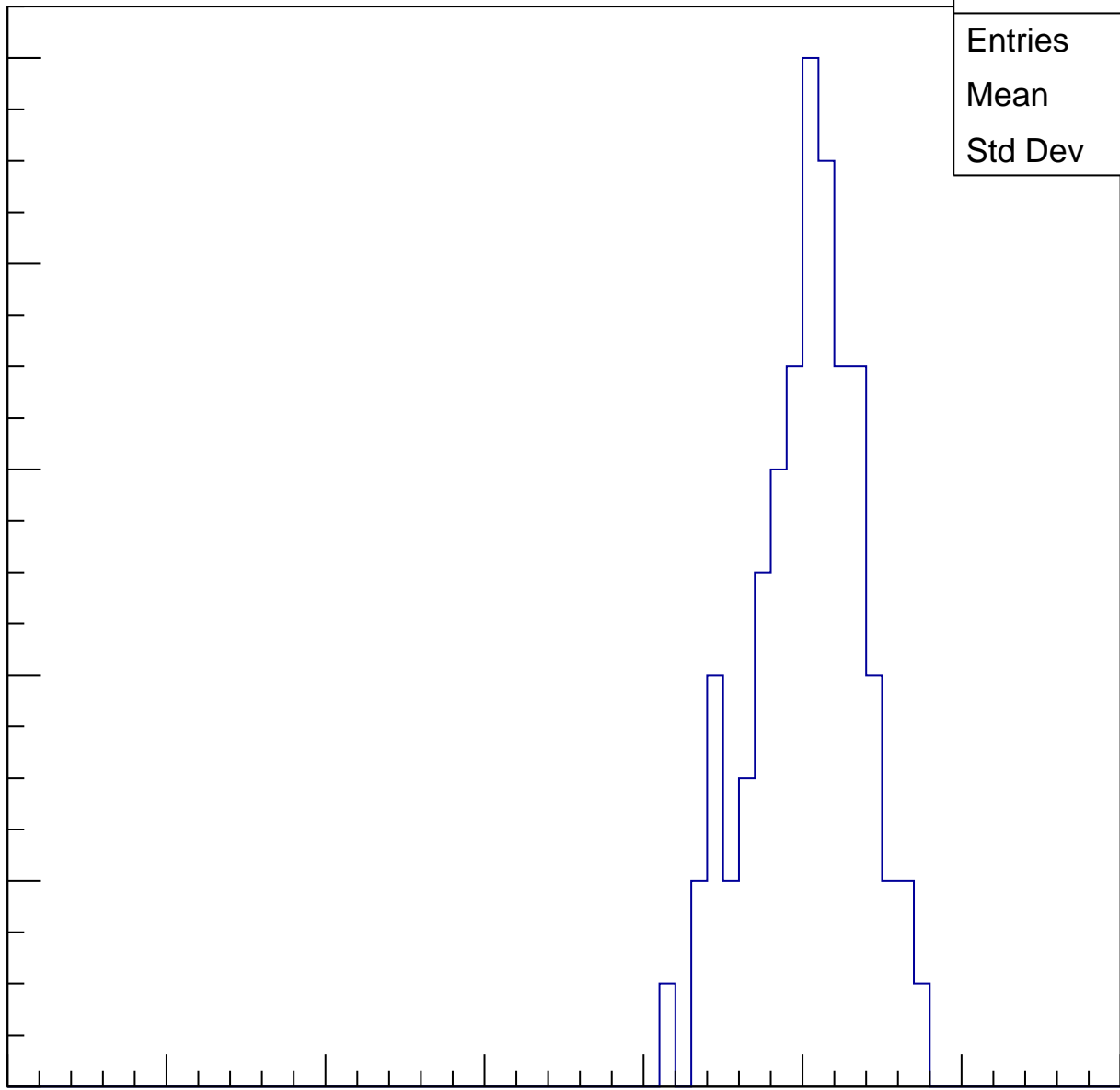
Entries	72
Mean	49.81
Std Dev	3.394

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

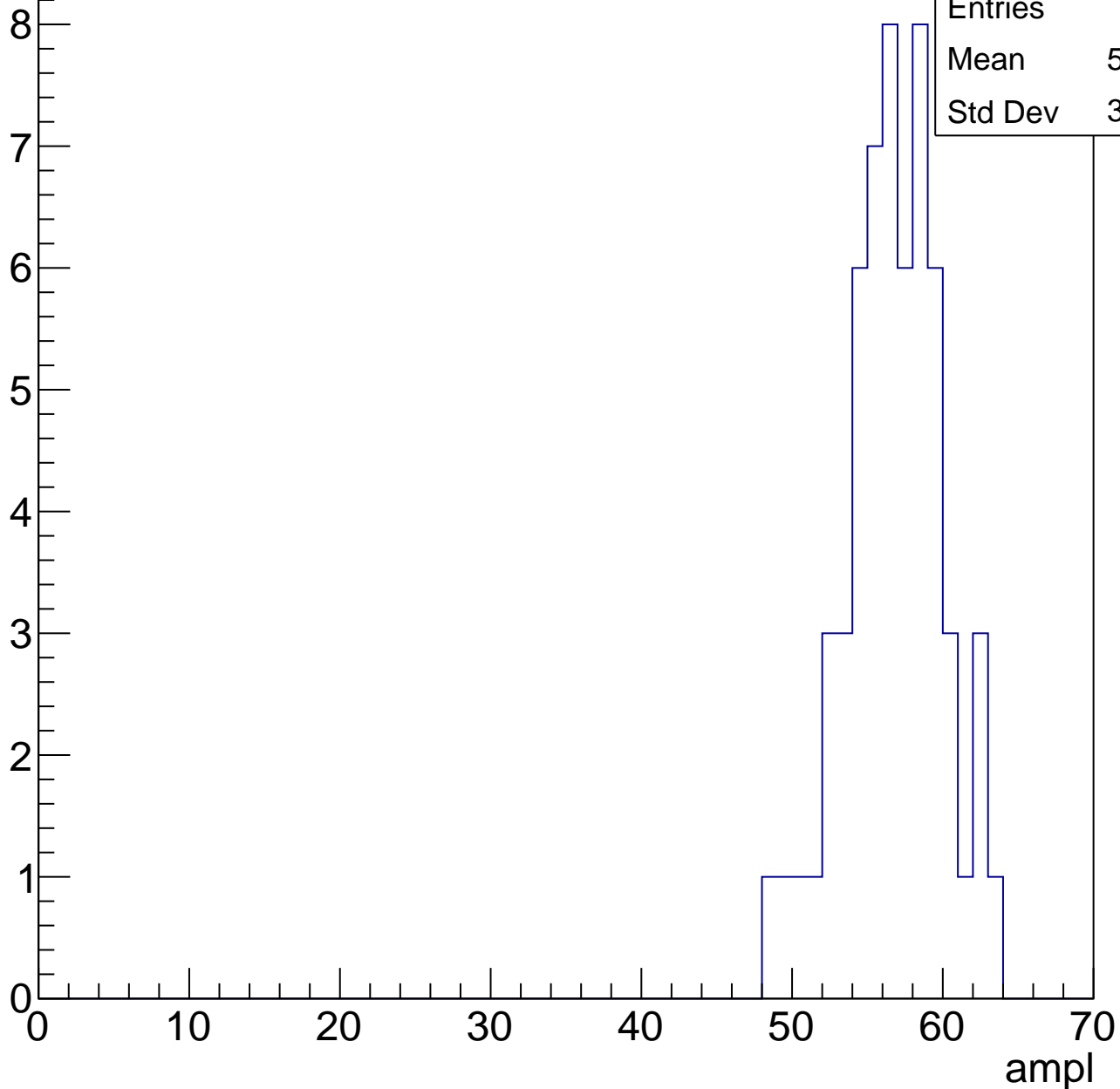


B0L001S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.27
Std Dev	3.183

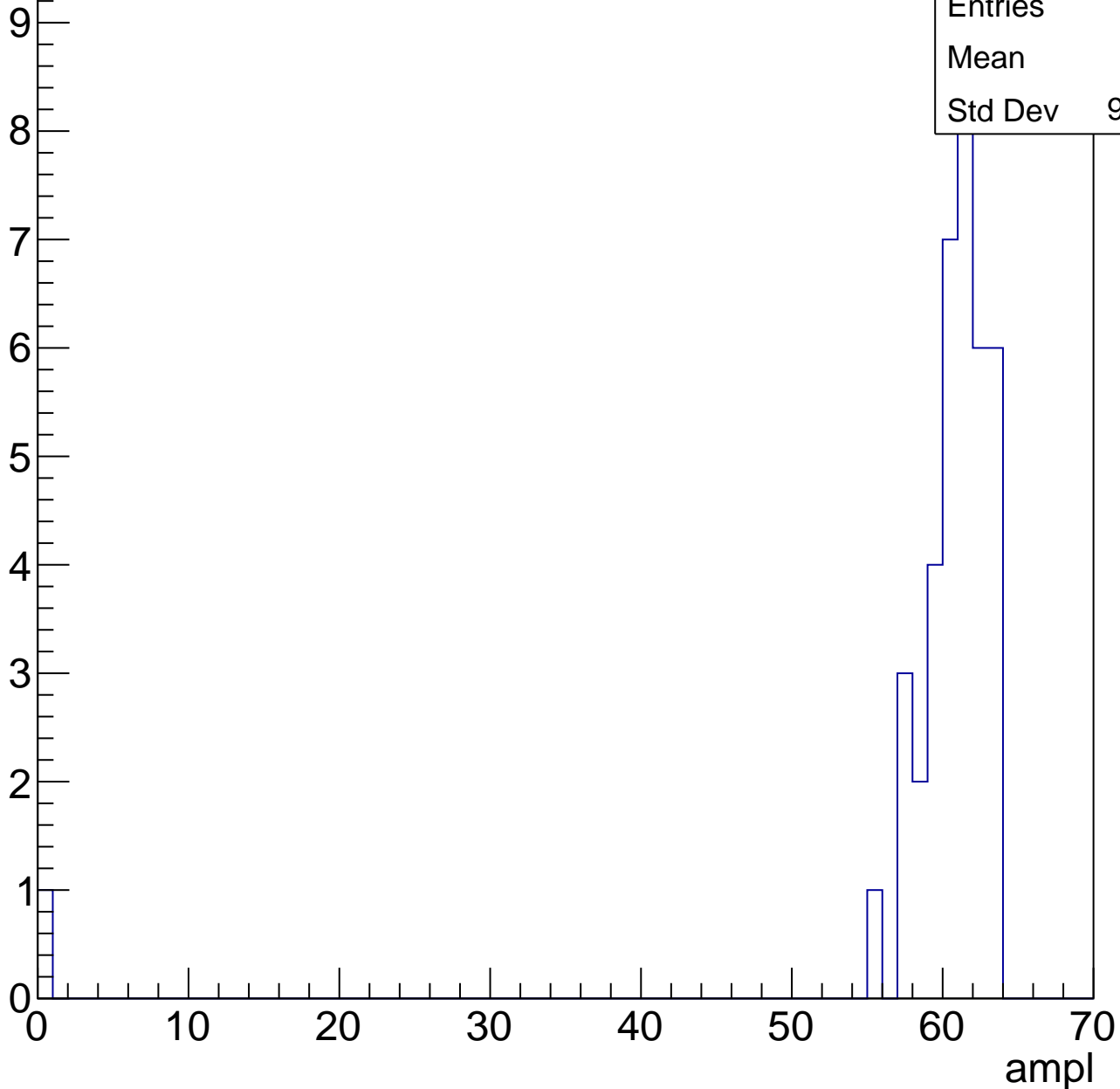


B0L001S, U15-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.9
Std Dev	9.745



B0L001S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch51, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	30.55
Std Dev	3.858

Gaus mean : 31.3983

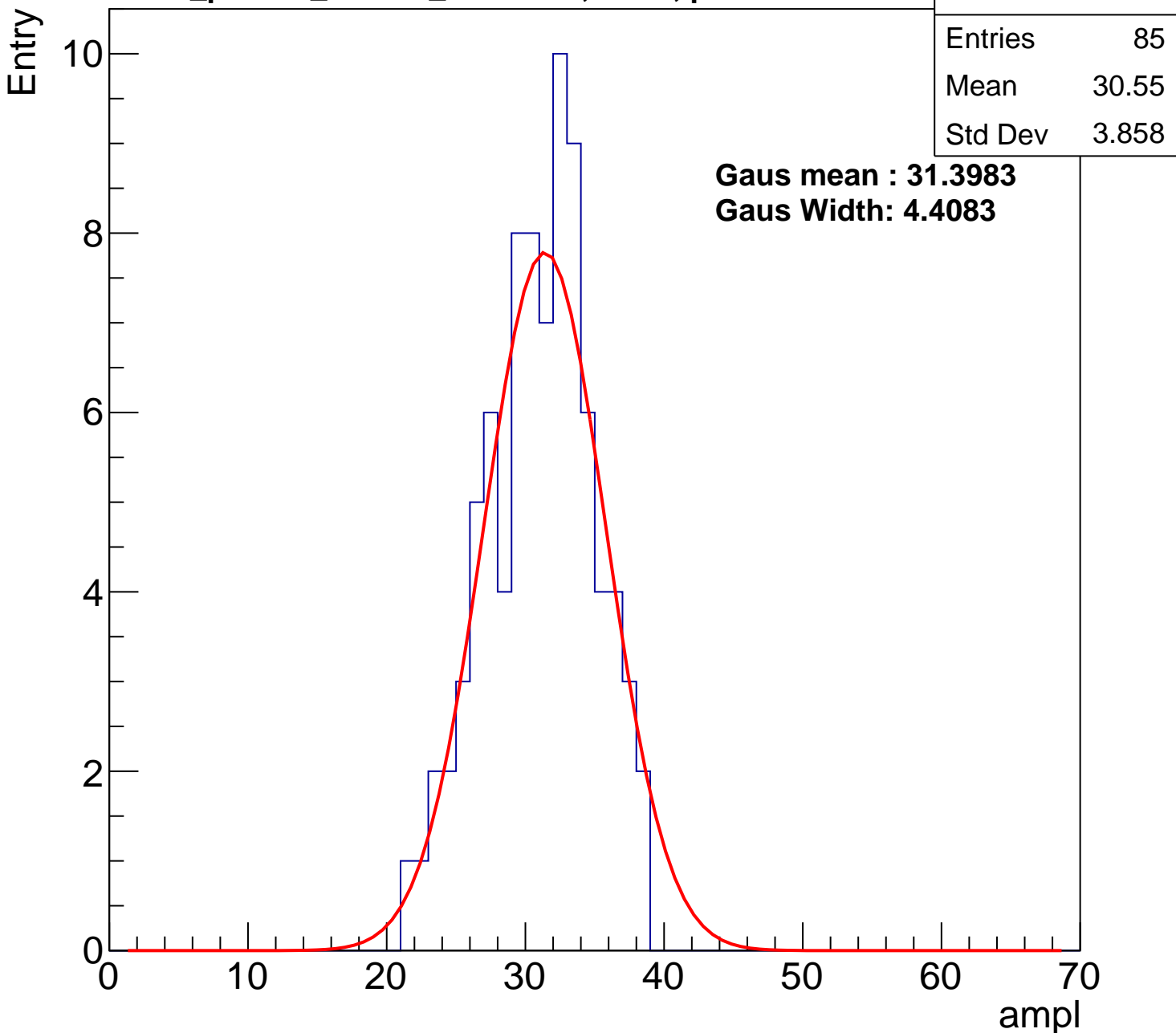
Gaus Width: 4.4083

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch51, adc1

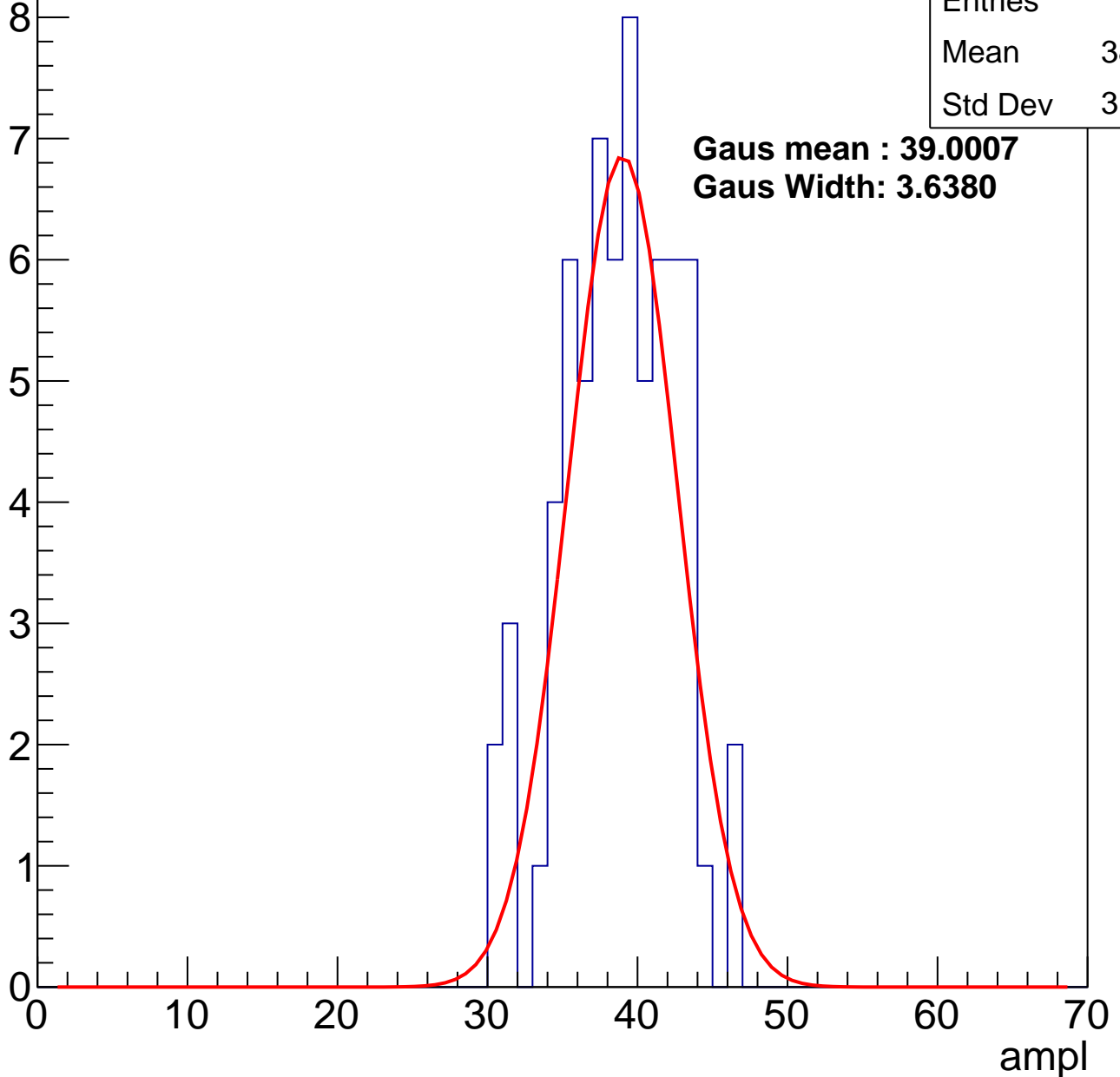
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.28
Std Dev	3.702

Gaus mean : 39.0007

Gaus Width: 3.6380



B0L001S, U15-ch51, adc2

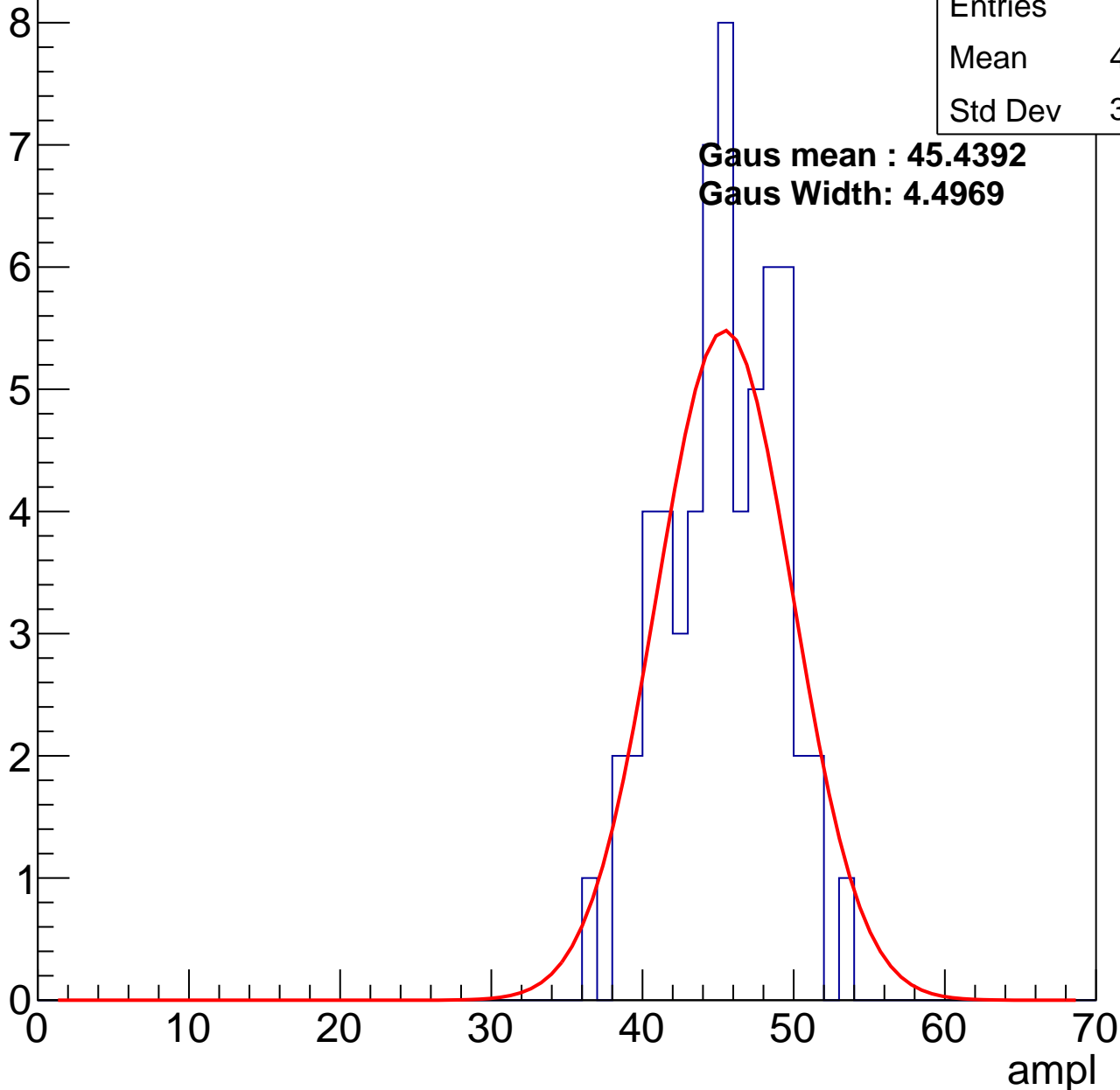
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.85
Std Dev	3.675

Gaus mean : 45.4392

Gaus Width: 4.4969

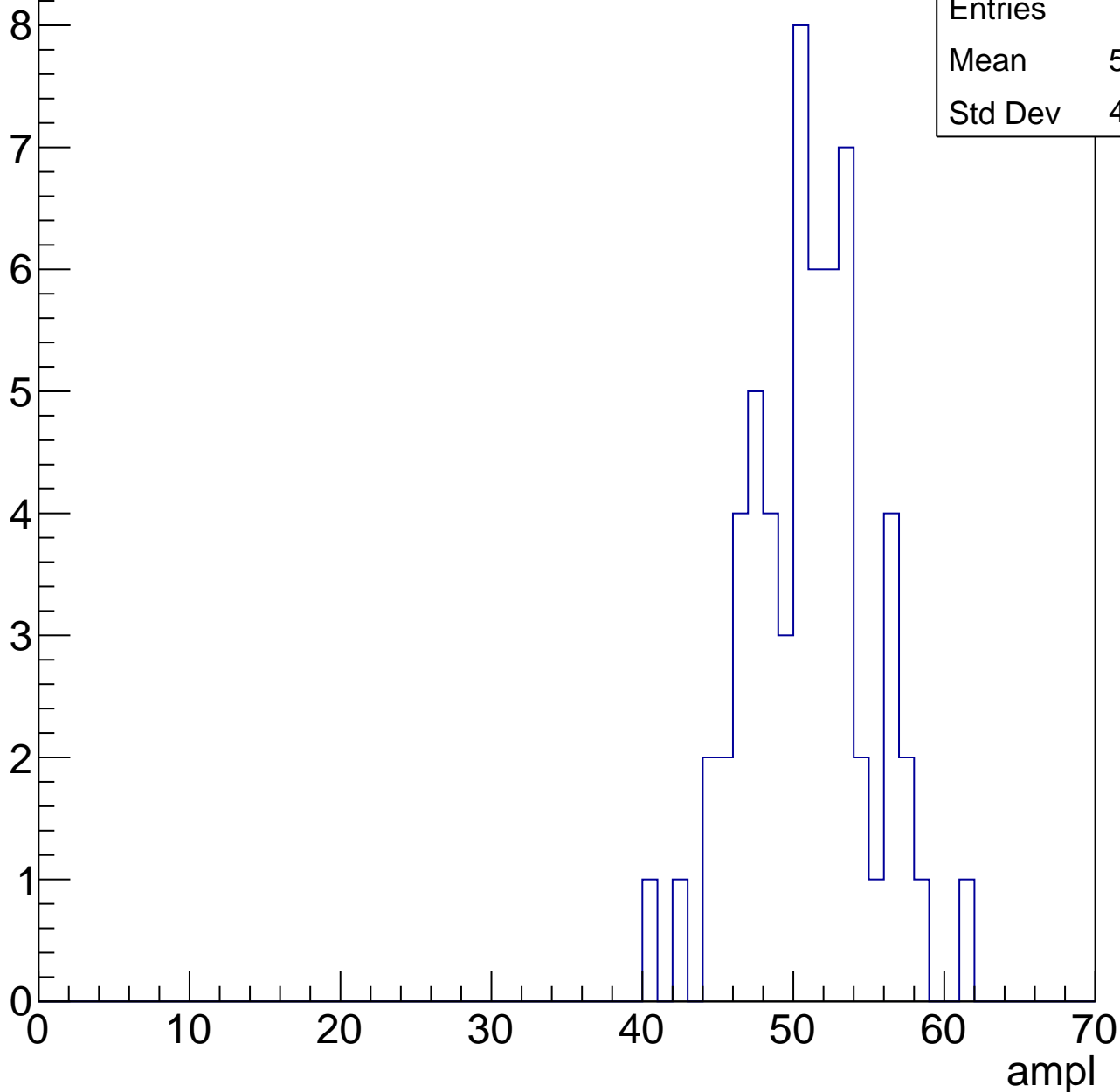


B0L001S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.45
Std Dev	4.039

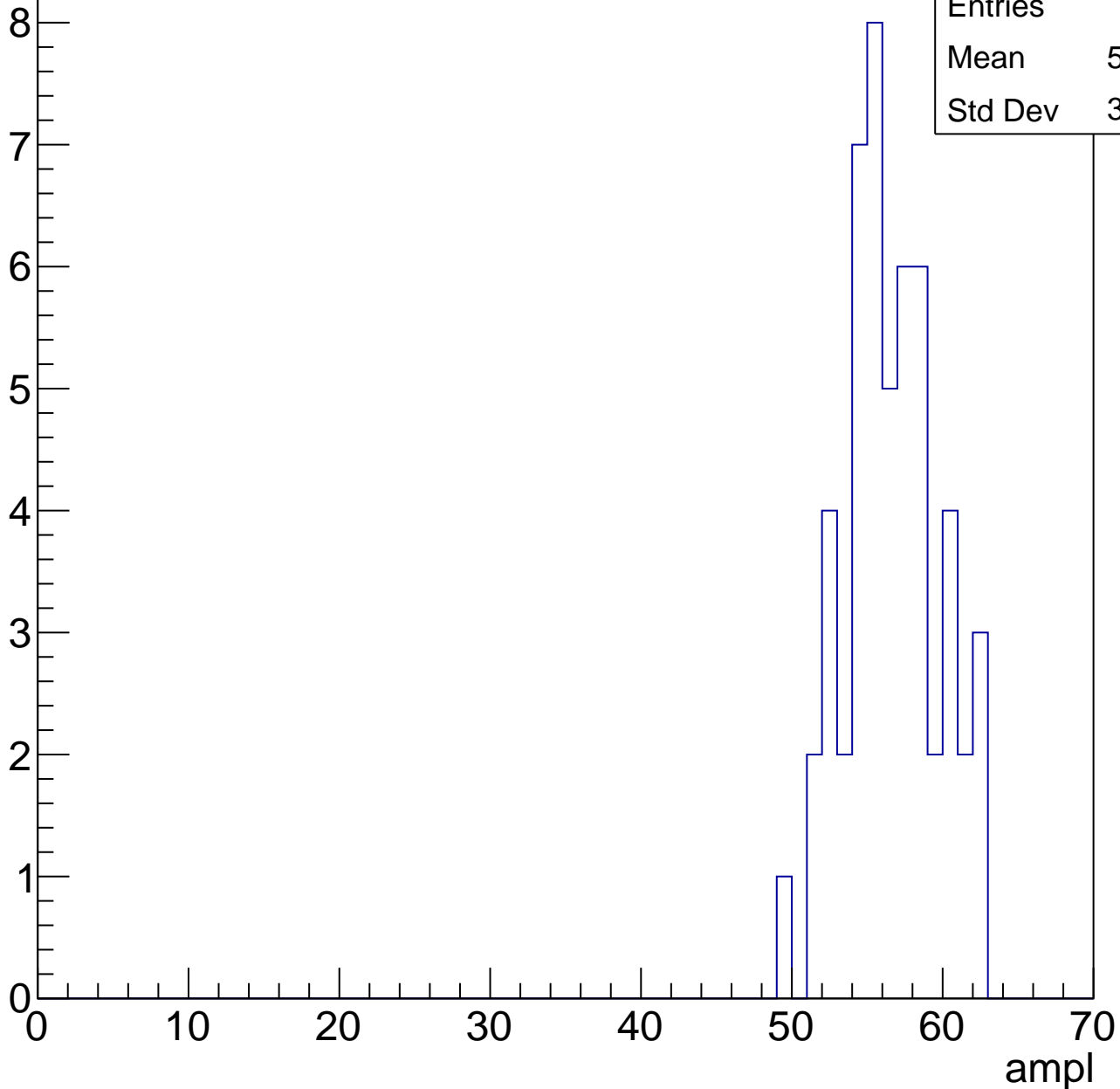


B0L001S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.13
Std Dev	3.057

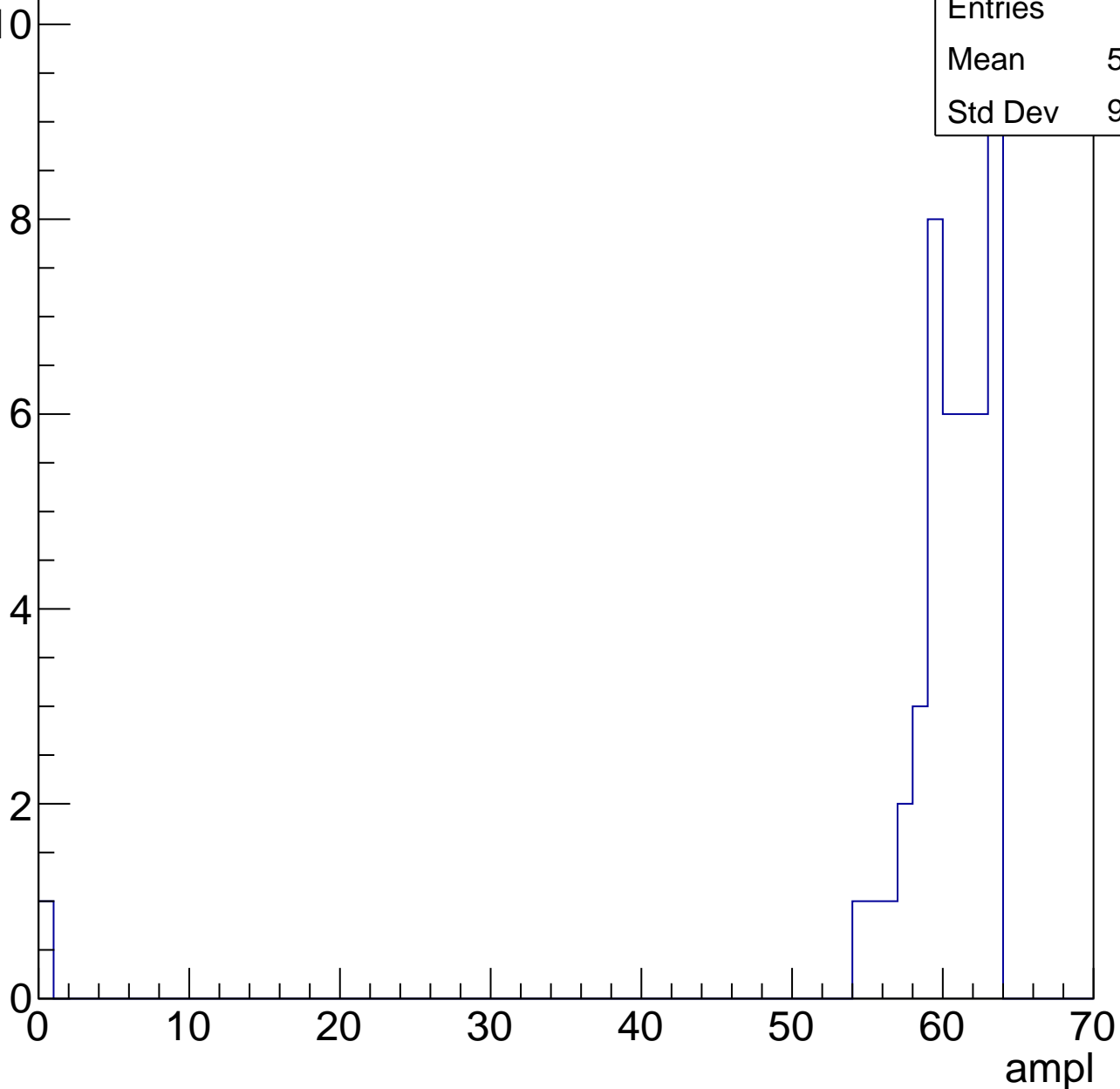


B0L001S, U15-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.96
Std Dev	9.172



B0L001S, U15-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch52, adc0

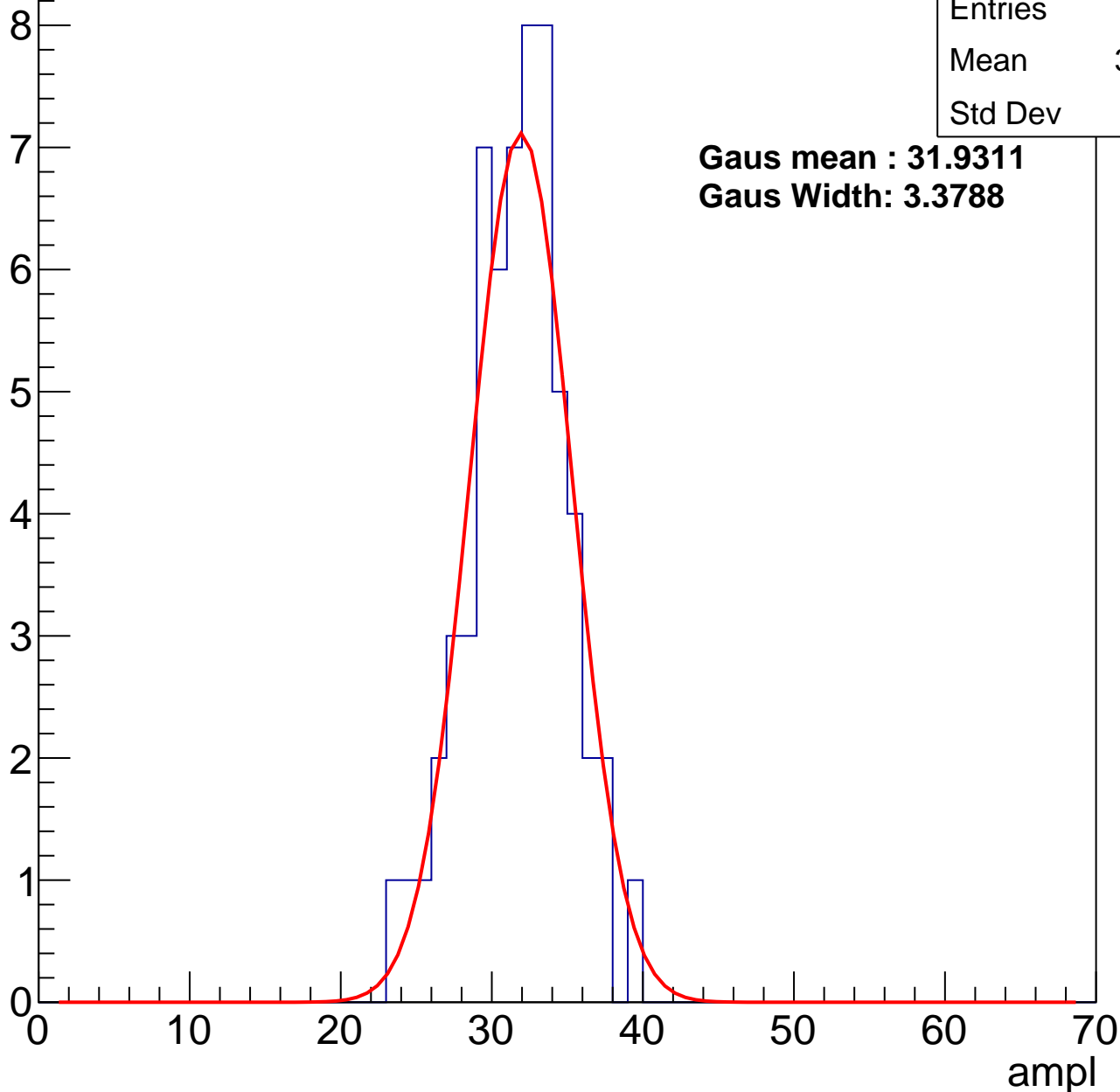
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.21
Std Dev	3.24

Gaus mean : 31.9311

Gaus Width: 3.3788



B0L001S, U15-ch52, adc1

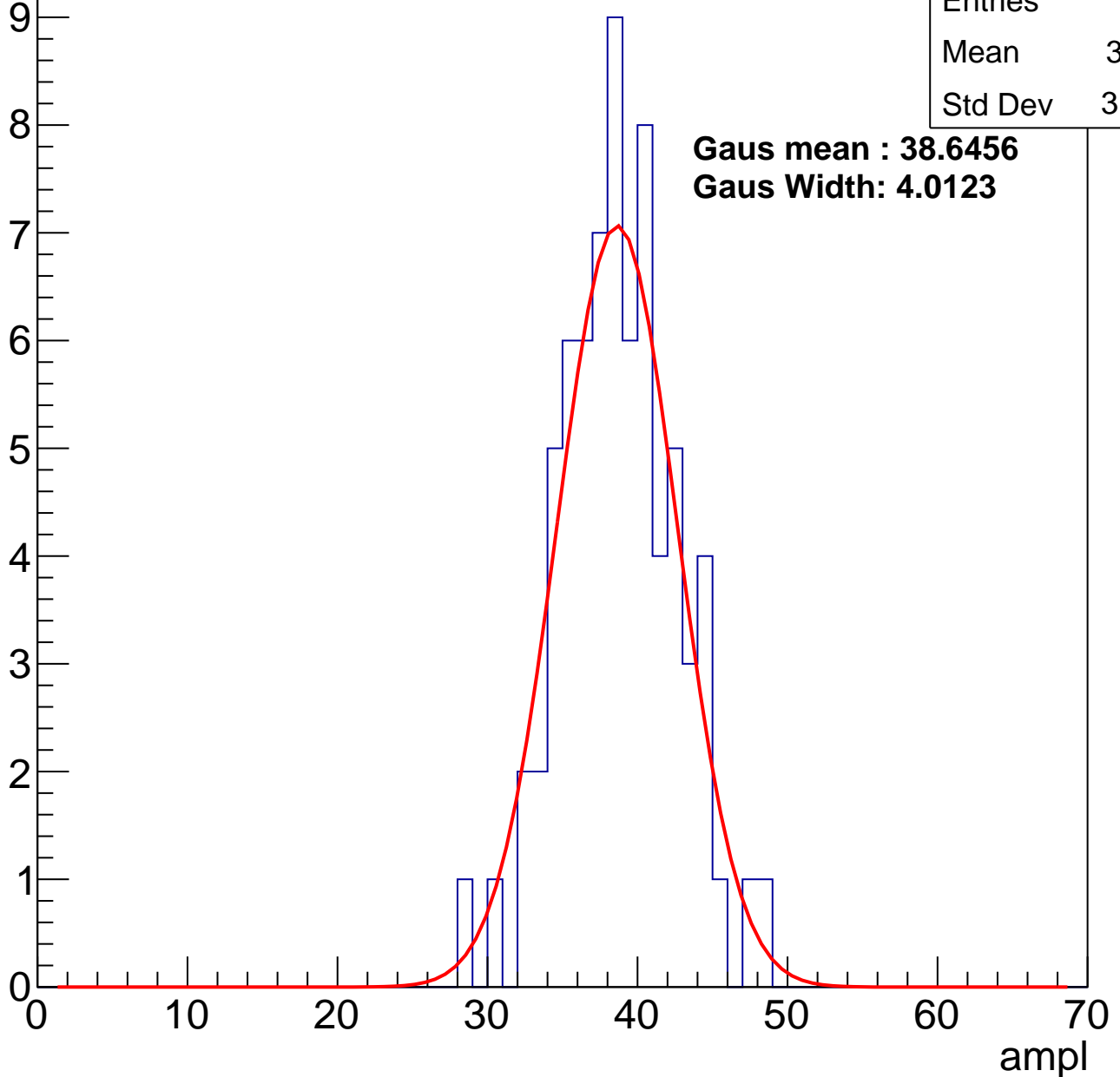
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.31
Std Dev	3.814

Gaus mean : 38.6456

Gaus Width: 4.0123



B0L001S, U15-ch52, adc2

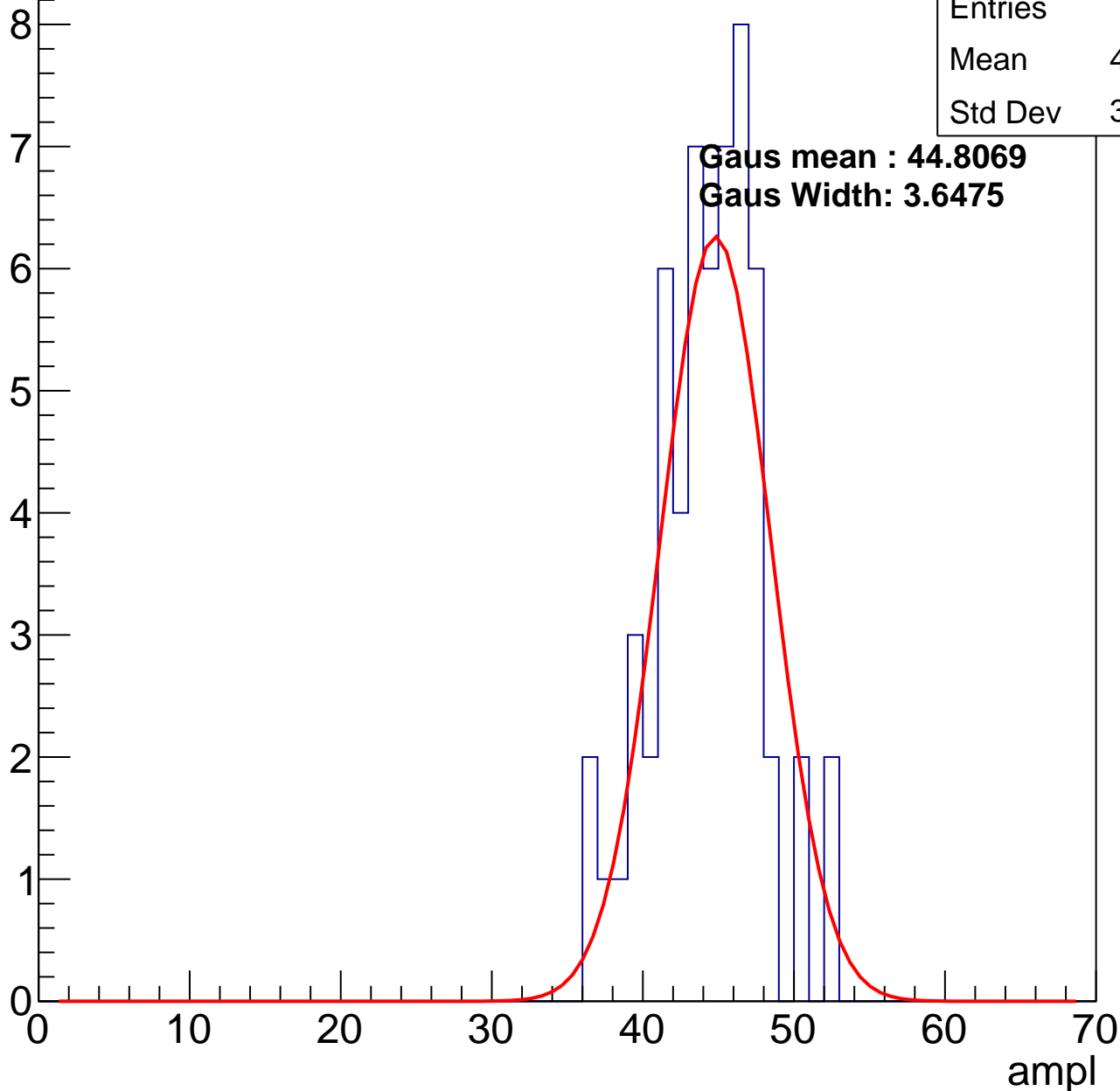
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.86
Std Dev	3.496

Gaus mean : 44.8069

Gaus Width: 3.6475

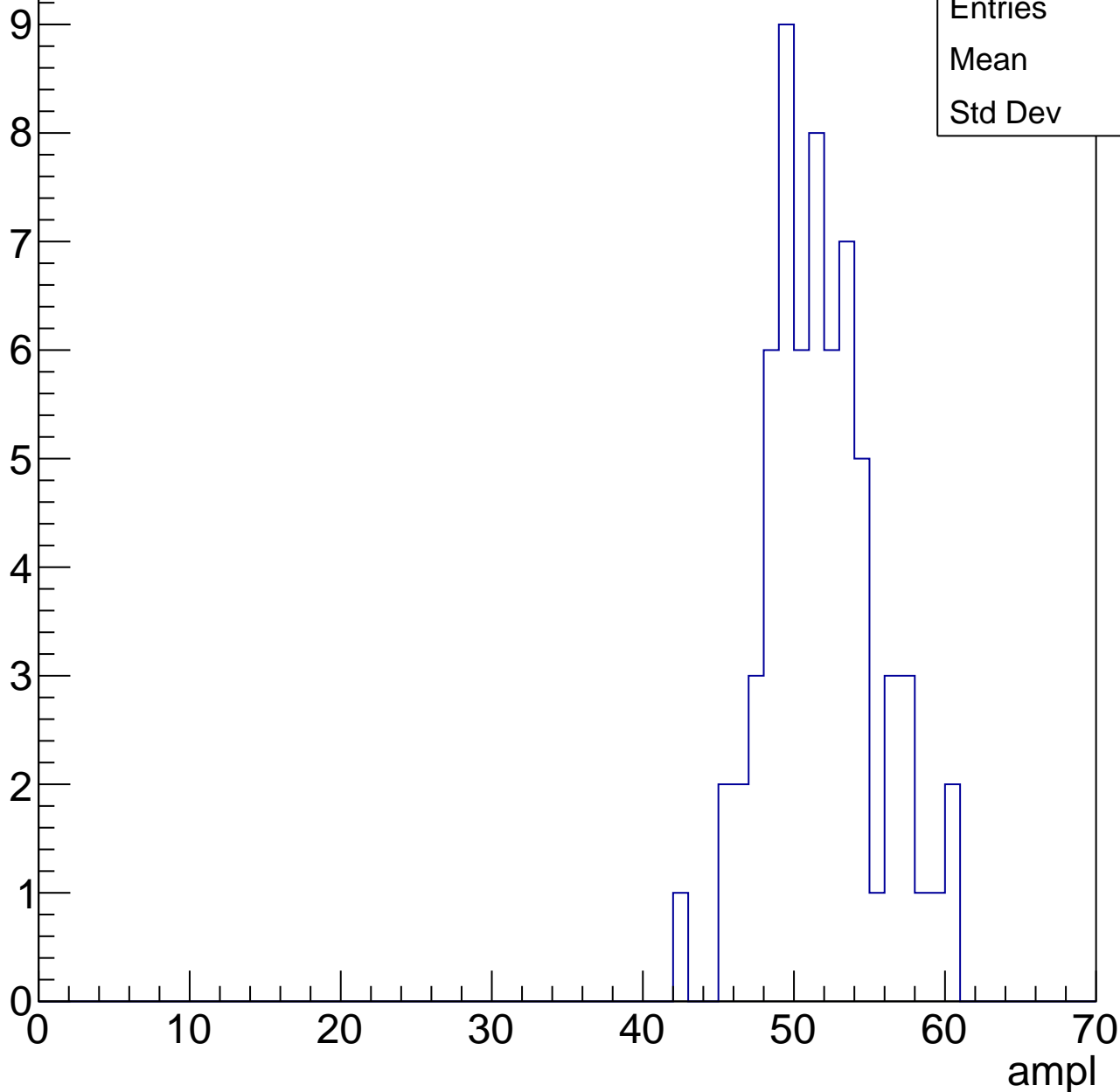


B0L001S, U15-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.3
Std Dev	3.68



B0L001S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 55.81

Std Dev 3.333

0

10

20

30

40

50

60

ampl

0

1

2

3

4

5

6

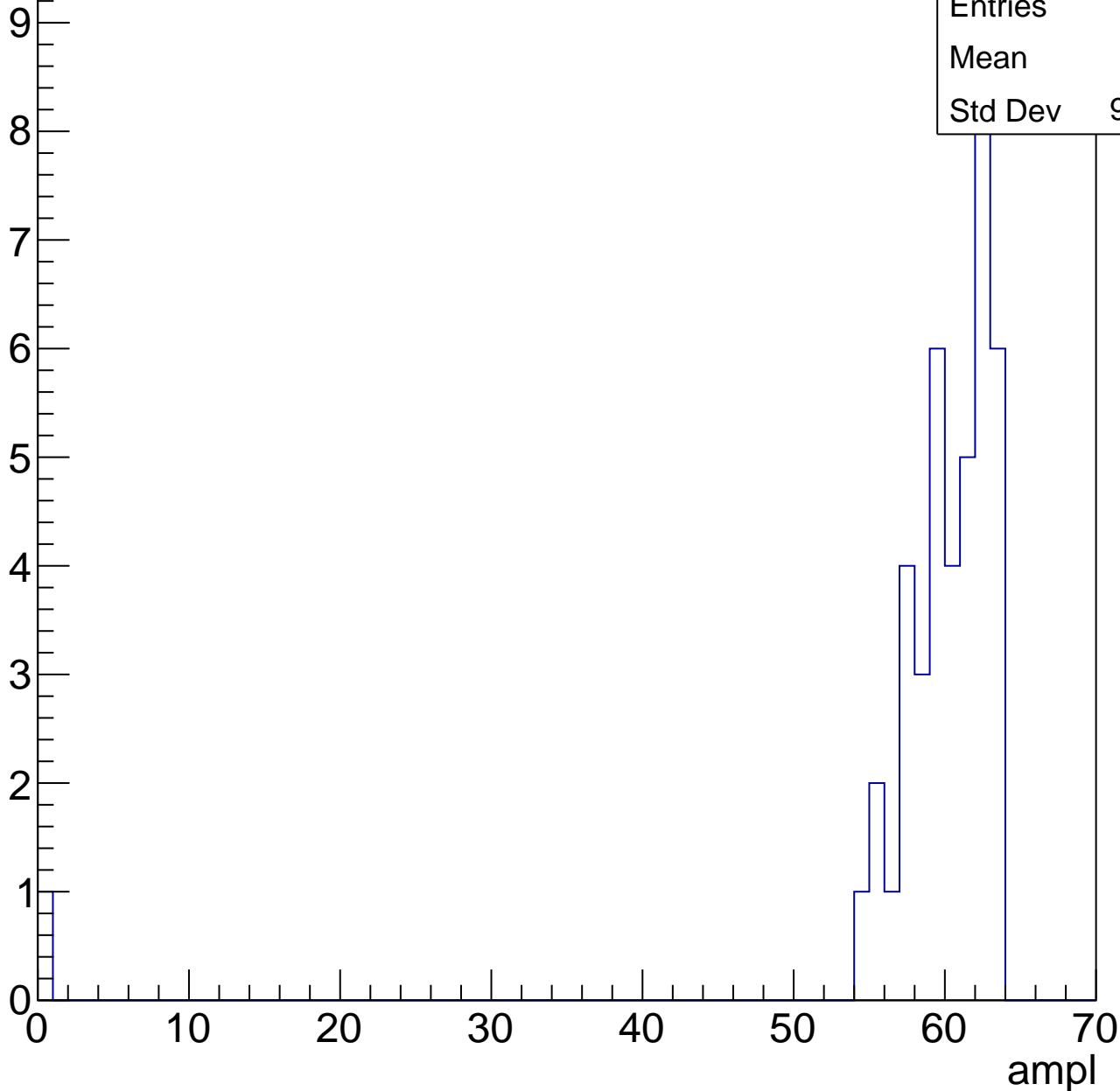
7

B0L001S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.5
Std Dev	9.457



B0L001S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L001S, U15-ch53, adc0

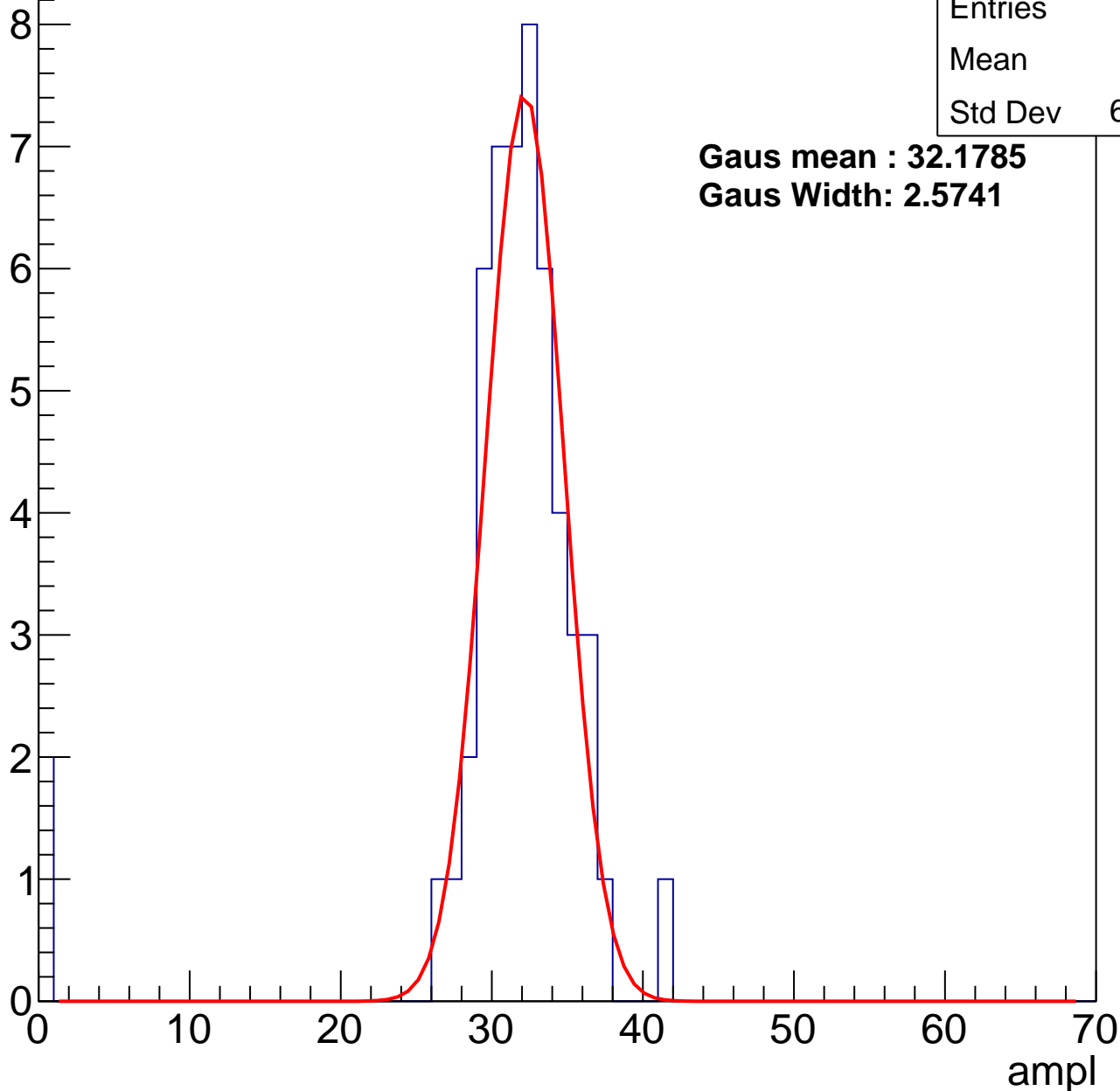
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	30.6
Std Dev	6.695

Gaus mean : 32.1785

Gaus Width: 2.5741



B0L001S, U15-ch53, adc1

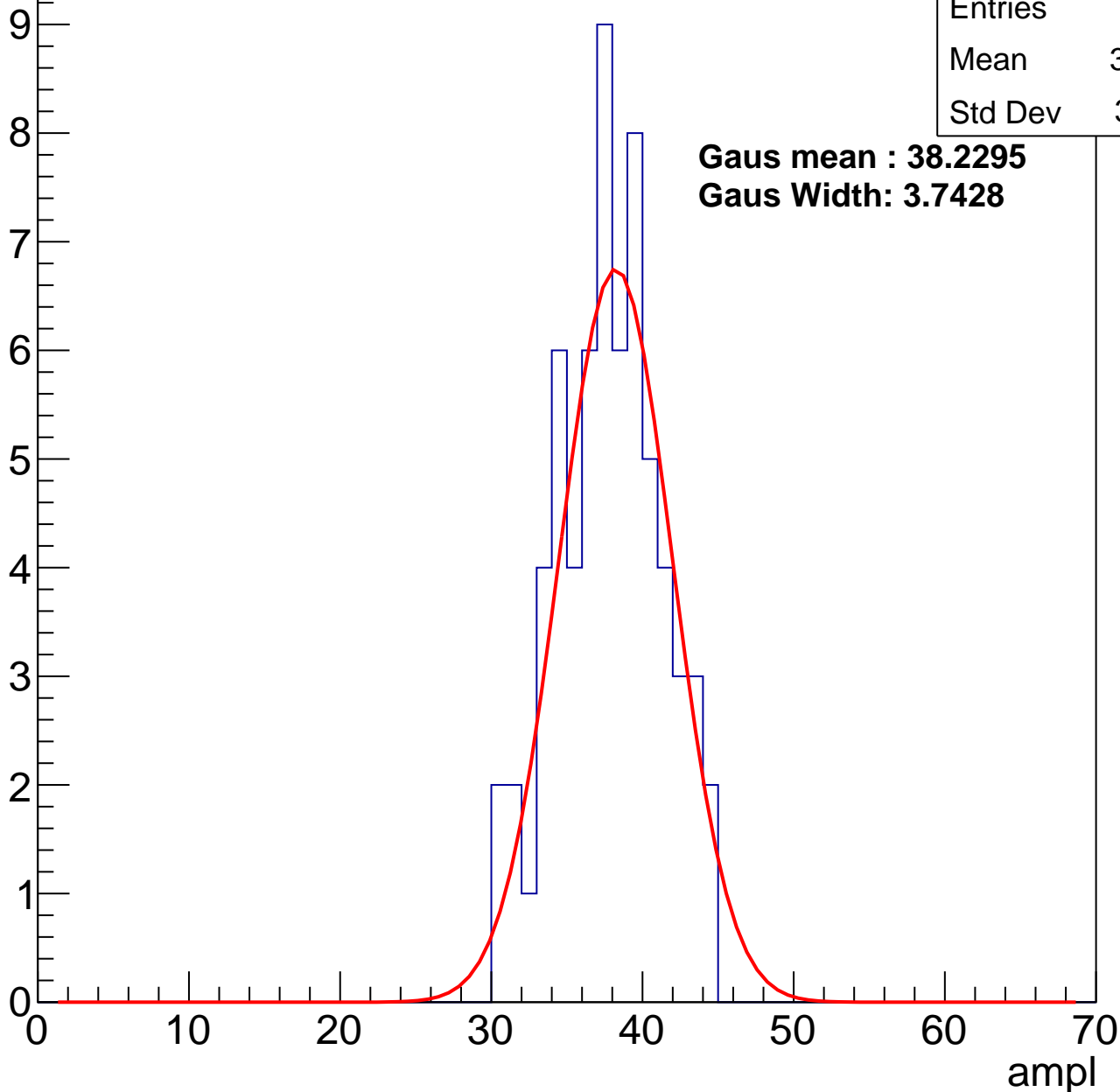
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.32
Std Dev	3.411

Gaus mean : 38.2295

Gaus Width: 3.7428



B0L001S, U15-ch53, adc2

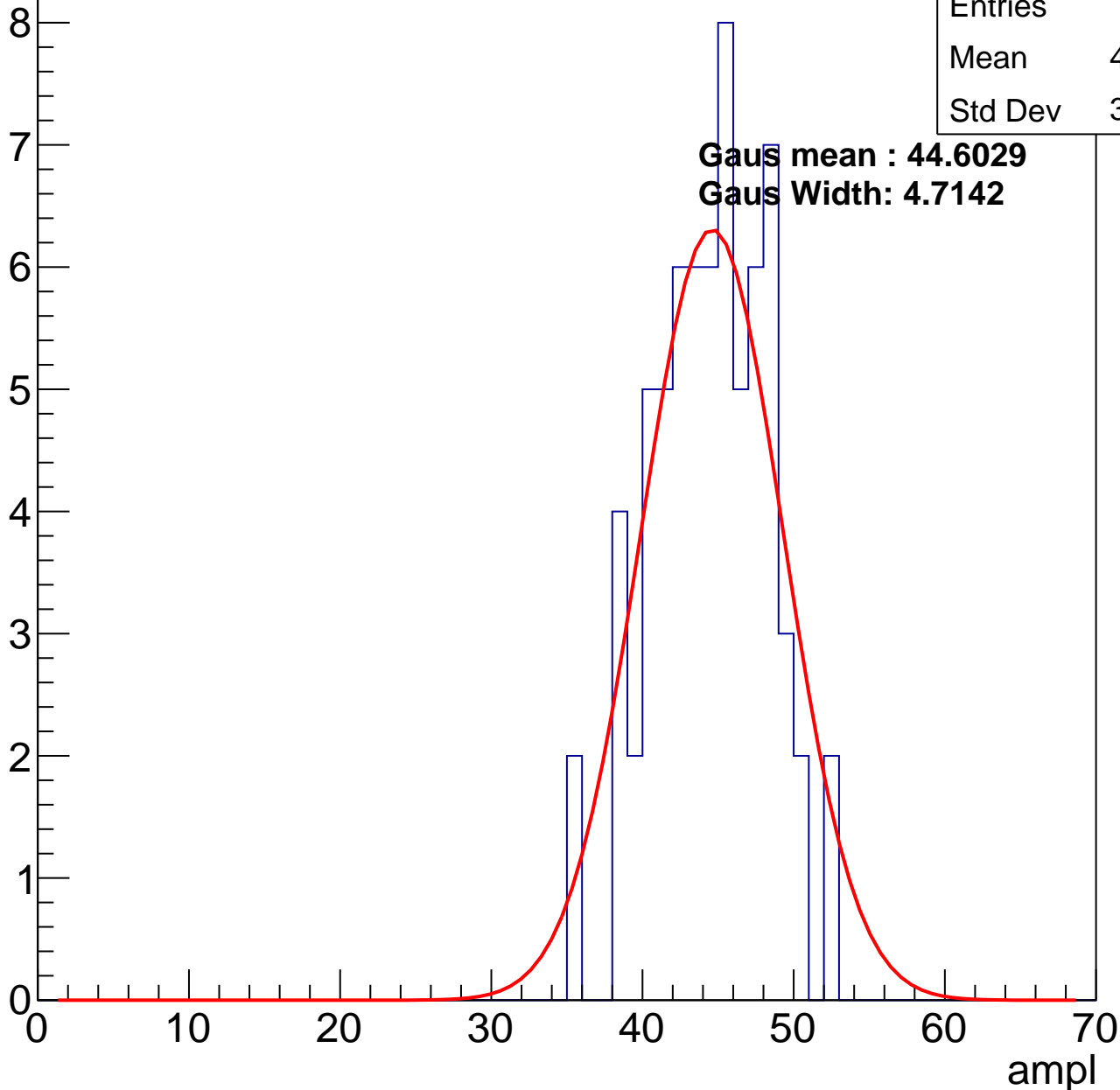
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.03
Std Dev	3.769

Gaus mean : 44.6029

Gaus Width: 4.7142

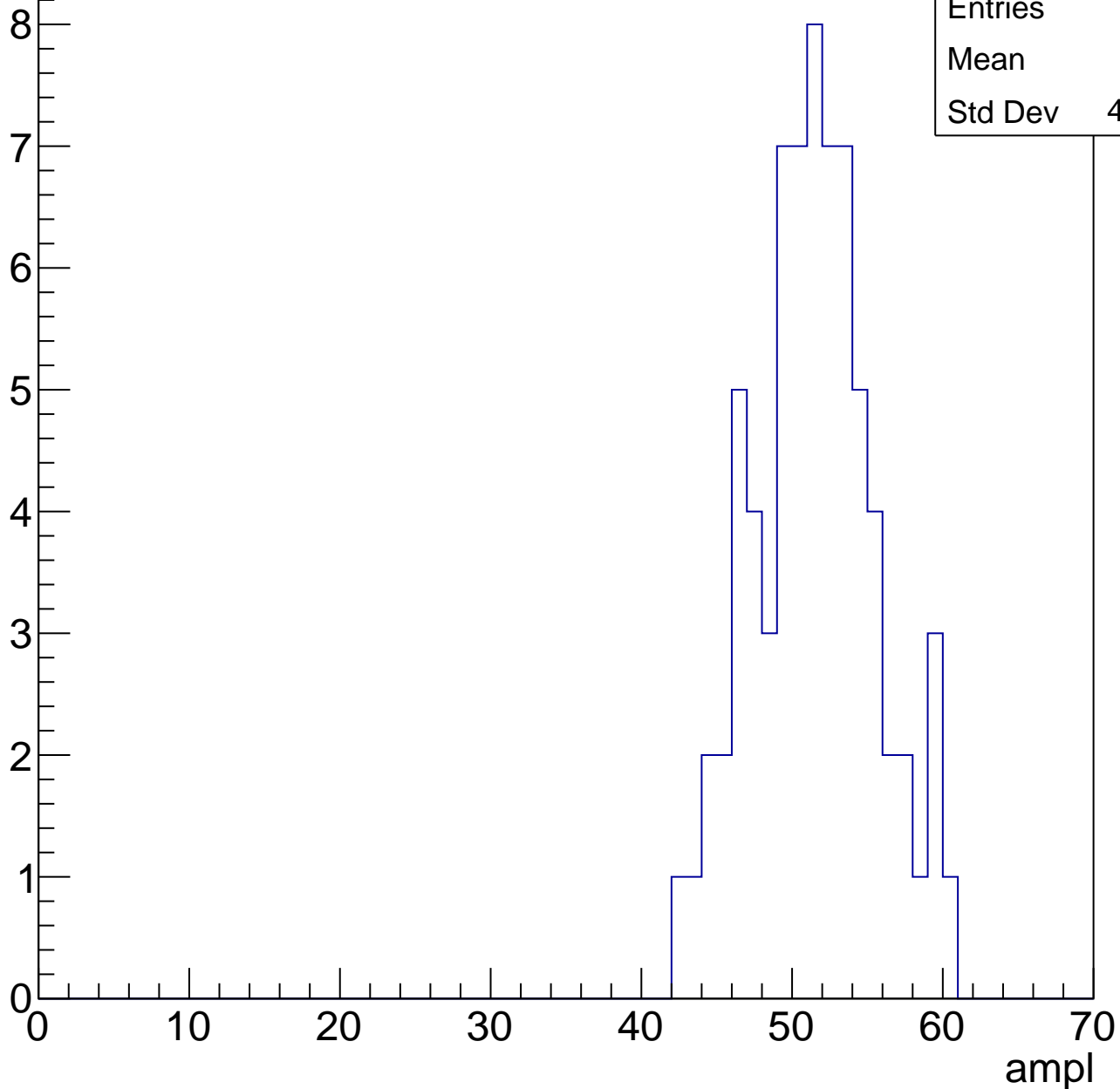


B0L001S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

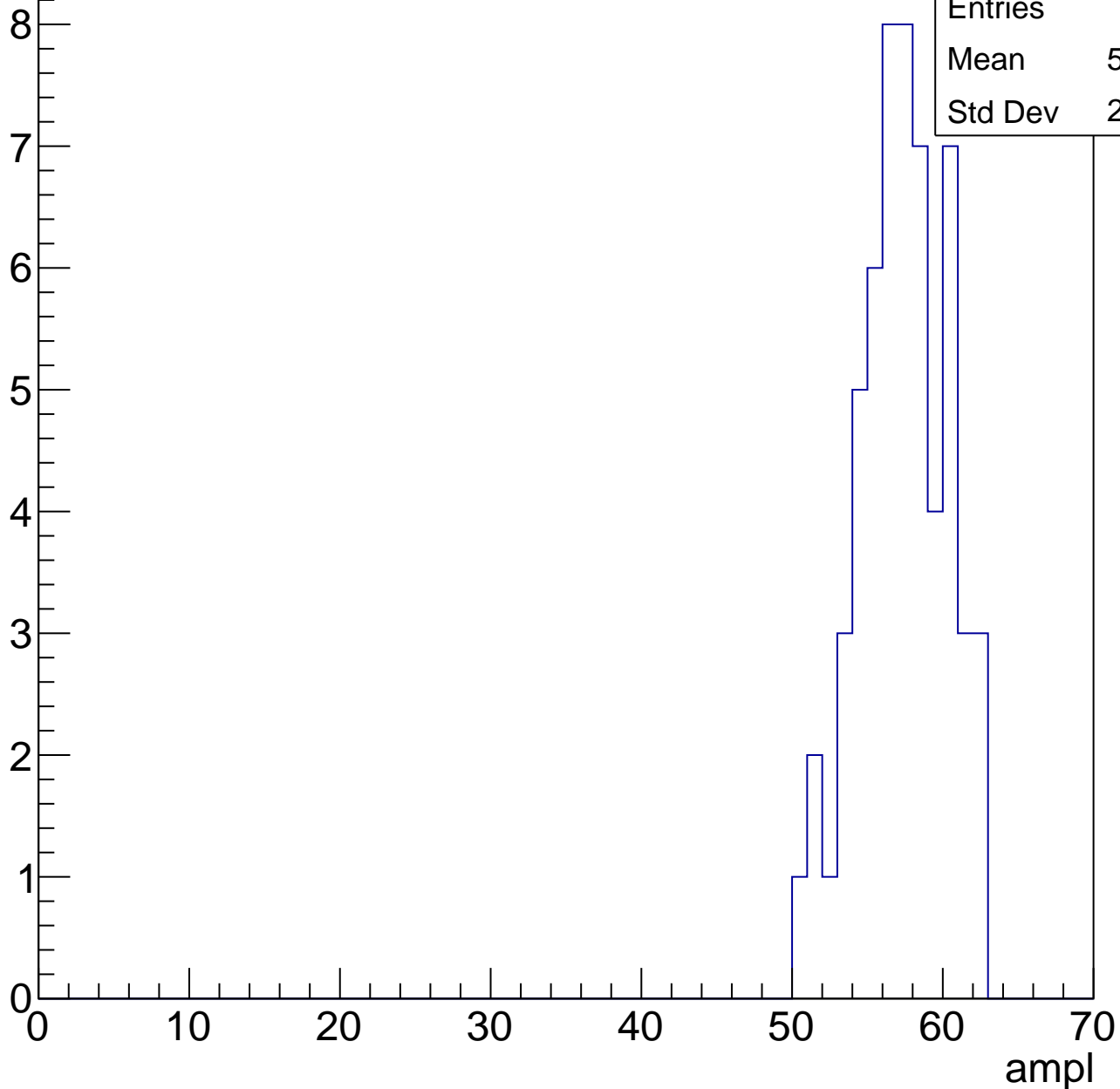
Entries	72
Mean	51
Std Dev	4.003



B0L001S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

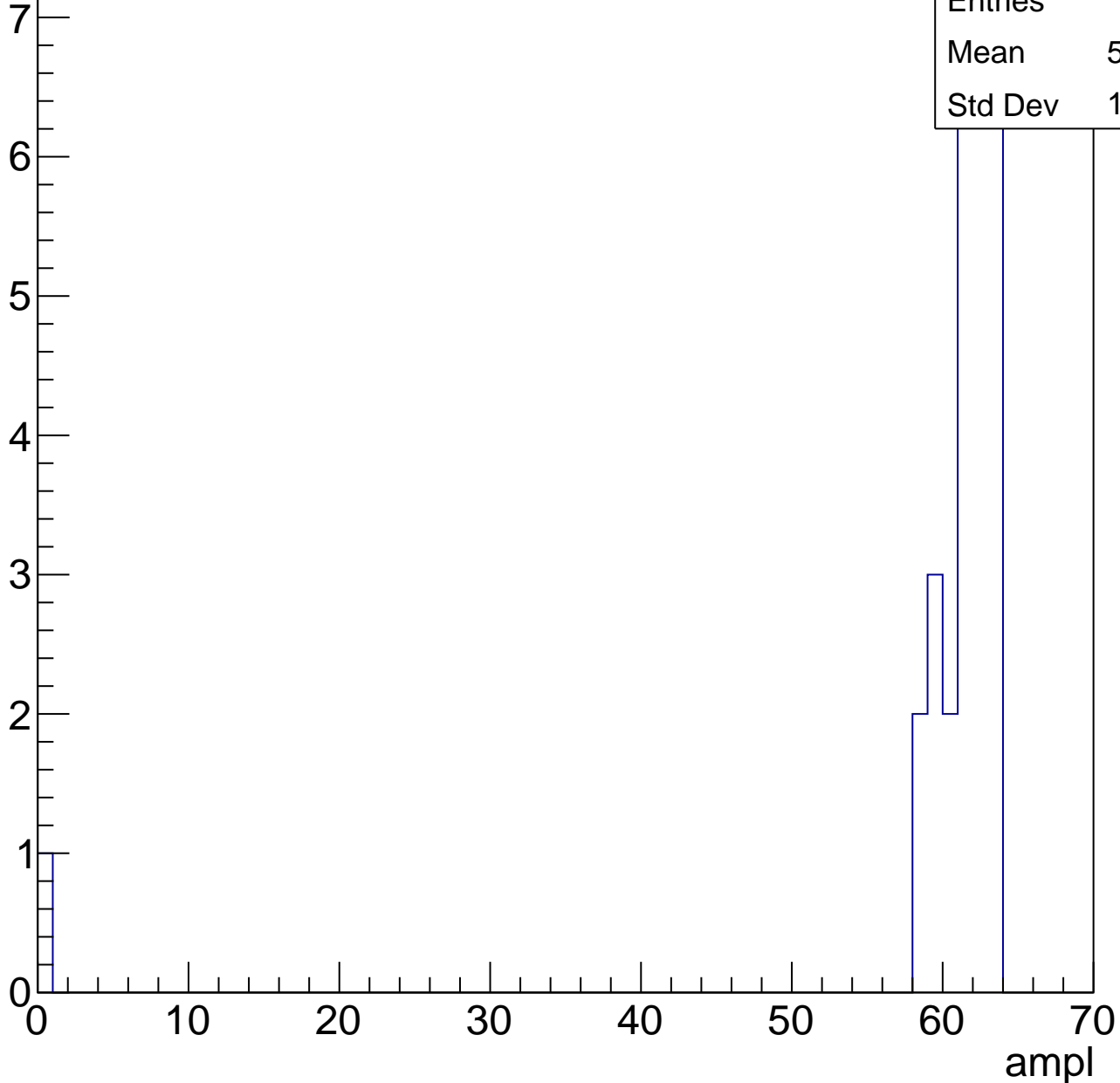


B0L001S, U15-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59.14
Std Dev	11.28



B0L001S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U15-ch54, adc0

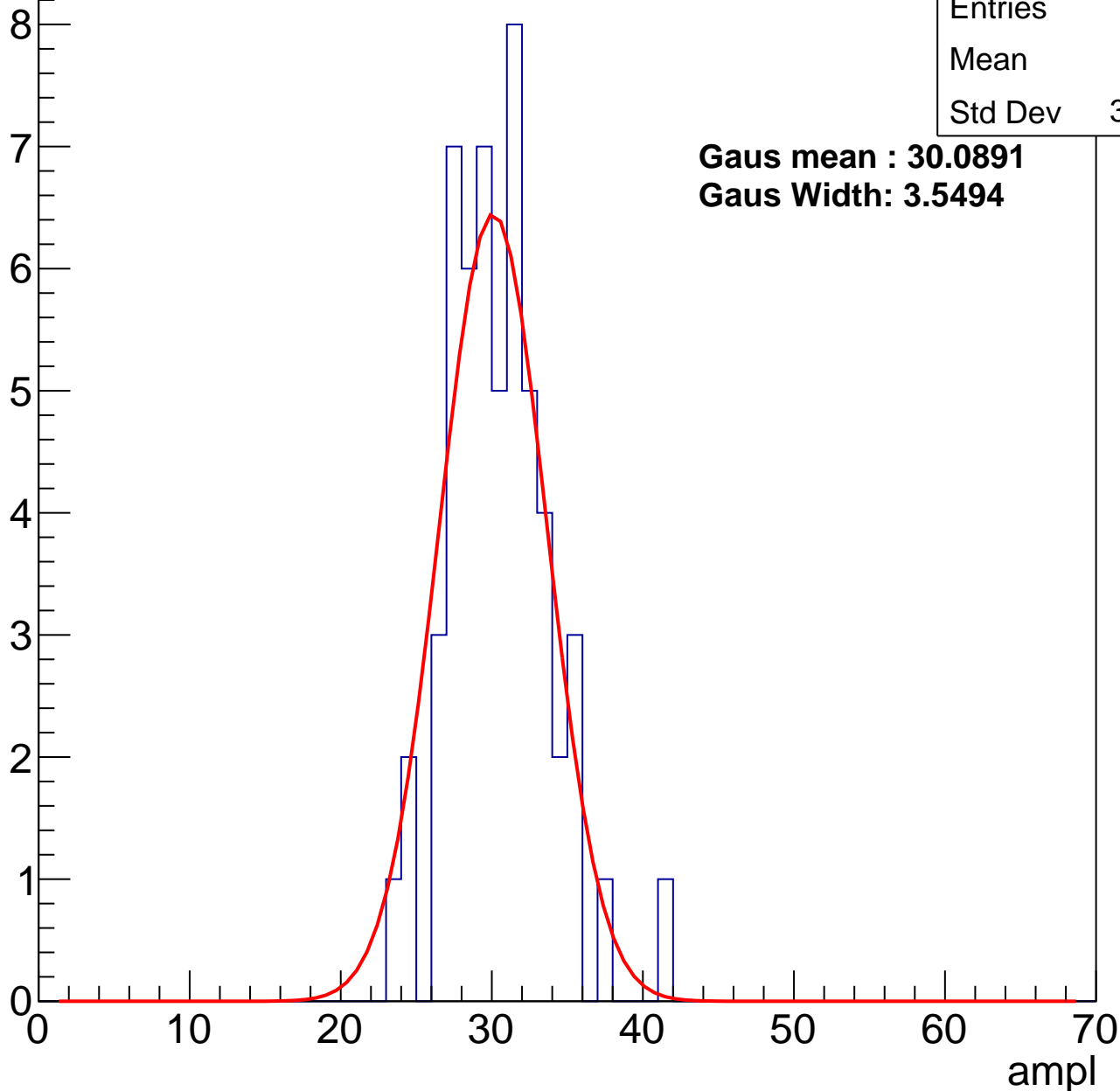
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	30
Std Dev	3.314

Gaus mean : 30.0891

Gaus Width: 3.5494



B0L001S, U15-ch54, adc1

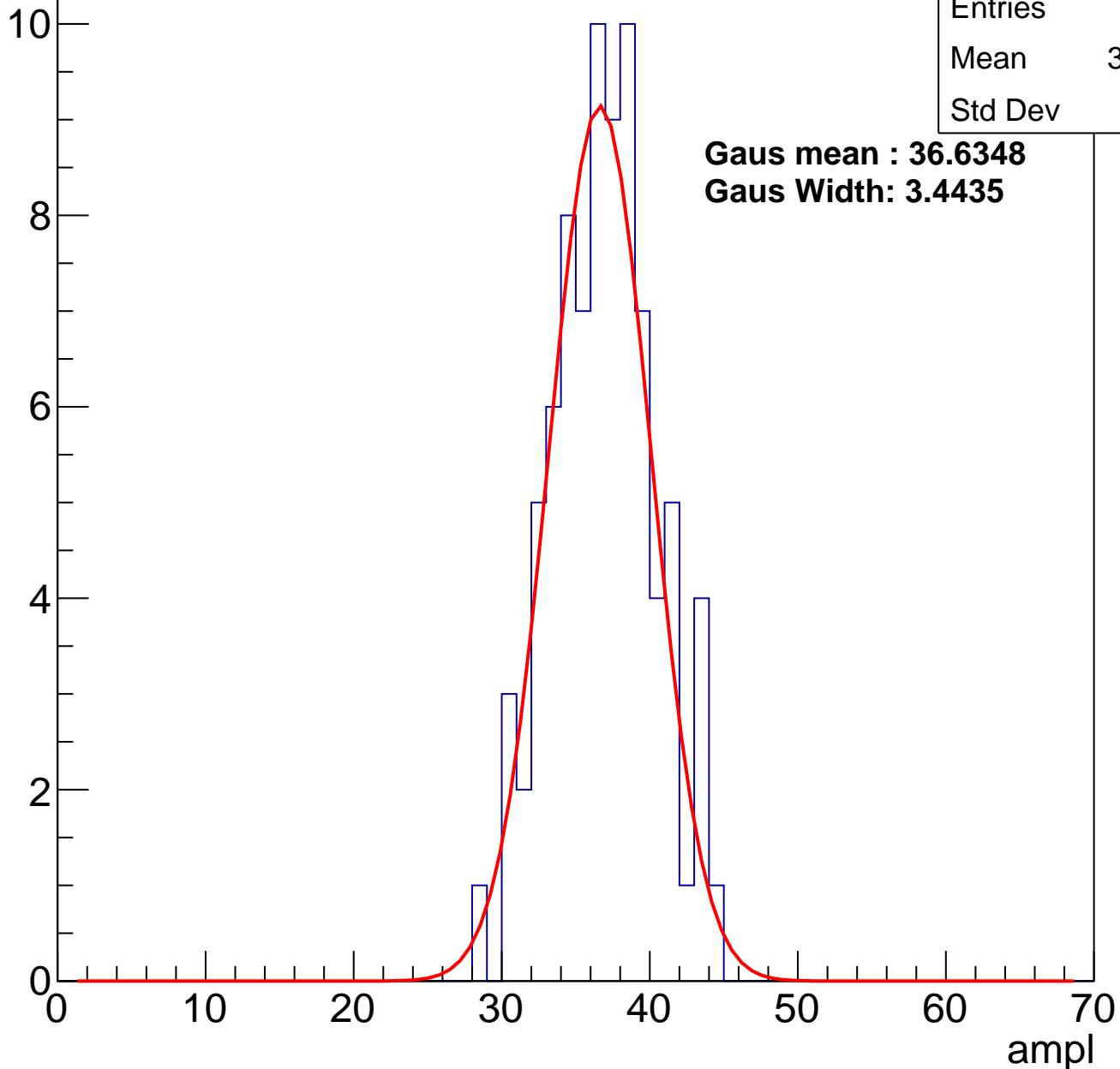
calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	36.43
Std Dev	3.43

Gaus mean : 36.6348

Gaus Width: 3.4435

Entry



B0L001S, U15-ch54, adc2

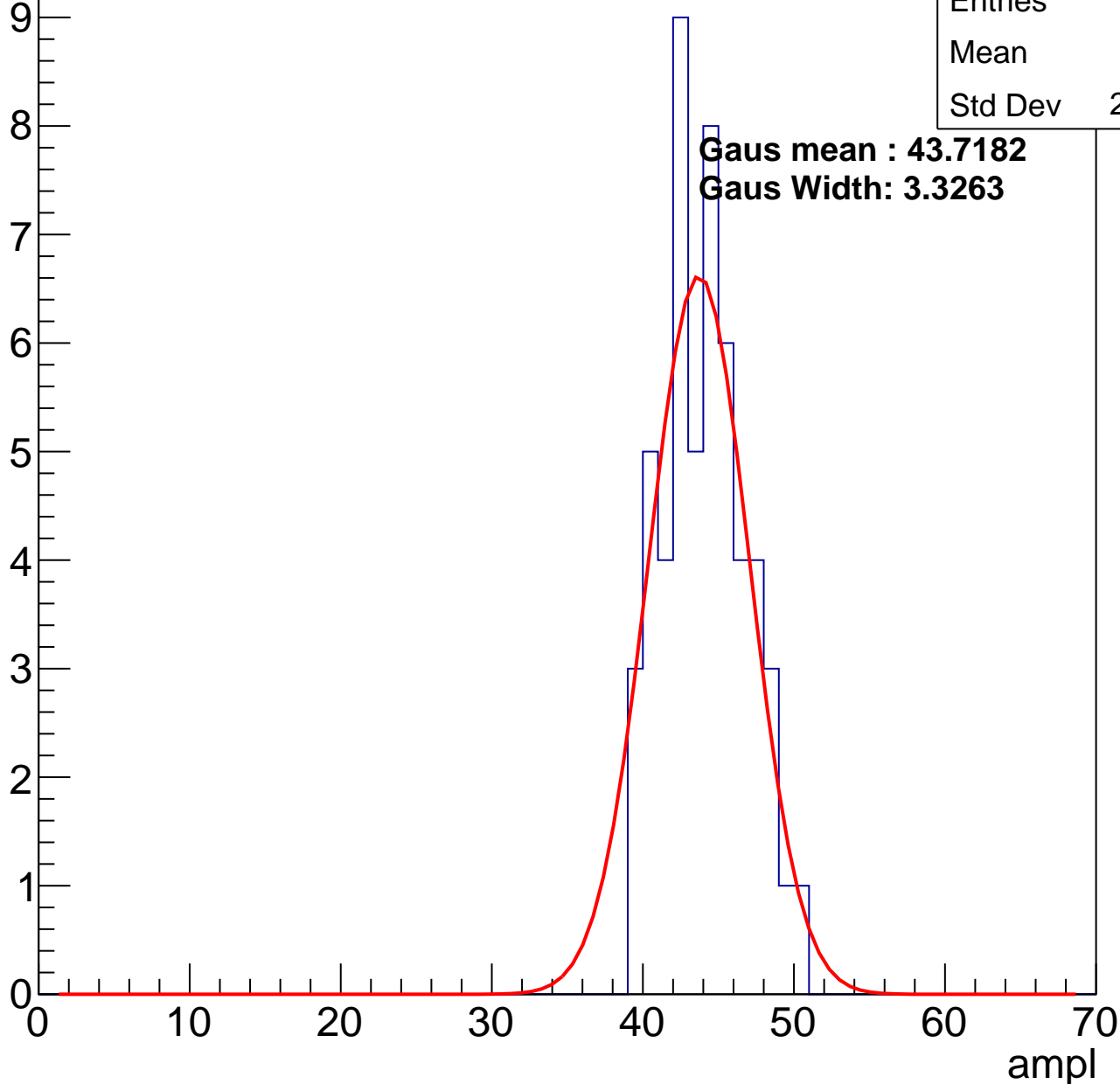
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	43.6
Std Dev	2.715

Gaus mean : 43.7182

Gaus Width: 3.3263

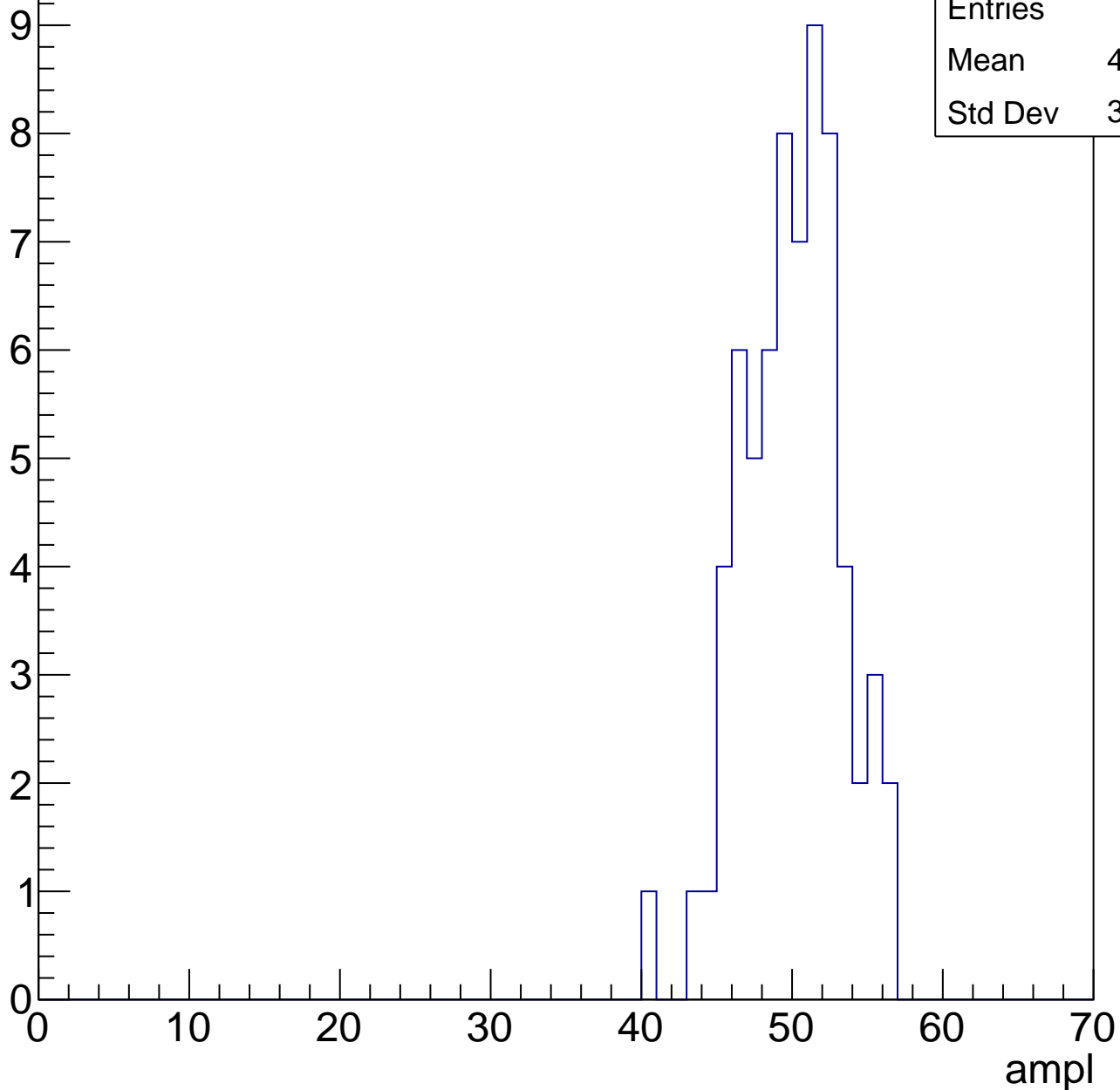


B0L001S, U15-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	49.55
Std Dev	3.234

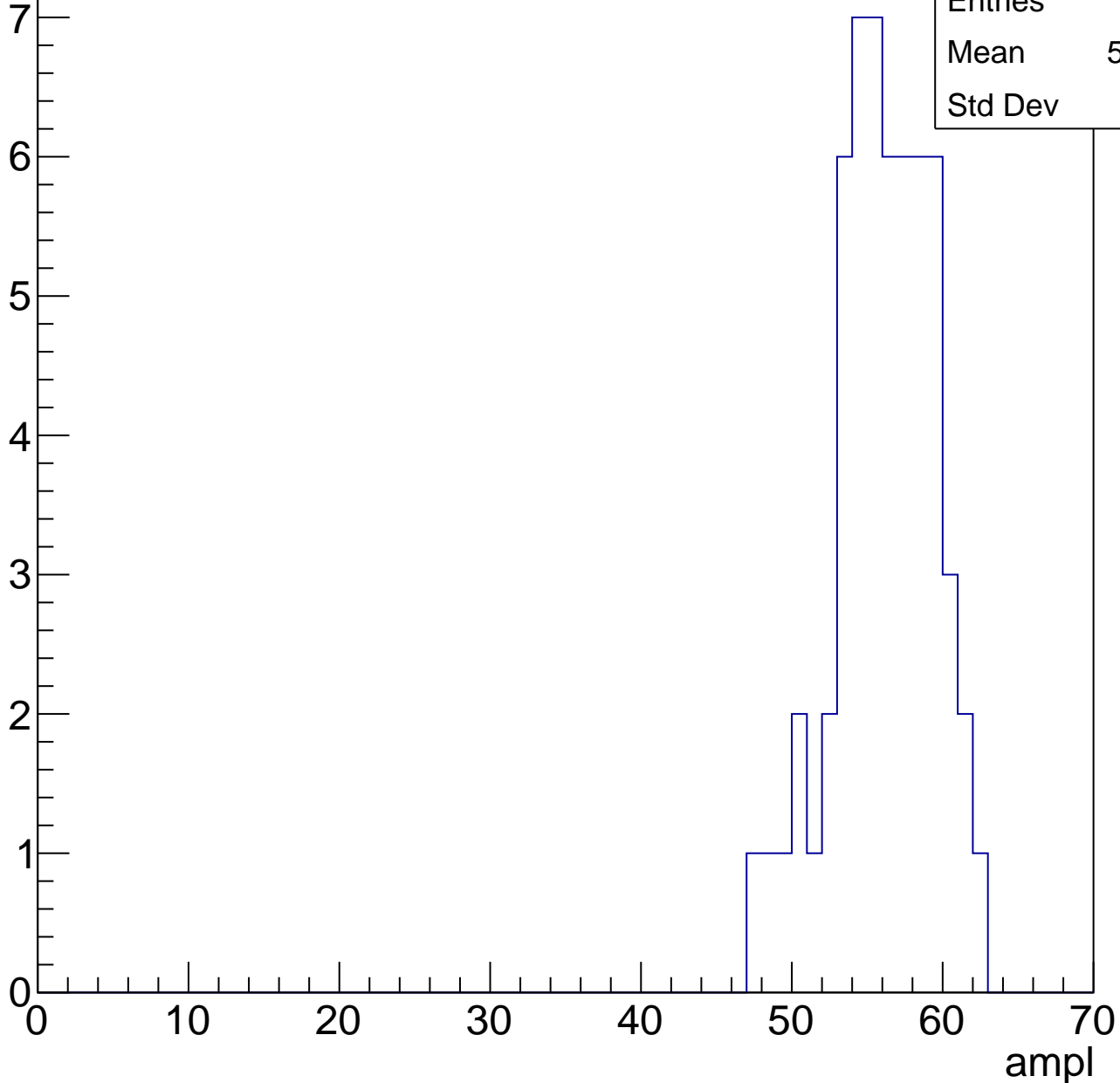


B0L001S, U15-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	55.59
Std Dev	3.27

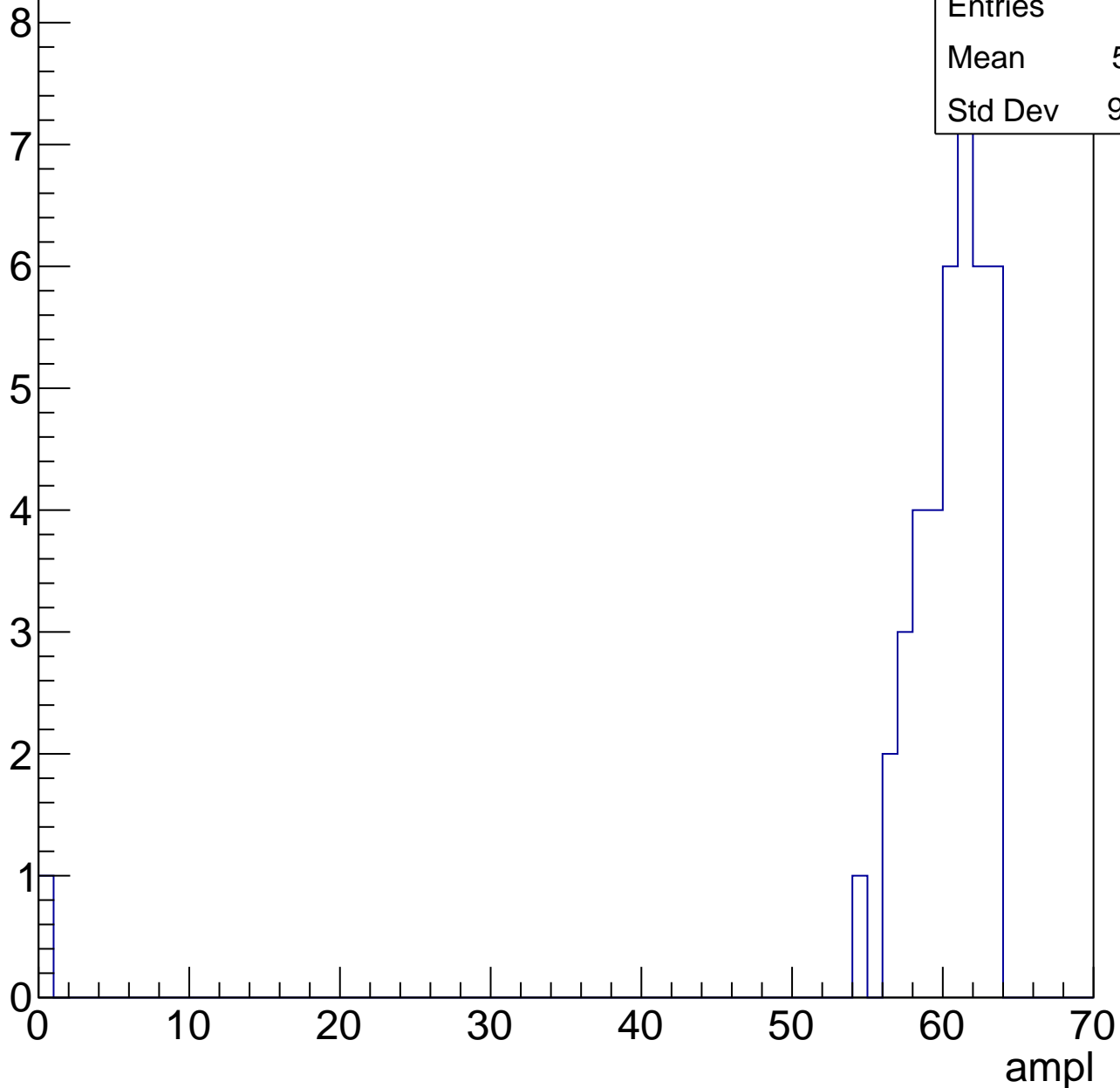


B0L001S, U15-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

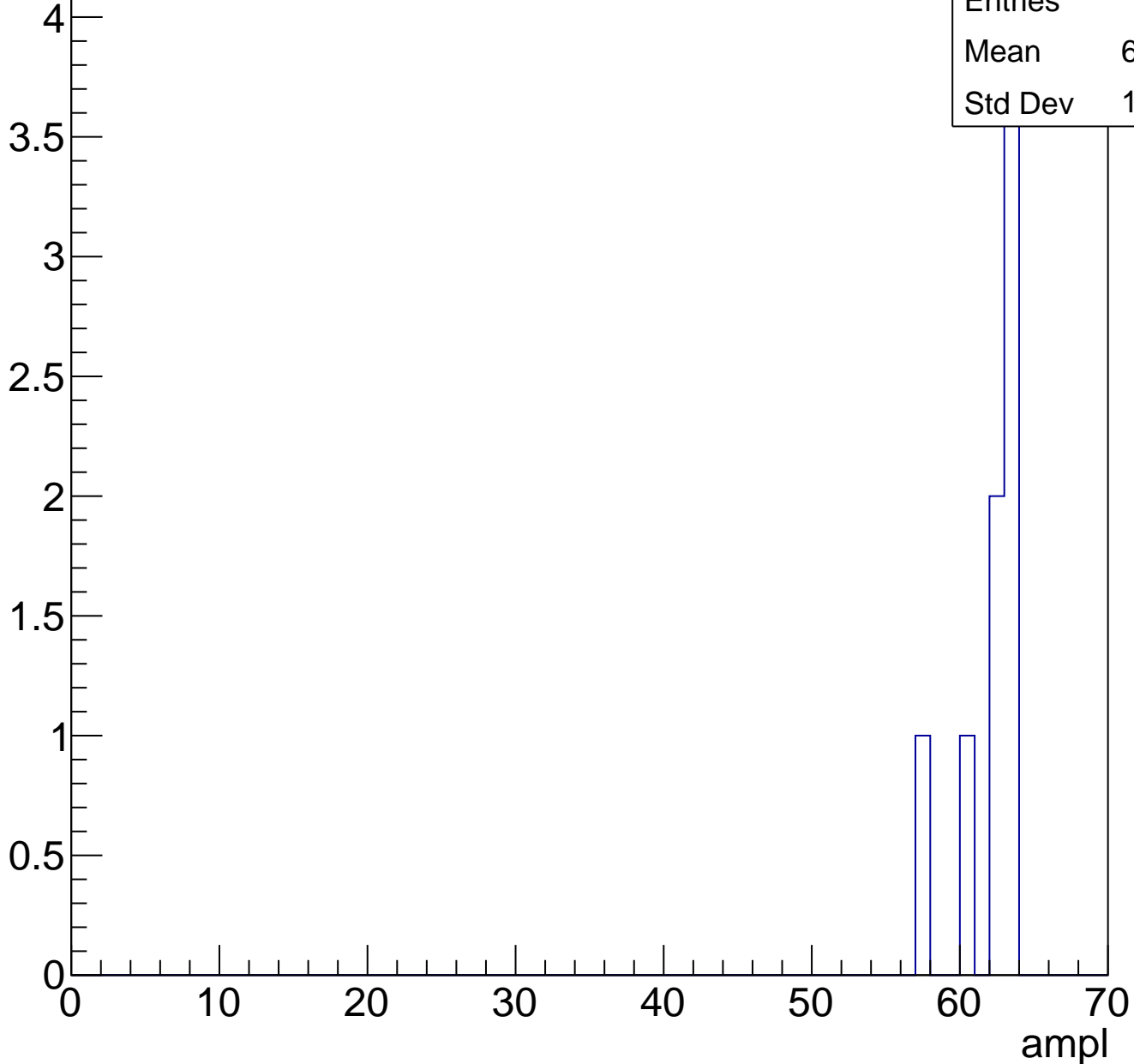
Entries	41
Mean	58.61
Std Dev	9.528



B0L001S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

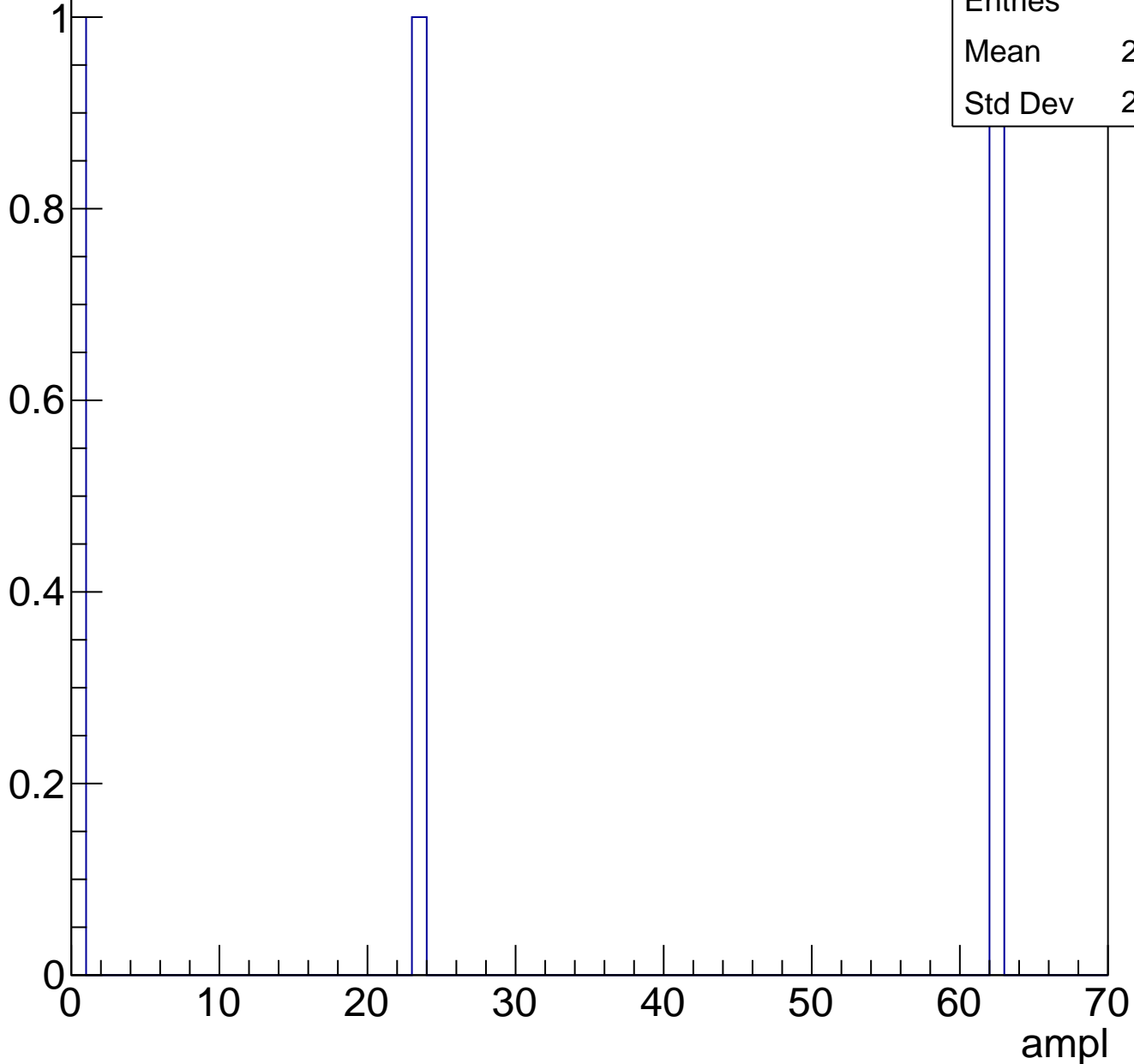
Entry



B0L001S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	28.33
Std Dev	25.59

B0L001S, U15-ch55, adc0

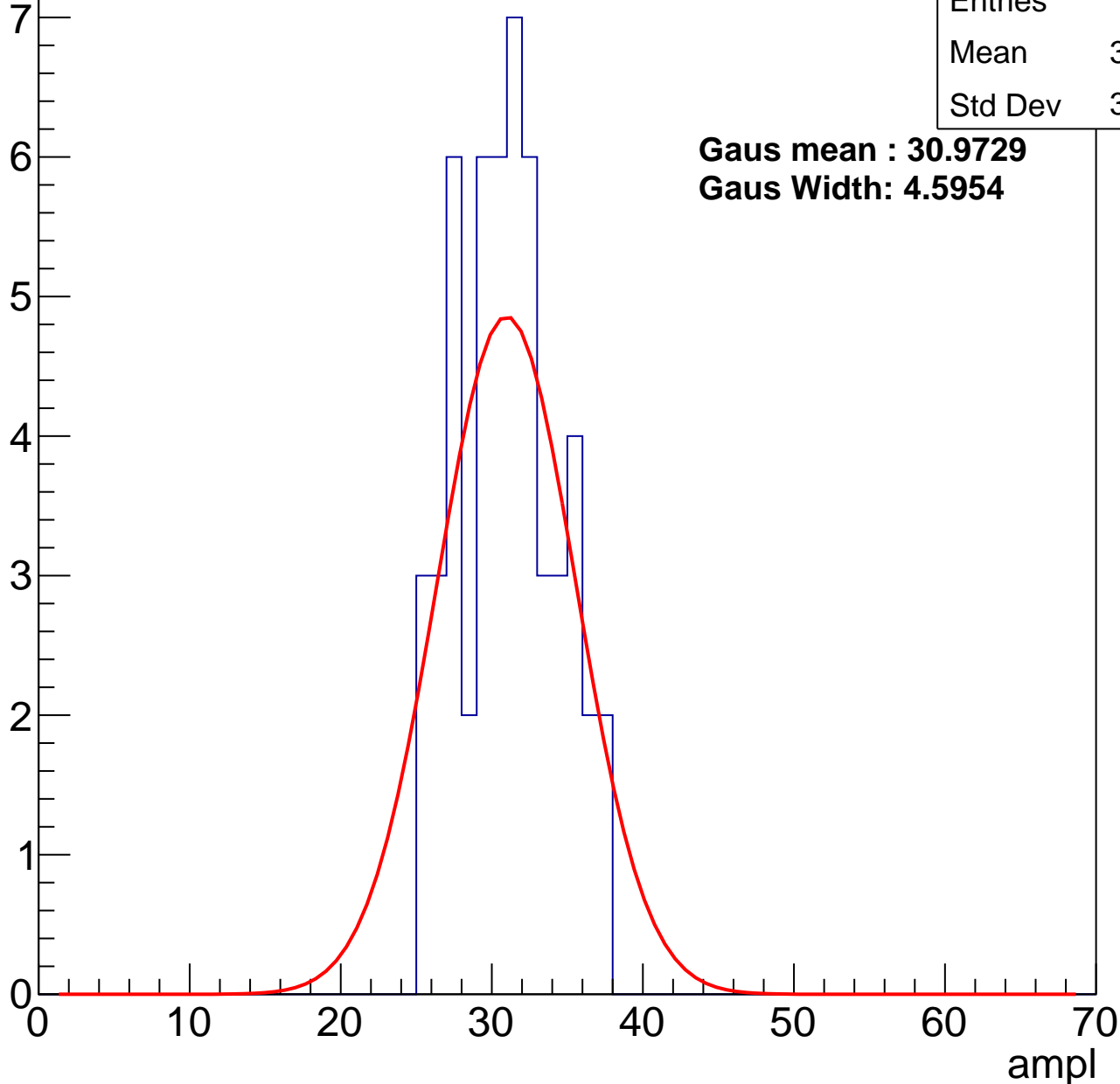
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.58
Std Dev	3.218

Gaus mean : 30.9729

Gaus Width: 4.5954



B0L001S, U15-ch55, adc1

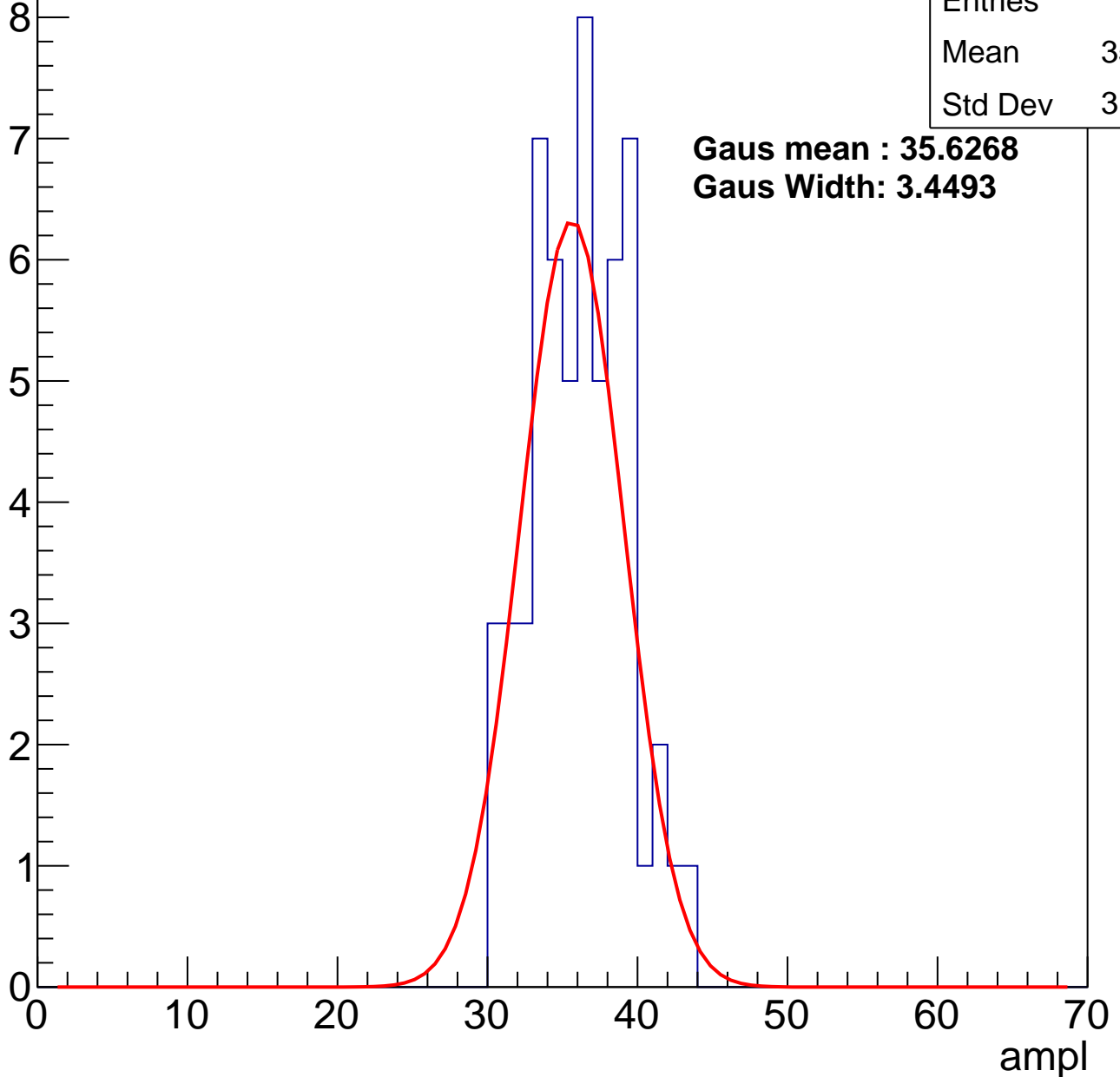
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	35.69
Std Dev	3.108

Gaus mean : 35.6268

Gaus Width: 3.4493



B0L001S, U15-ch55, adc2

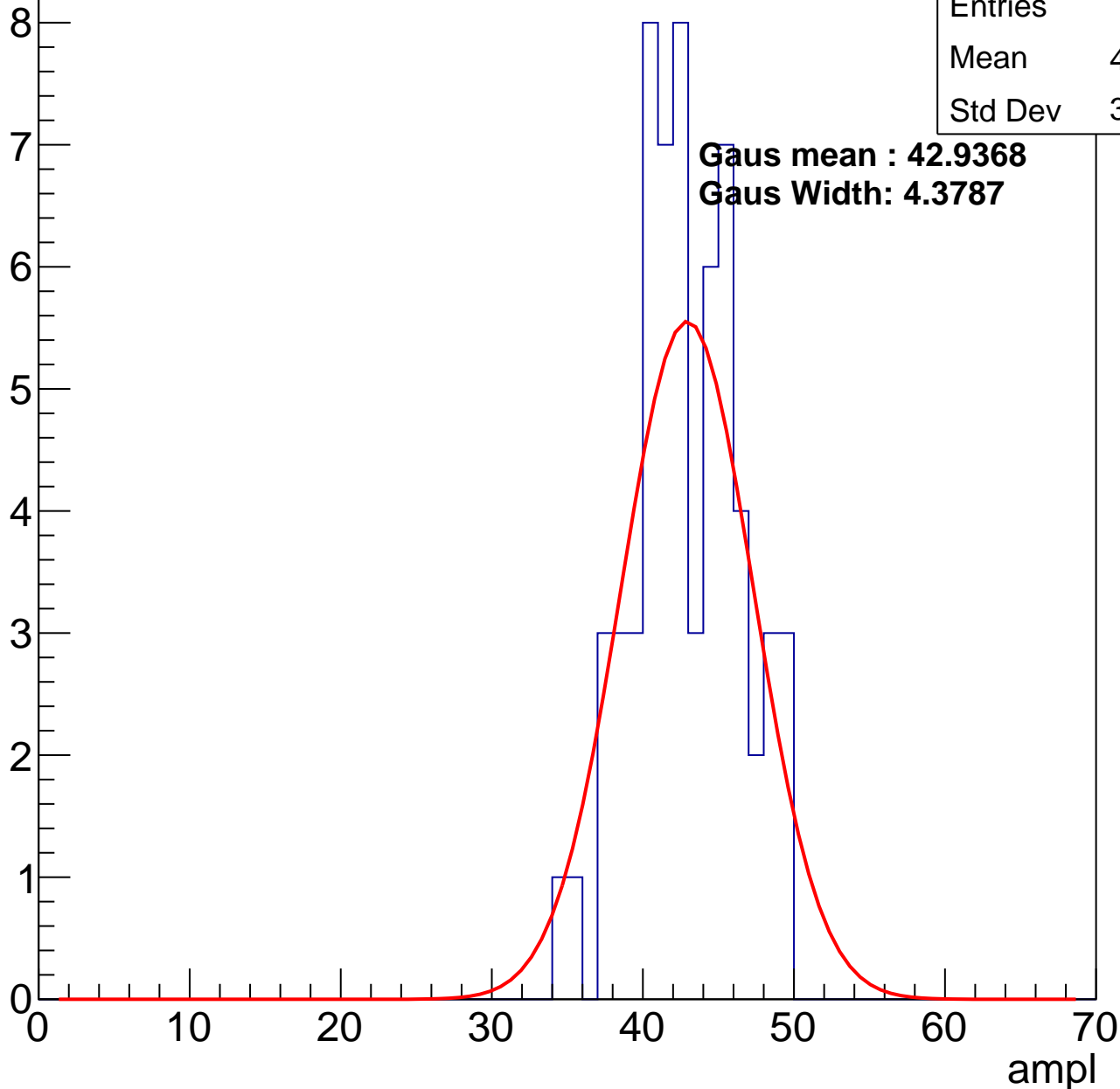
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	42.44
Std Dev	3.476

Gaus mean : 42.9368

Gaus Width: 4.3787

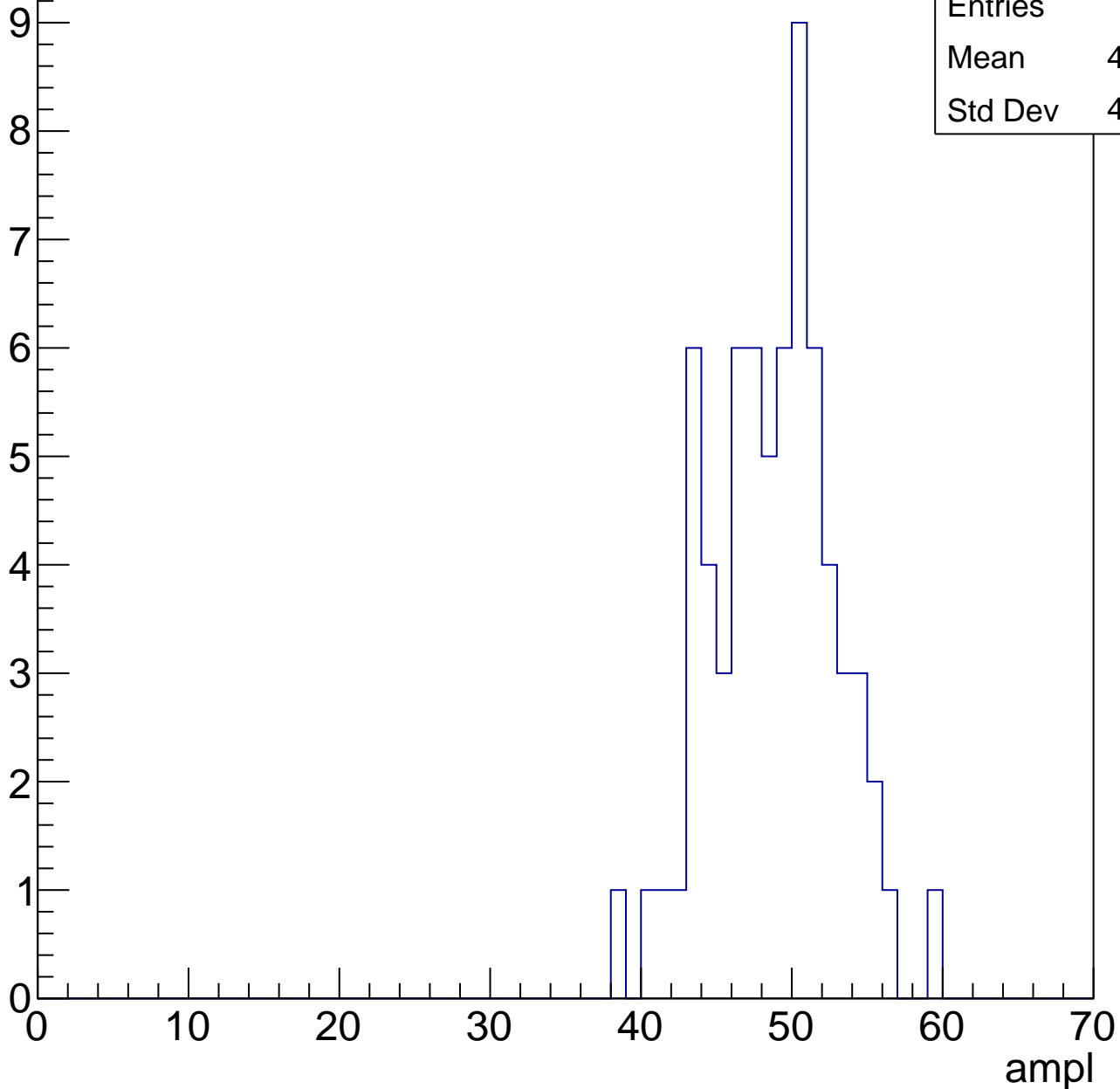


B0L001S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

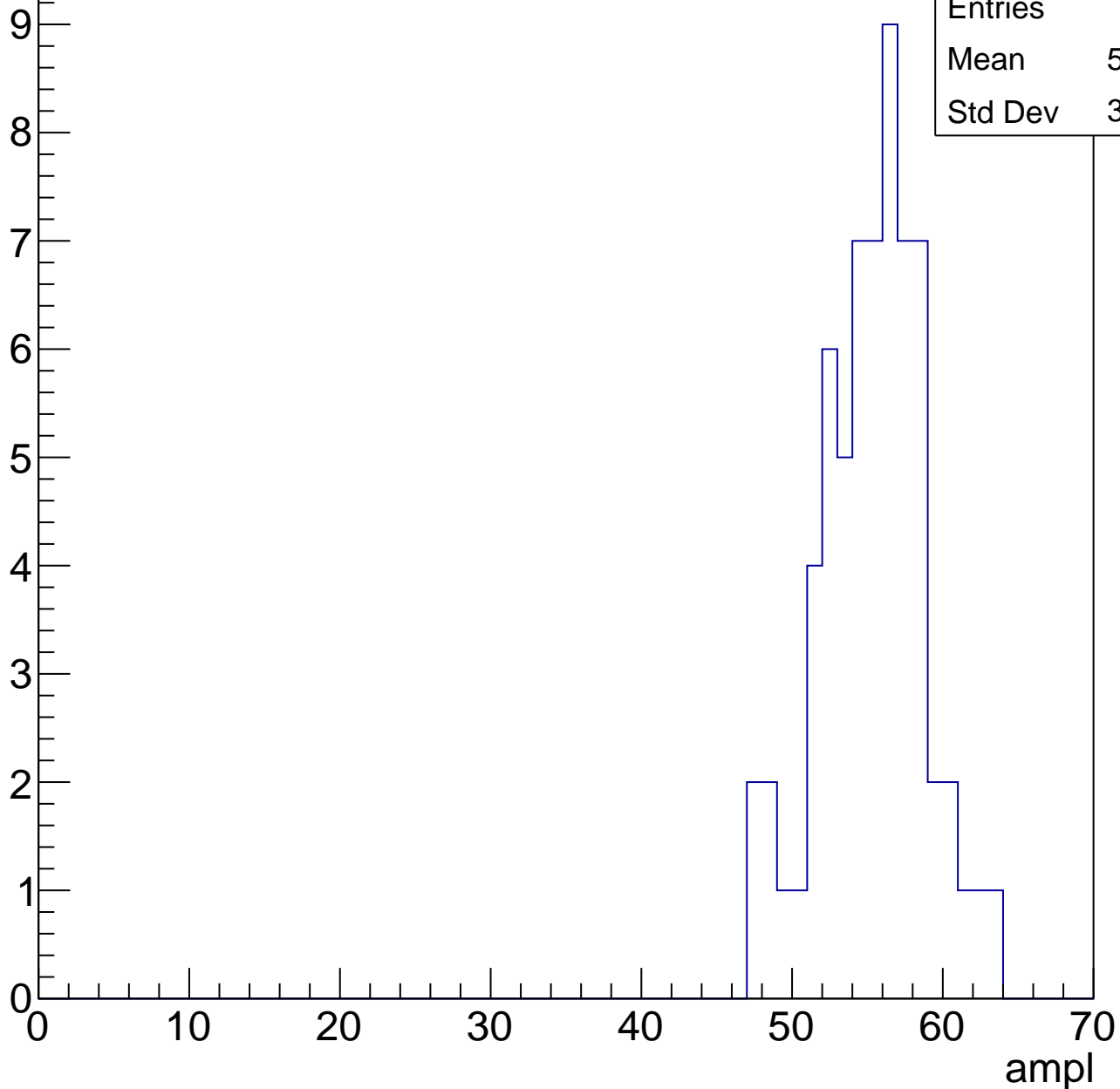
Entries	69
Mean	48.29
Std Dev	4.065



B0L001S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

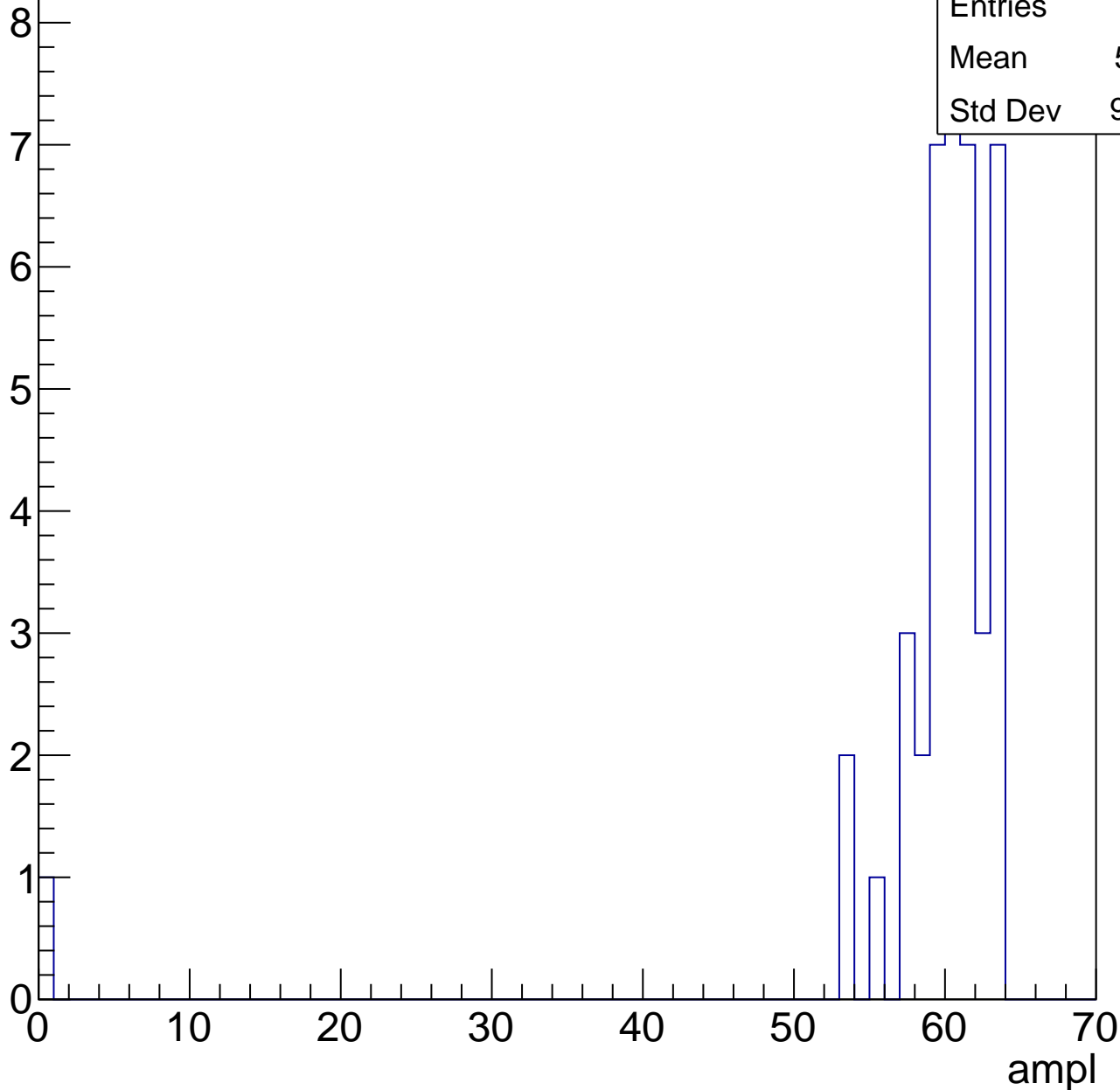


B0L001S, U15-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.41
Std Dev	9.556

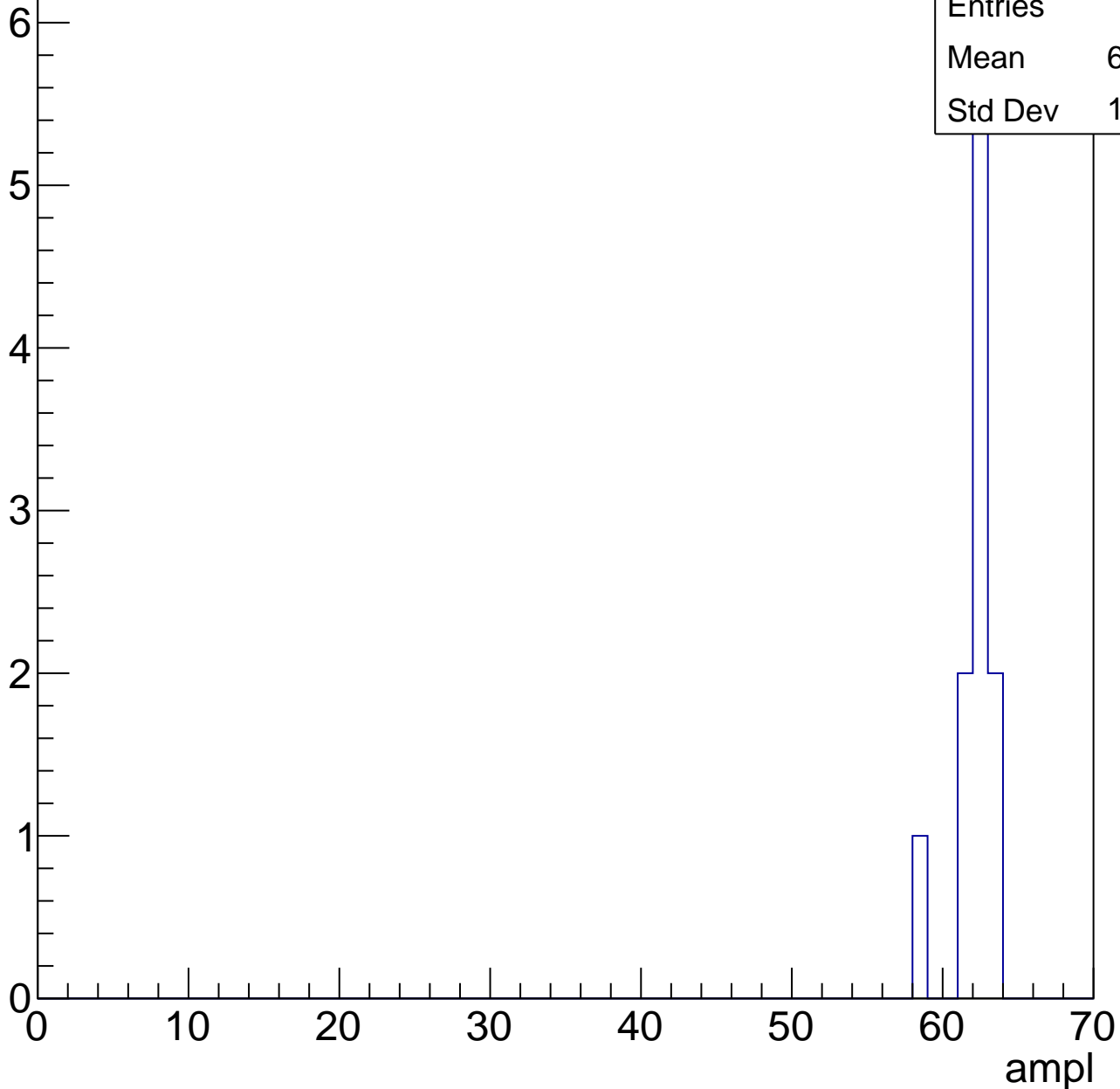


B0L001S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	11
Mean	61.64
Std Dev	1.298



B0L001S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U15-ch56, adc0

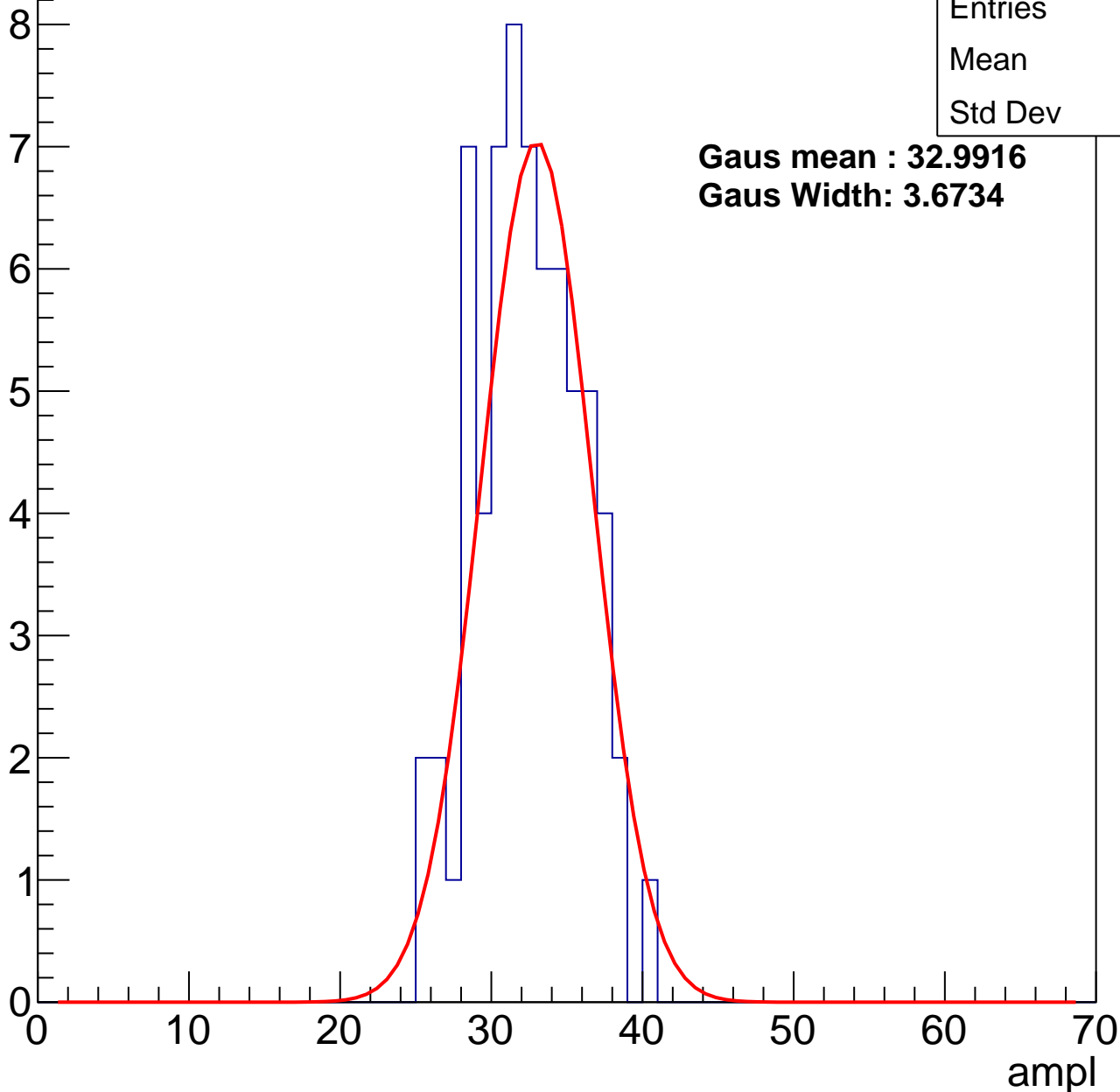
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	32
Std Dev	3.39

Gaus mean : 32.9916

Gaus Width: 3.6734



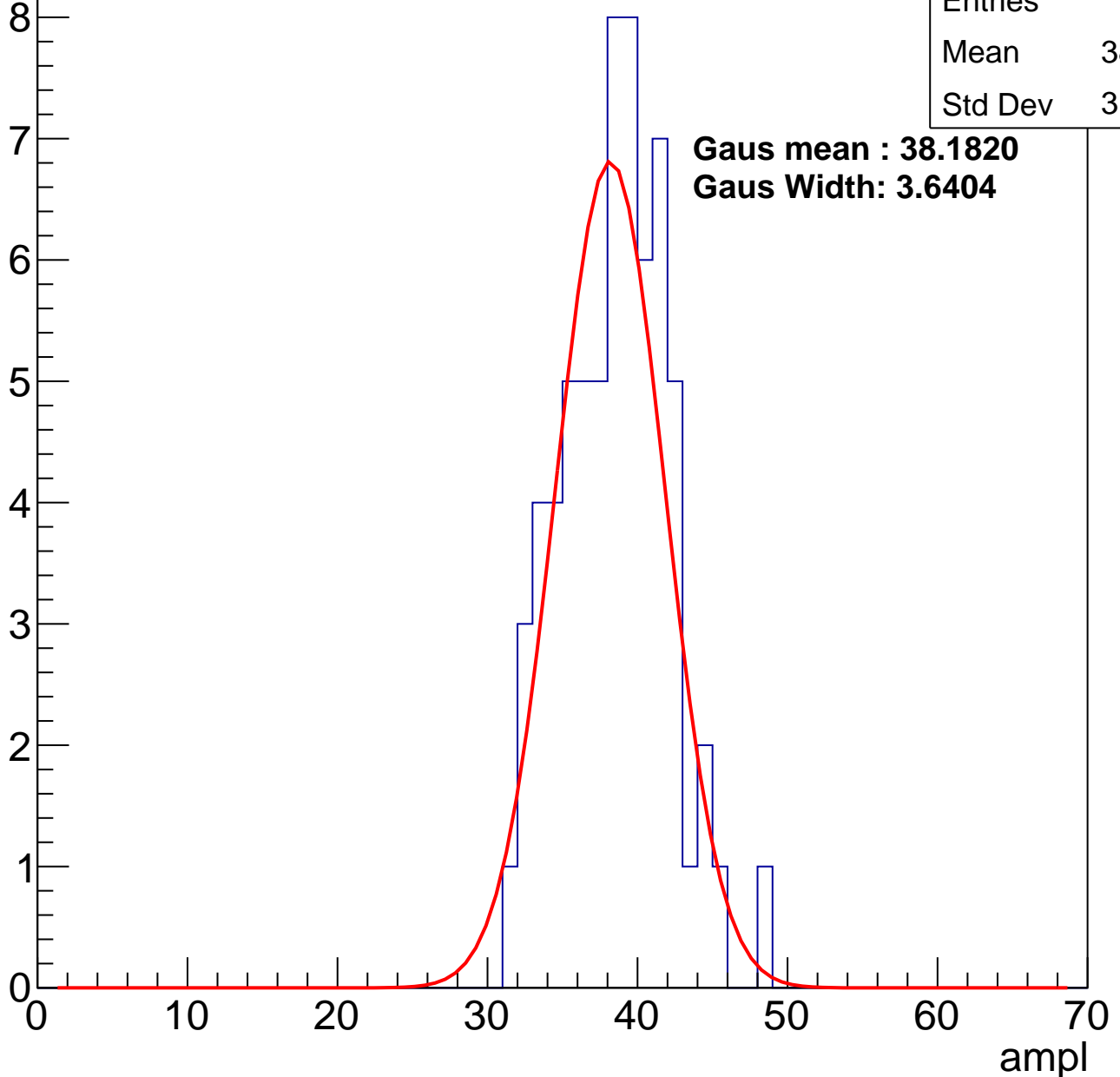
B0L001S, U15-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.06
Std Dev	3.507

Gaus mean : 38.1820
Gaus Width: 3.6404



B0L001S, U15-ch56, adc2

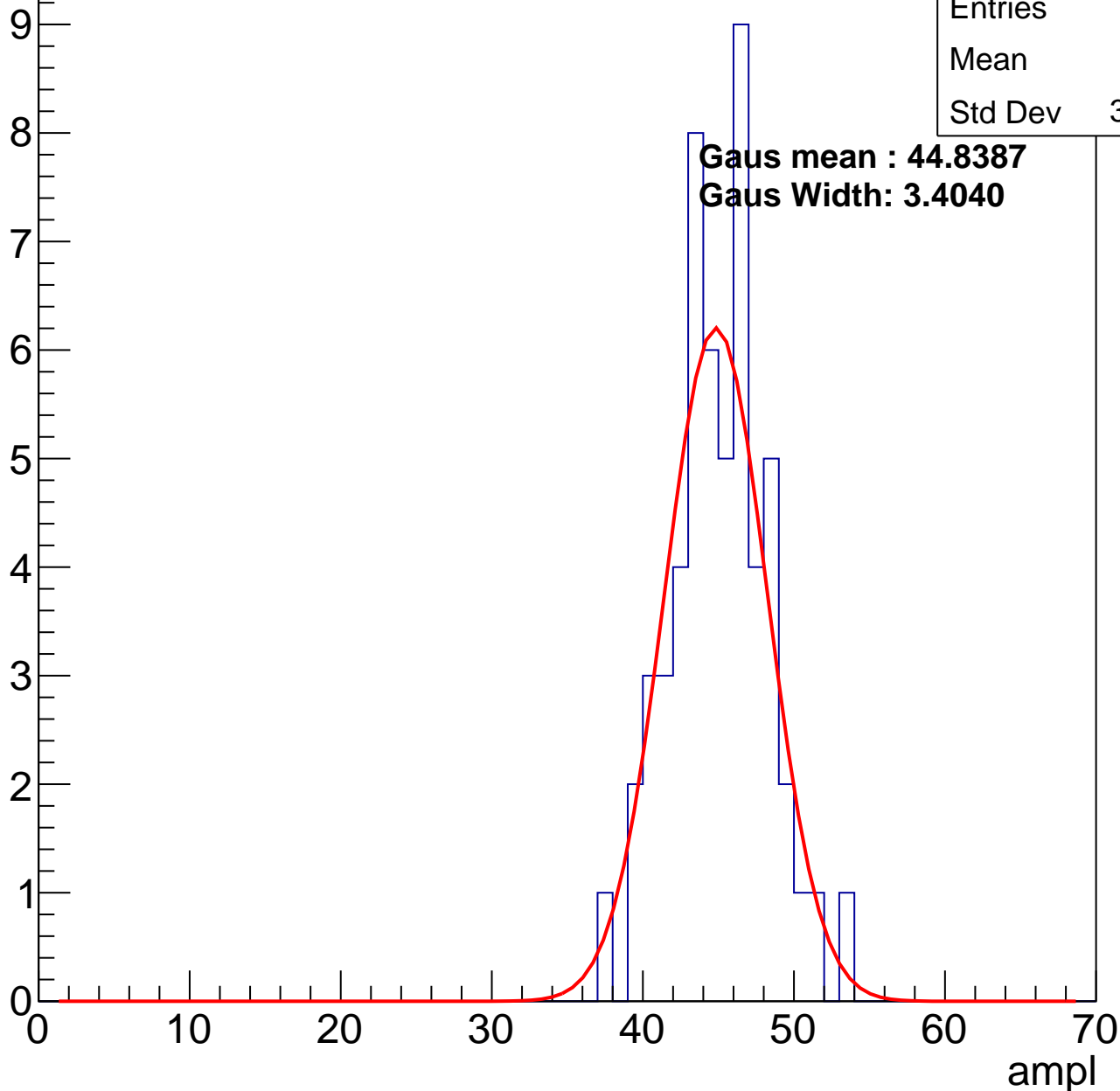
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.6
Std Dev	3.166

Gaus mean : 44.8387

Gaus Width: 3.4040

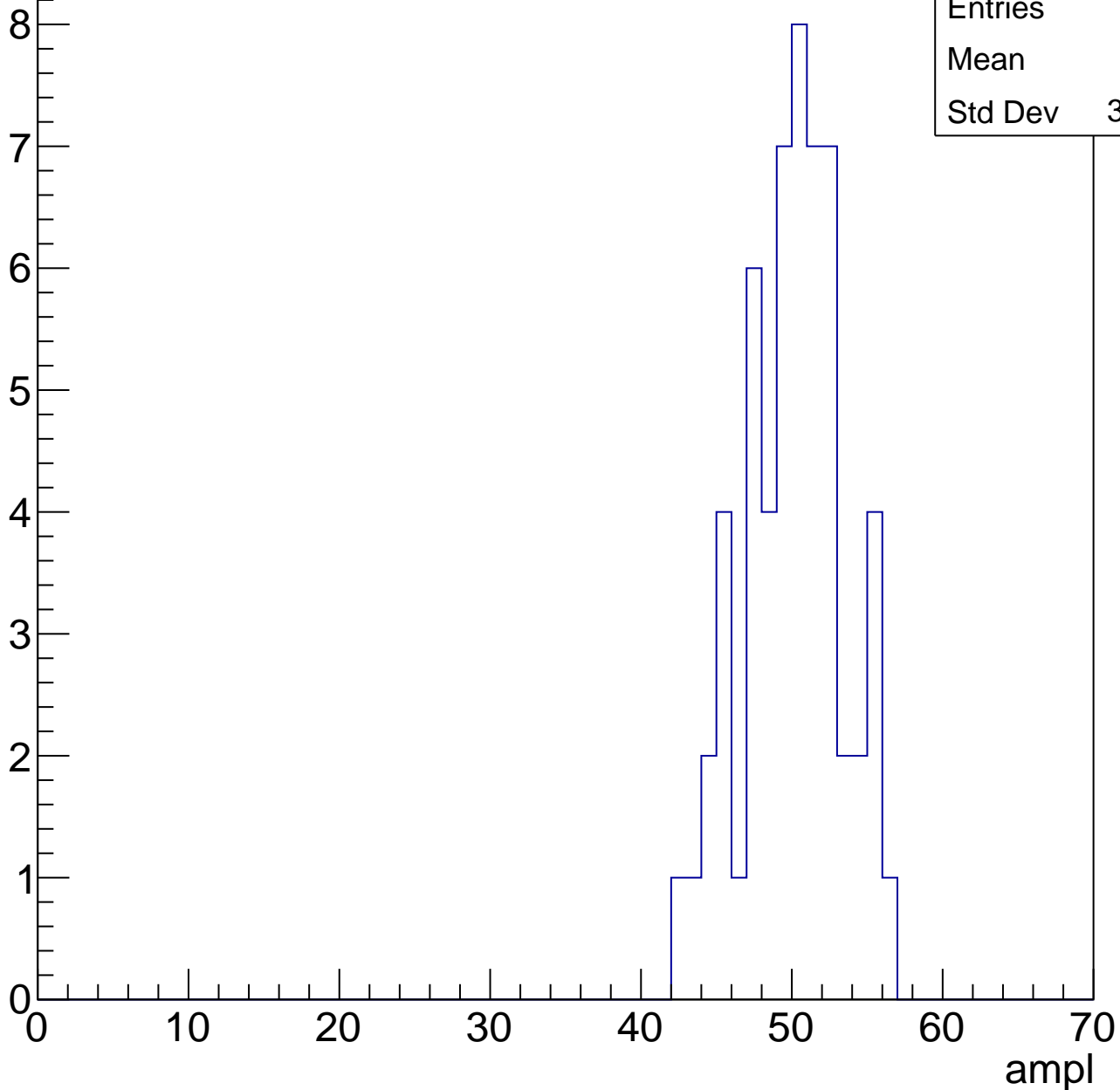


B0L001S, U15-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

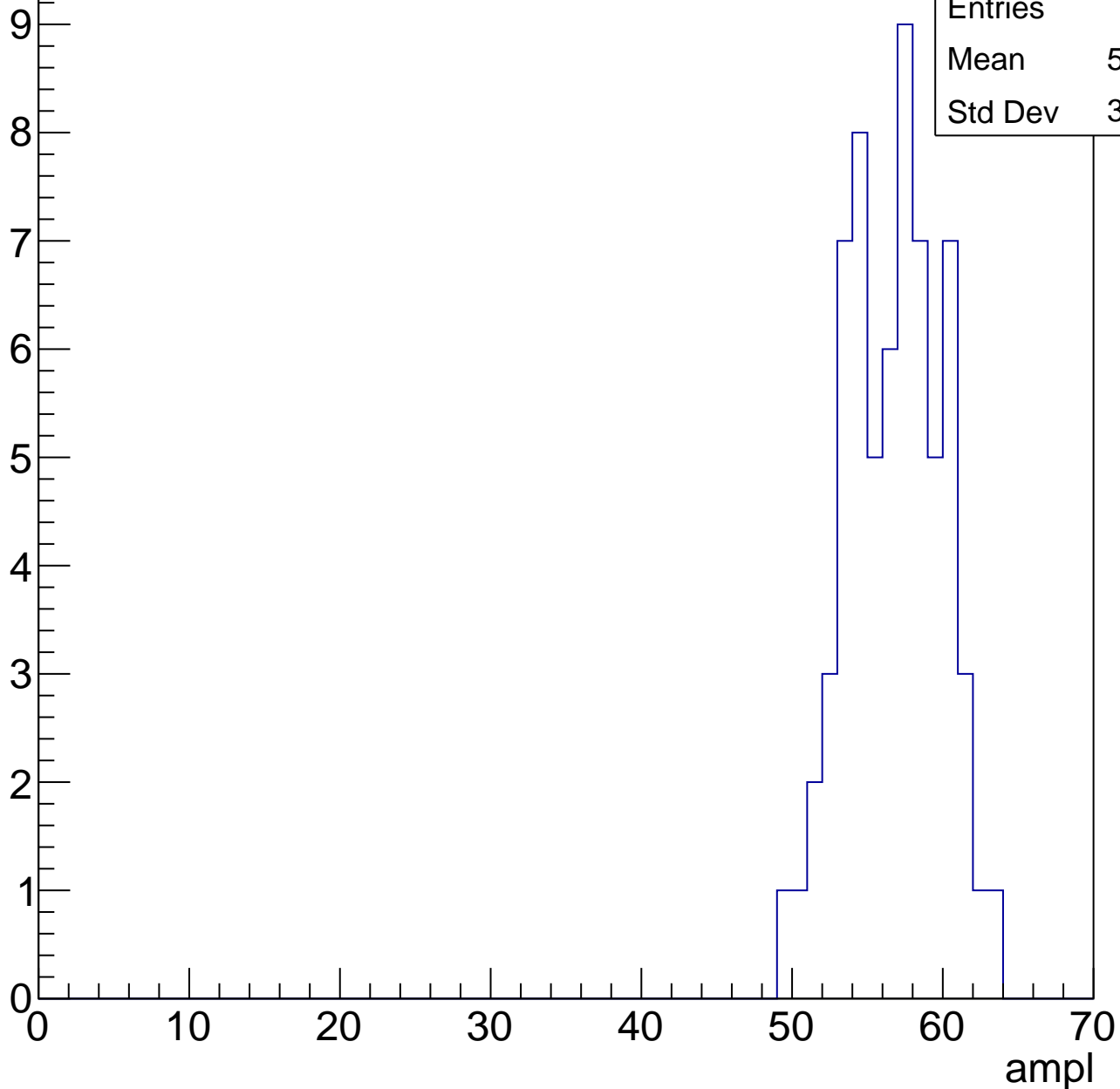
Entries	57
Mean	49.6
Std Dev	3.217



B0L001S, U15-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

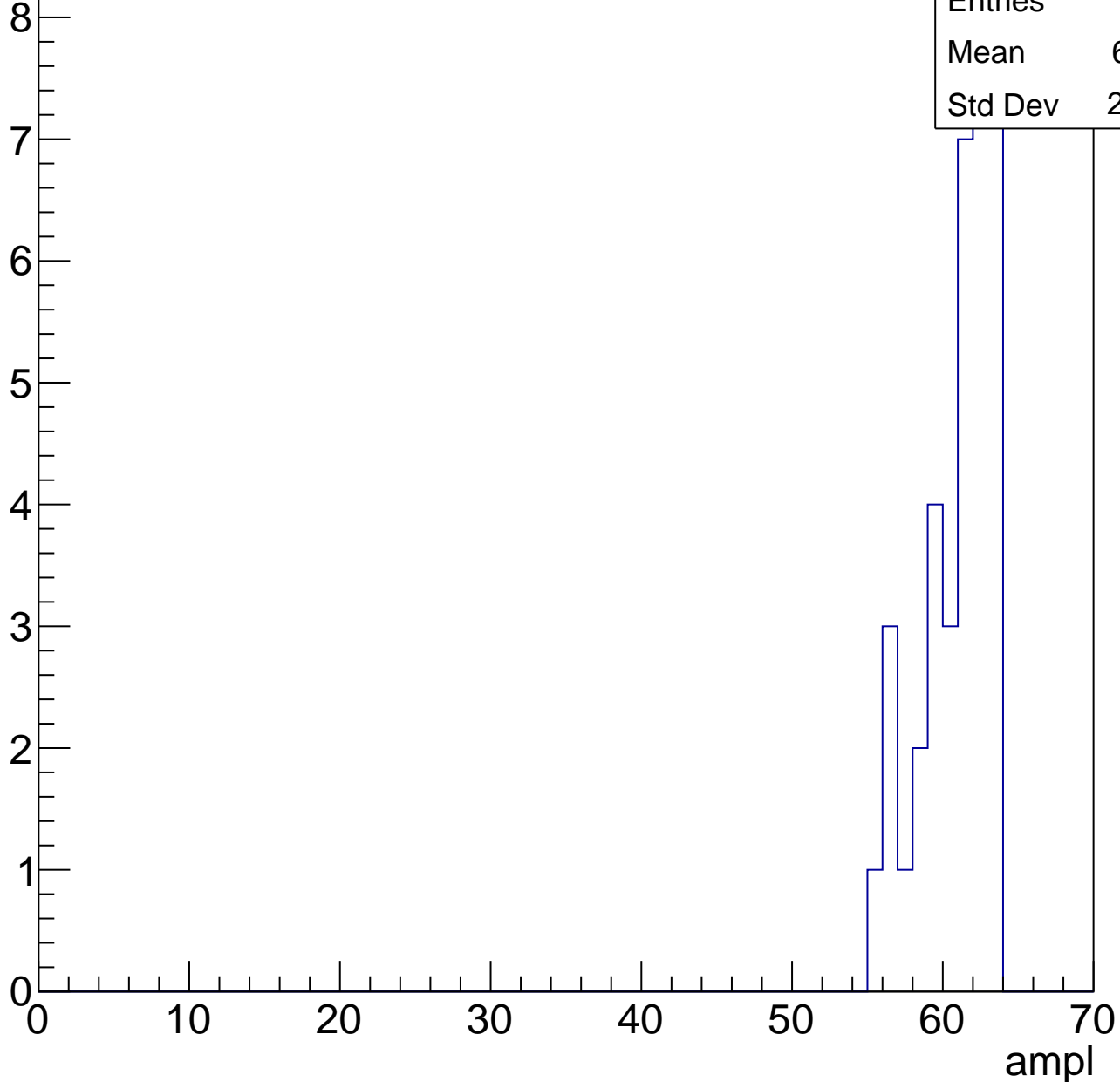


B0L001S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

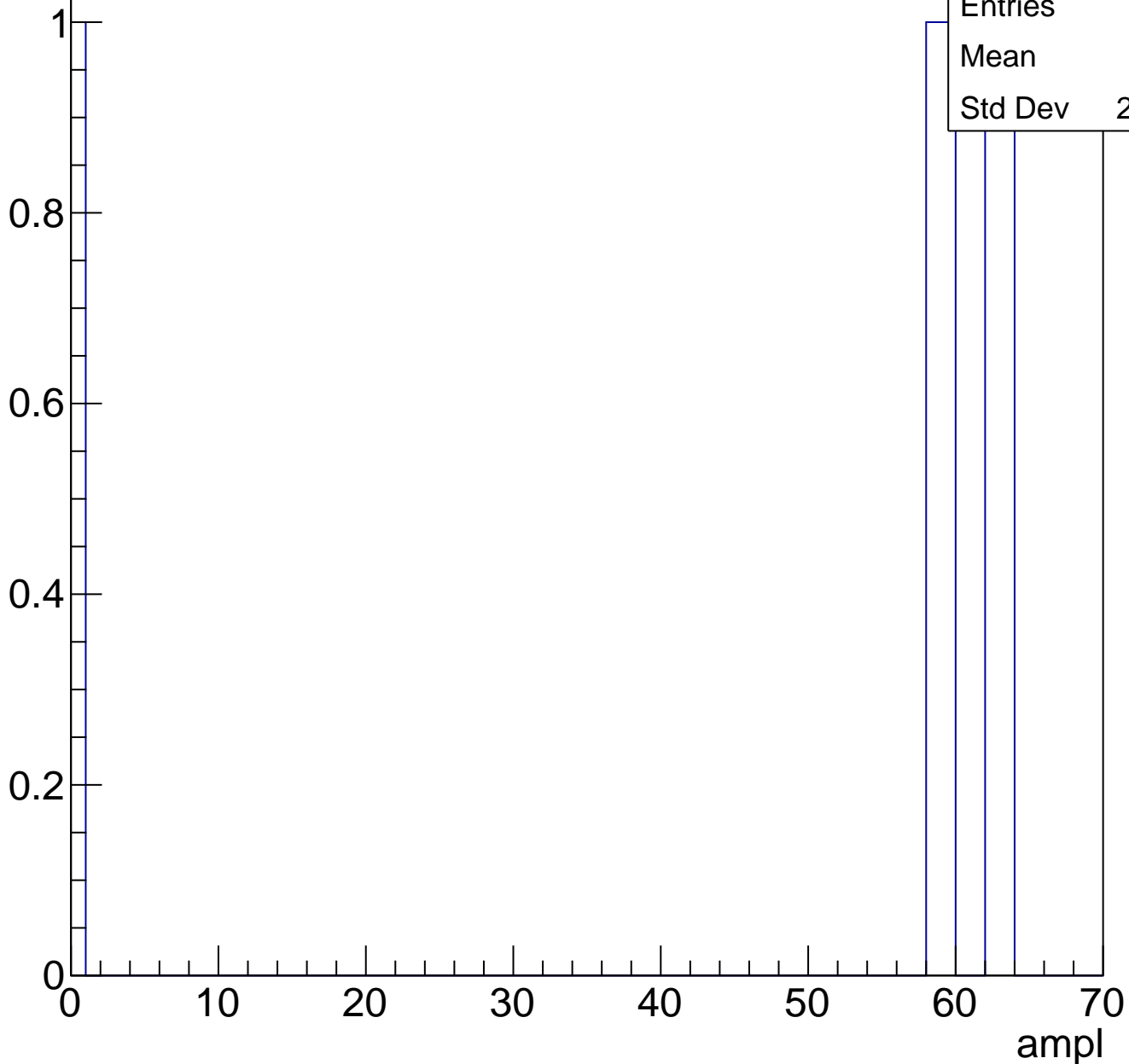
Entries	37
Mean	60.51
Std Dev	2.297



B0L001S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch57, adc0

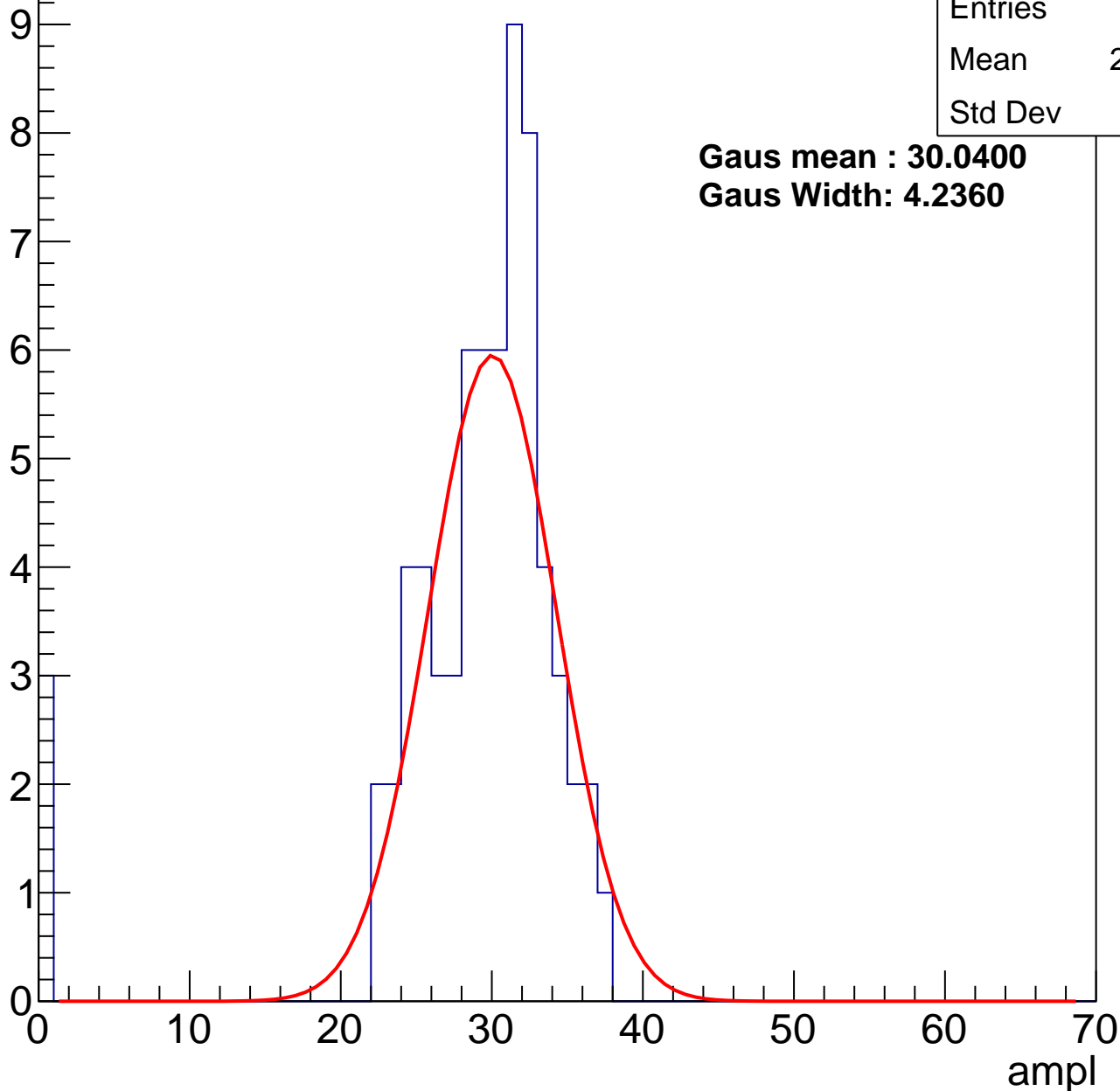
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.16
Std Dev	7.01

Gaus mean : 30.0400

Gaus Width: 4.2360



B0L001S, U15-ch57, adc1

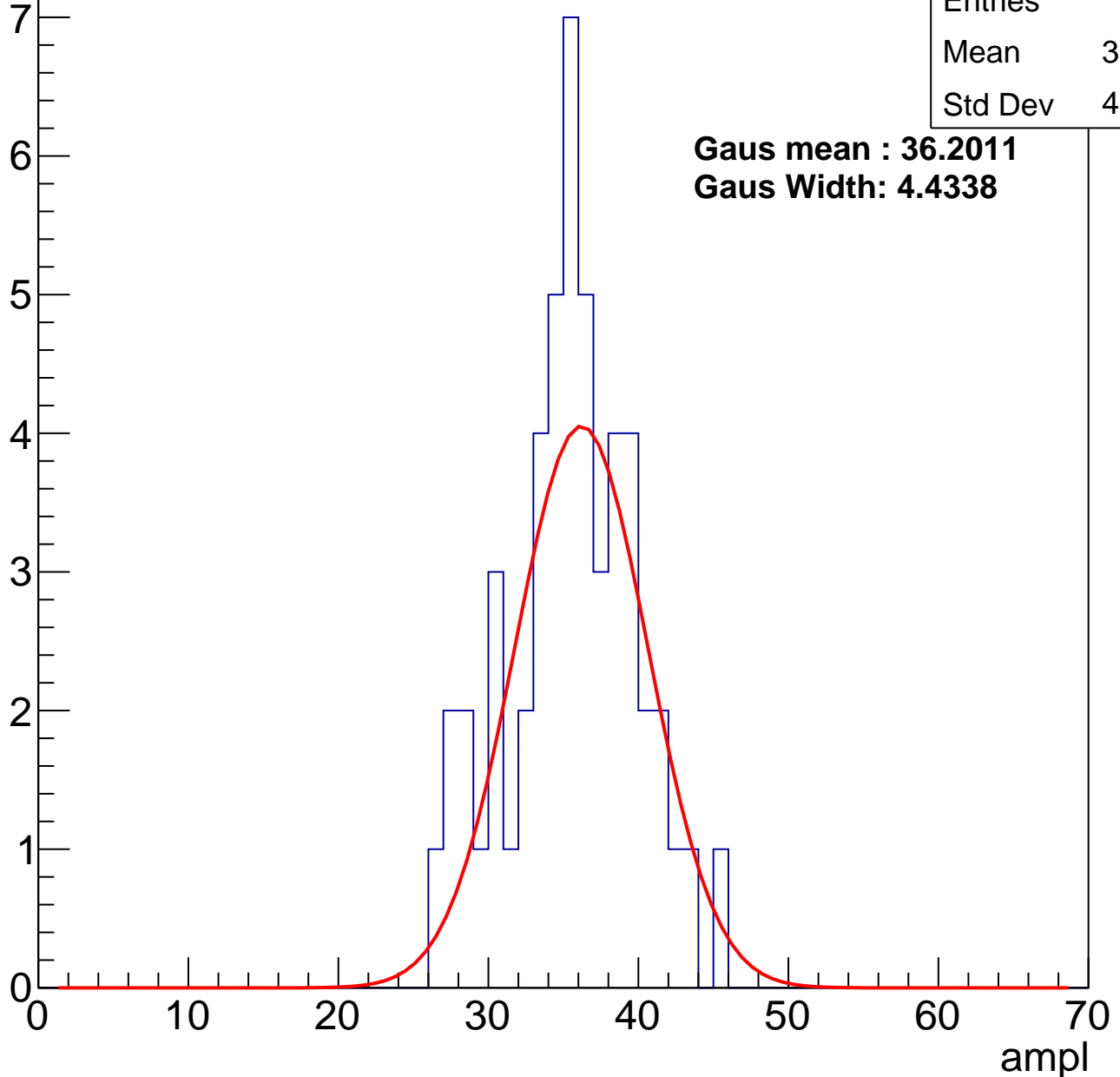
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	35.06
Std Dev	4.258

Gaus mean : 36.2011

Gaus Width: 4.4338



B0L001S, U15-ch57, adc2

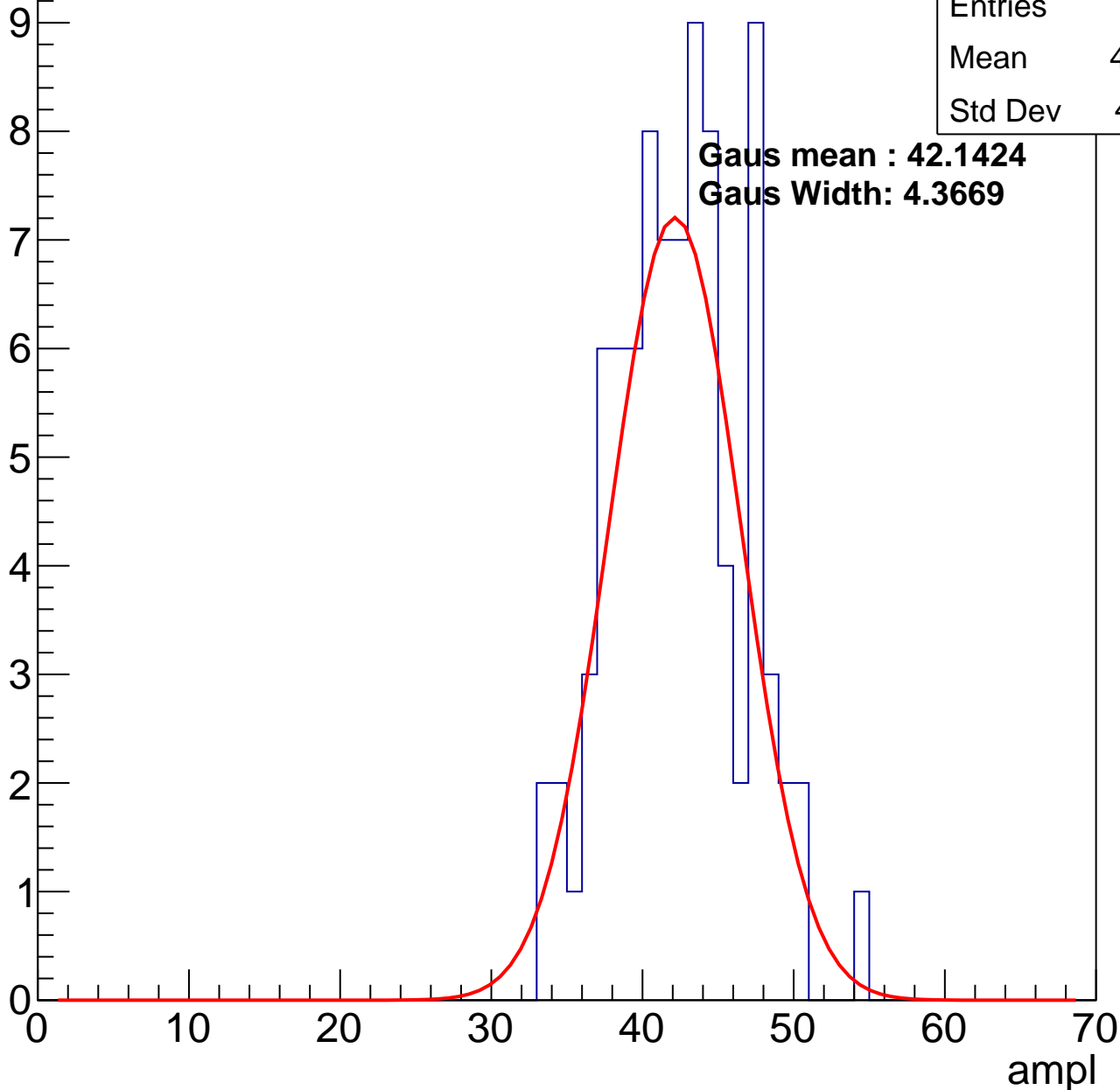
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	41.95
Std Dev	4.261

Gaus mean : 42.1424

Gaus Width: 4.3669

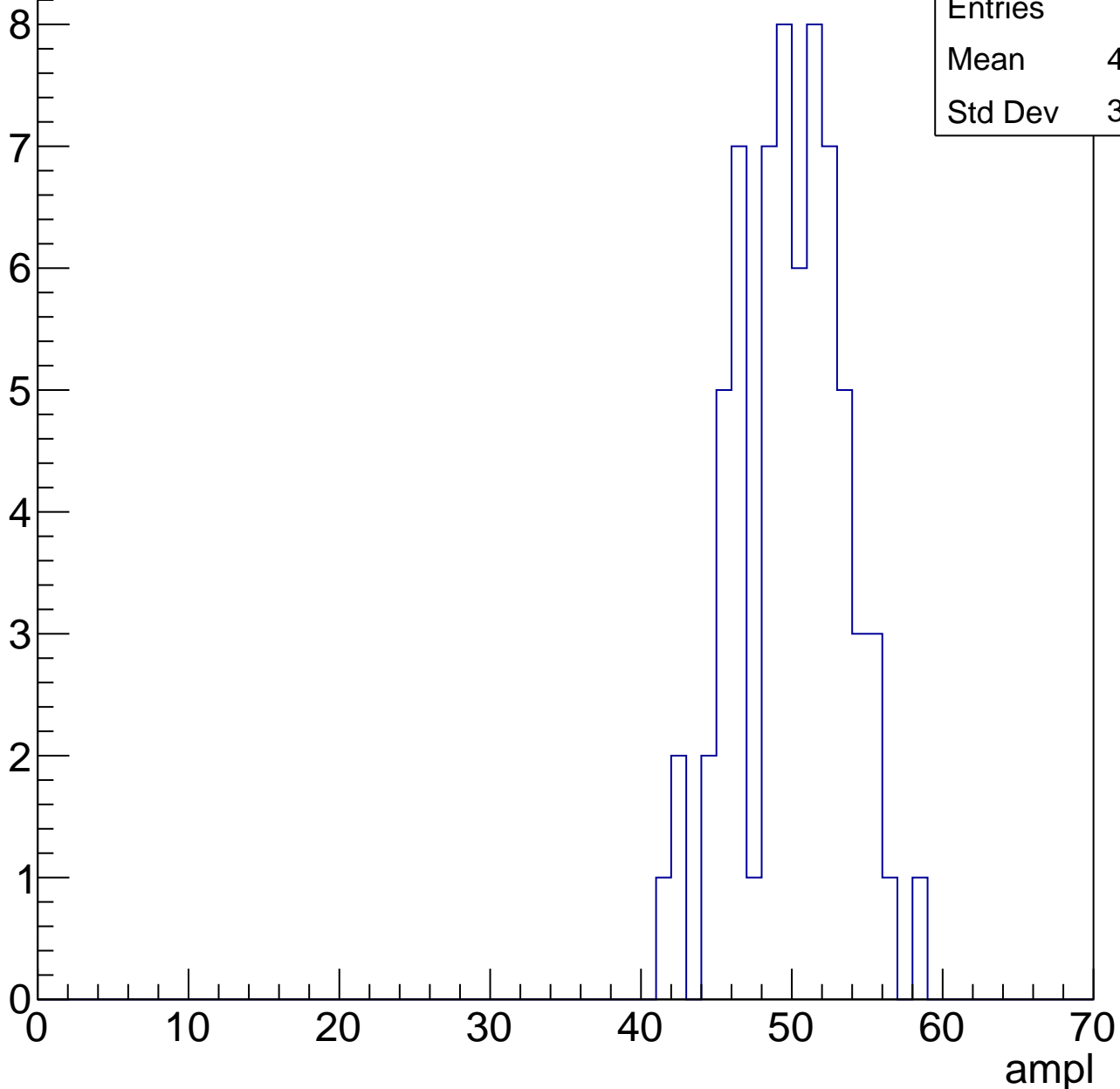


B0L001S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	49.45
Std Dev	3.555

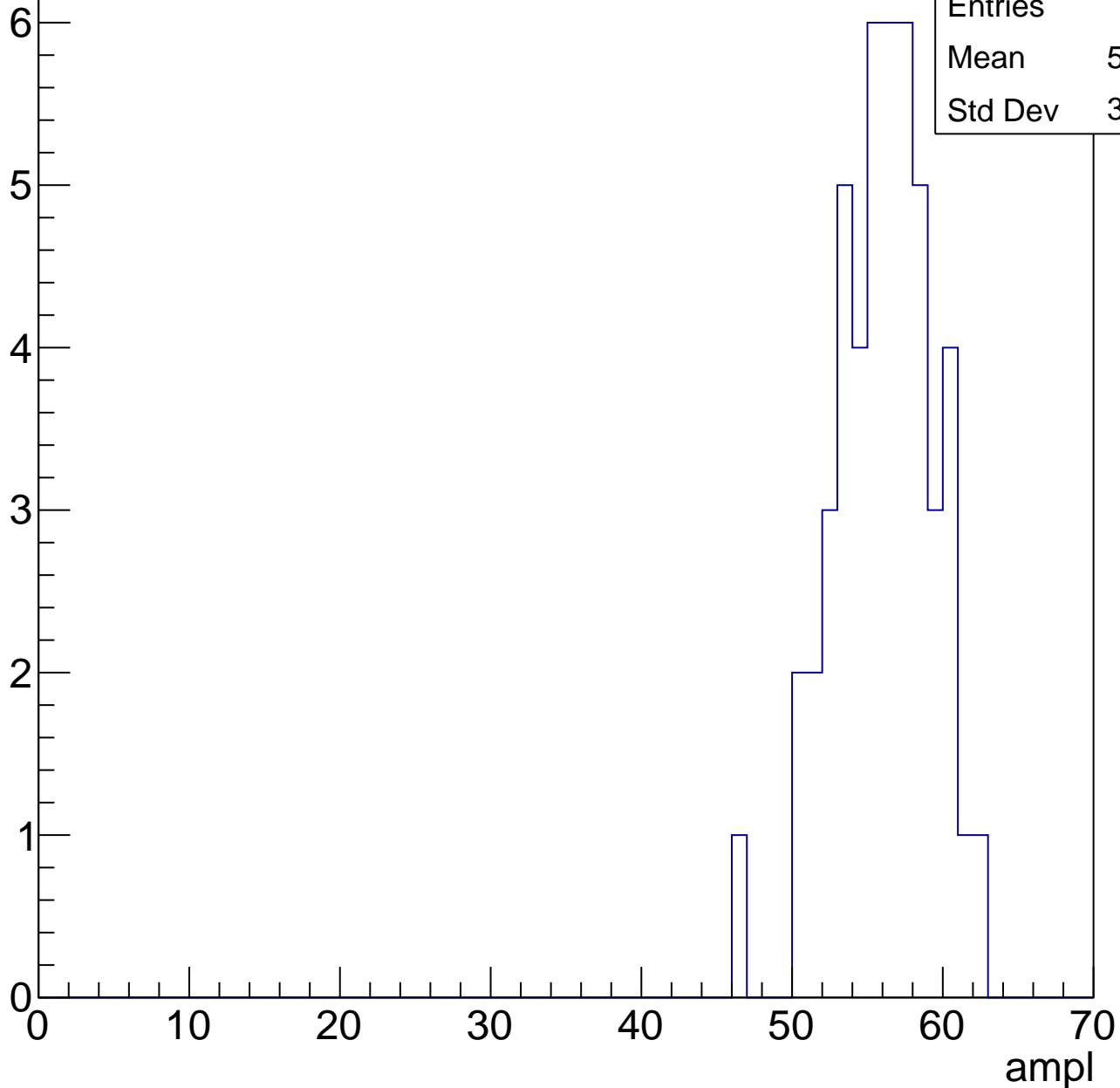


B0L001S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

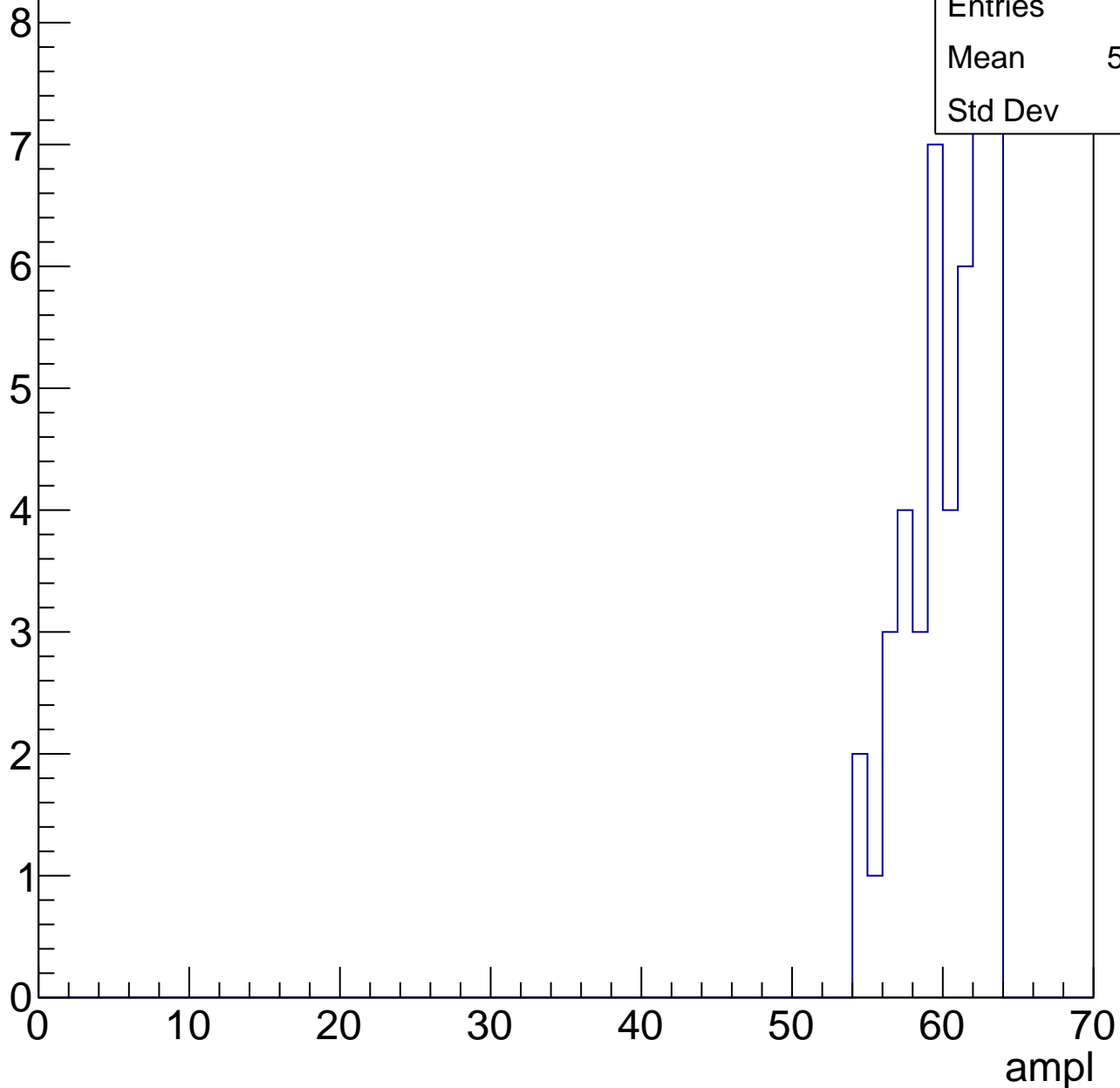
Entries	49
Mean	55.57
Std Dev	3.213



B0L001S, U15-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

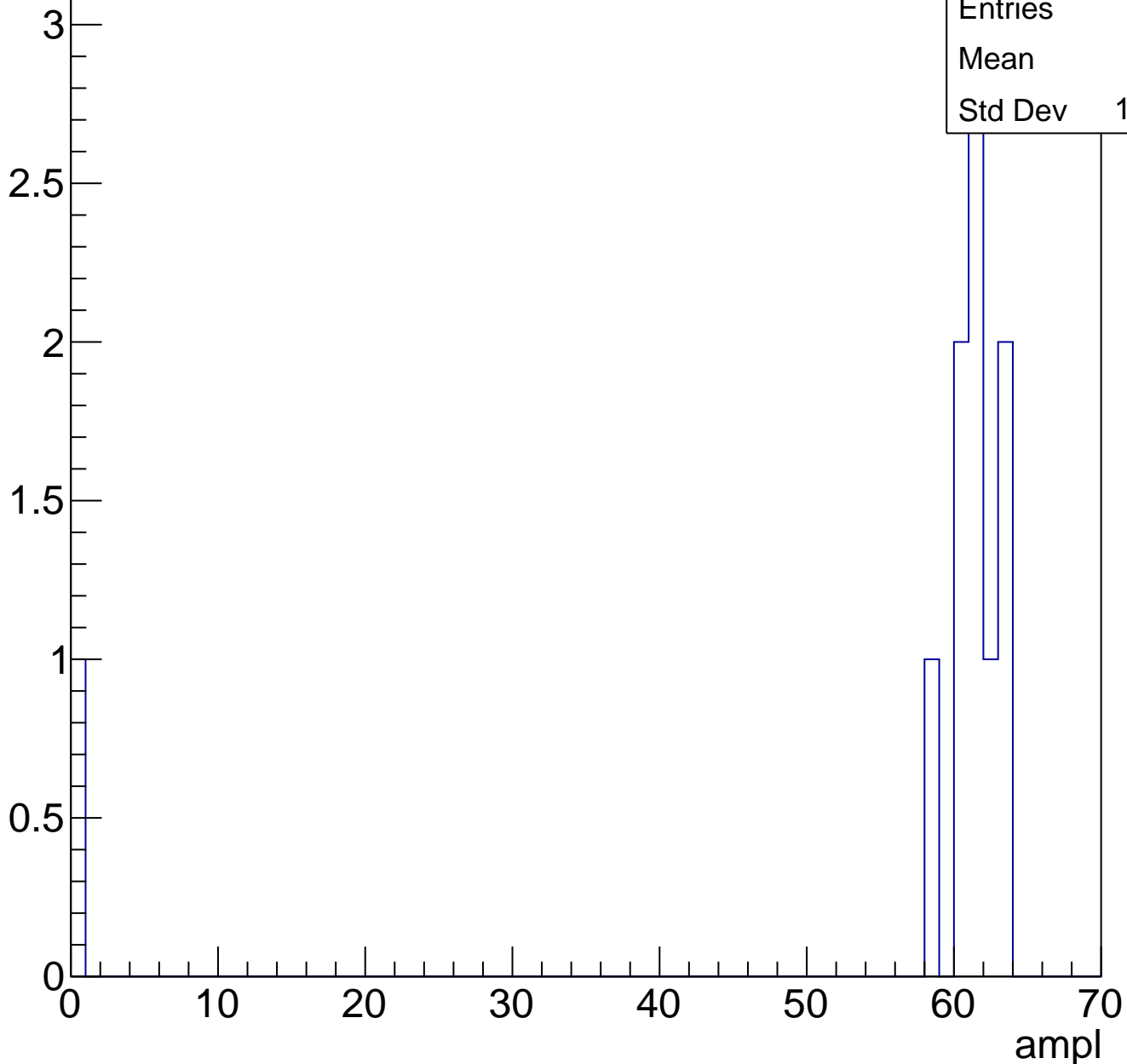
Entry



B0L001S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch58, adc0

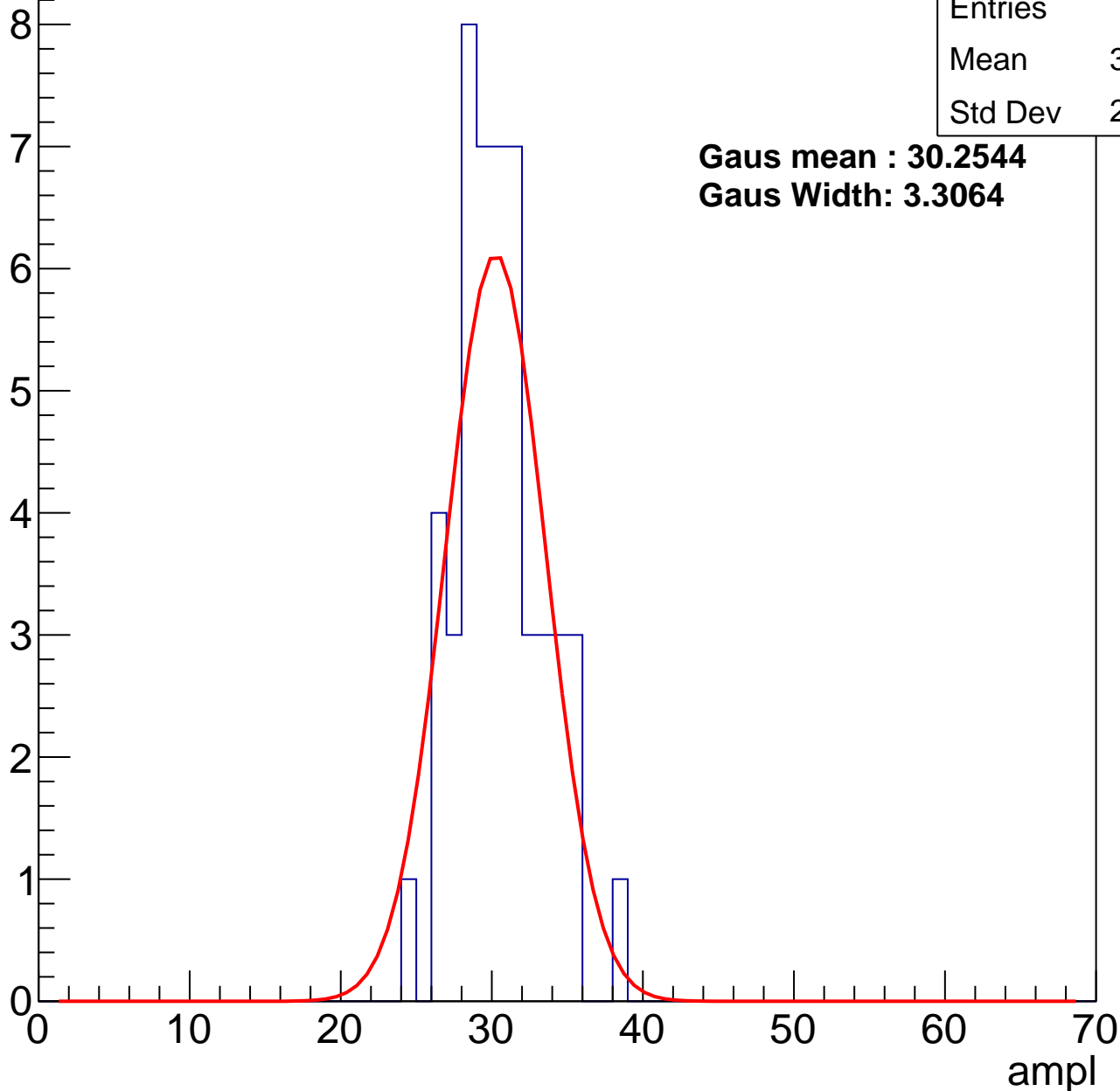
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	30.06
Std Dev	2.824

Gaus mean : 30.2544

Gaus Width: 3.3064



B0L001S, U15-ch58, adc1

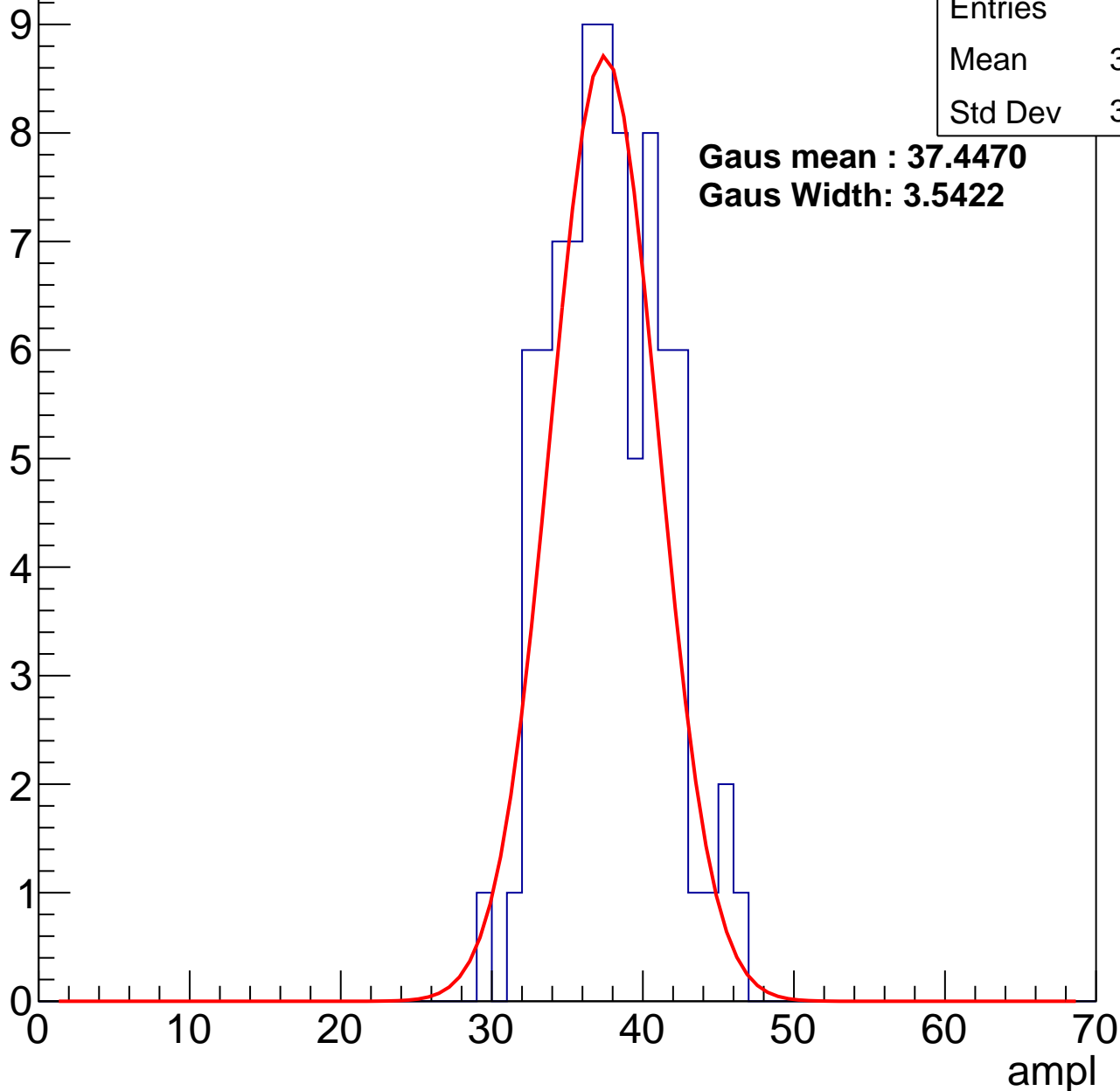
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	37.26
Std Dev	3.586

Gaus mean : 37.4470

Gaus Width: 3.5422



B0L001S, U15-ch58, adc2

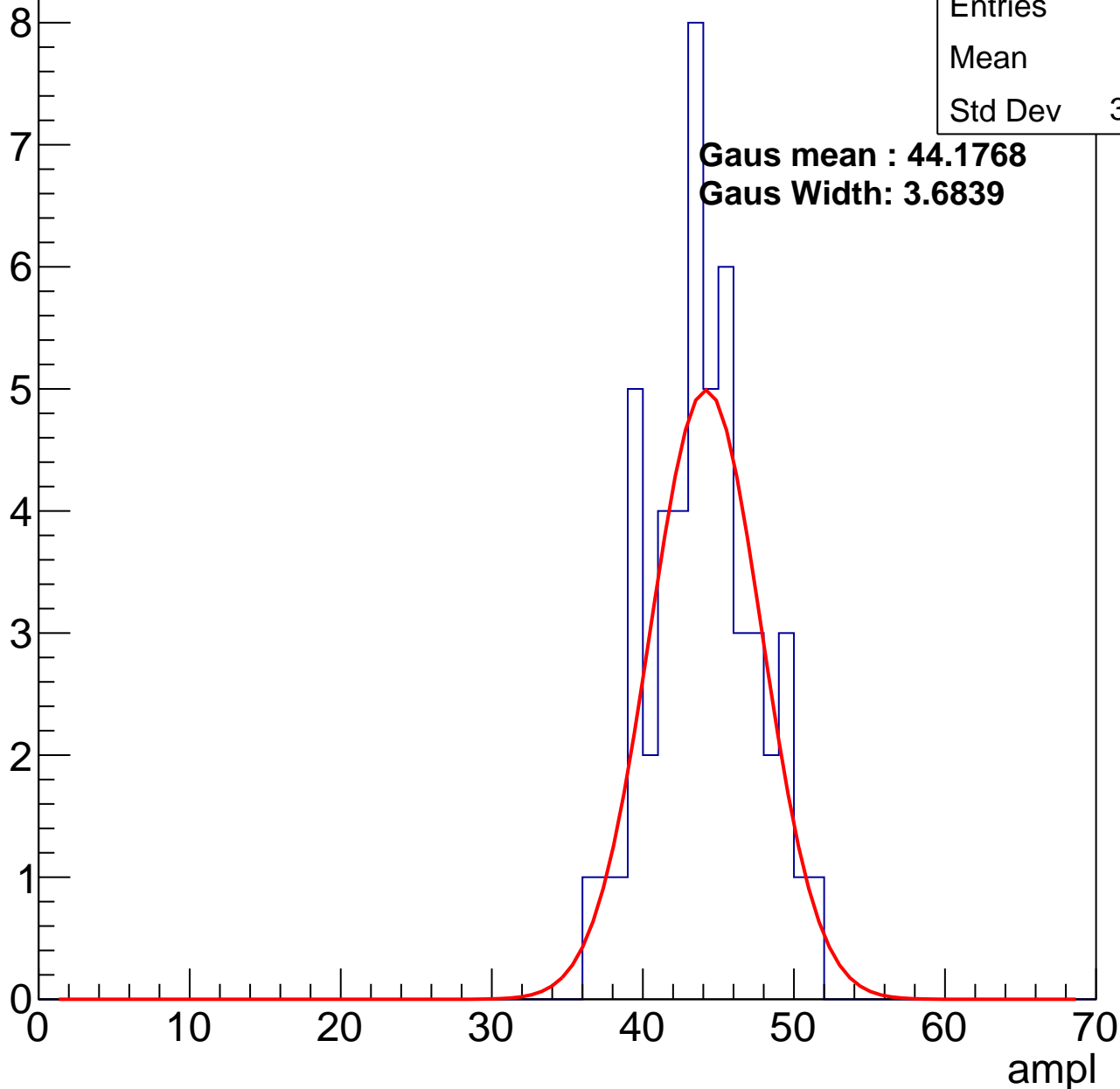
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	43.5
Std Dev	3.437

Gaus mean : 44.1768

Gaus Width: 3.6839

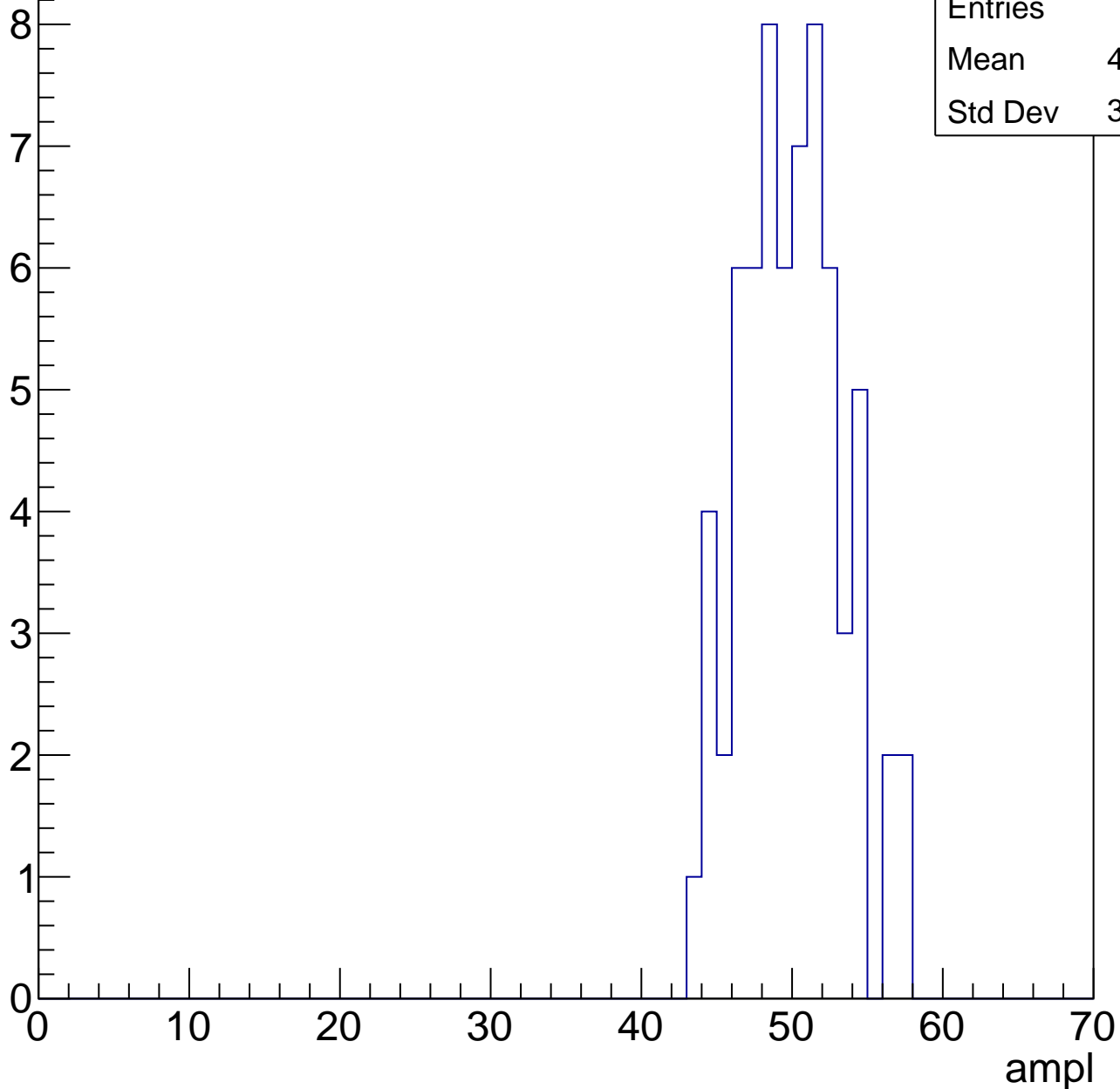


B0L001S, U15-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.55
Std Dev	3.317

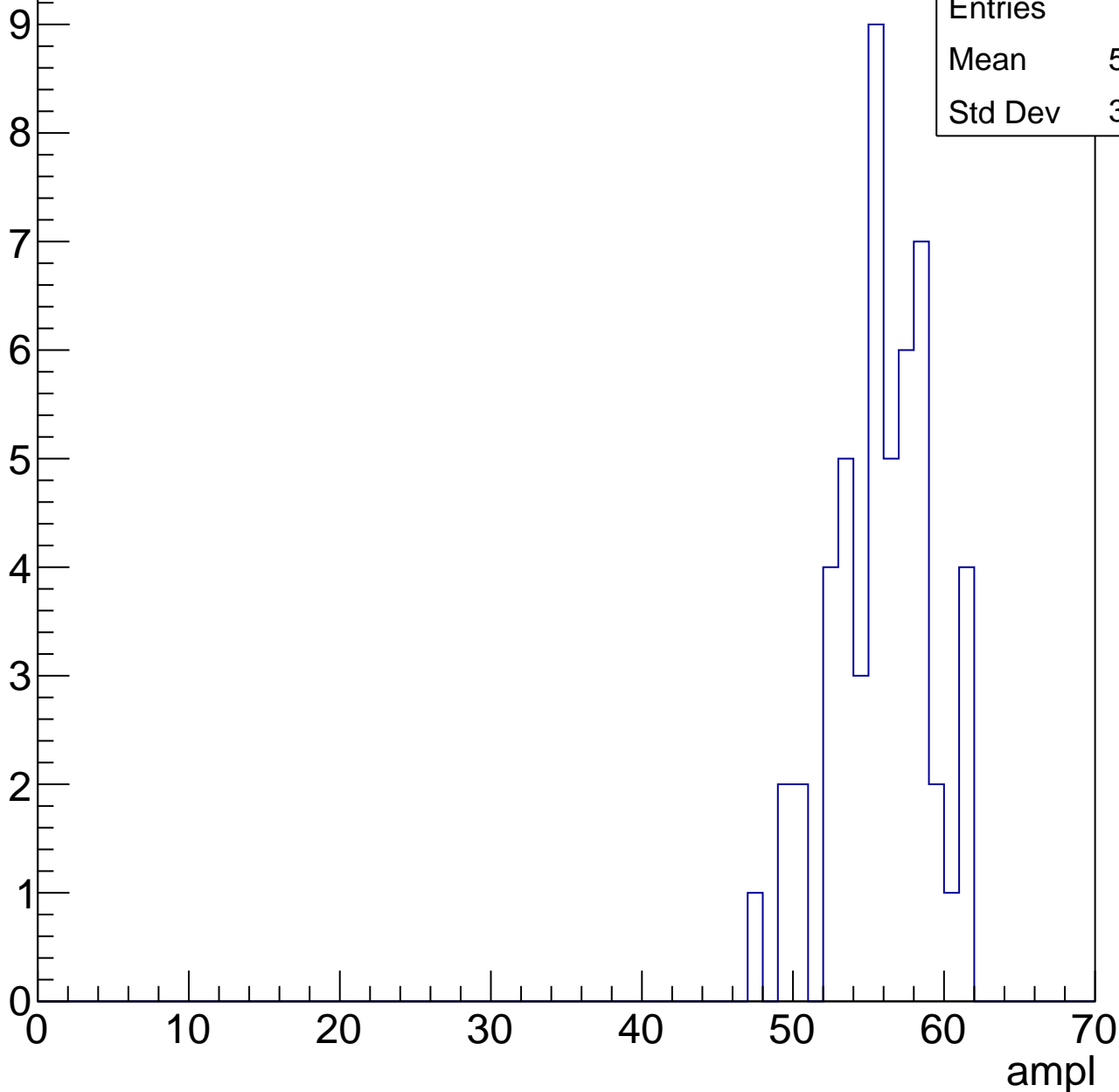


B0L001S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

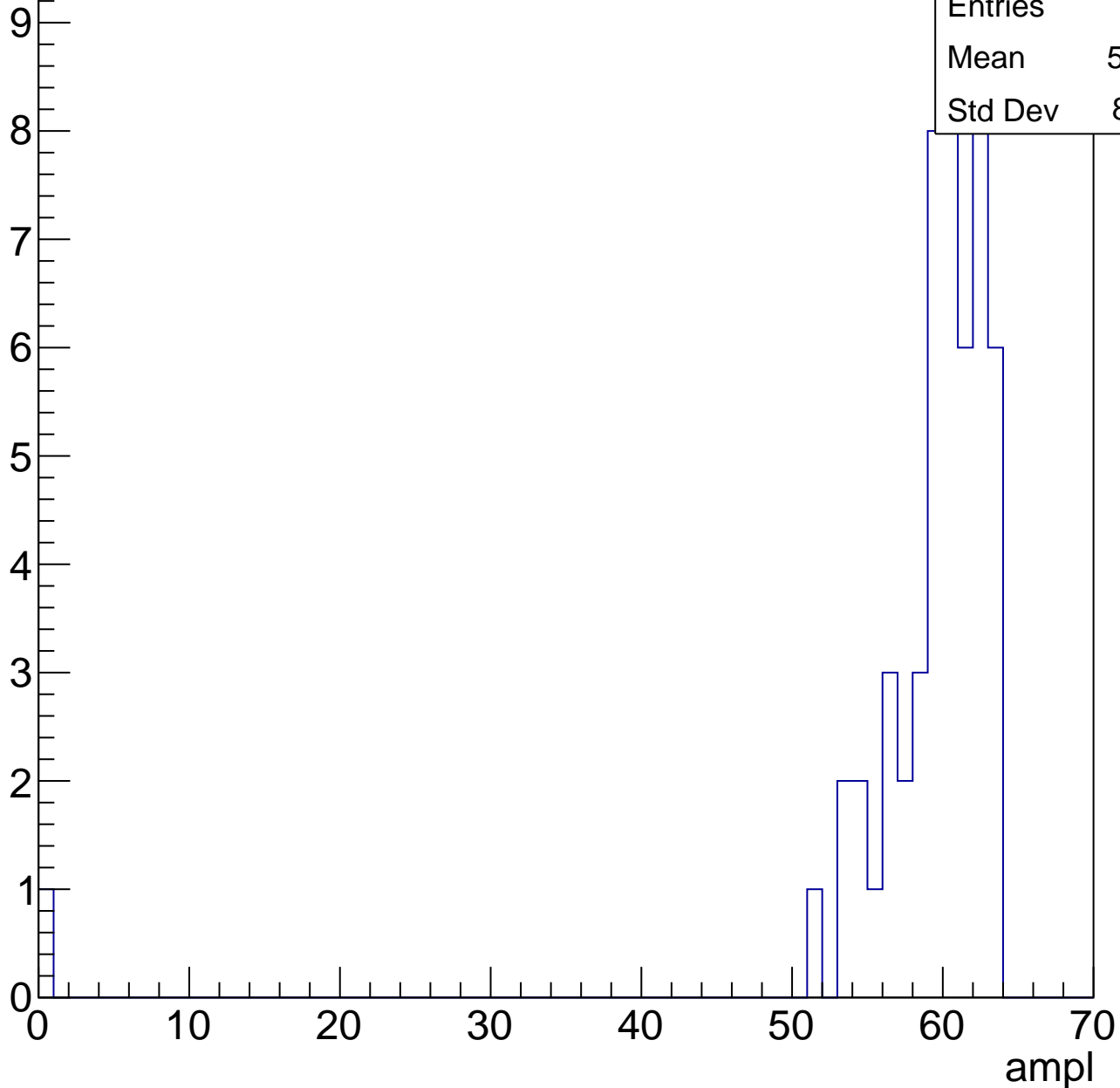
Entries	51
Mean	55.39
Std Dev	3.224



B0L001S, U15-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch59, adc0

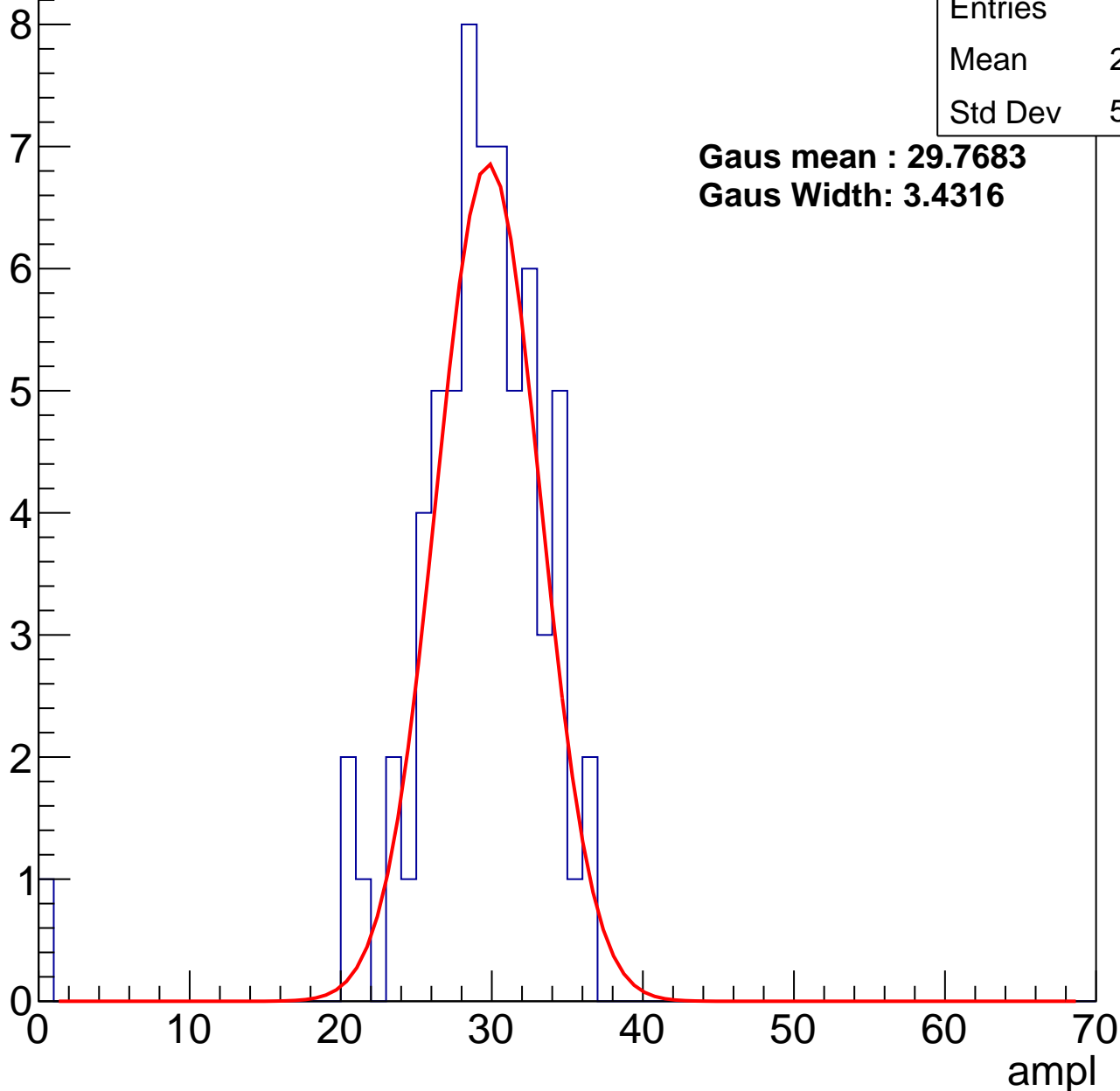
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	28.55
Std Dev	5.078

Gaus mean : 29.7683

Gaus Width: 3.4316



B0L001S, U15-ch59, adc1

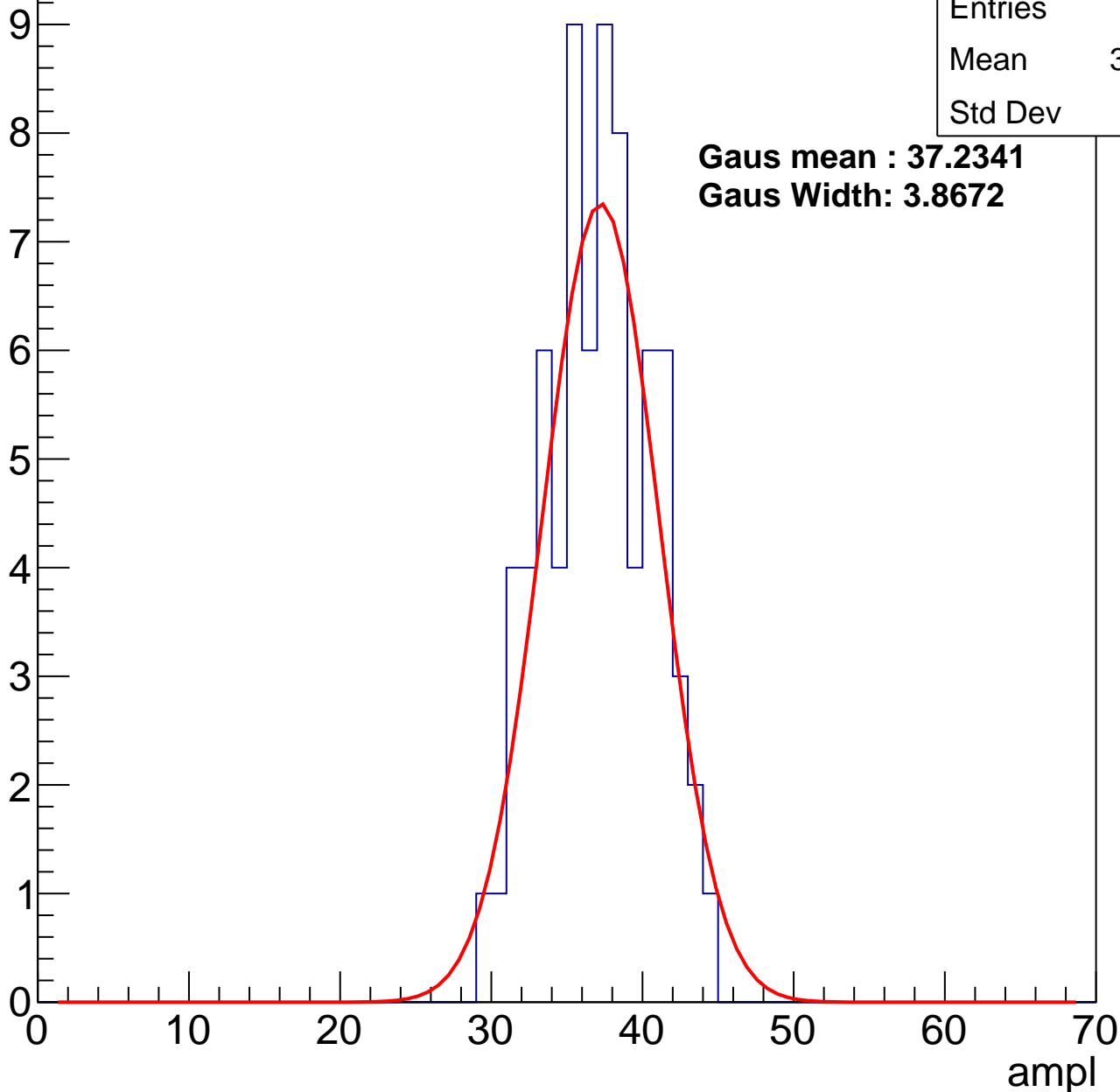
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	36.64
Std Dev	3.47

Gaus mean : 37.2341

Gaus Width: 3.8672



B0L001S, U15-ch59, adc2

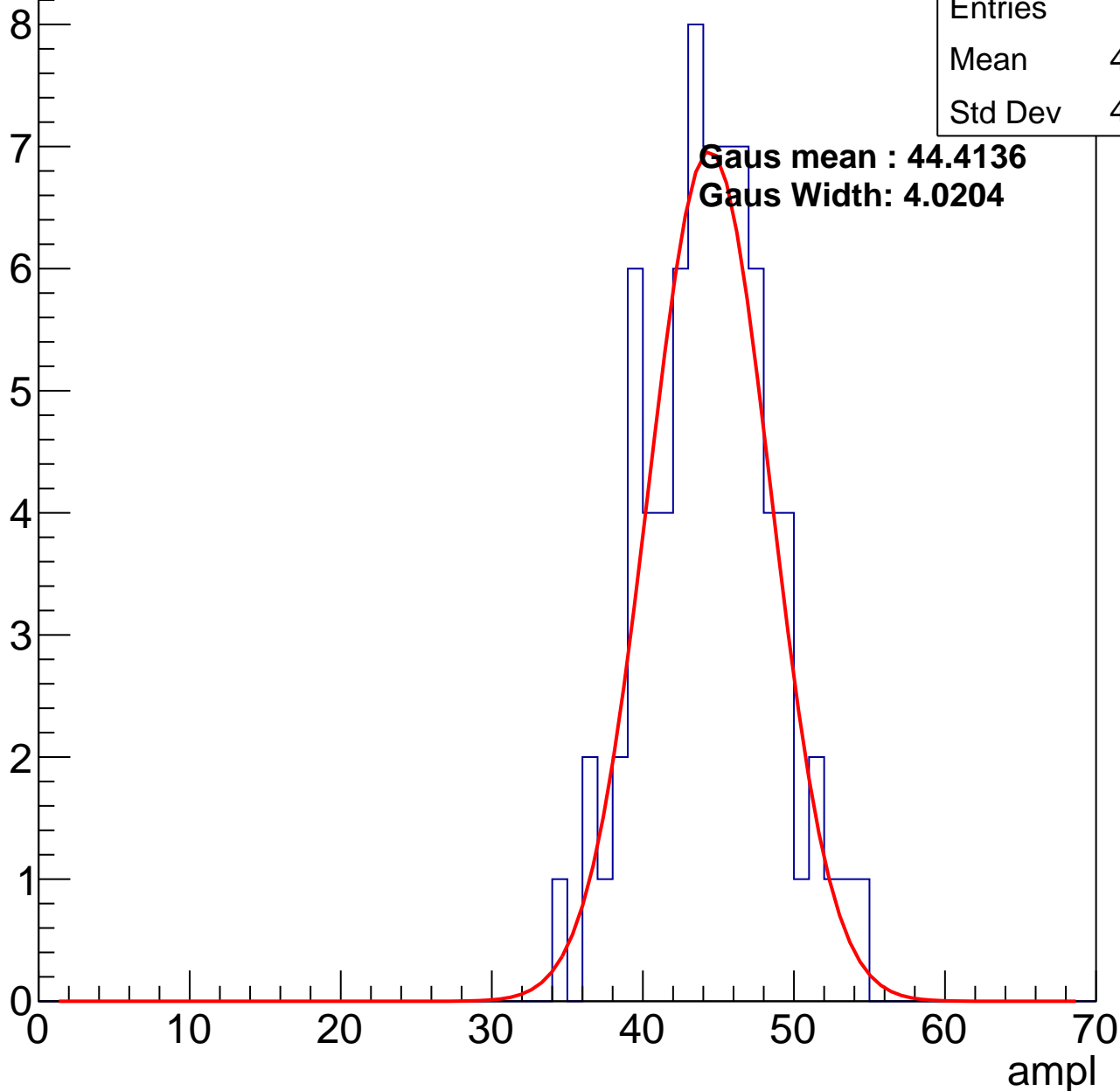
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.99
Std Dev	4.104

Gaus mean : 44.4136

Gaus Width: 4.0204

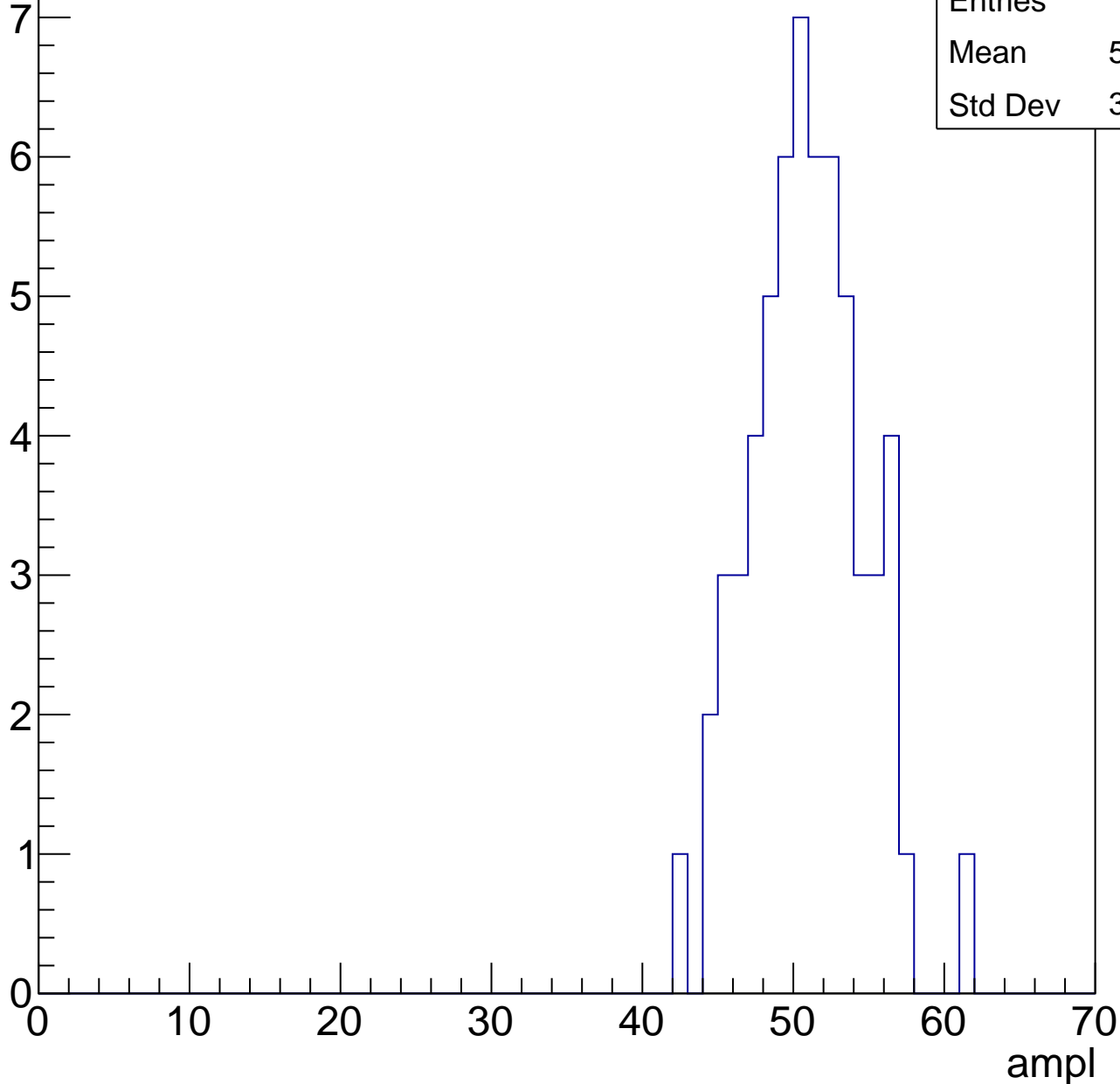


B0L001S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.45
Std Dev	3.699

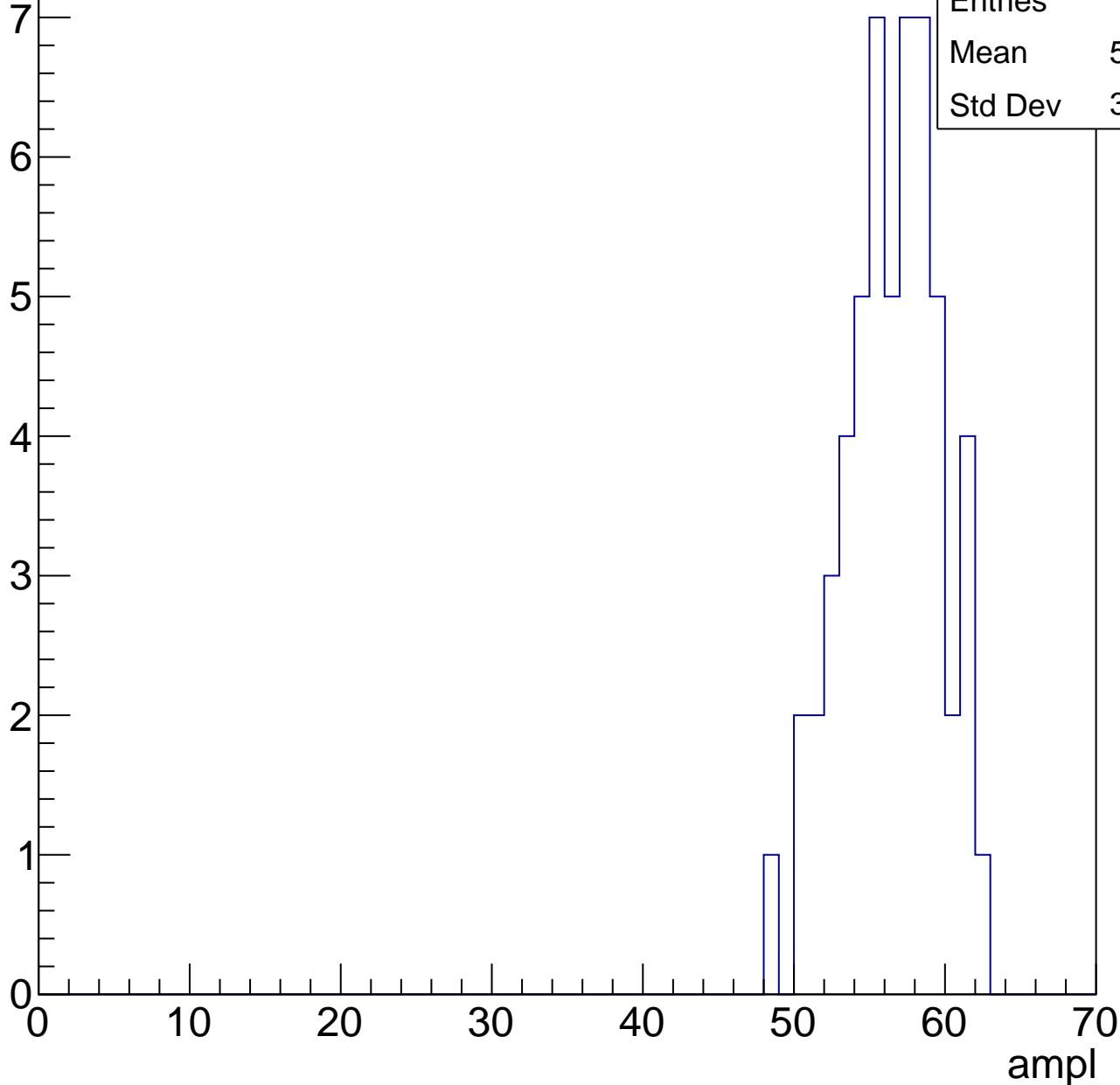


B0L001S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	55.98
Std Dev	3.148

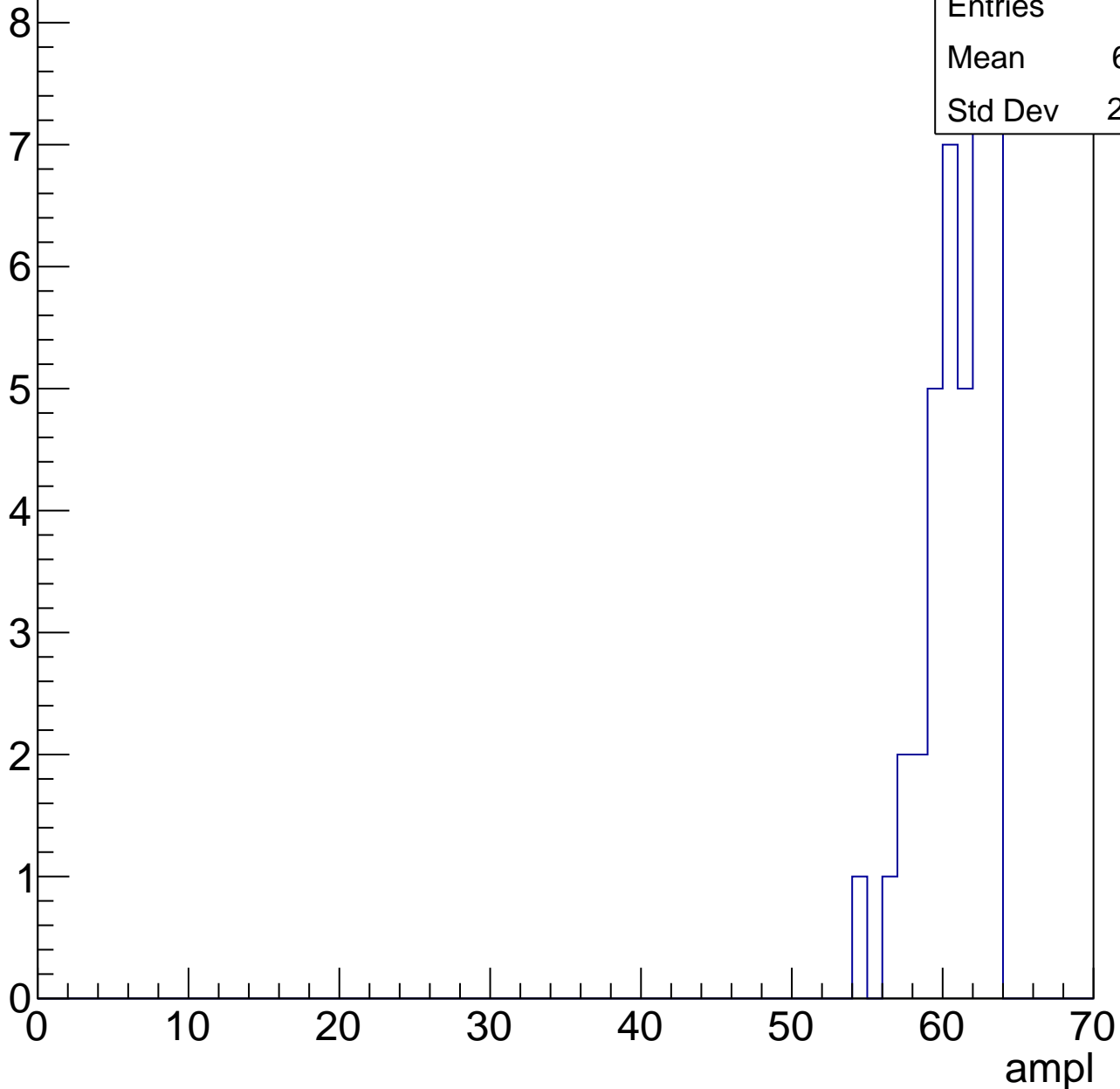


B0L001S, U15-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	60.51
Std Dev	2.159



B0L001S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

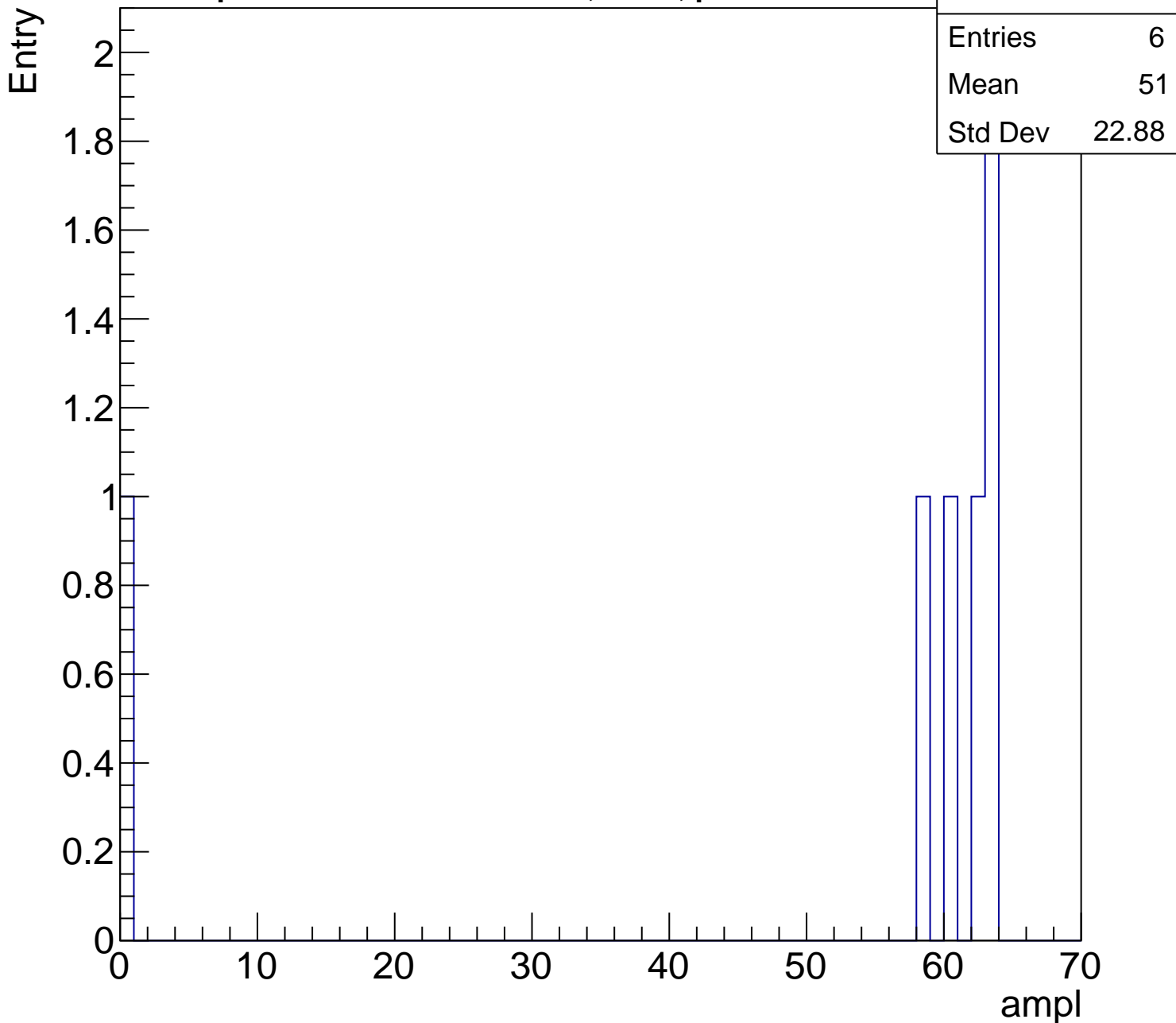
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51
Std Dev	22.88

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch60, adc0

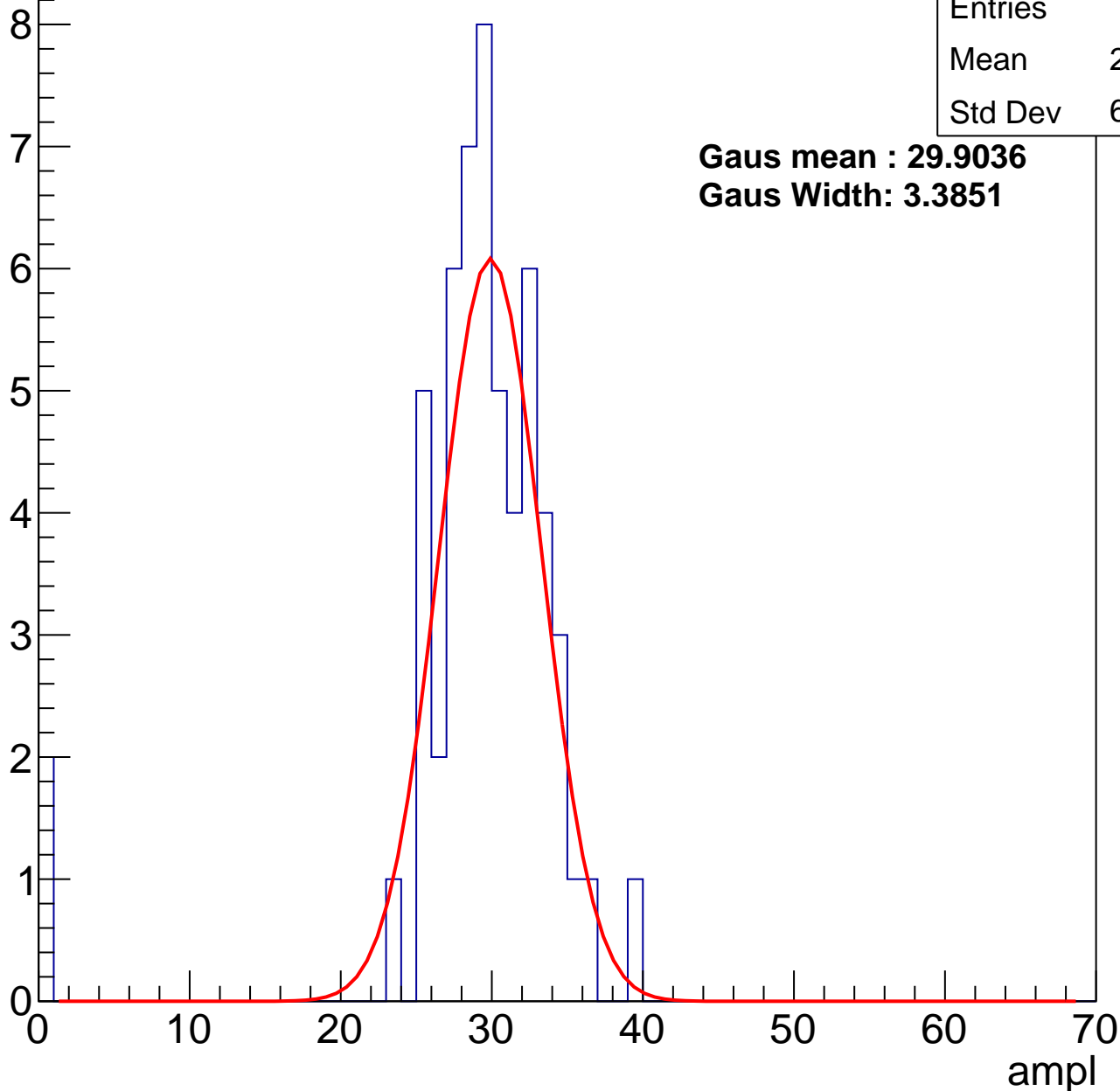
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	28.57
Std Dev	6.319

Gaus mean : 29.9036

Gaus Width: 3.3851



B0L001S, U15-ch60, adc1

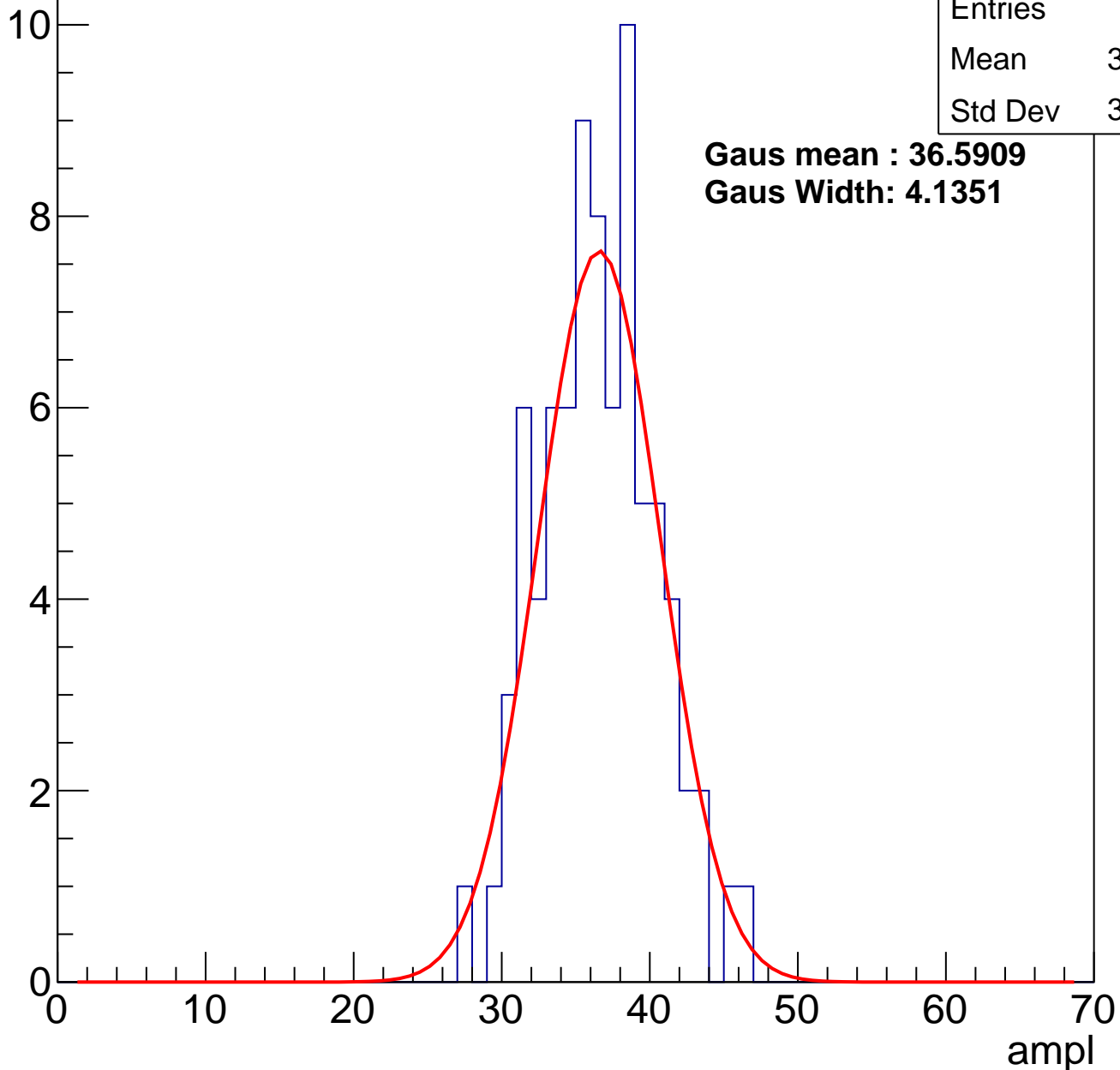
calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	36.09
Std Dev	3.809

Gaus mean : 36.5909

Gaus Width: 4.1351

Entry



B0L001S, U15-ch60, adc2

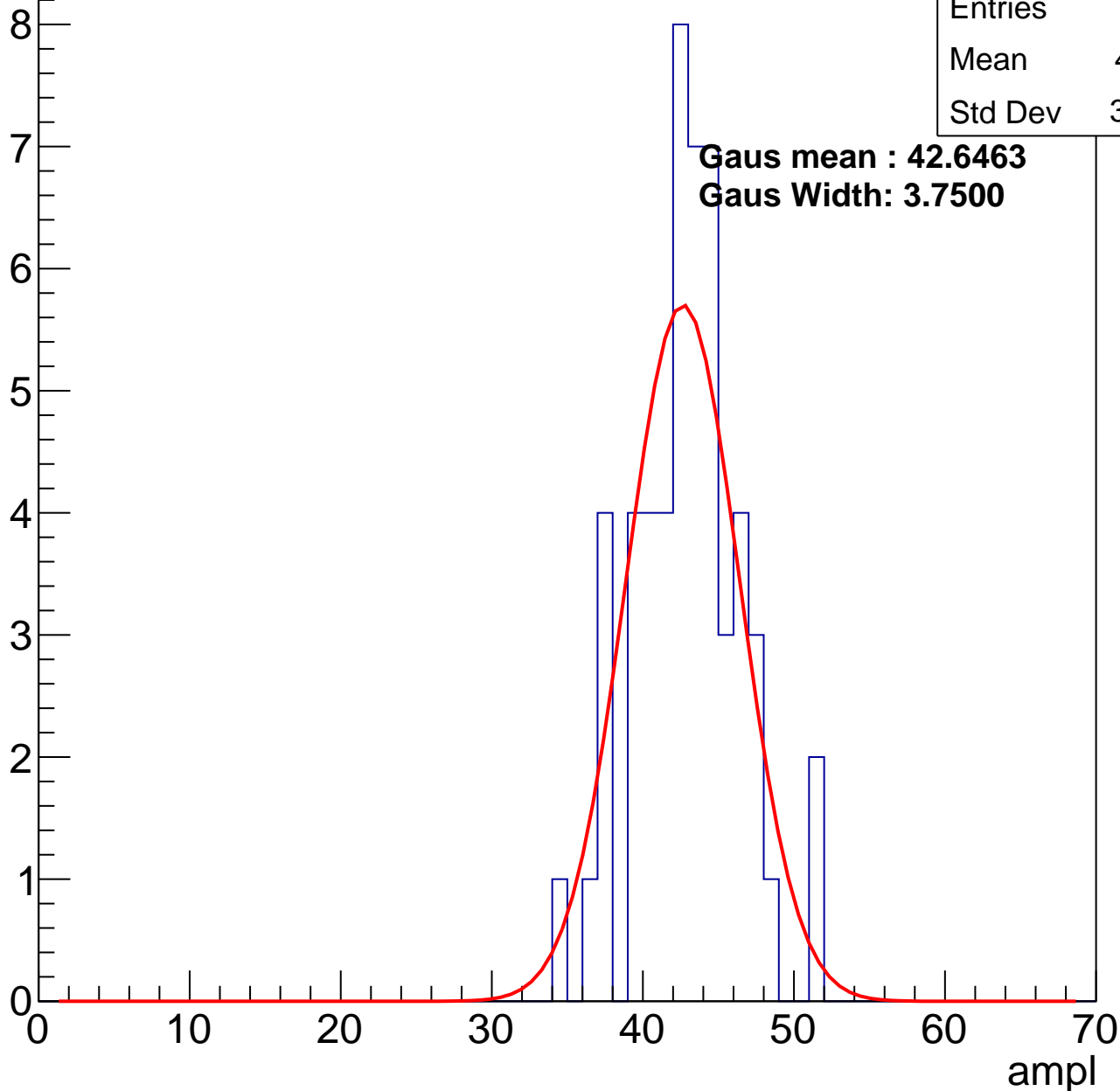
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.51
Std Dev	3.478

Gaus mean : 42.6463

Gaus Width: 3.7500

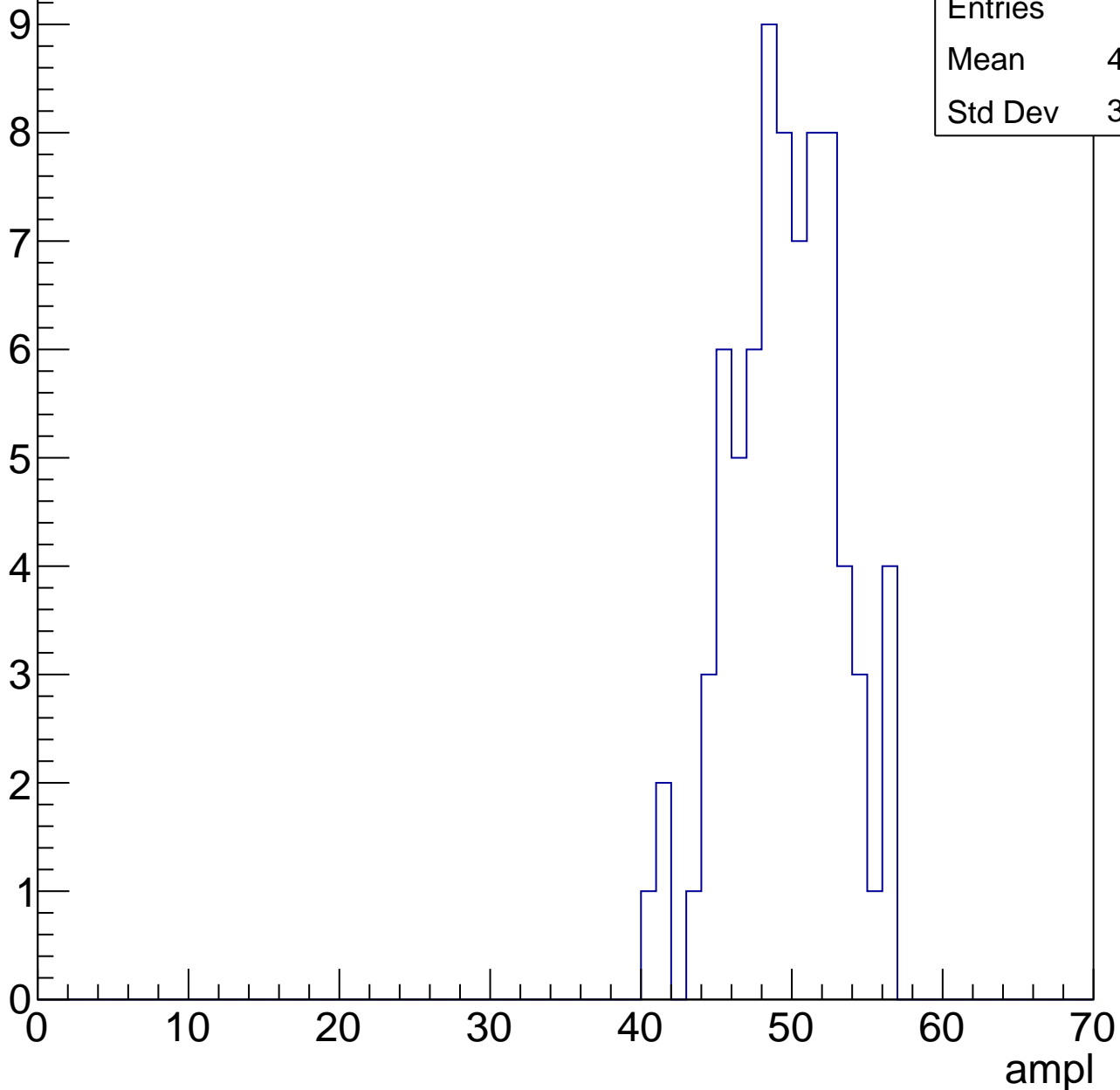


B0L001S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	49.08
Std Dev	3.583

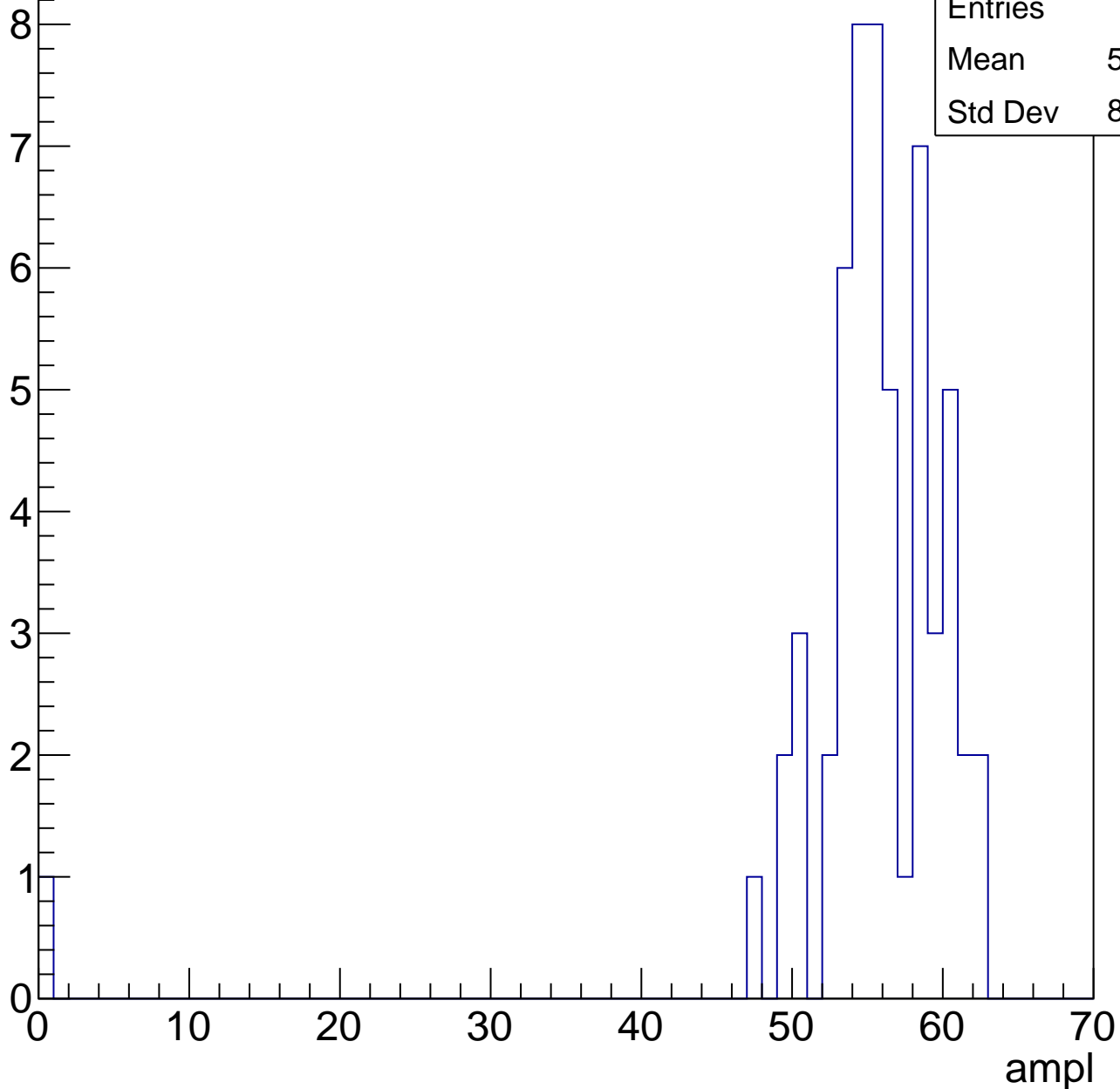


B0L001S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

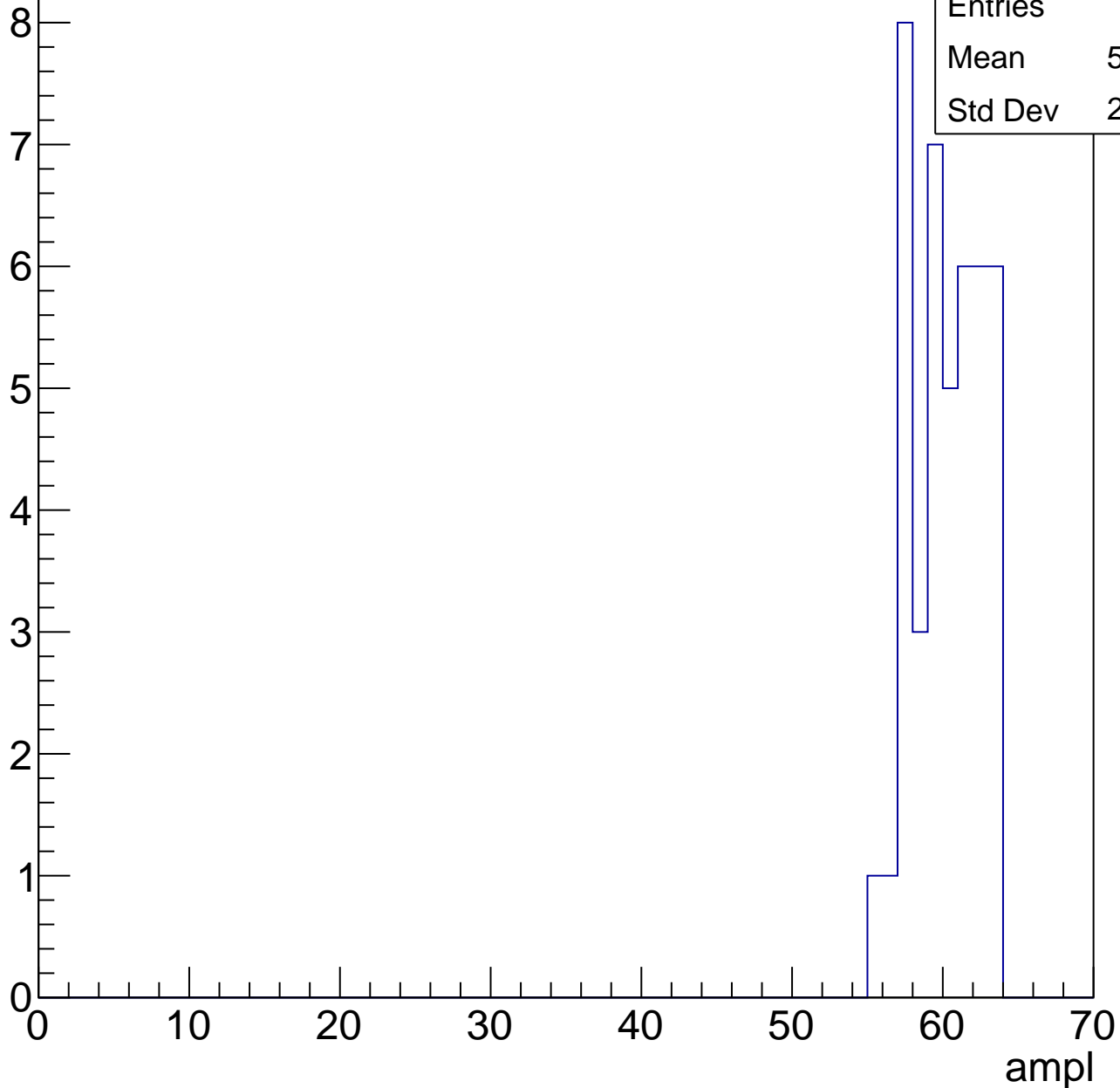
Entries	56
Mean	54.55
Std Dev	8.117



B0L001S, U15-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

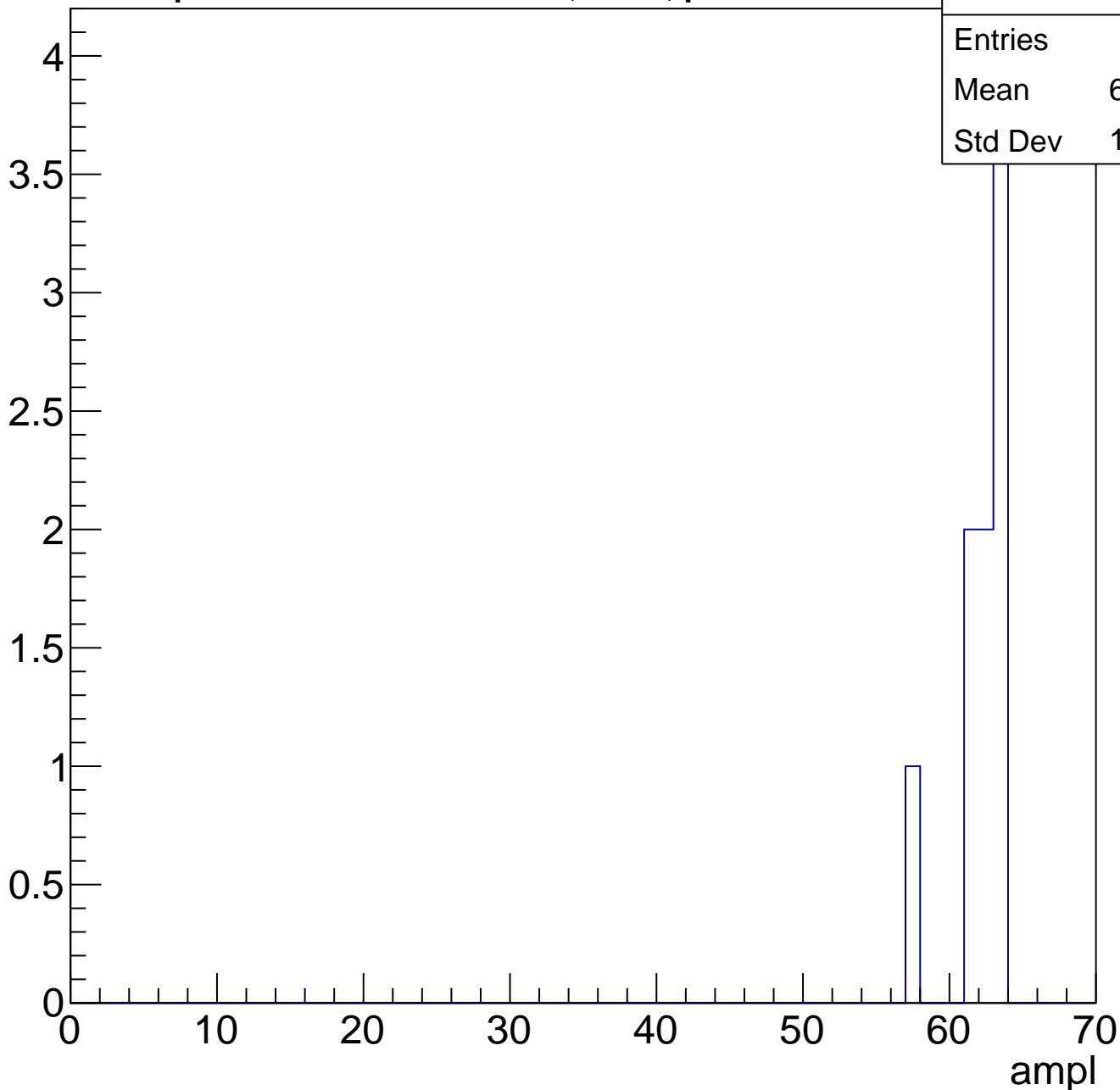
Entry



B0L001S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch61, adc0

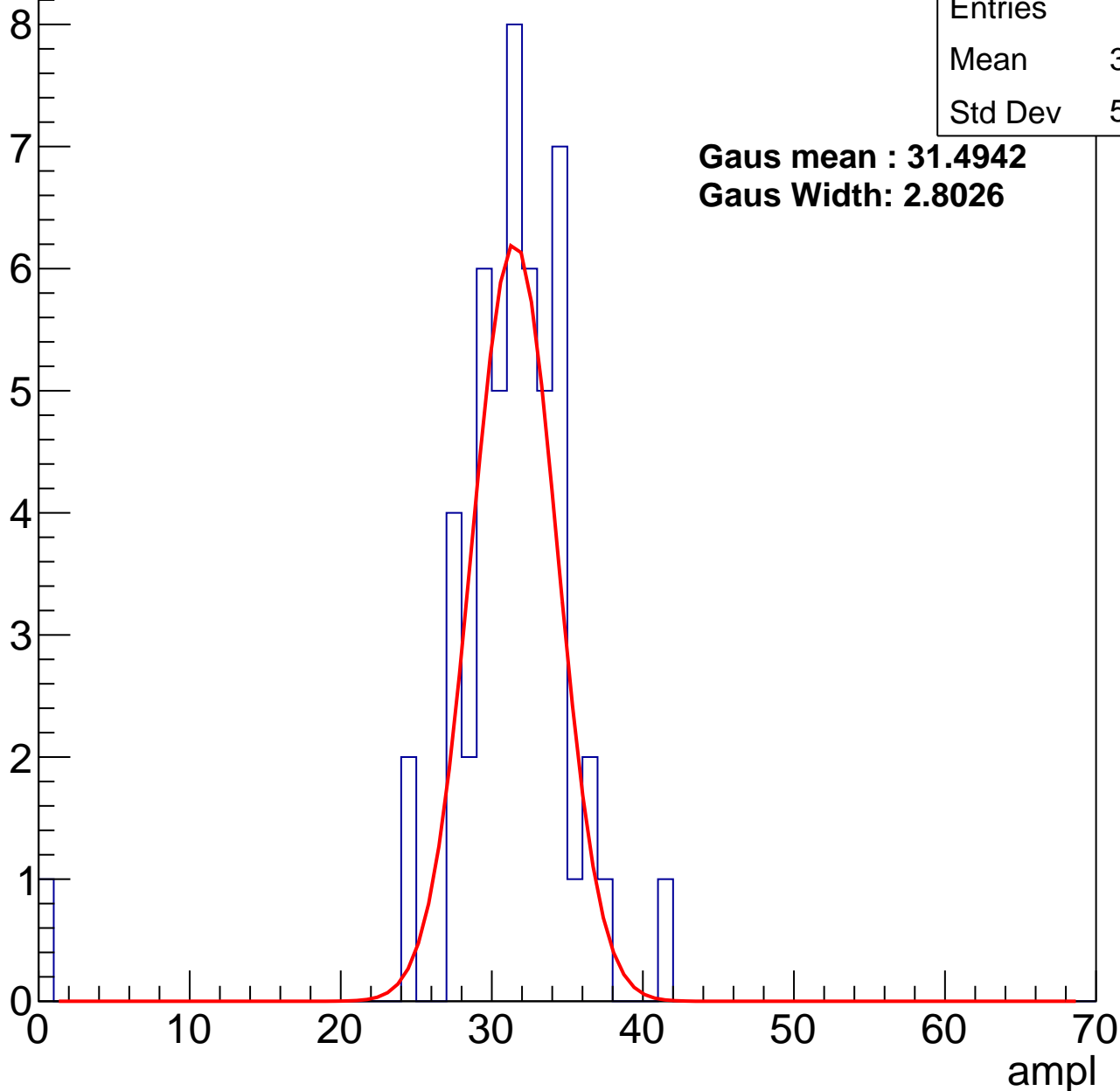
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30.67
Std Dev	5.342

Gaus mean : 31.4942

Gaus Width: 2.8026



B0L001S, U15-ch61, adc1

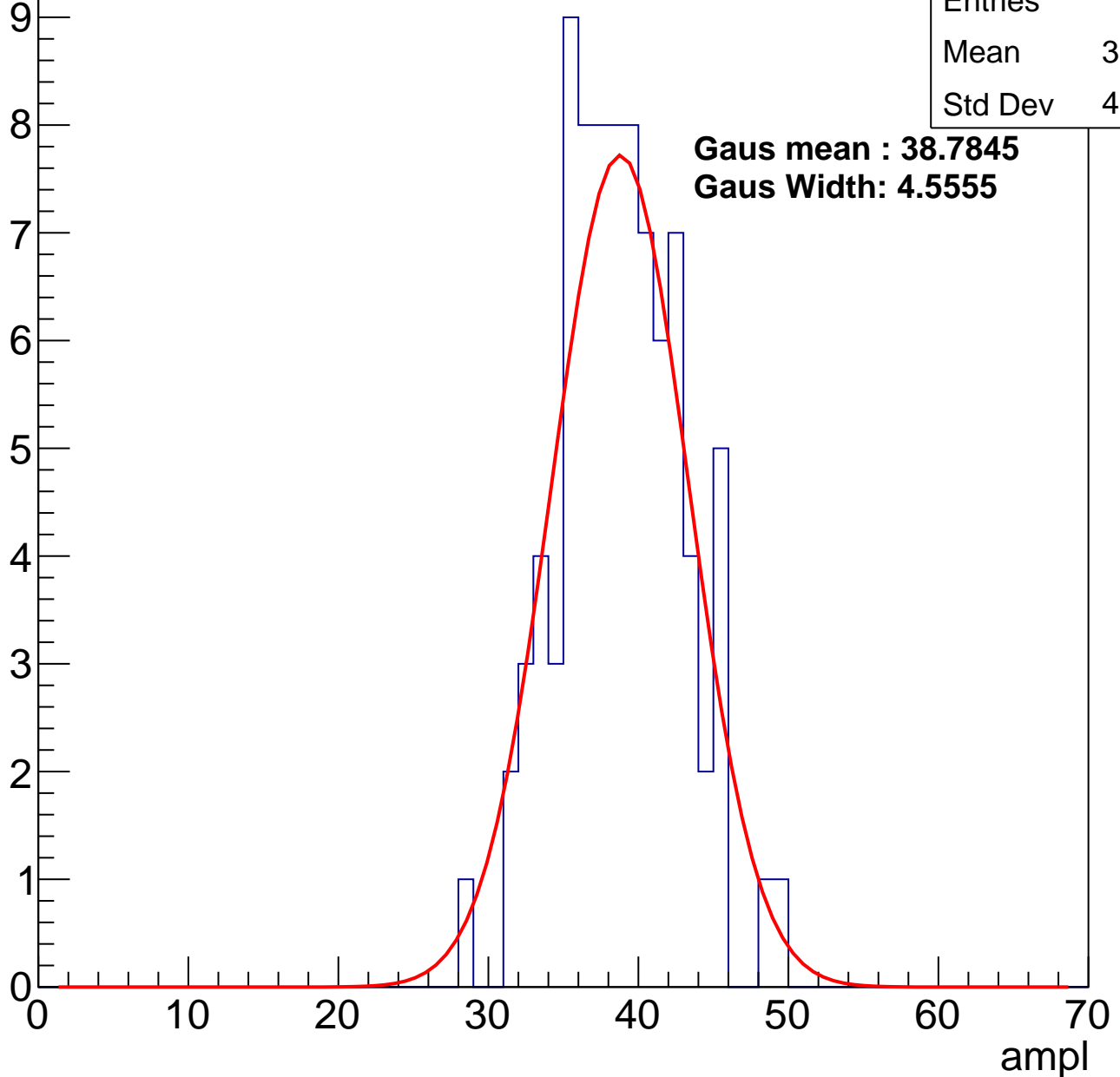
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	38.36
Std Dev	4.017

Gaus mean : 38.7845

Gaus Width: 4.5555



B0L001S, U15-ch61, adc2

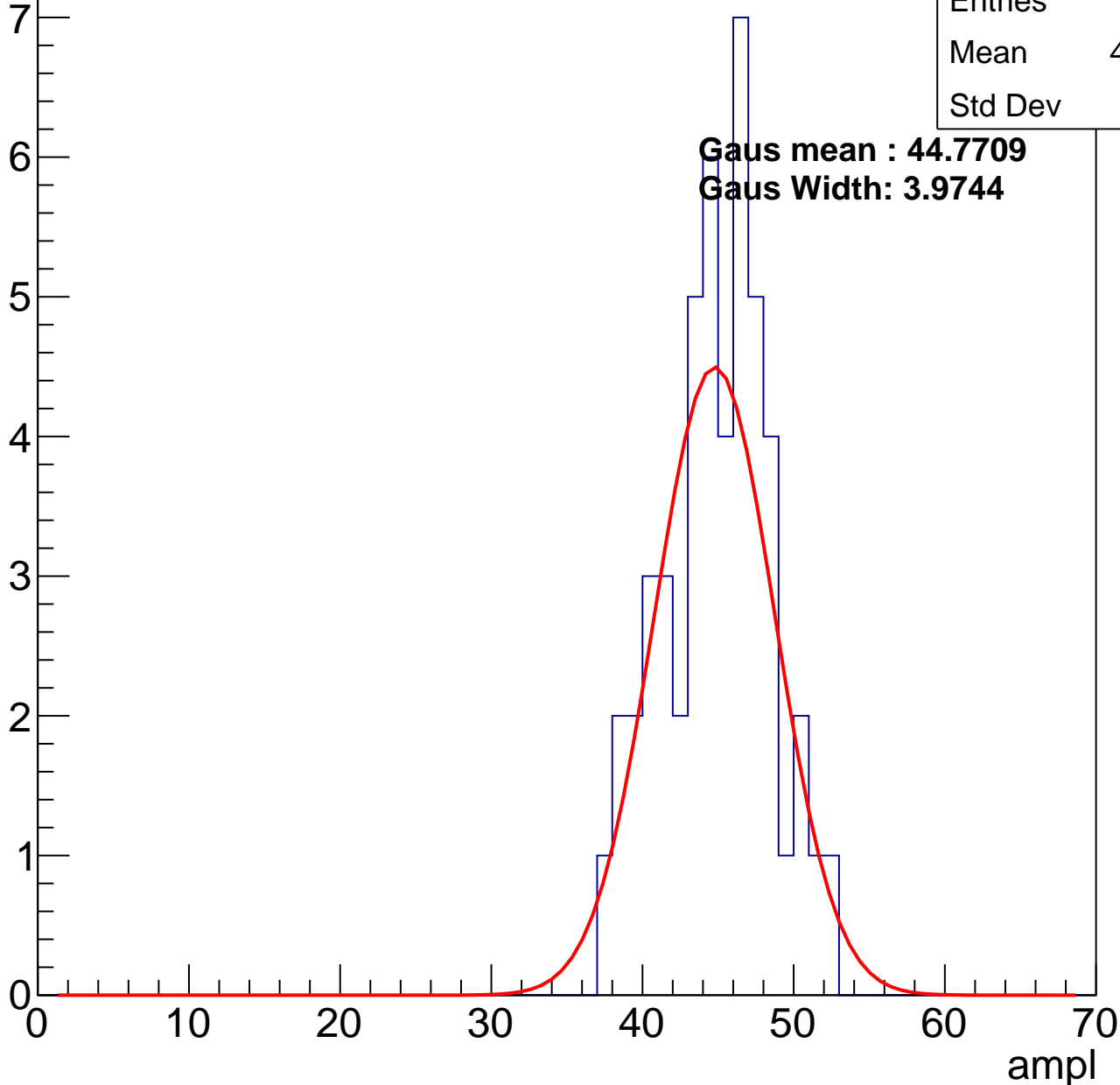
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	44.45
Std Dev	3.5

Gaus mean : 44.7709

Gaus Width: 3.9744

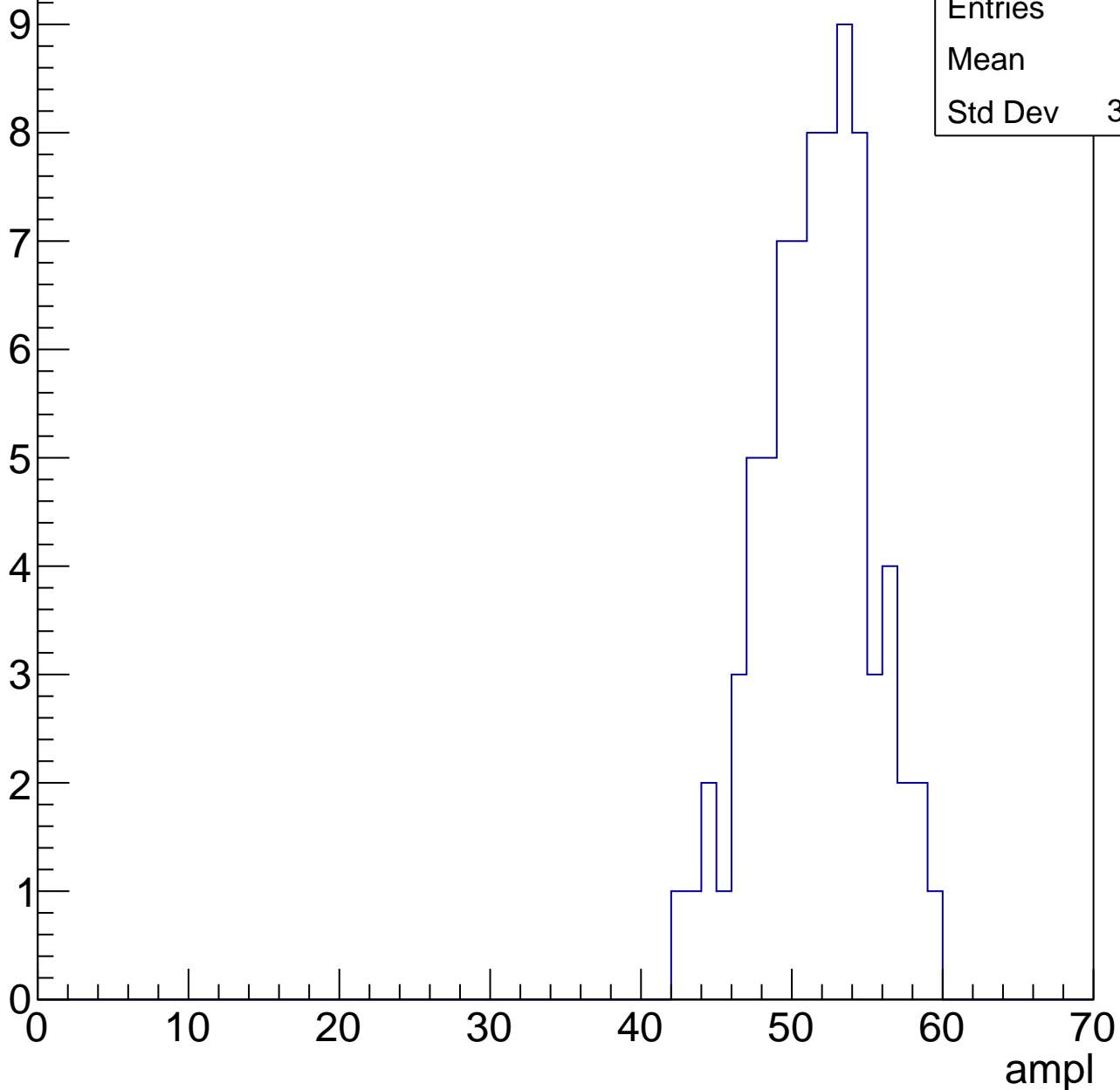


B0L001S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	51.1
Std Dev	3.624

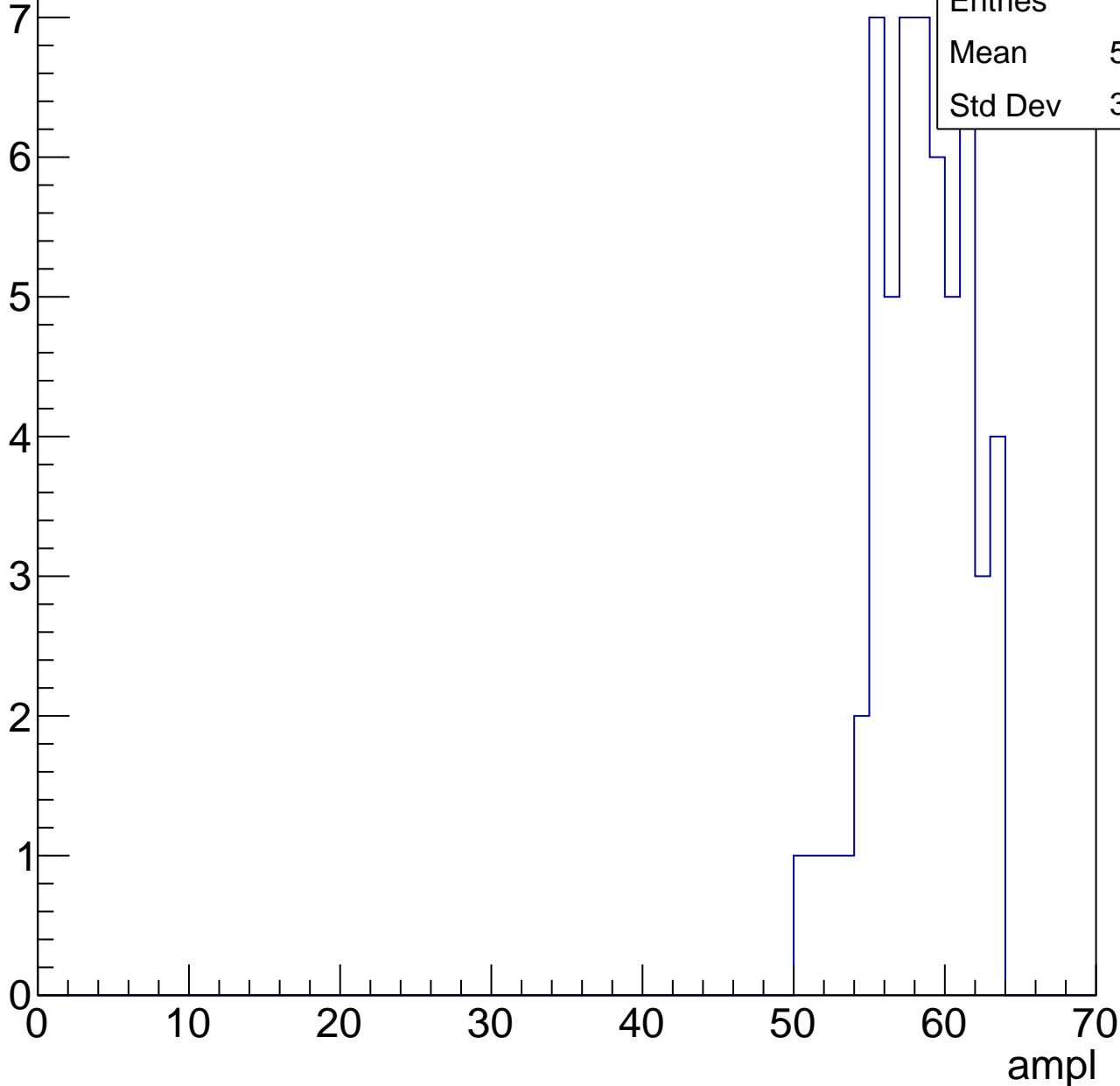


B0L001S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.95
Std Dev	3.057

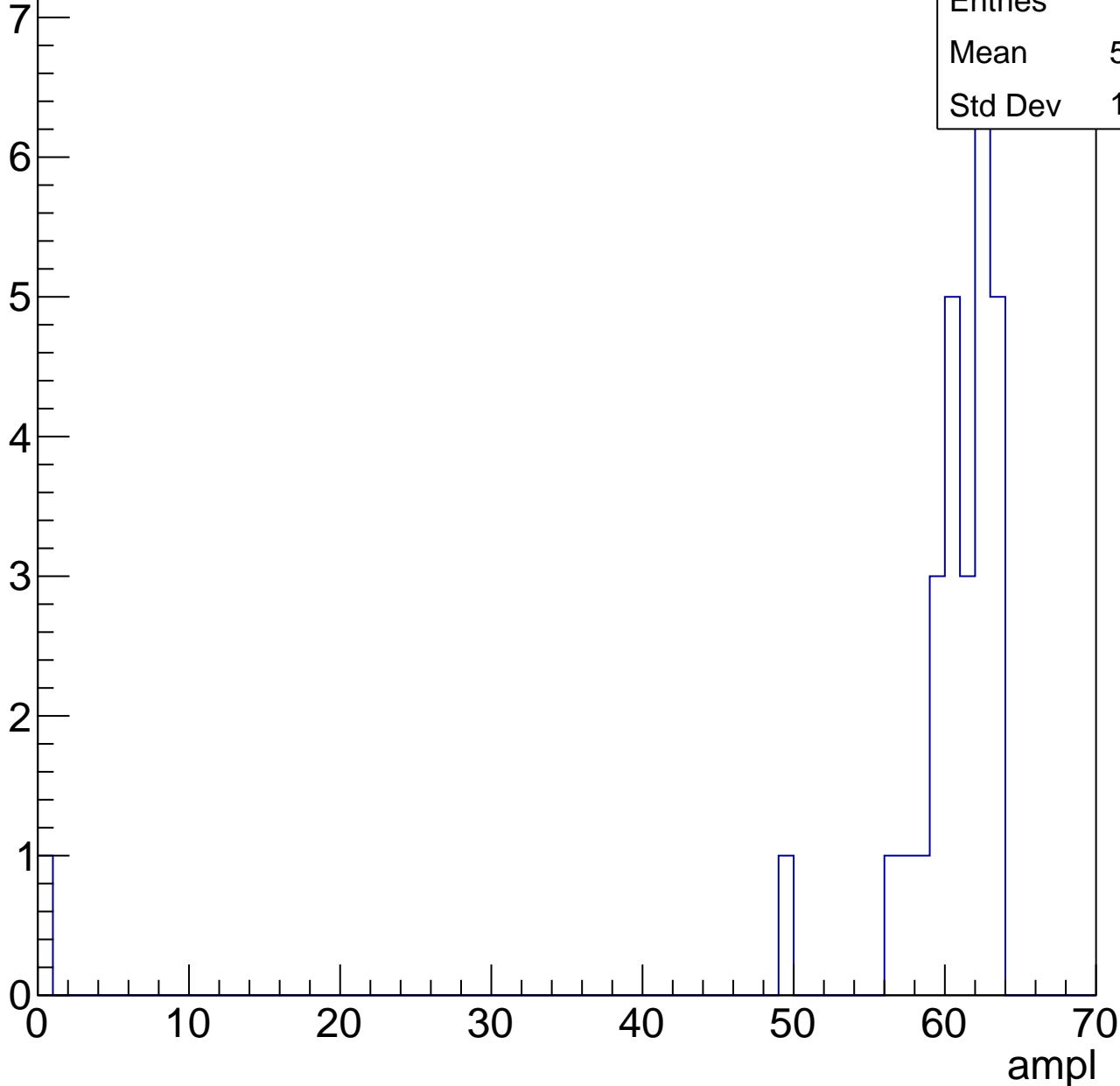


B0L001S, U15-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.18
Std Dev	11.55



B0L001S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch62, adc0

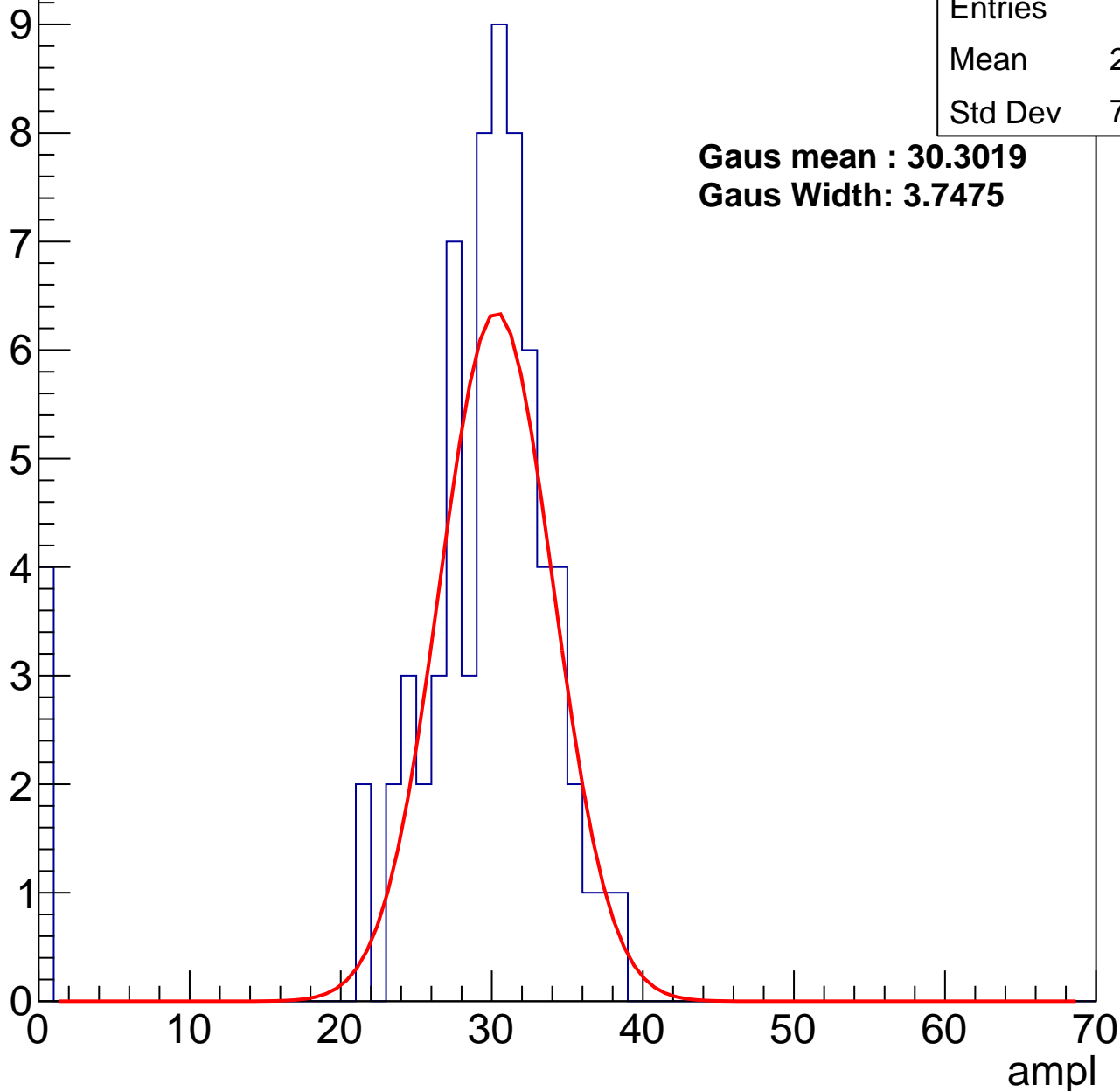
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	27.89
Std Dev	7.716

Gaus mean : 30.3019

Gaus Width: 3.7475



B0L001S, U15-ch62, adc1

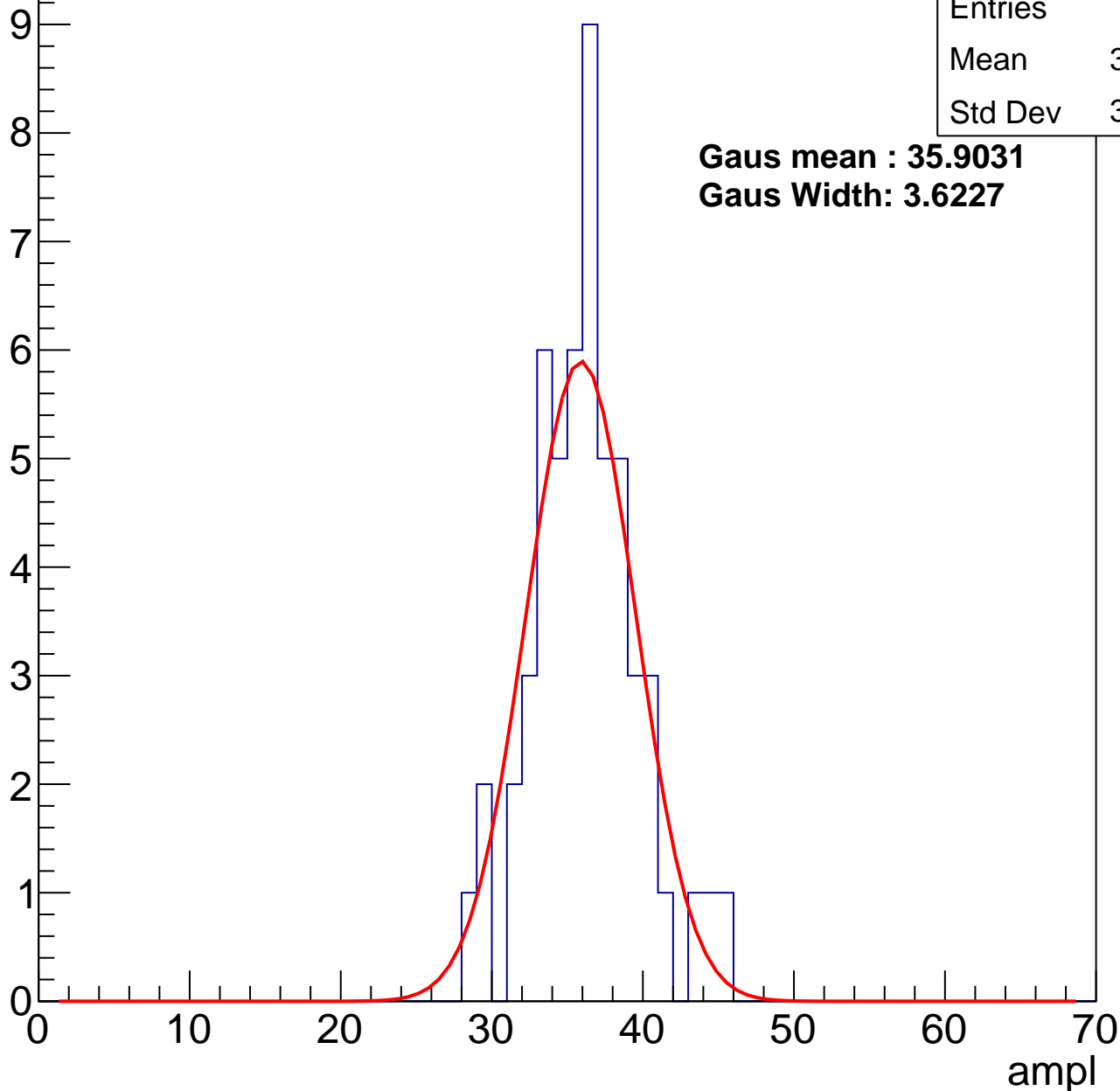
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	35.76
Std Dev	3.485

Gaus mean : 35.9031

Gaus Width: 3.6227



B0L001S, U15-ch62, adc2

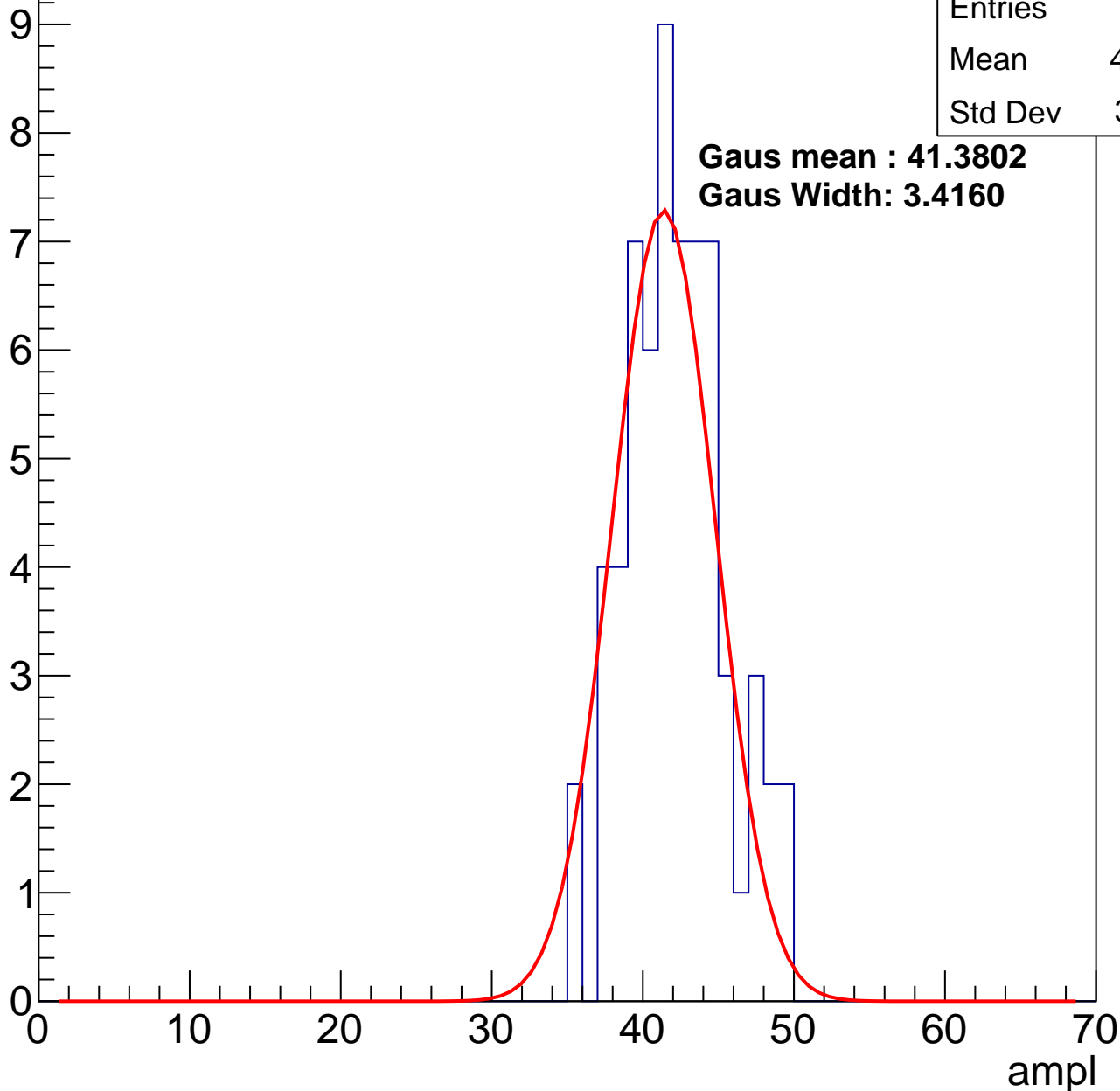
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	41.73
Std Dev	3.251

Gaus mean : 41.3802

Gaus Width: 3.4160

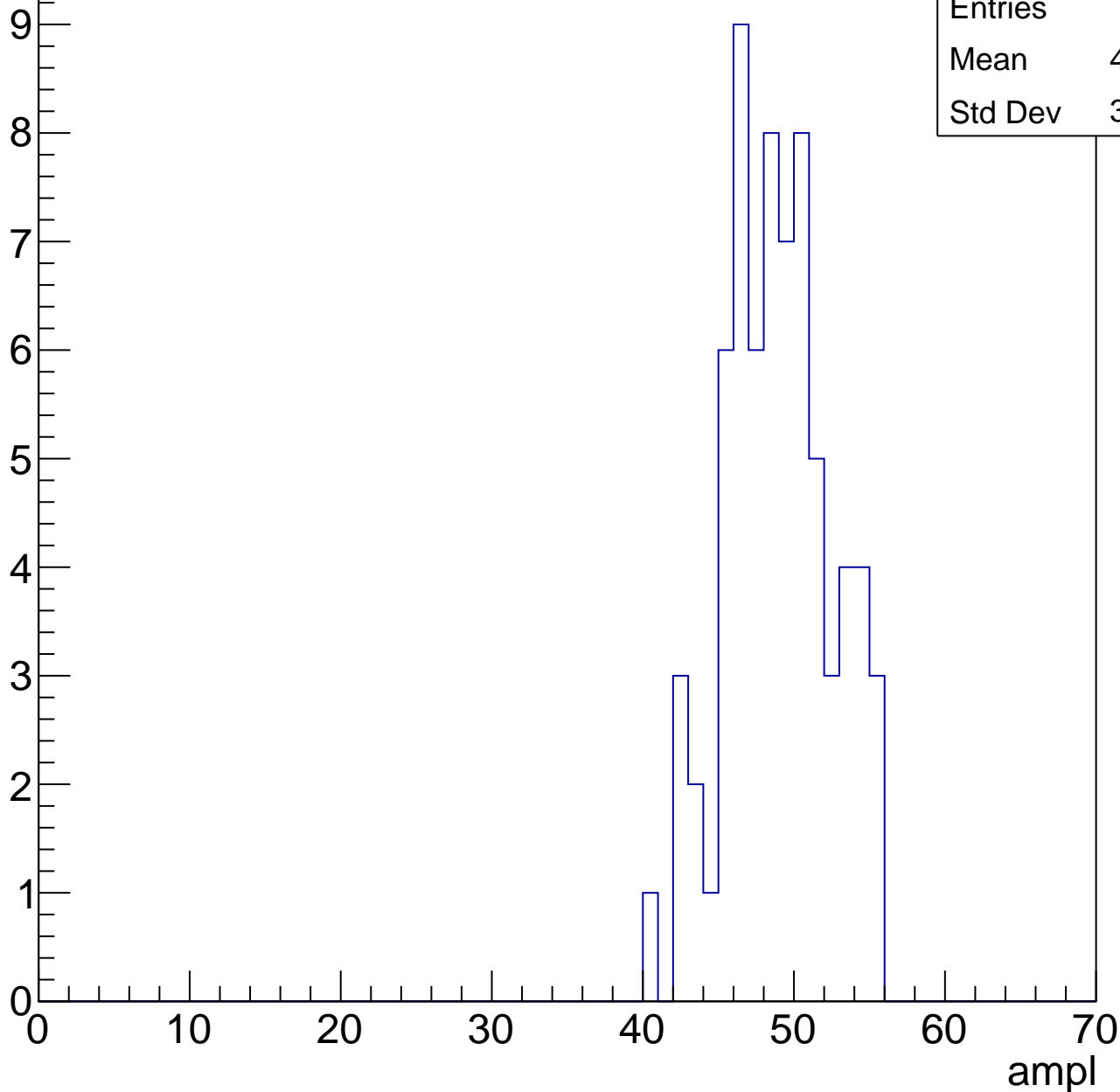


B0L001S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	48.47
Std Dev	3.467

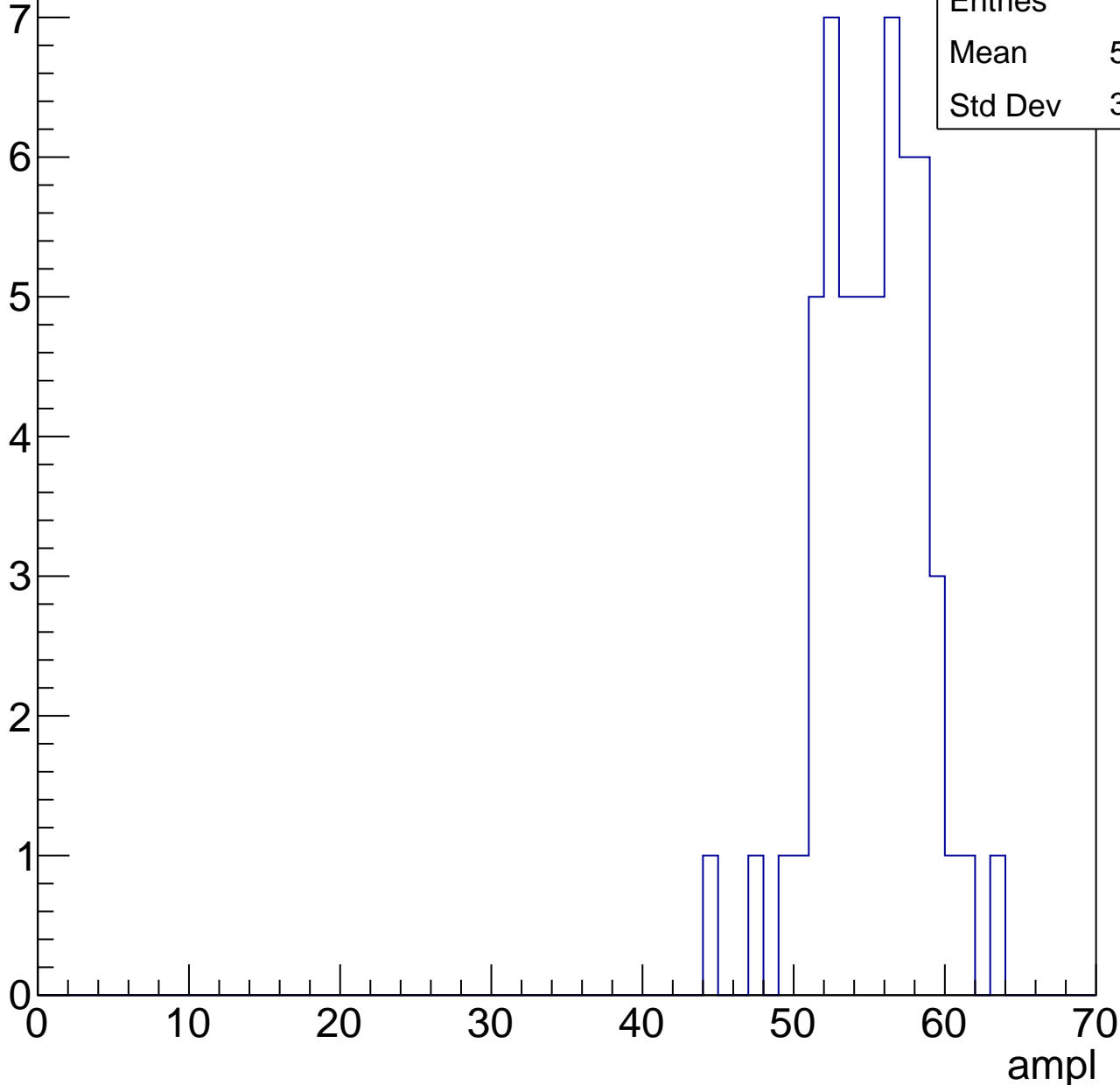


B0L001S, U15-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	54.68
Std Dev	3.449



B0L001S, U15-ch62, adc5

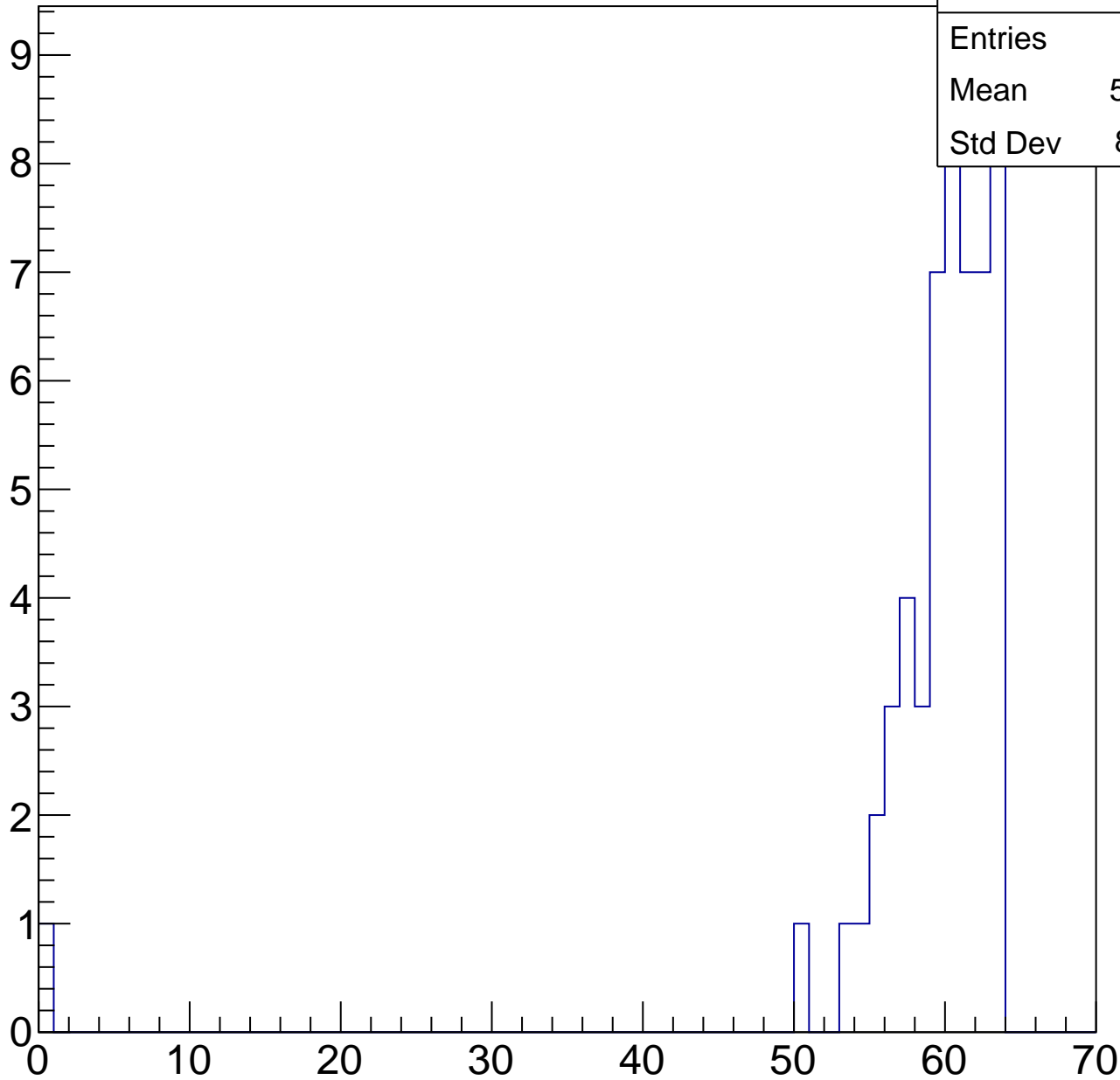
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.43
Std Dev	8.511

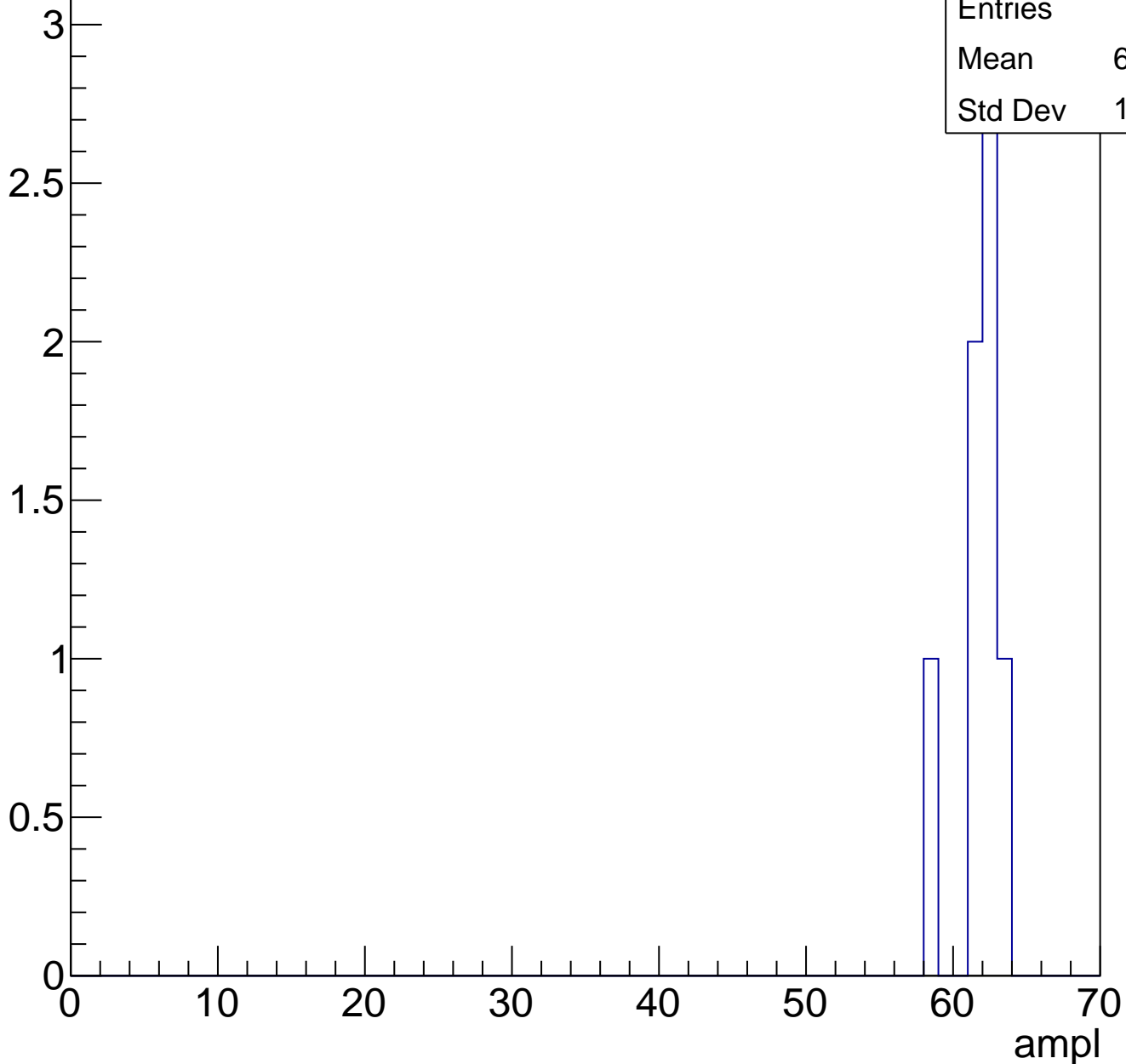
ampl



B0L001S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch63, adc0

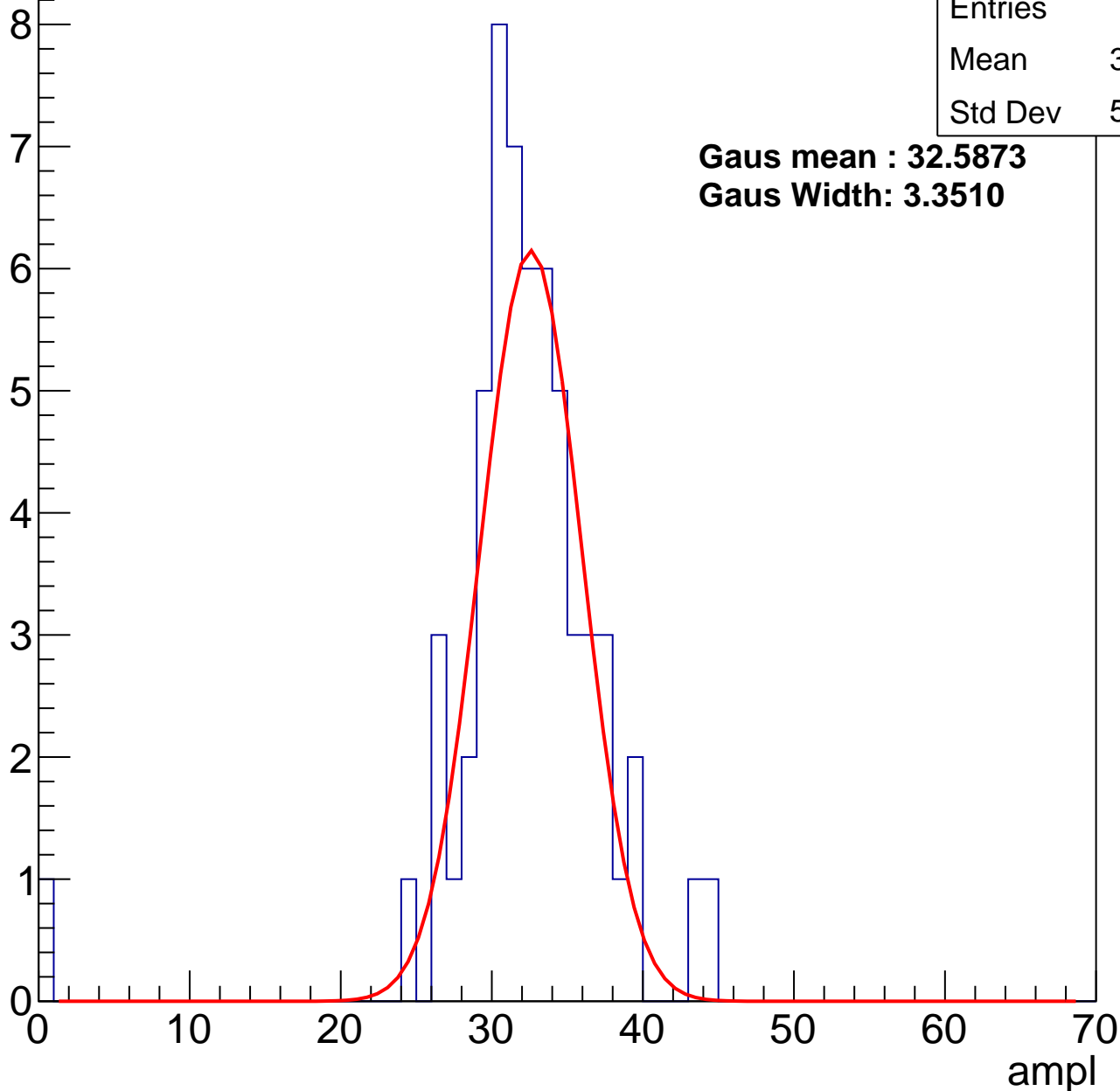
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.76
Std Dev	5.688

Gaus mean : 32.5873

Gaus Width: 3.3510



B0L001S, U15-ch63, adc1

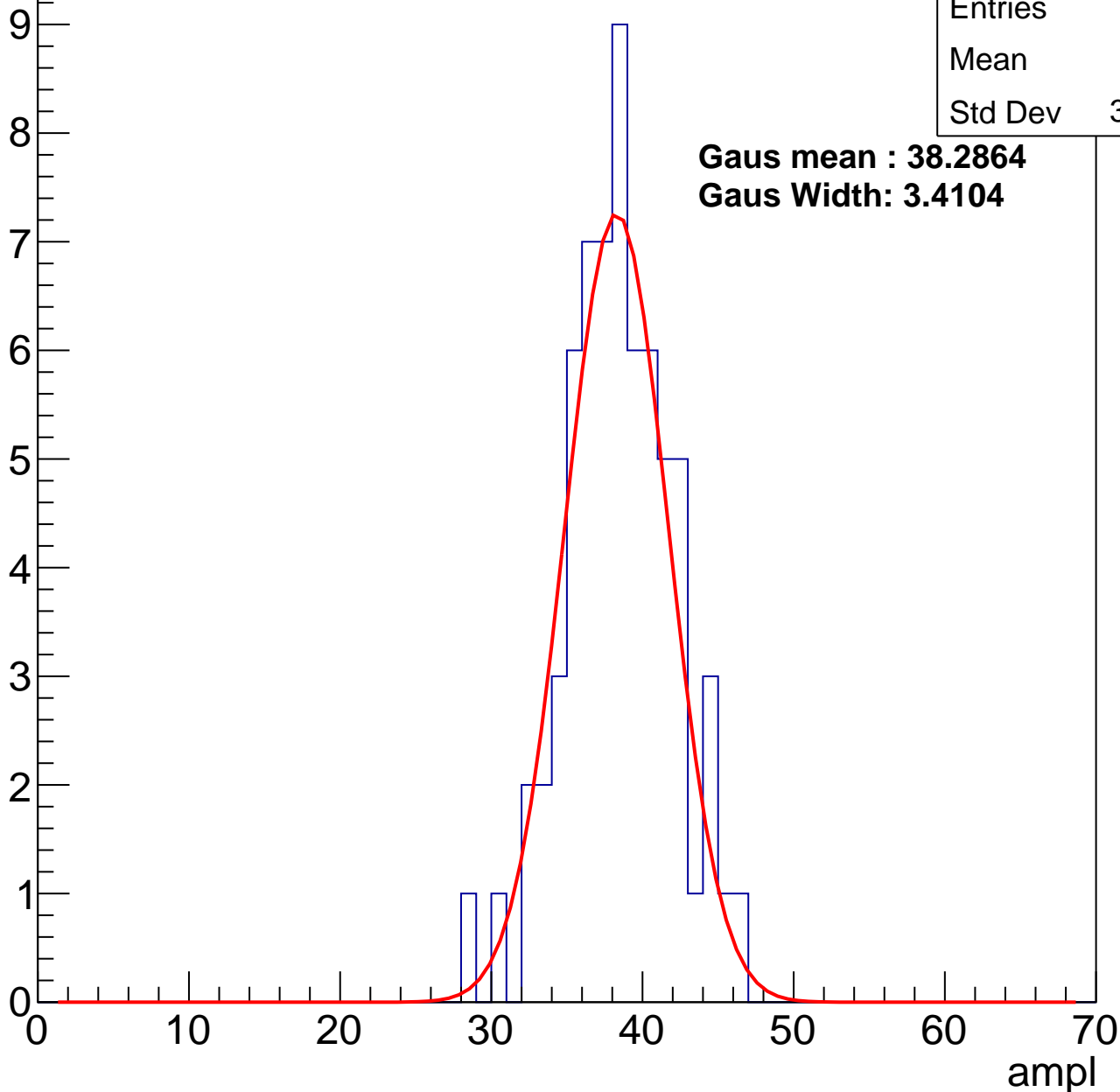
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38
Std Dev	3.533

Gaus mean : 38.2864

Gaus Width: 3.4104



B0L001S, U15-ch63, adc2

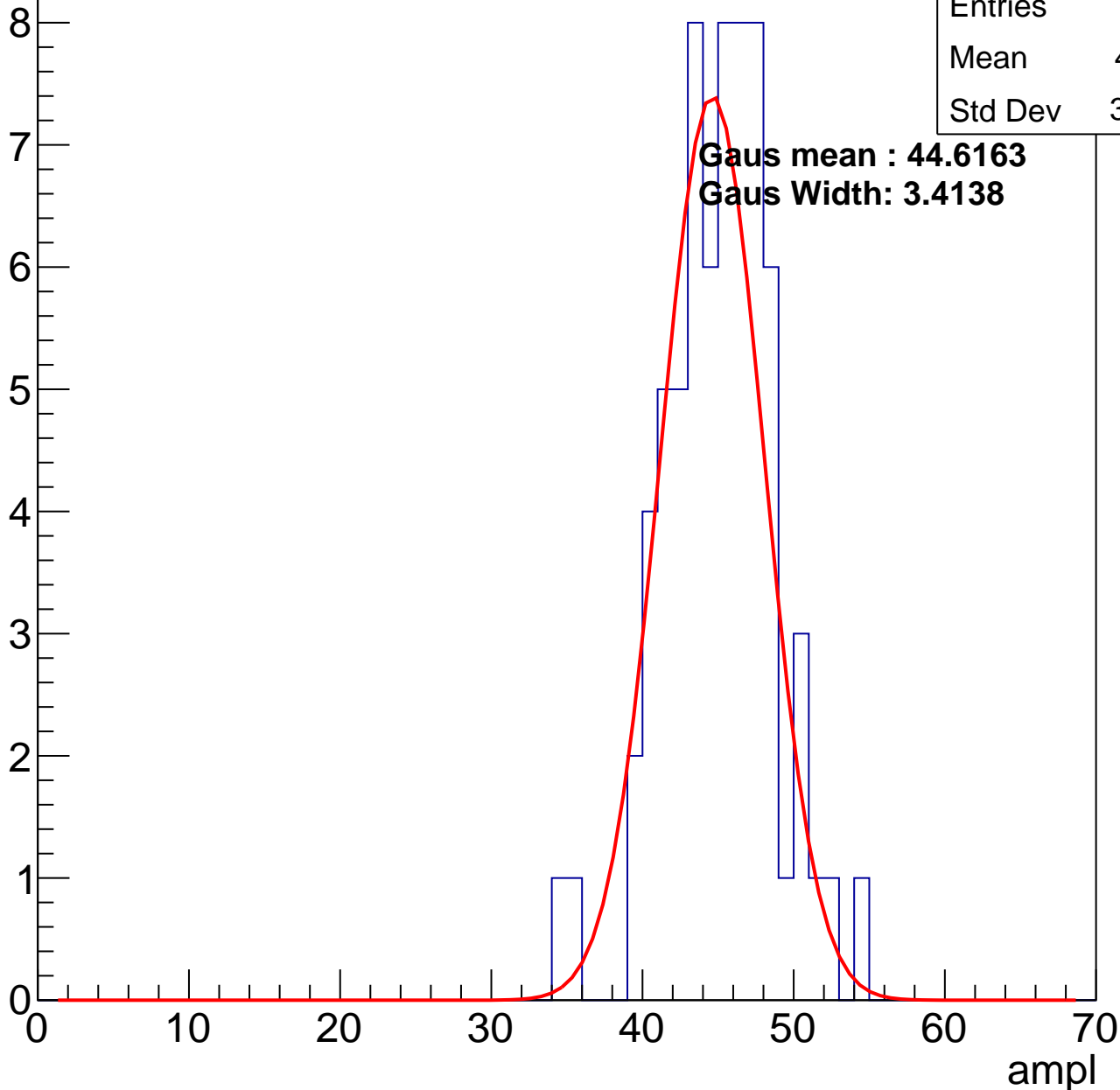
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.61
Std Dev	3.604

Gaus mean : 44.6163

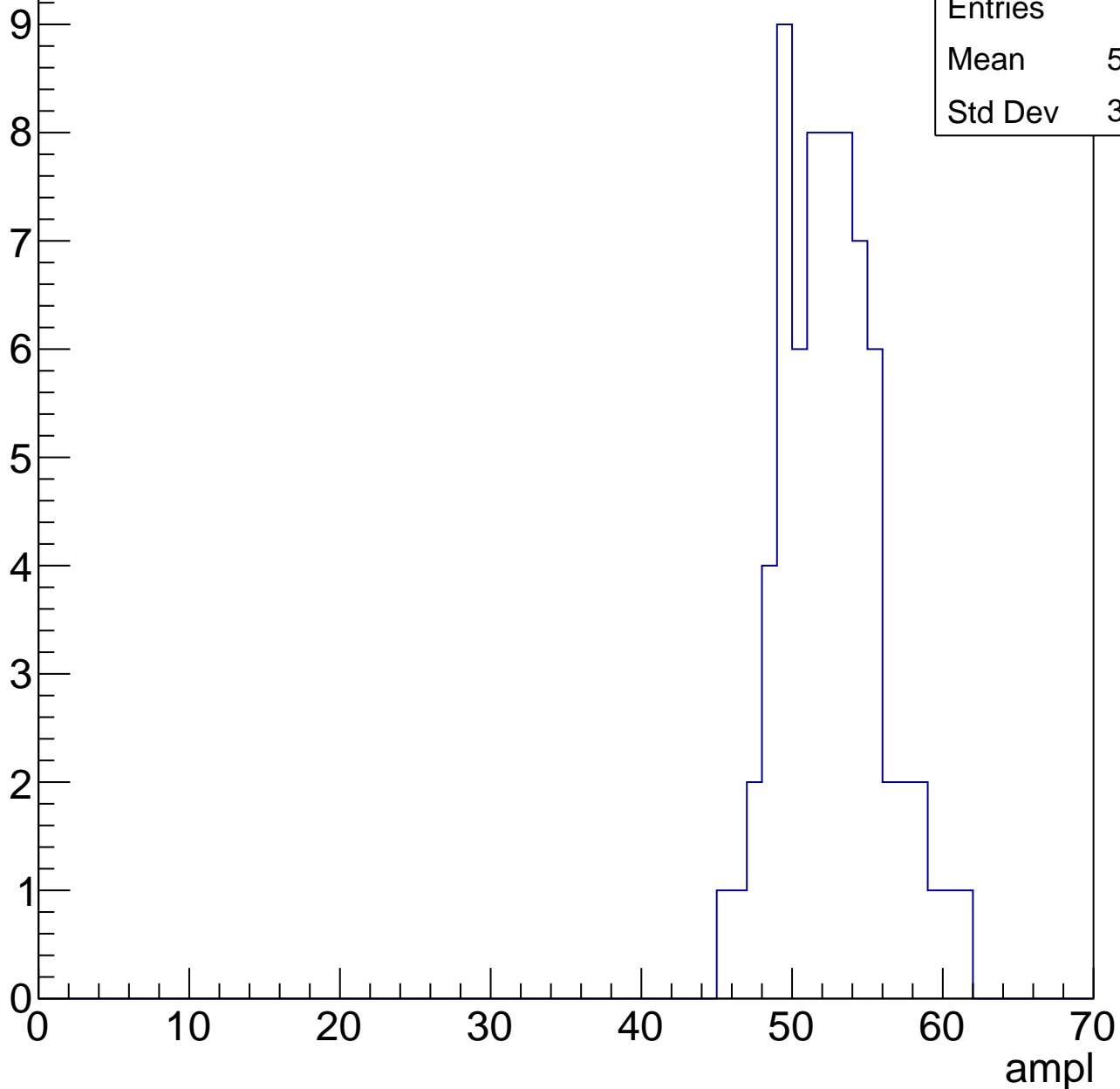
Gaus Width: 3.4138



B0L001S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



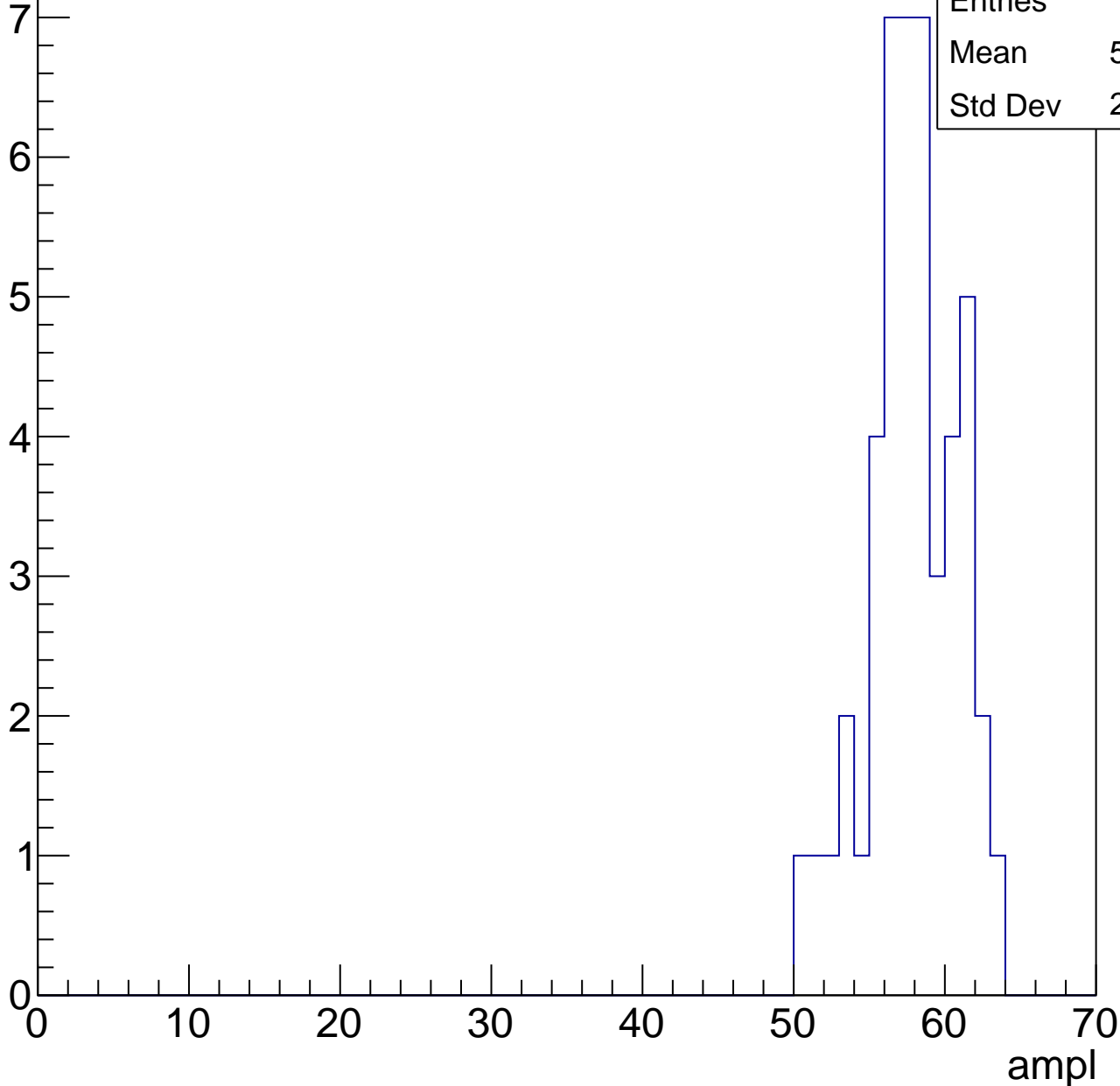
Entries	69
Mean	52.12
Std Dev	3.295

B0L001S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.37
Std Dev	2.907

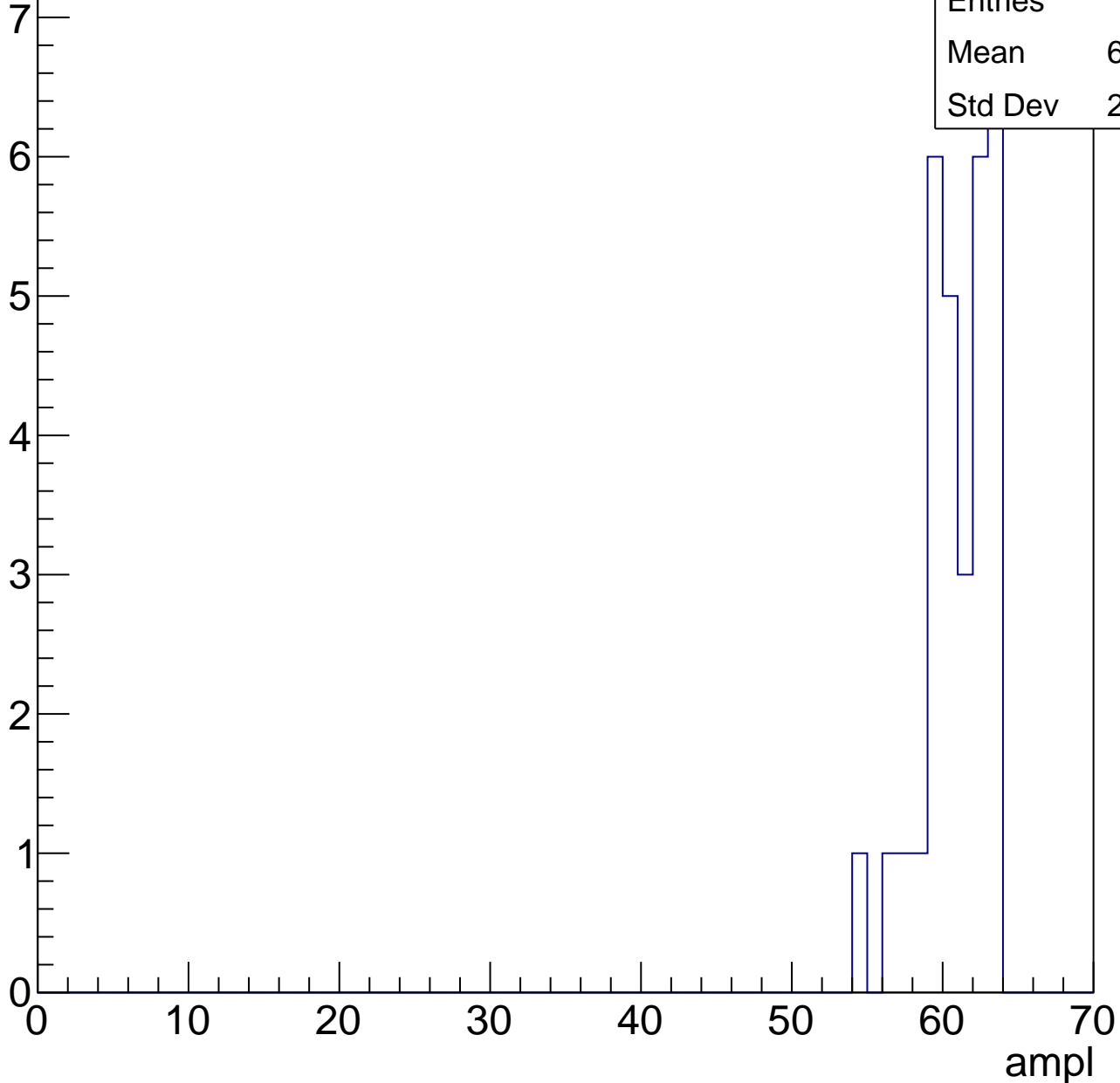


B0L001S, U15-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

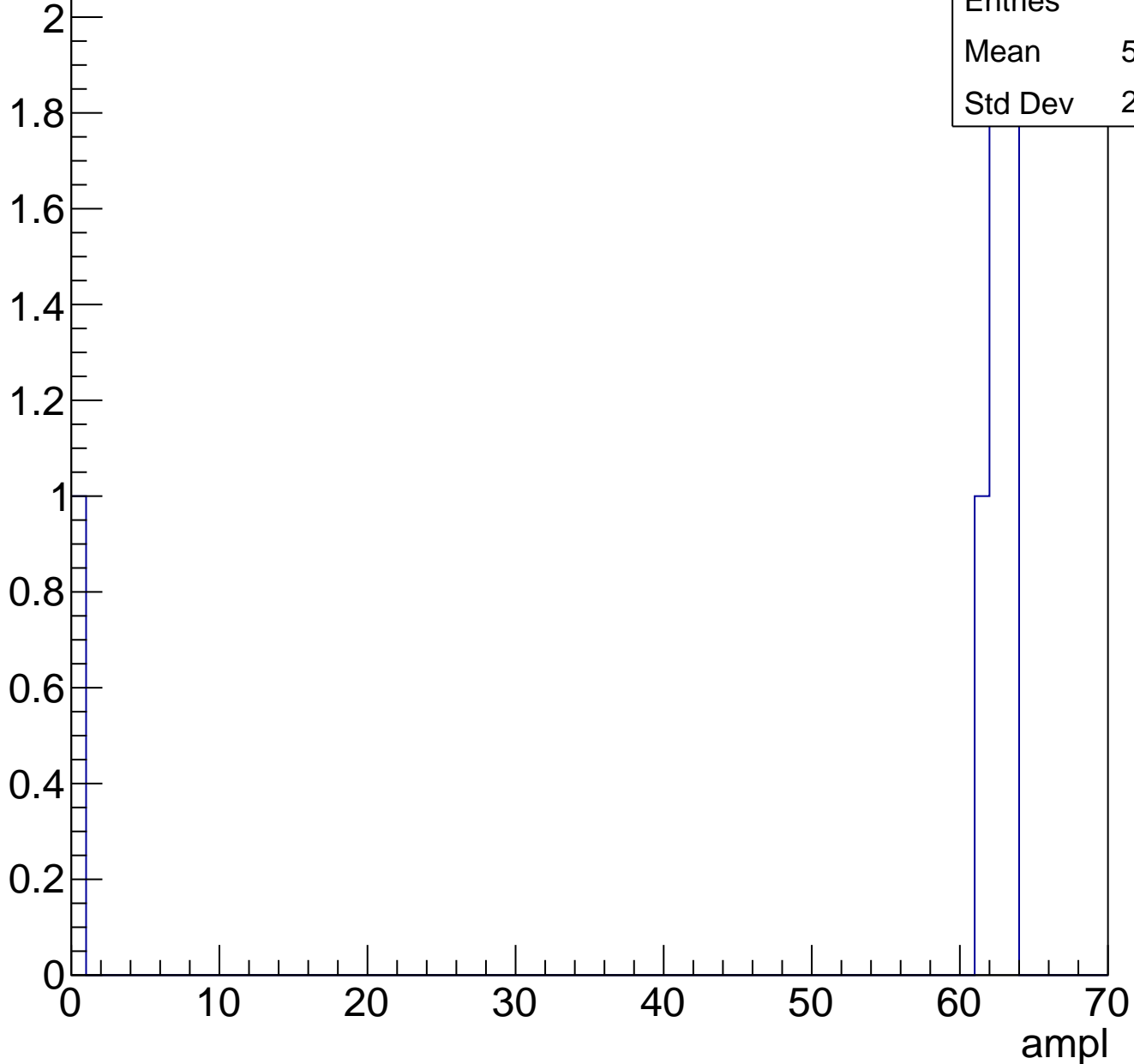
Entries	31
Mean	60.48
Std Dev	2.227



B0L001S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch64, adc0

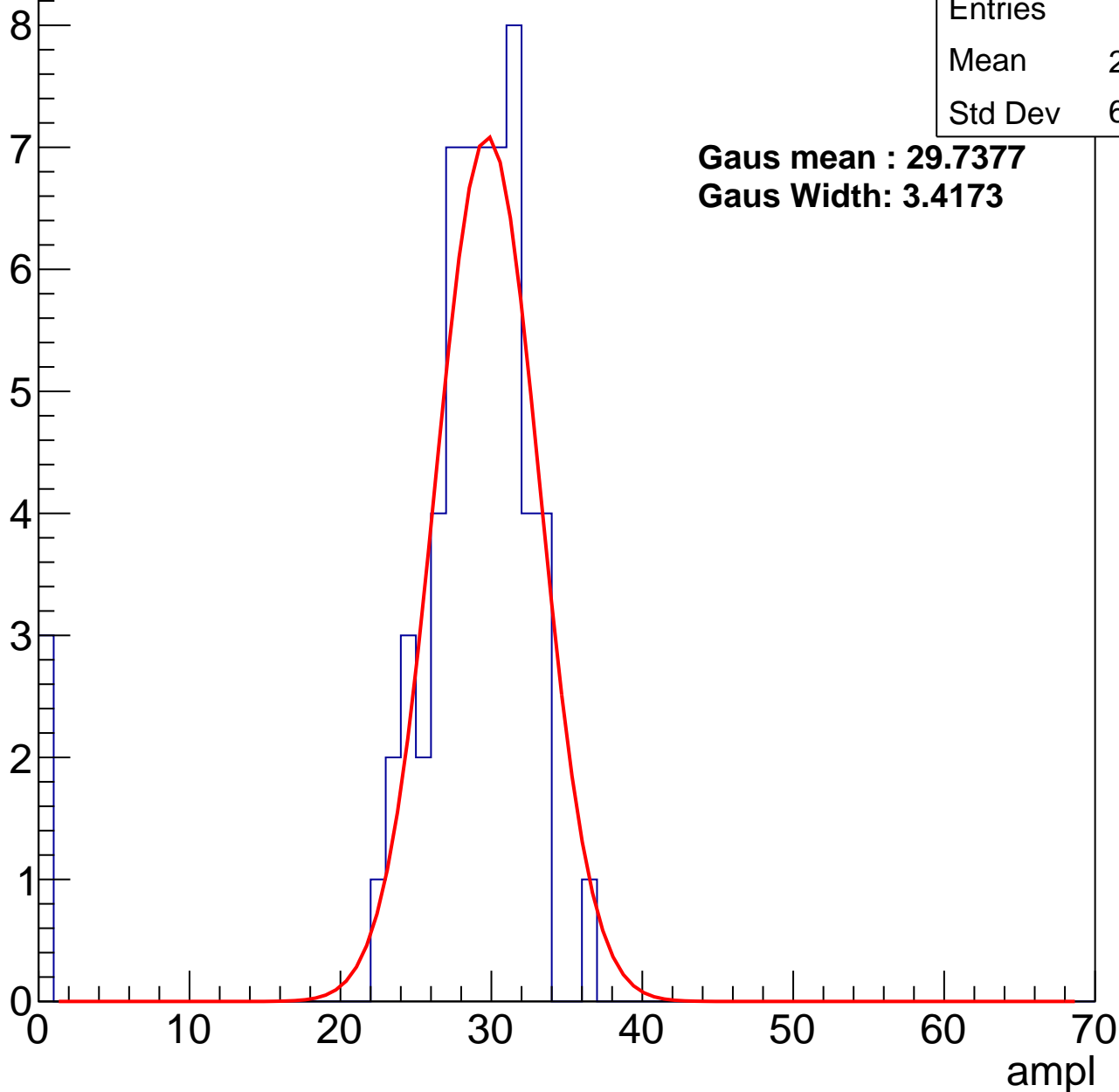
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	27.27
Std Dev	6.867

Gaus mean : 29.7377

Gaus Width: 3.4173



B0L001S, U15-ch64, adc1

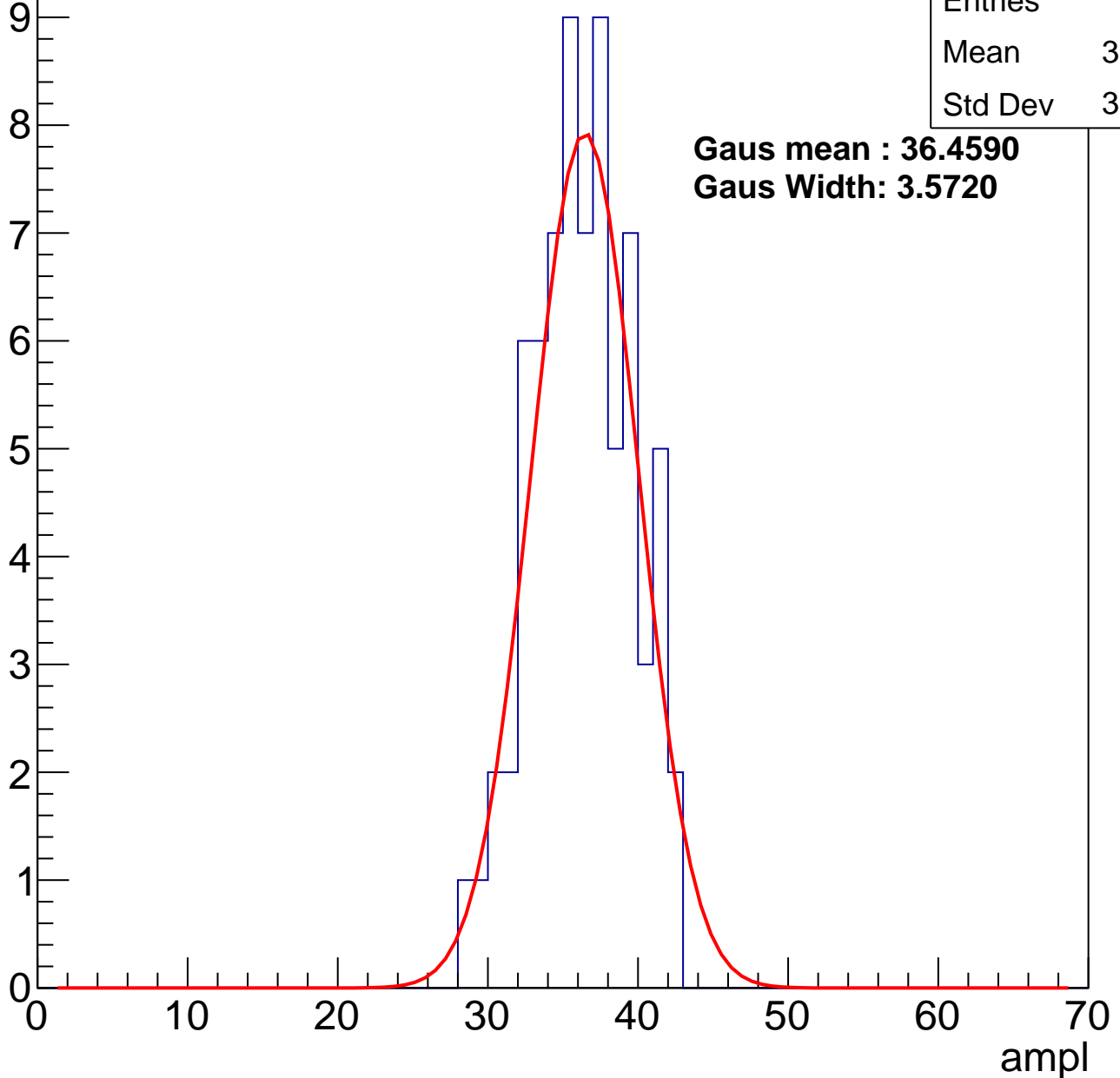
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	35.82
Std Dev	3.242

Gaus mean : 36.4590

Gaus Width: 3.5720



B0L001S, U15-ch64, adc2

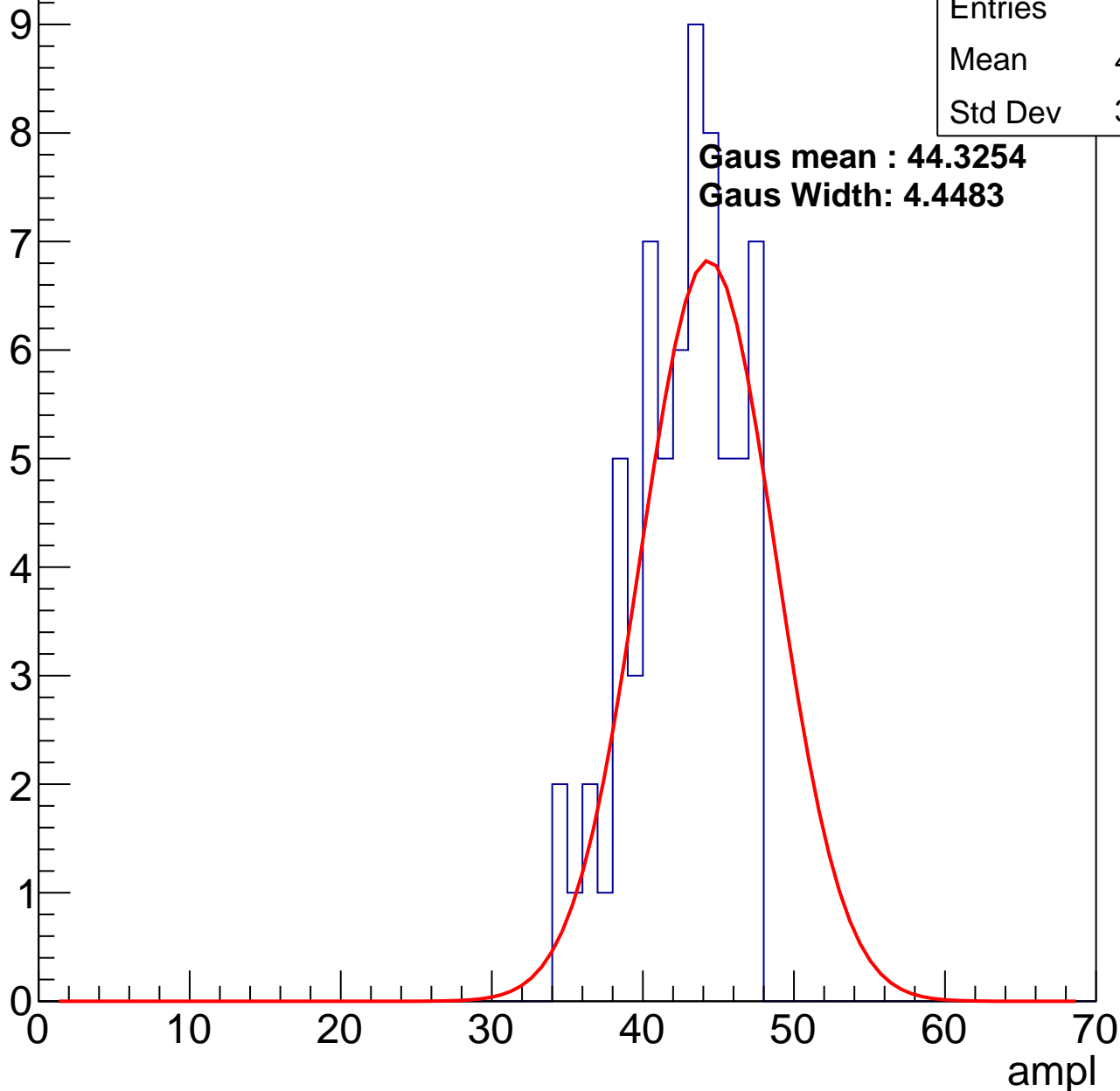
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	42.11
Std Dev	3.381

Gaus mean : 44.3254

Gaus Width: 4.4483

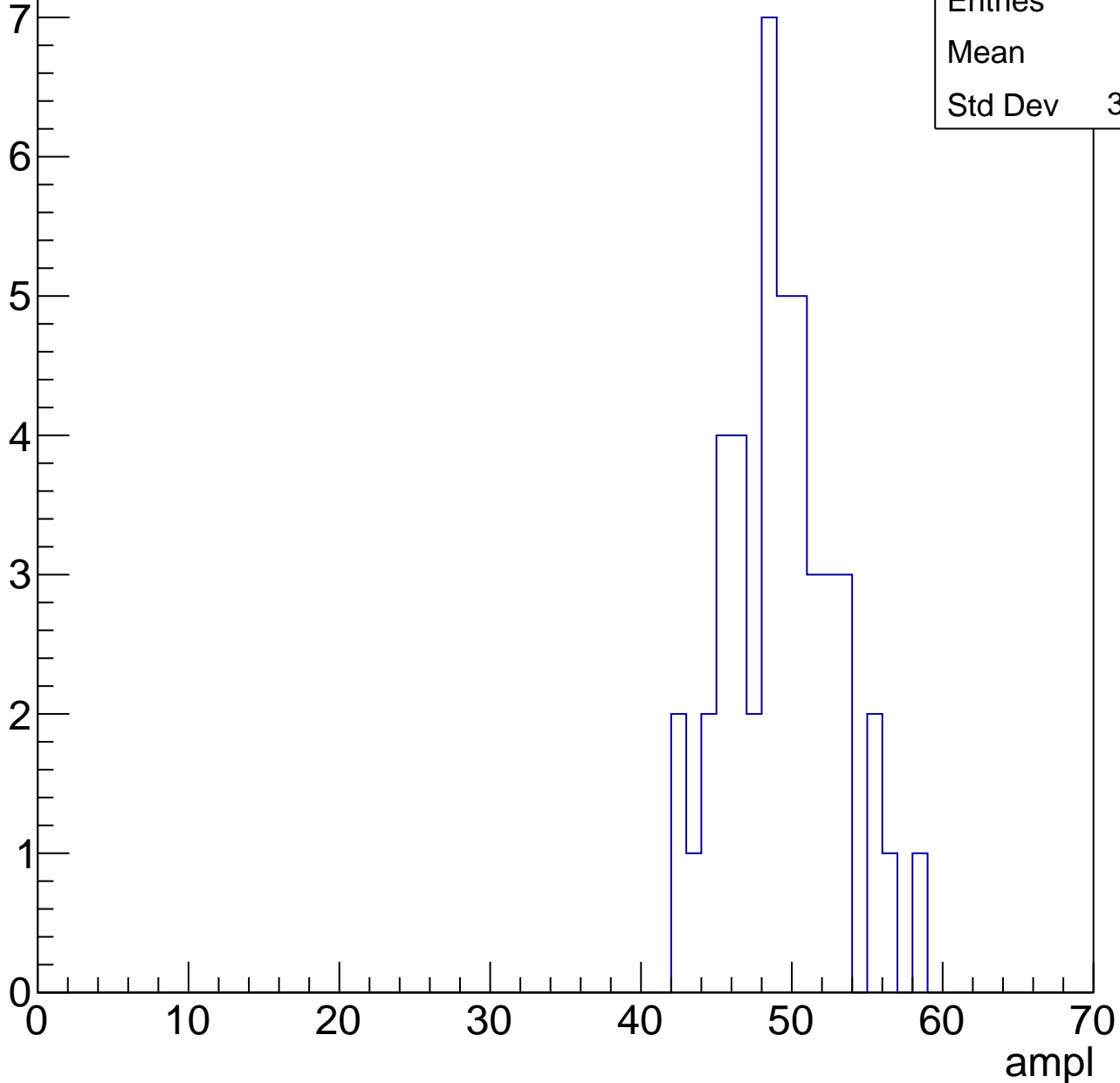


B0L001S, U15-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	48.8
Std Dev	3.625

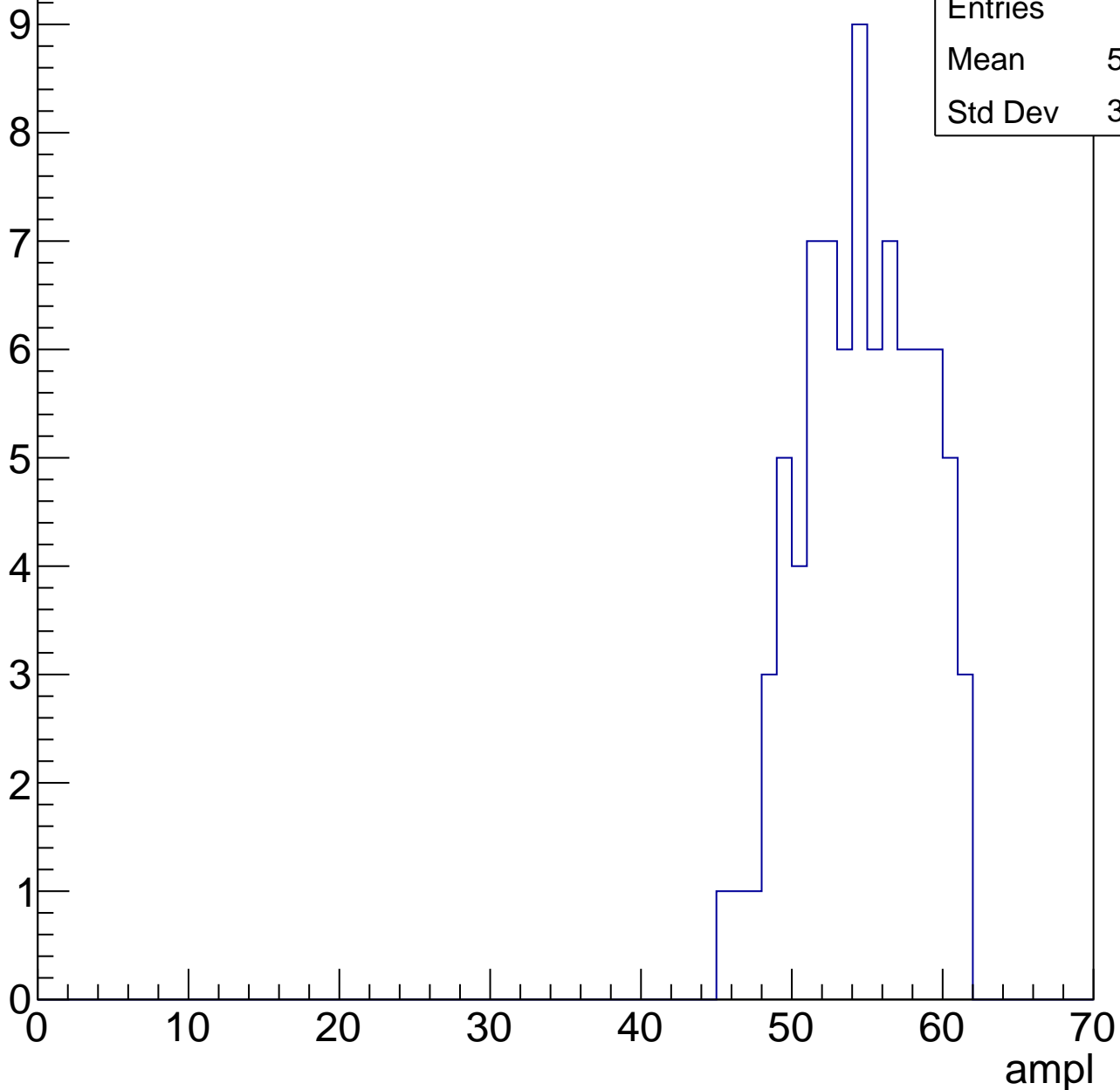


B0L001S, U15-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	54.23
Std Dev	3.872

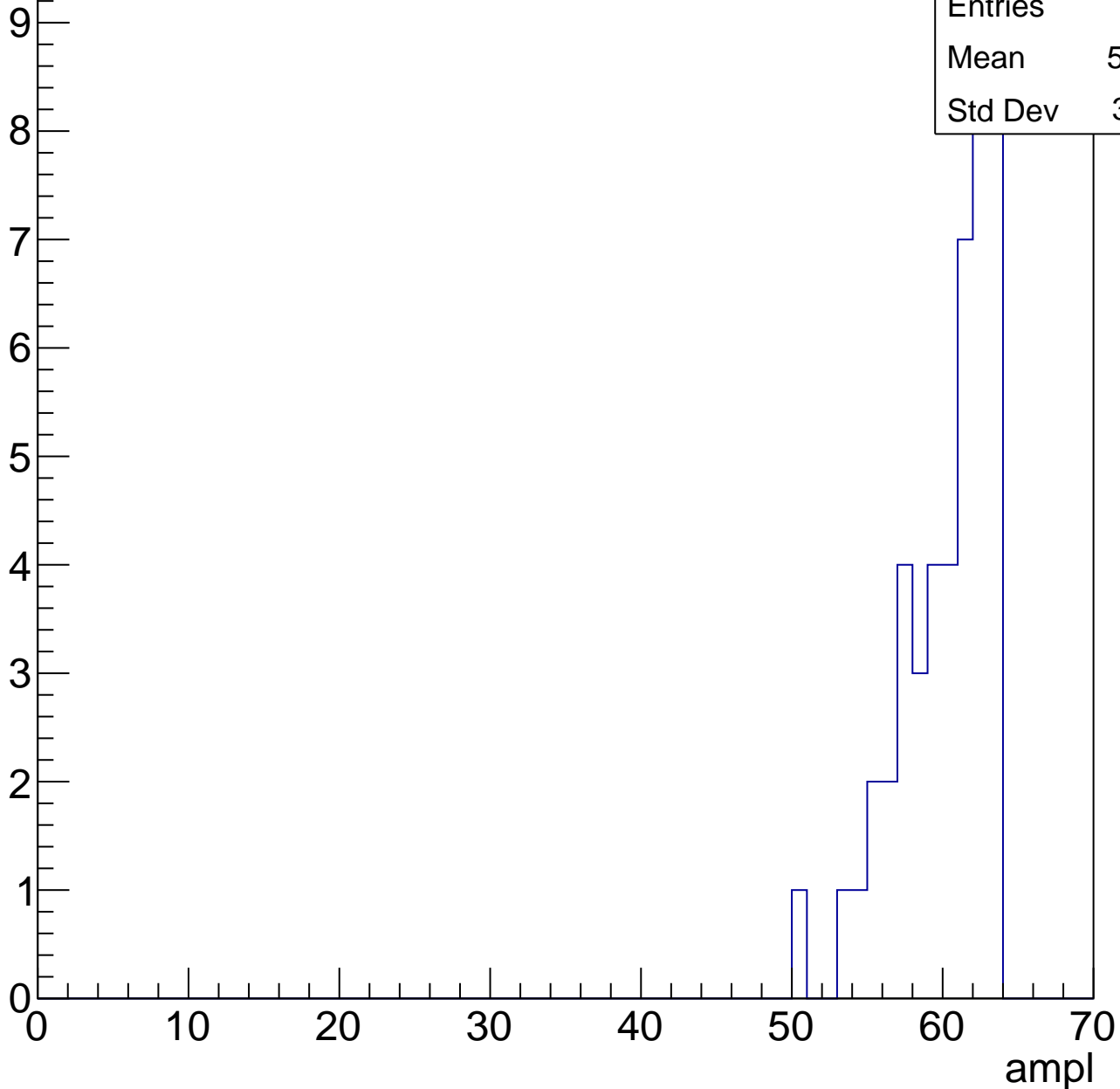


B0L001S, U15-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

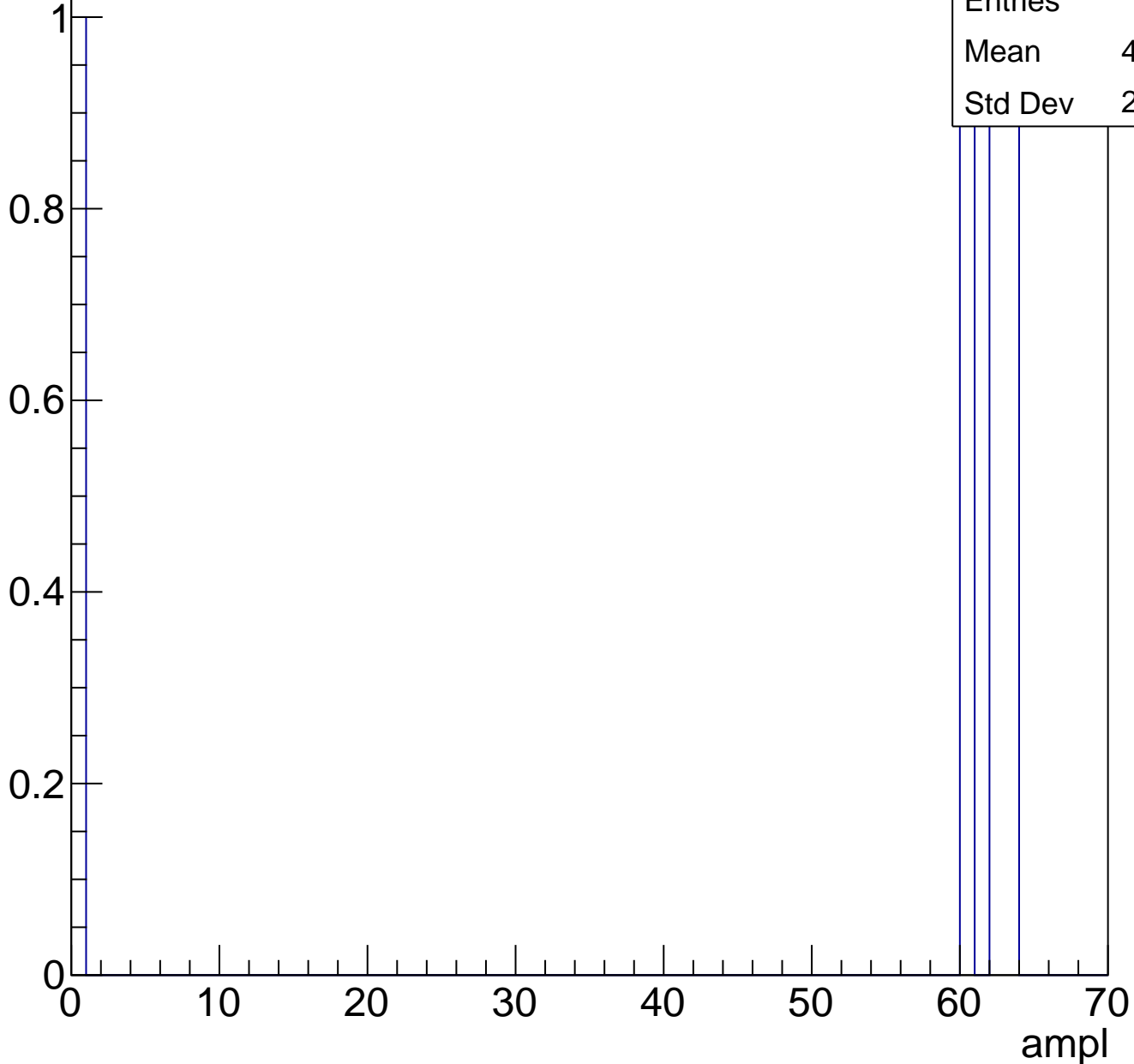
Entries	47
Mean	59.77
Std Dev	3.061



B0L001S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch65, adc0

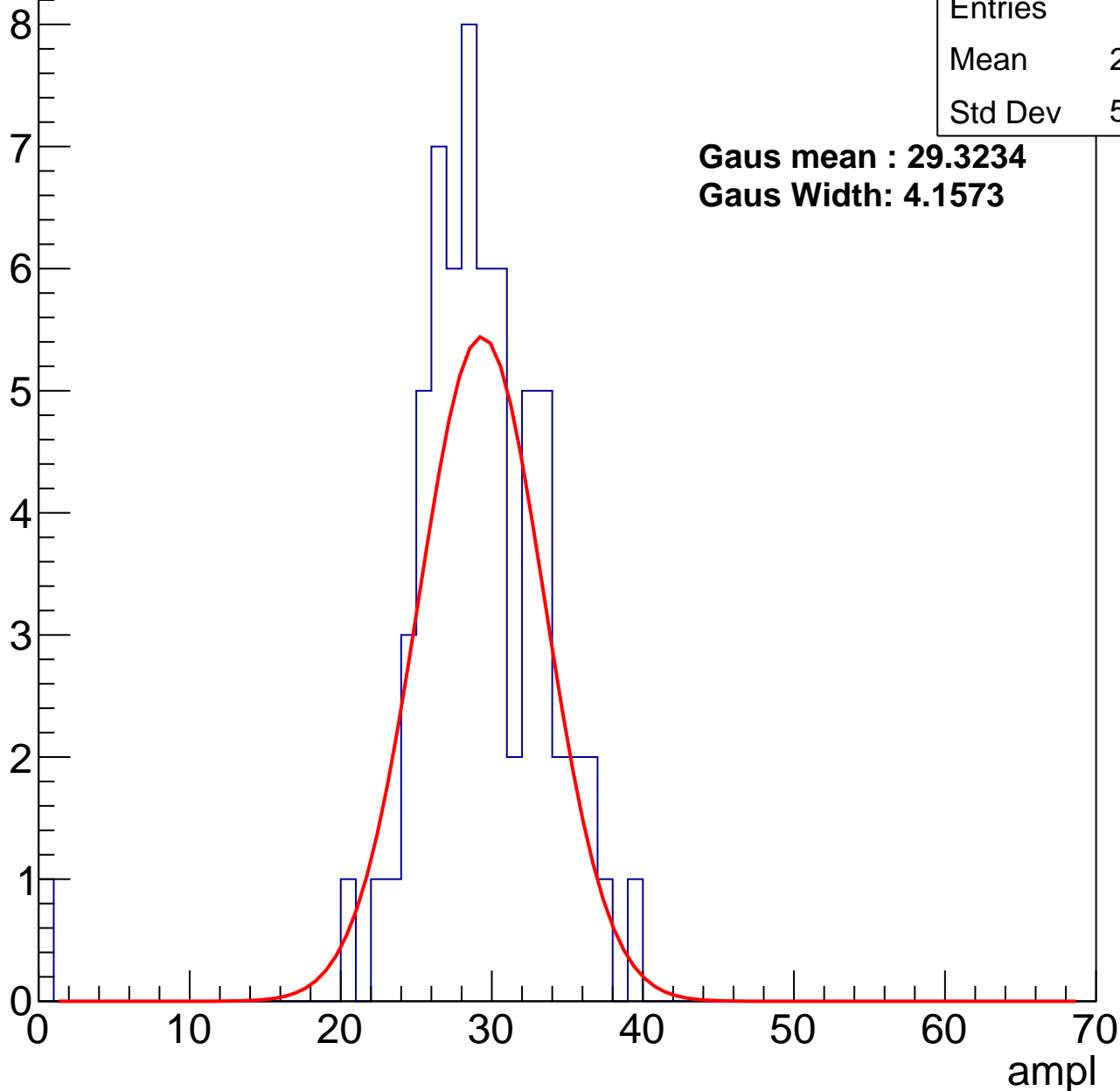
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	28.57
Std Dev	5.215

Gaus mean : 29.3234

Gaus Width: 4.1573



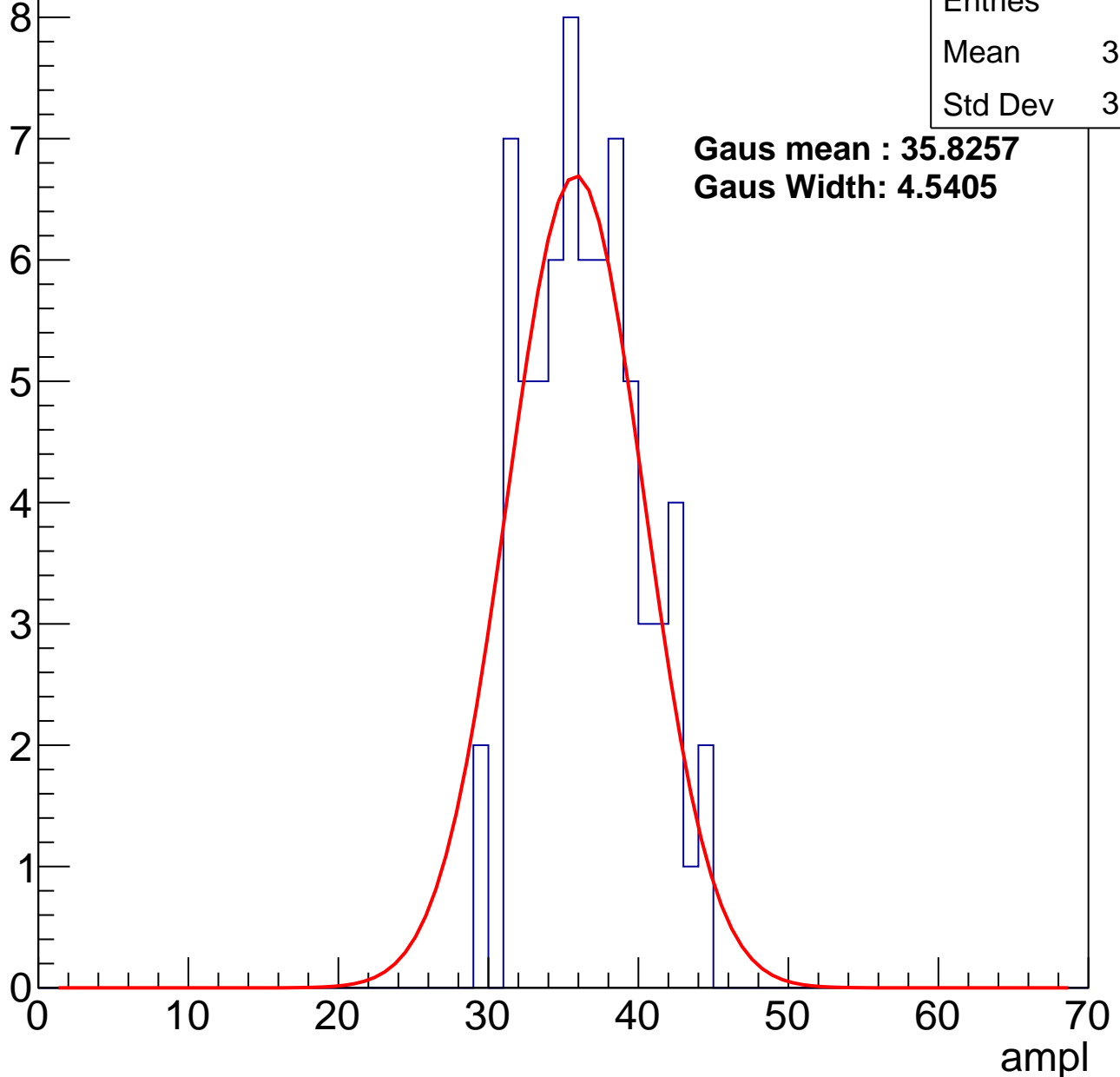
B0L001S, U15-ch65, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.07
Std Dev	3.689

Gaus mean : 35.8257
Gaus Width: 4.5405



B0L001S, U15-ch65, adc2

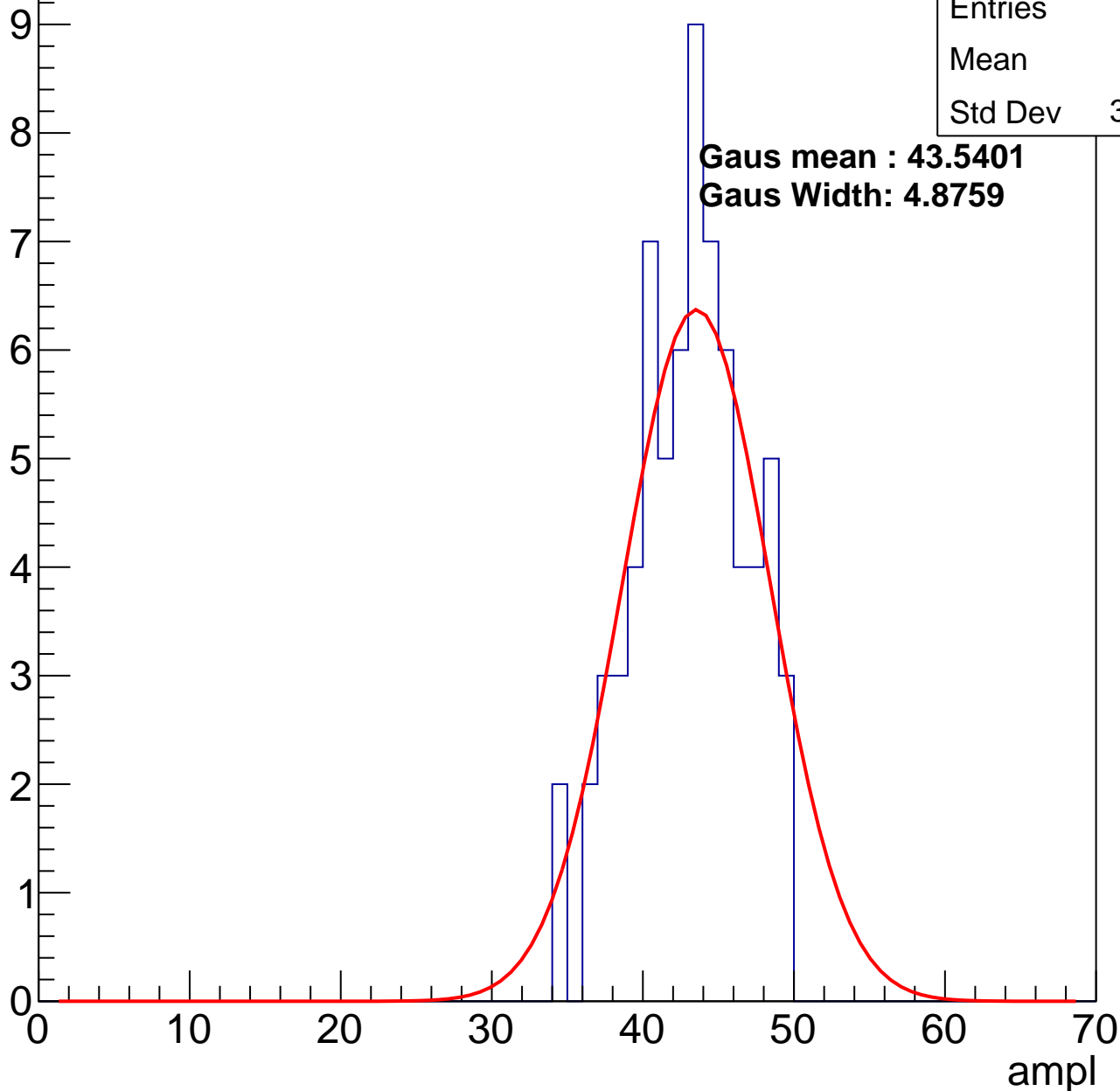
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	42.6
Std Dev	3.682

Gaus mean : 43.5401

Gaus Width: 4.8759

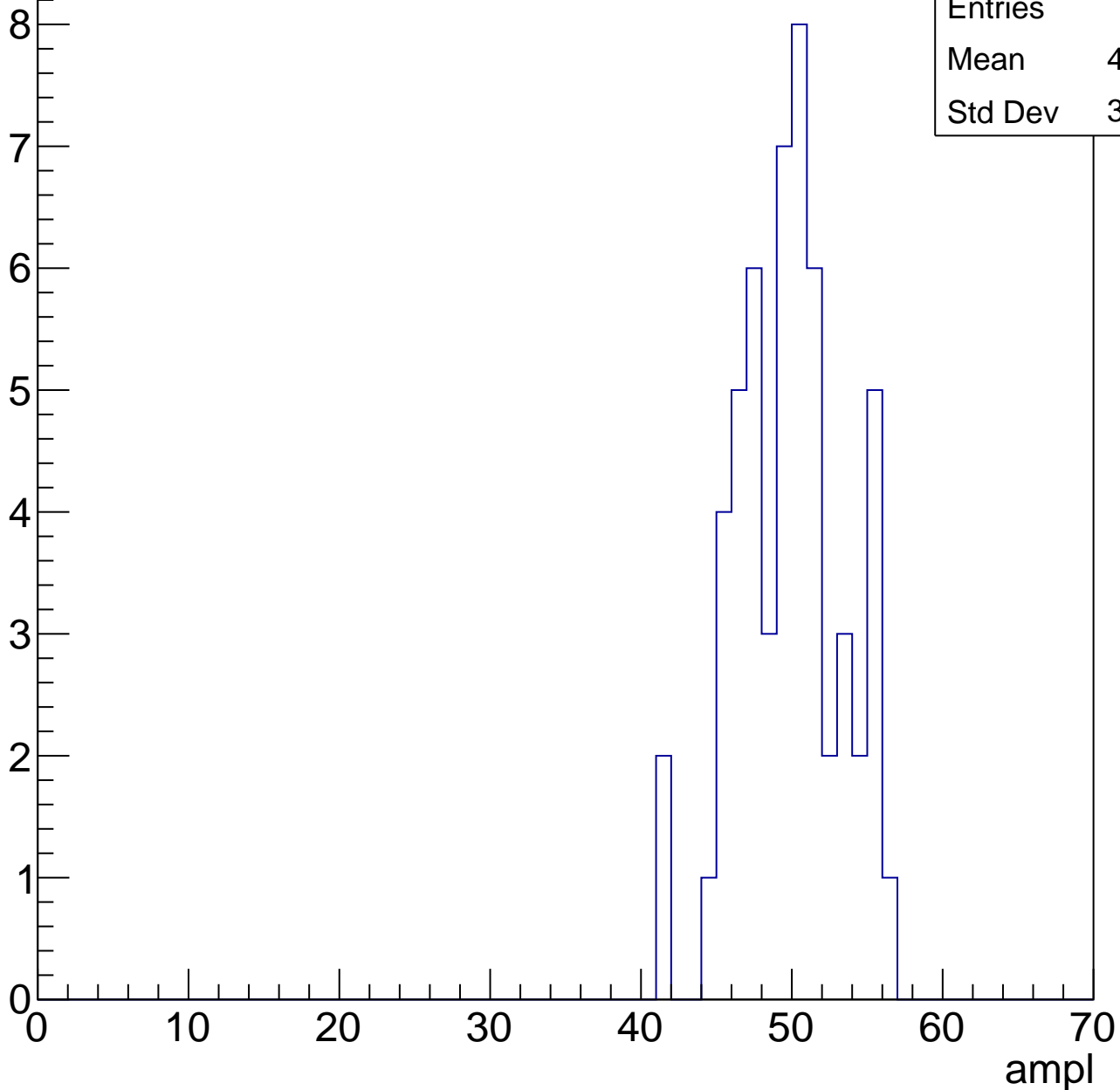


B0L001S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.33
Std Dev	3.464

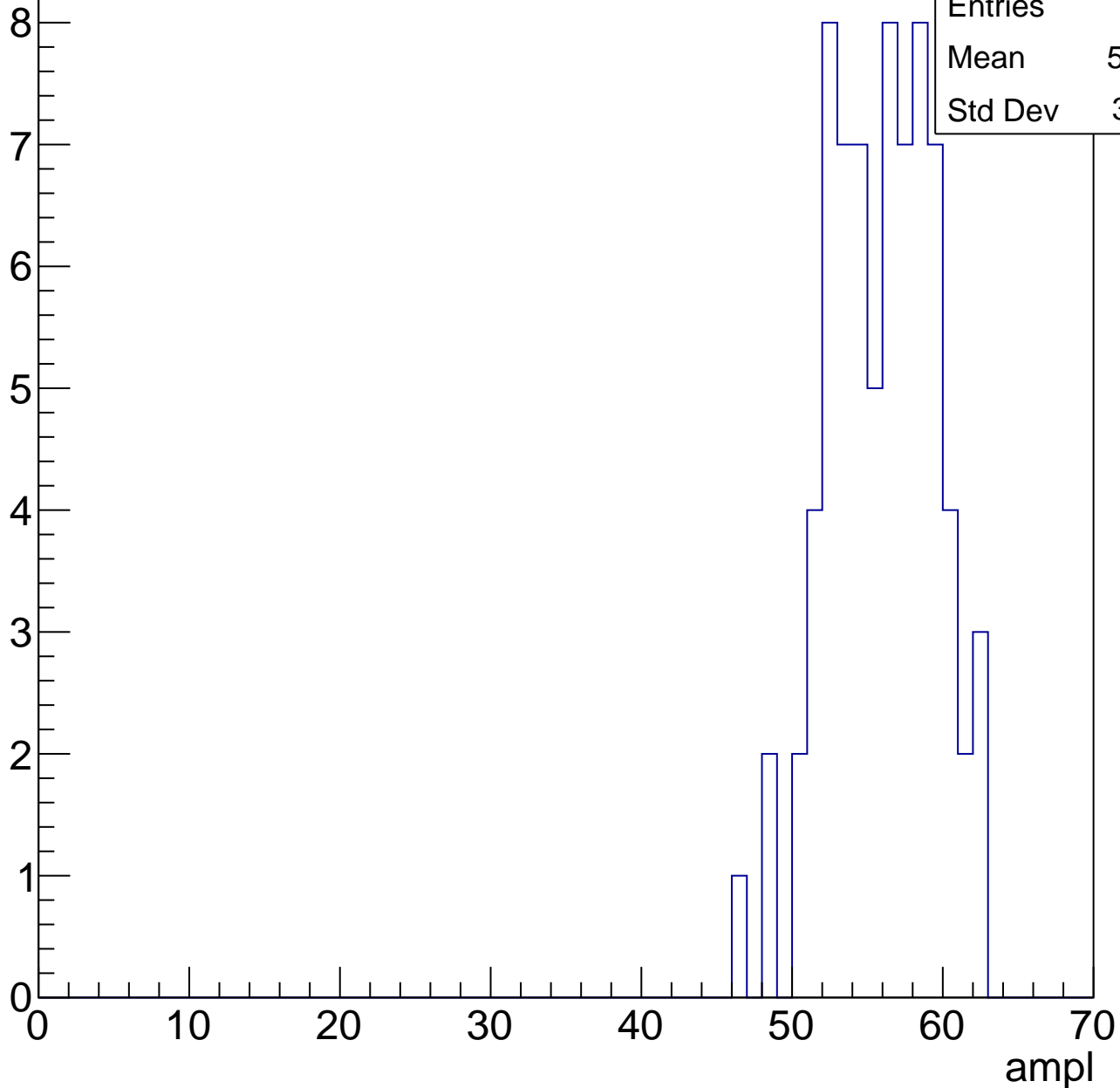


B0L001S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	55.44
Std Dev	3.511

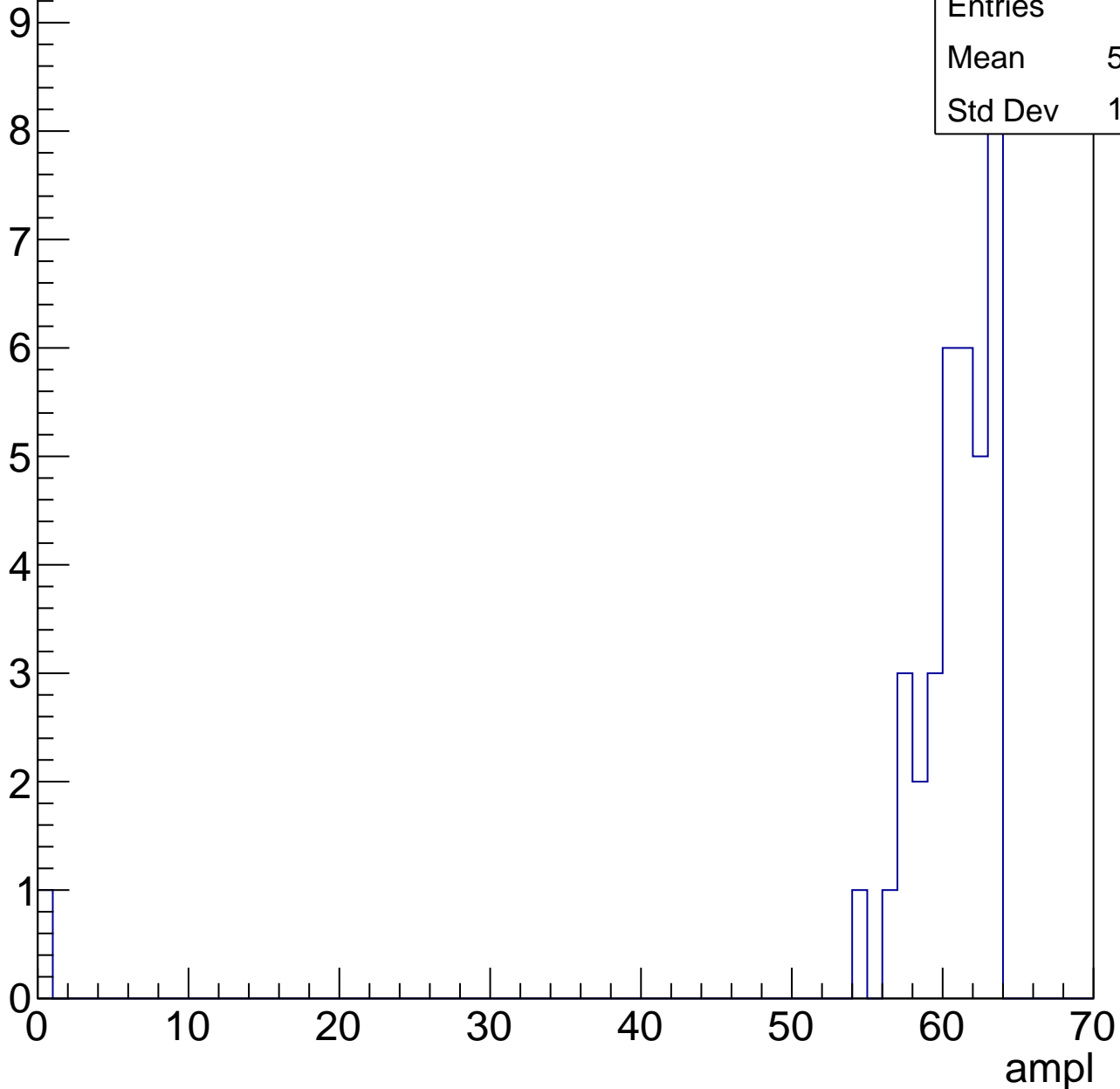


B0L001S, U15-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.84
Std Dev	10.06



B0L001S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B0L001S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch66, adc0

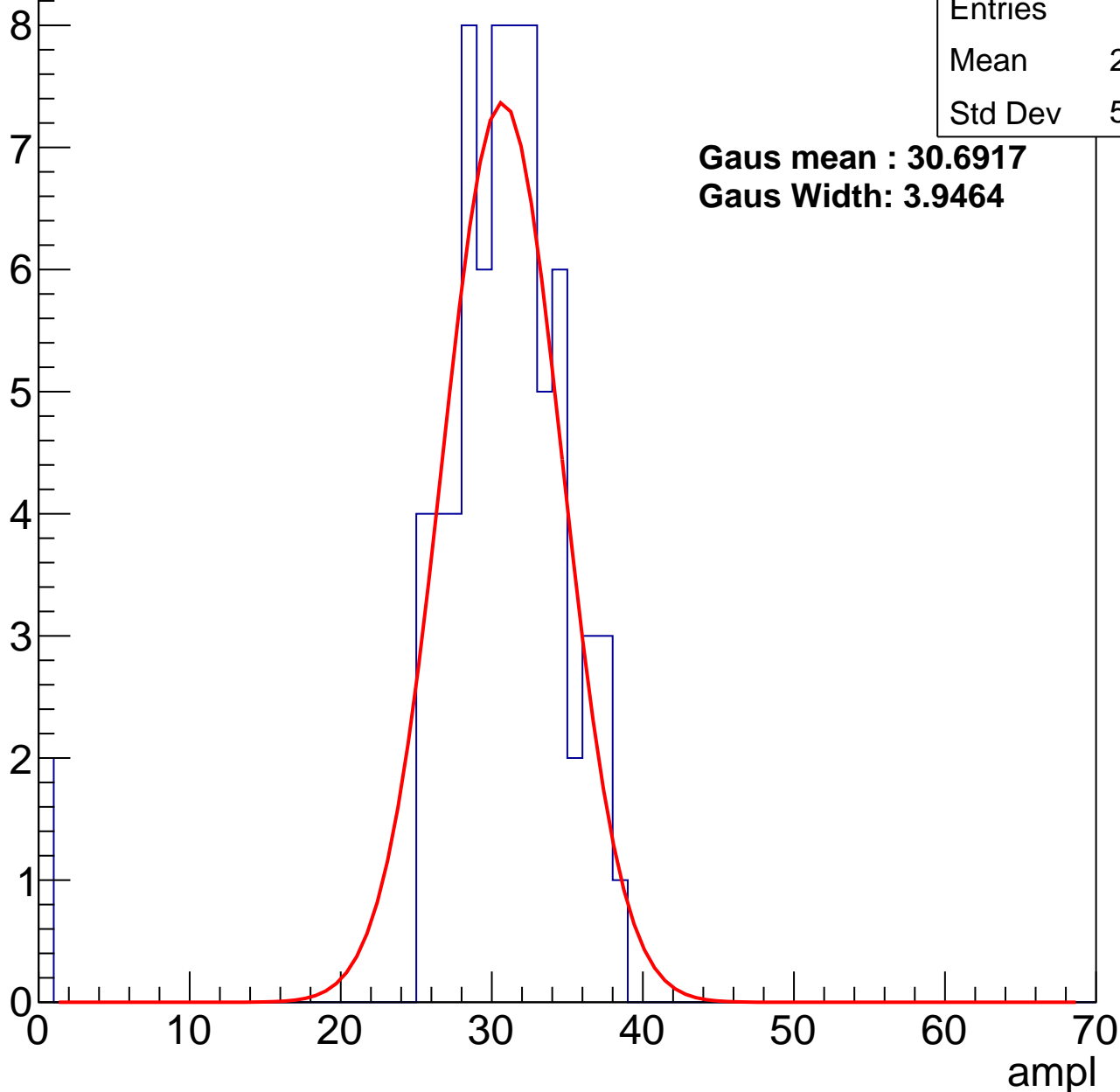
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.86
Std Dev	5.994

Gaus mean : 30.6917

Gaus Width: 3.9464



B0L001S, U15-ch66, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	37.83
Std Dev	3.527

Gaus mean : 38.1942

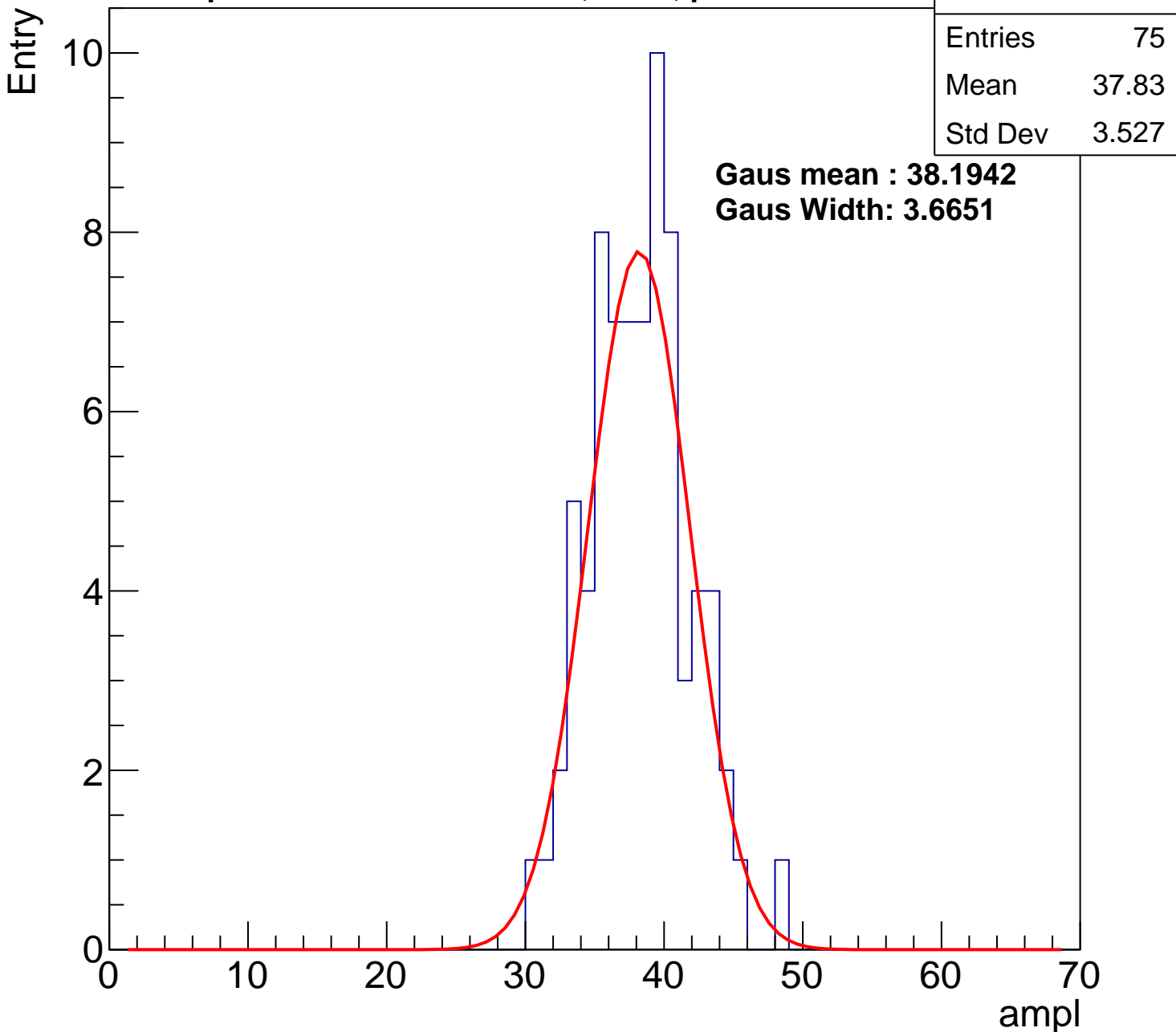
Gaus Width: 3.6651

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch66, adc2

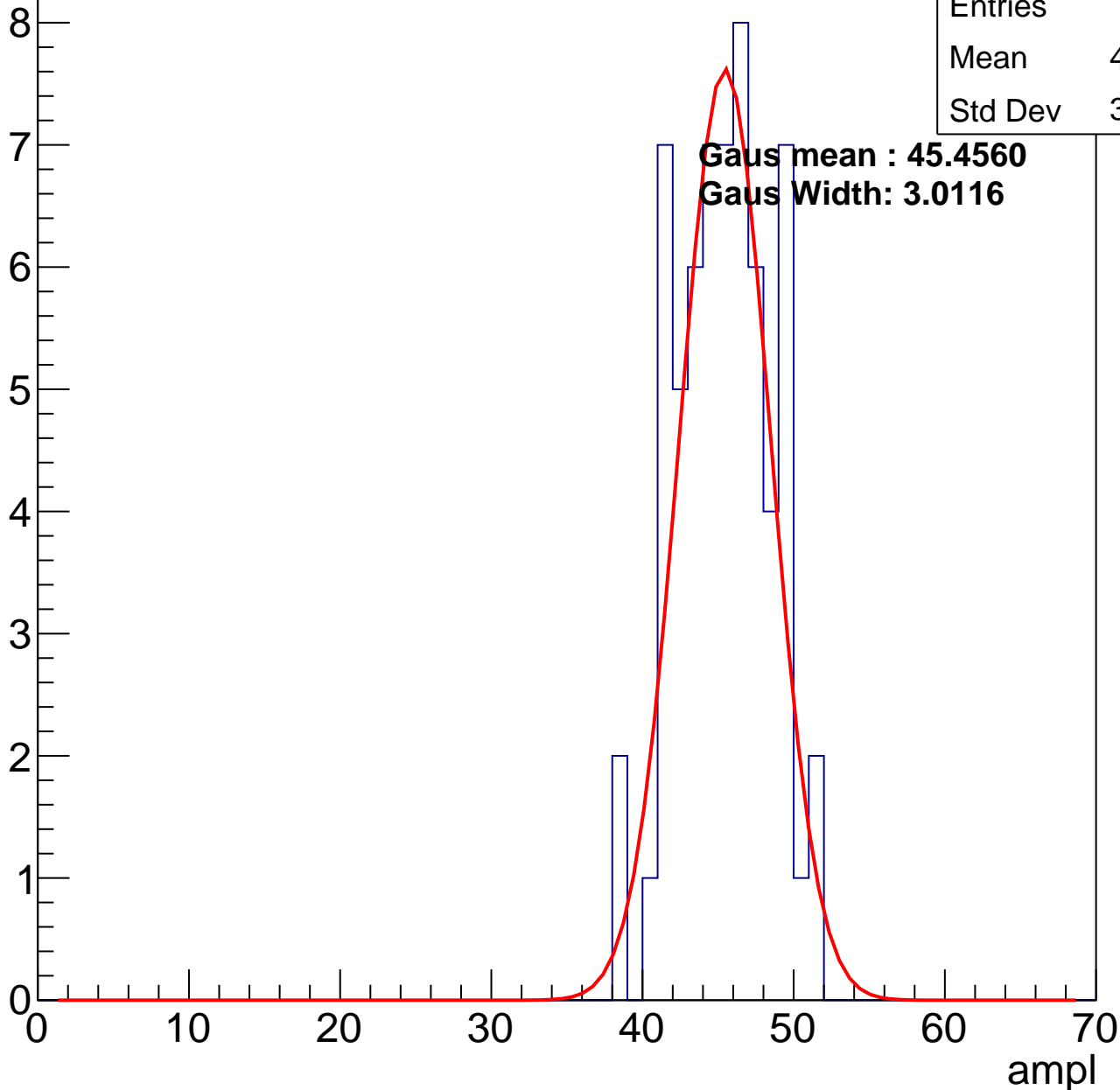
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.94
Std Dev	3.054

Gaus mean : 45.4560

Gaus Width: 3.0116

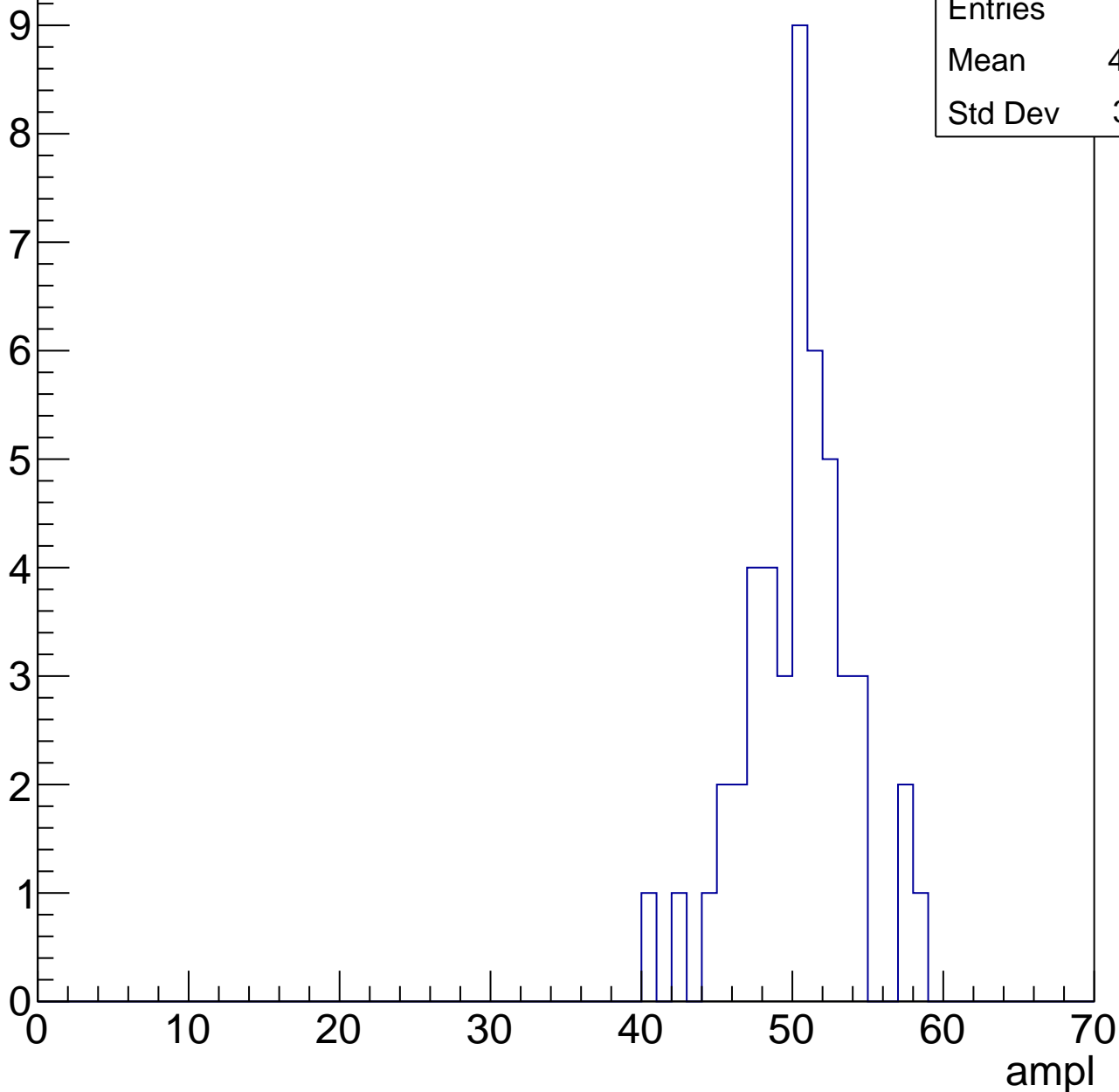


B0L001S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	49.87
Std Dev	3.571

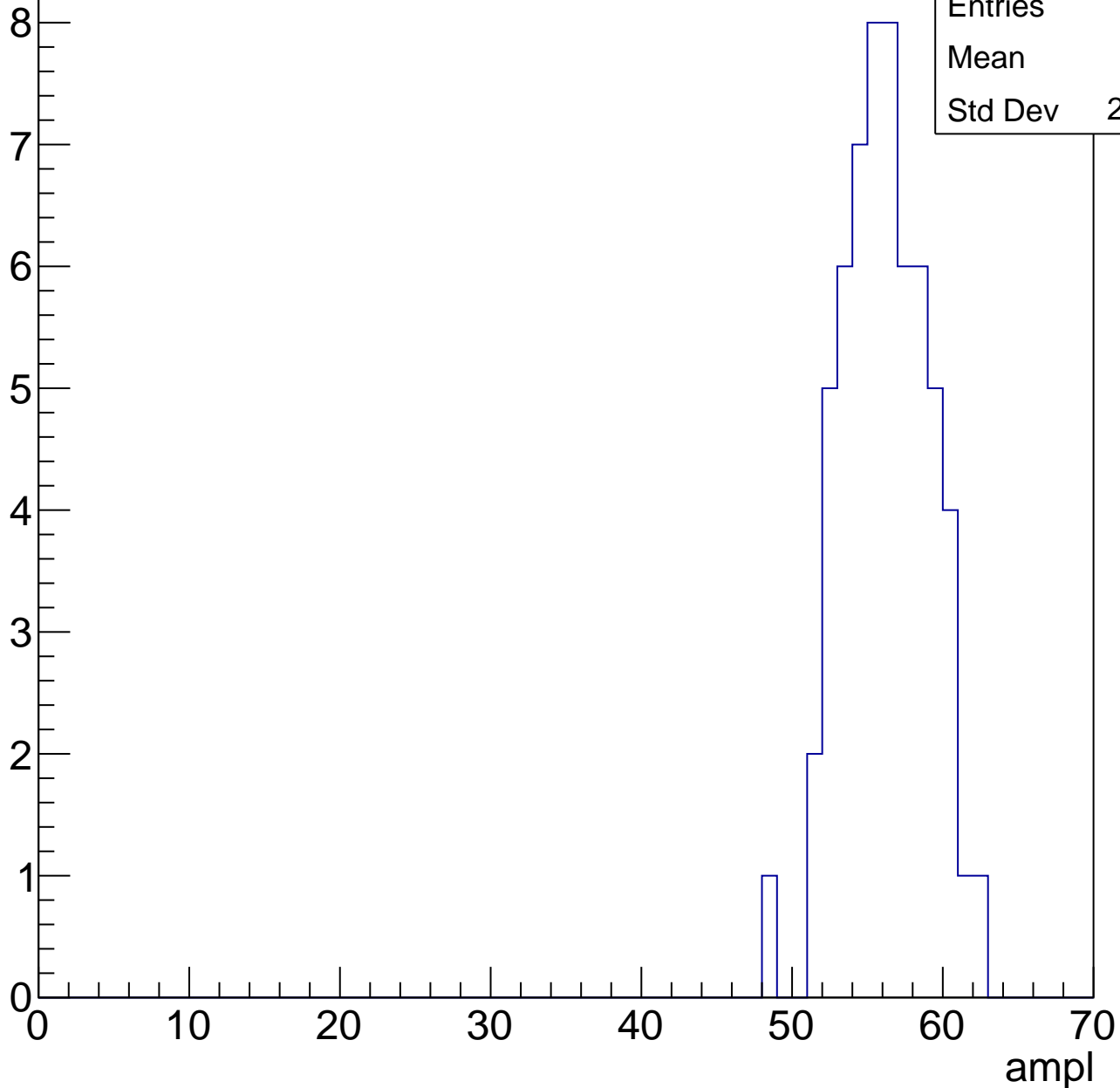


B0L001S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.7
Std Dev	2.824

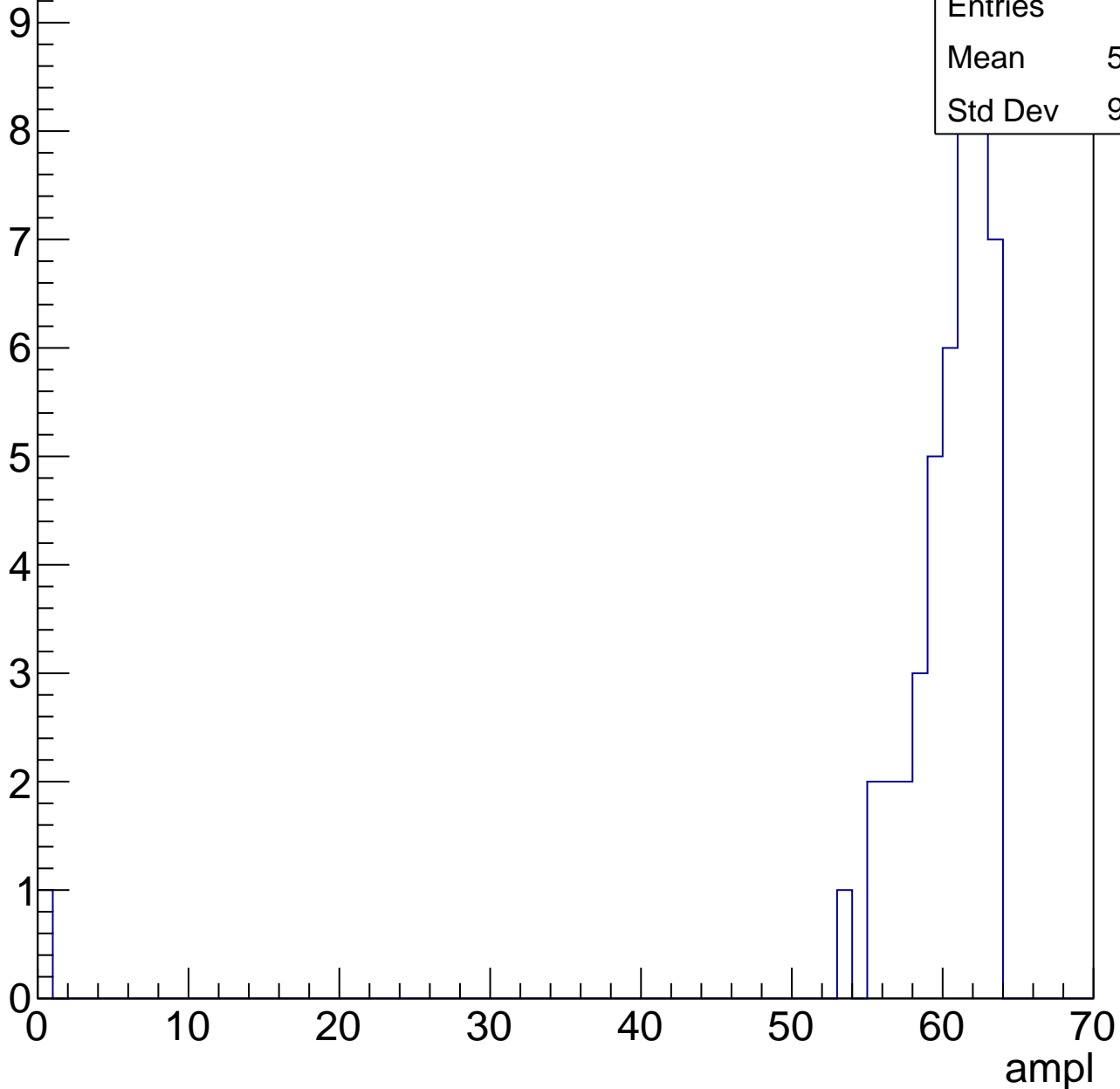


B0L001S, U15-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	58.85
Std Dev	9.006



B0L001S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch67, adc0

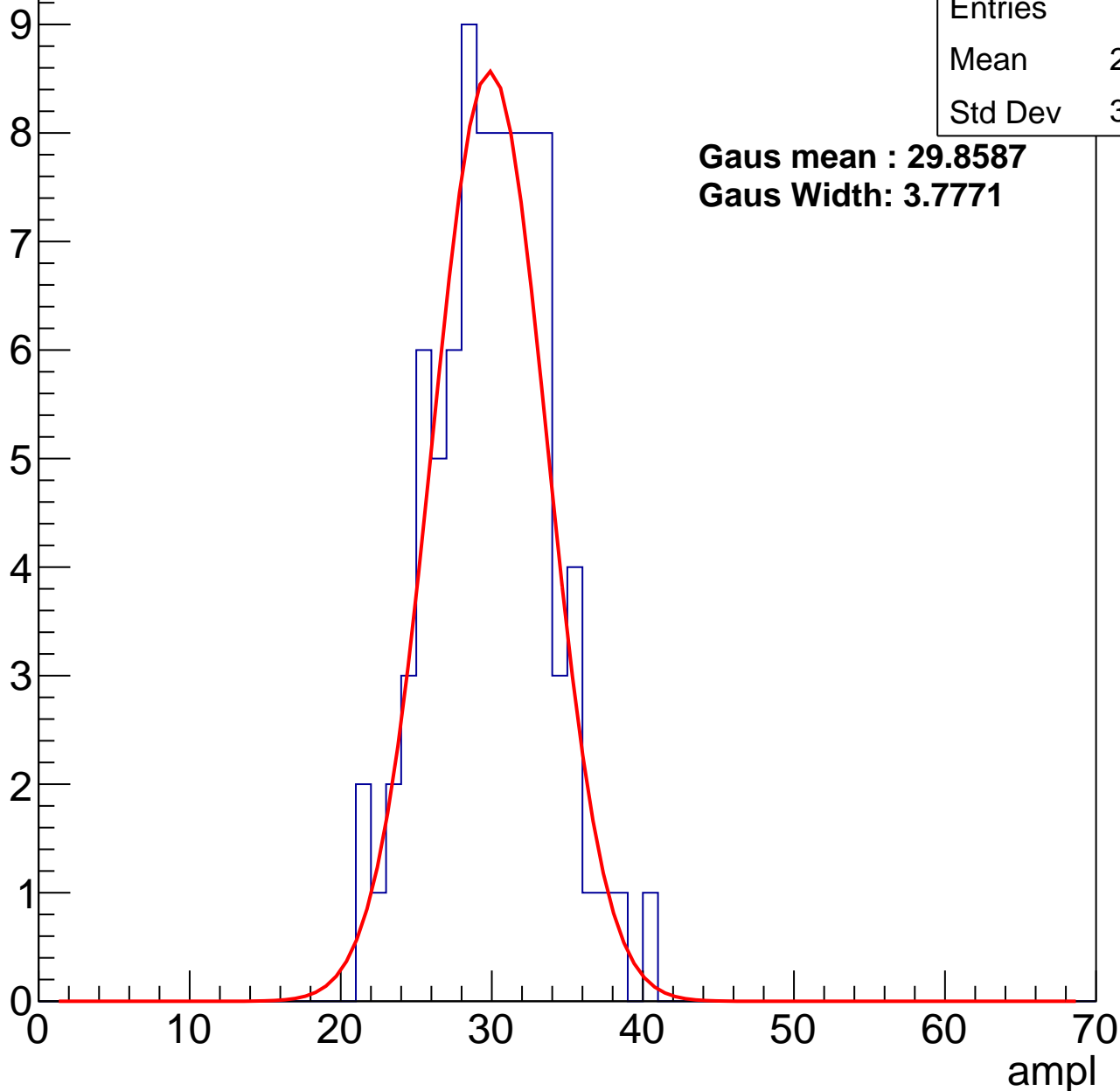
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	29.52
Std Dev	3.834

Gaus mean : 29.8587

Gaus Width: 3.7771



B0L001S, U15-ch67, adc1

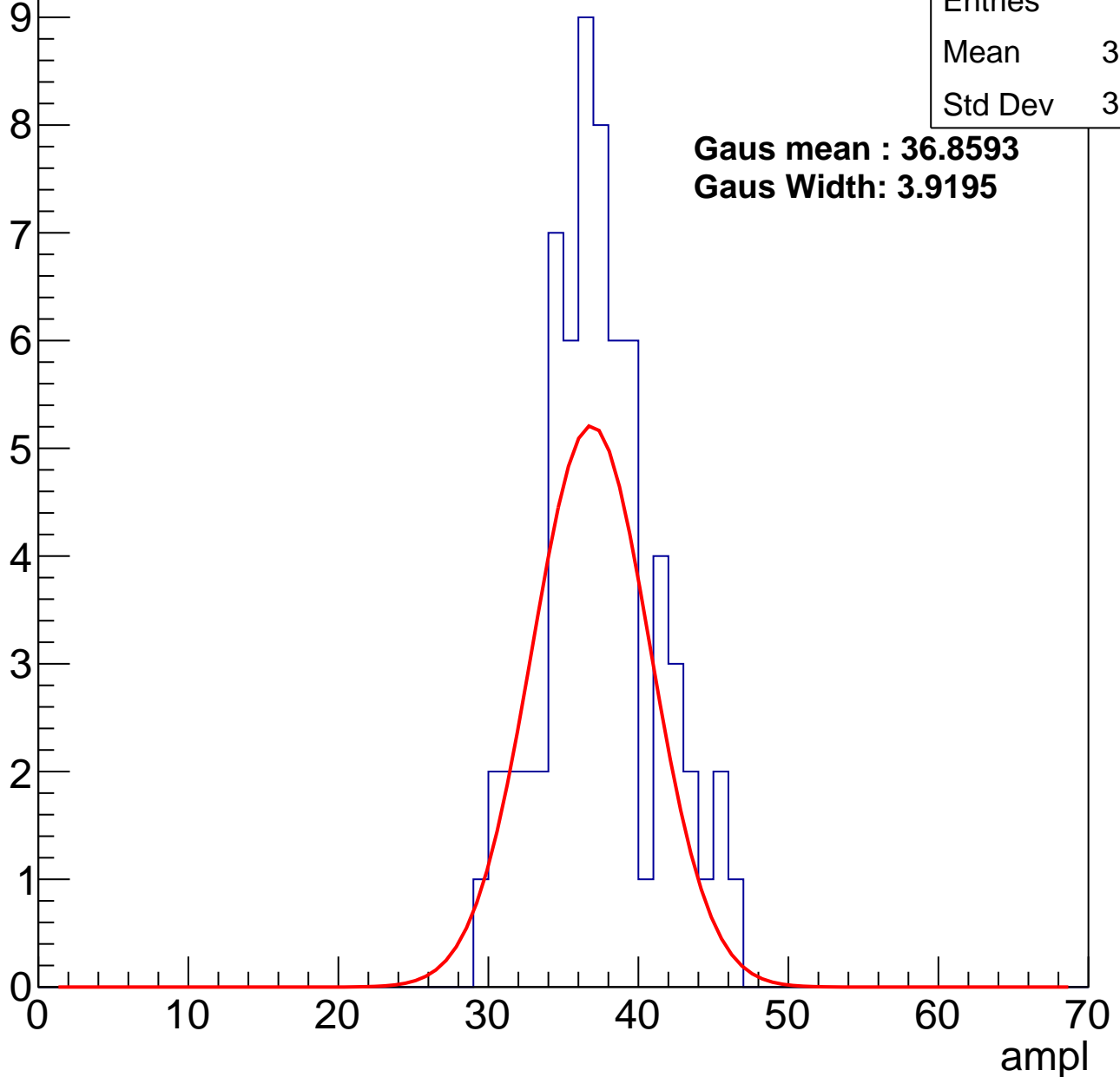
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.03
Std Dev	3.762

Gaus mean : 36.8593

Gaus Width: 3.9195



B0L001S, U15-ch67, adc2

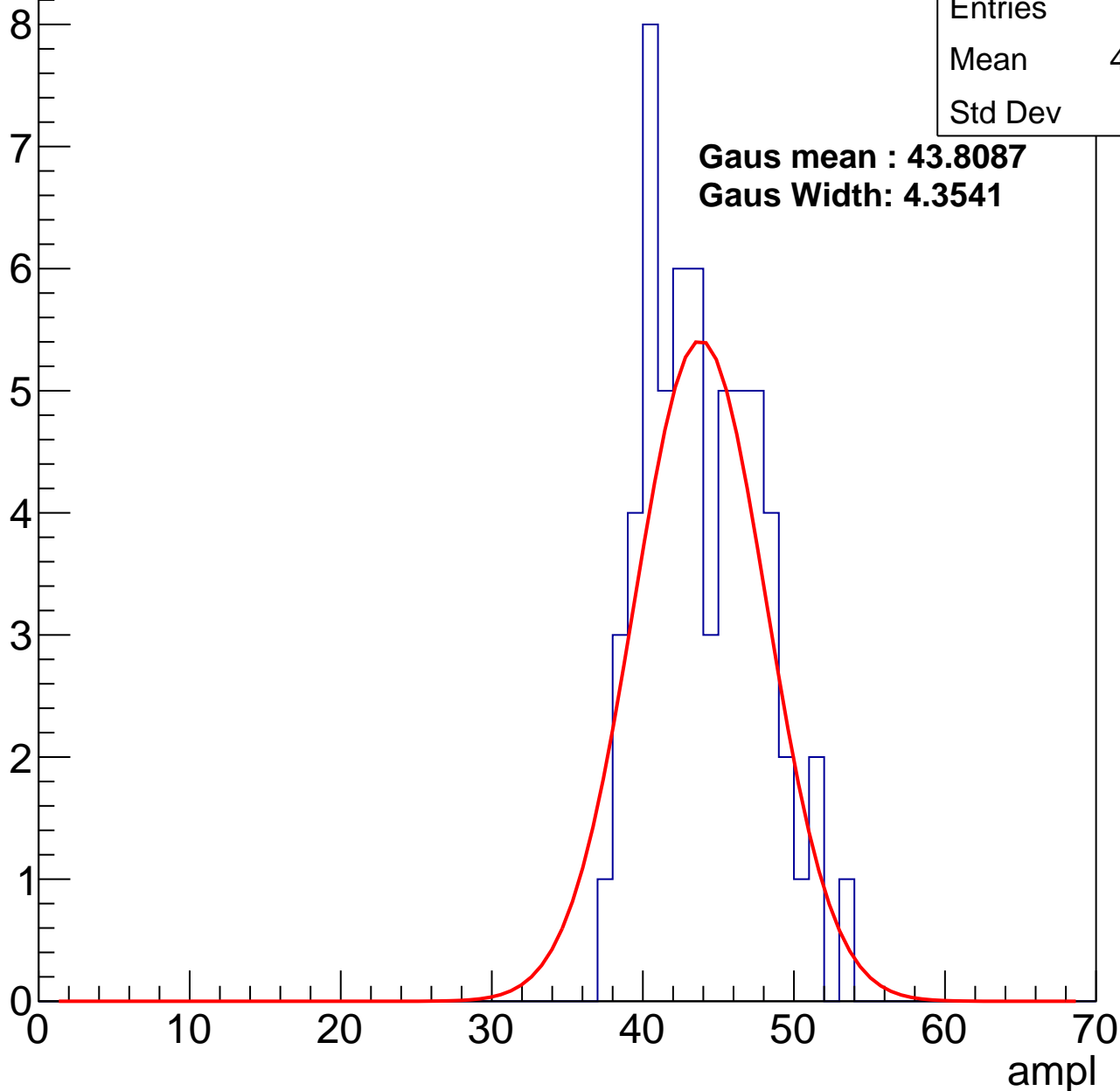
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.59
Std Dev	3.73

Gaus mean : 43.8087

Gaus Width: 4.3541

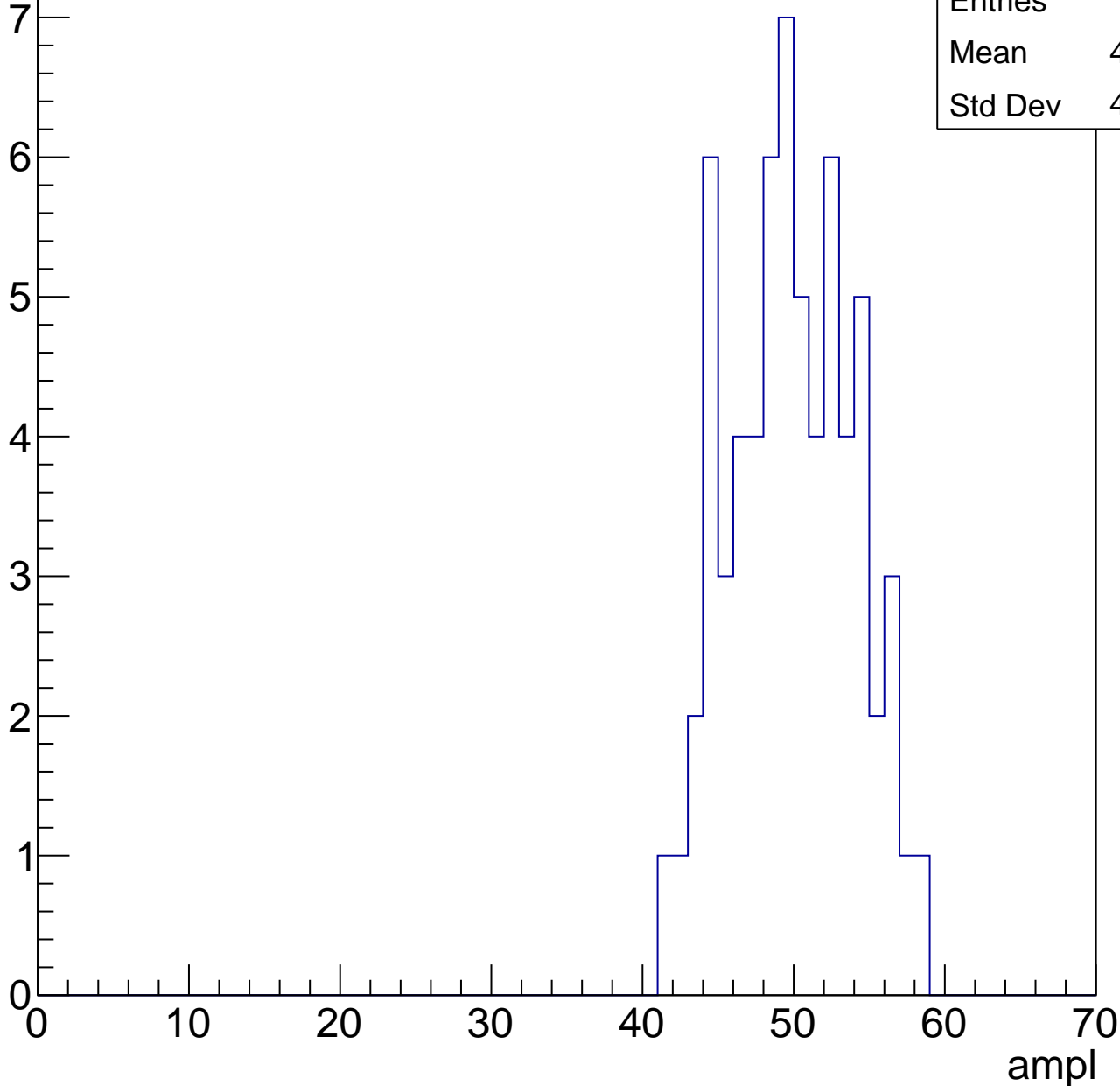


B0L001S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.42
Std Dev	4.034

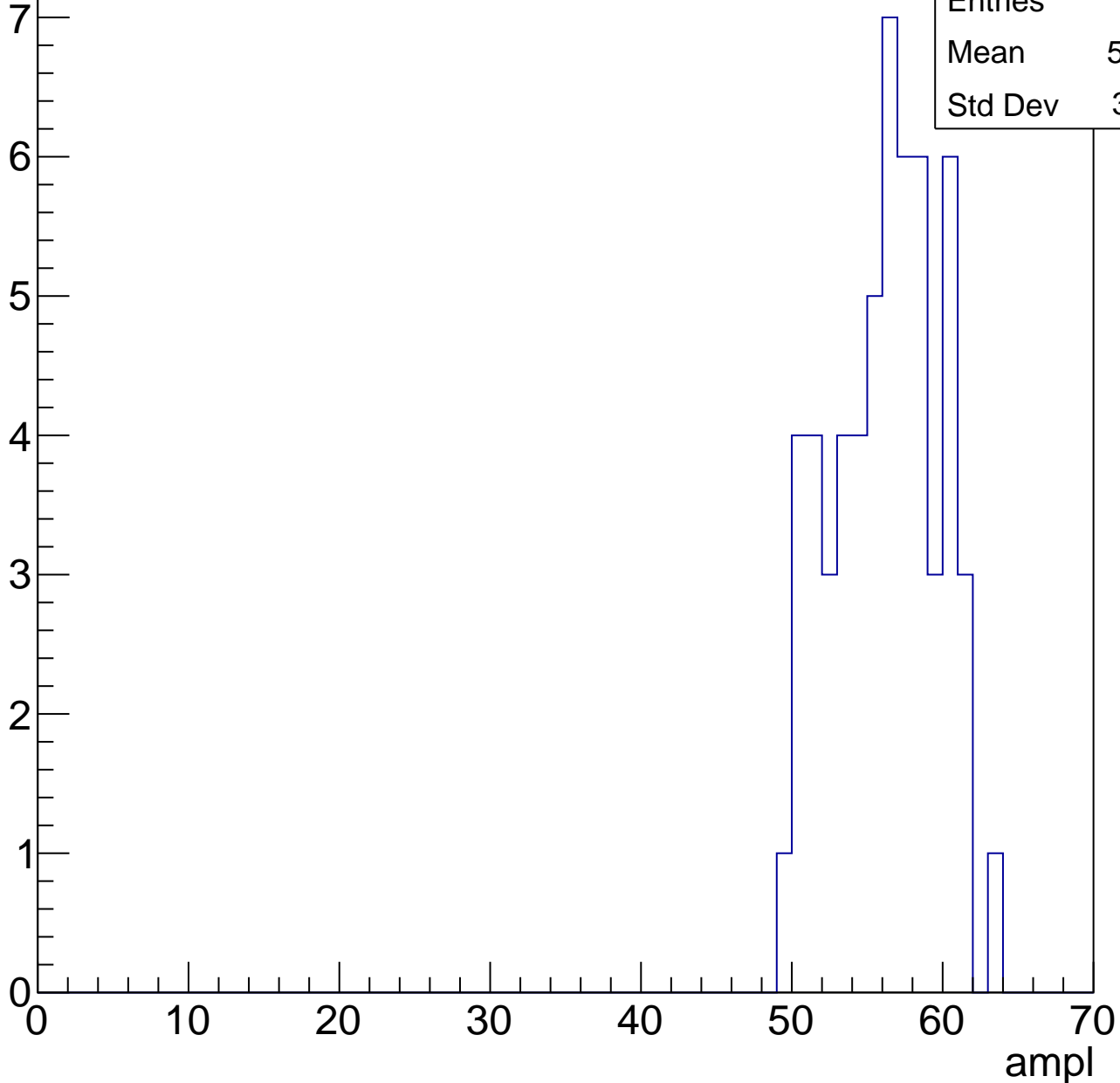


B0L001S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.74
Std Dev	3.431



B0L001S, U15-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	50
Mean	58.4
Std Dev	8.978

Entry

10

8

6

4

2

0

0

10

20

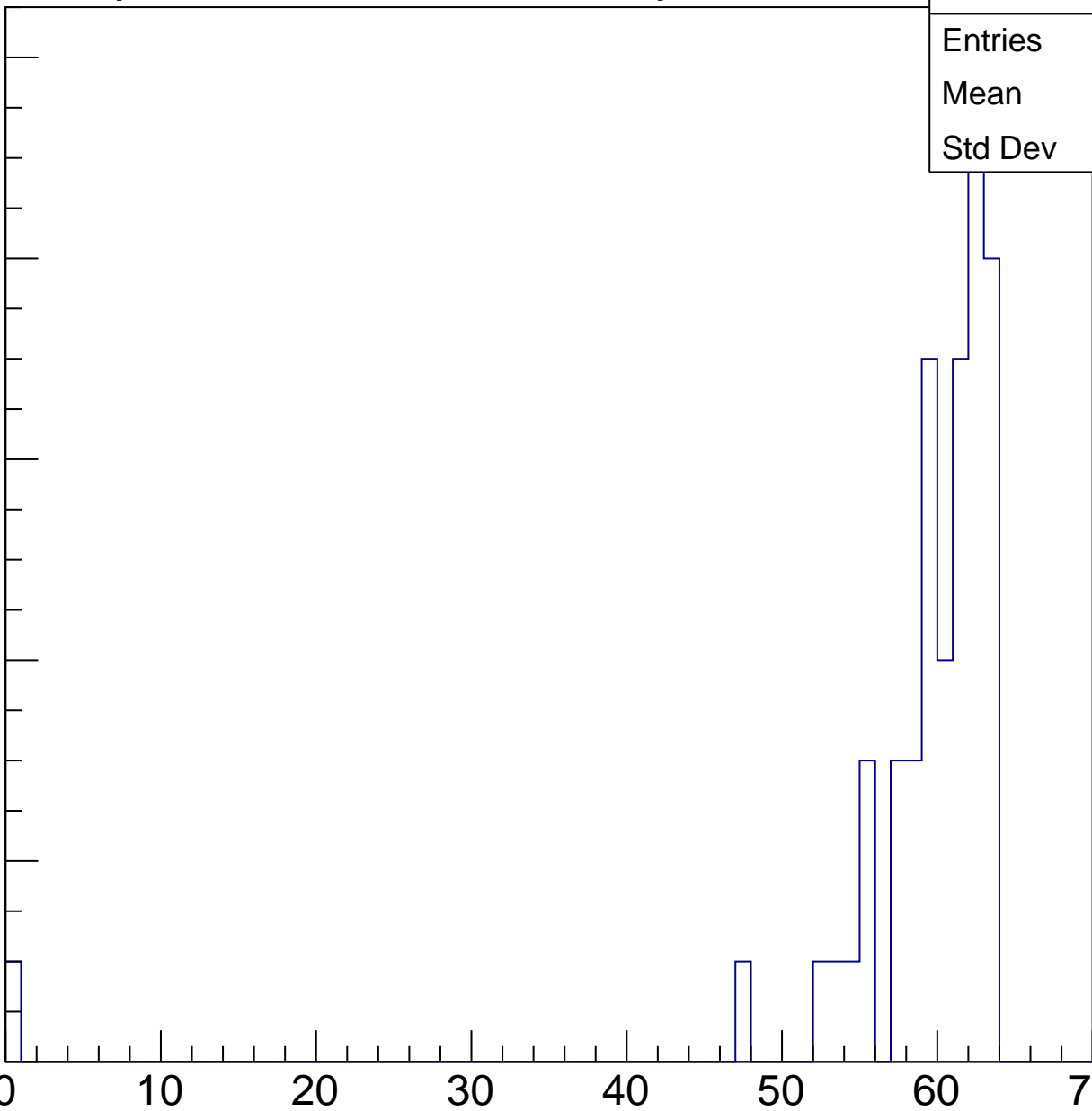
30

40

50

60

ampl



B0L001S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch68, adc0

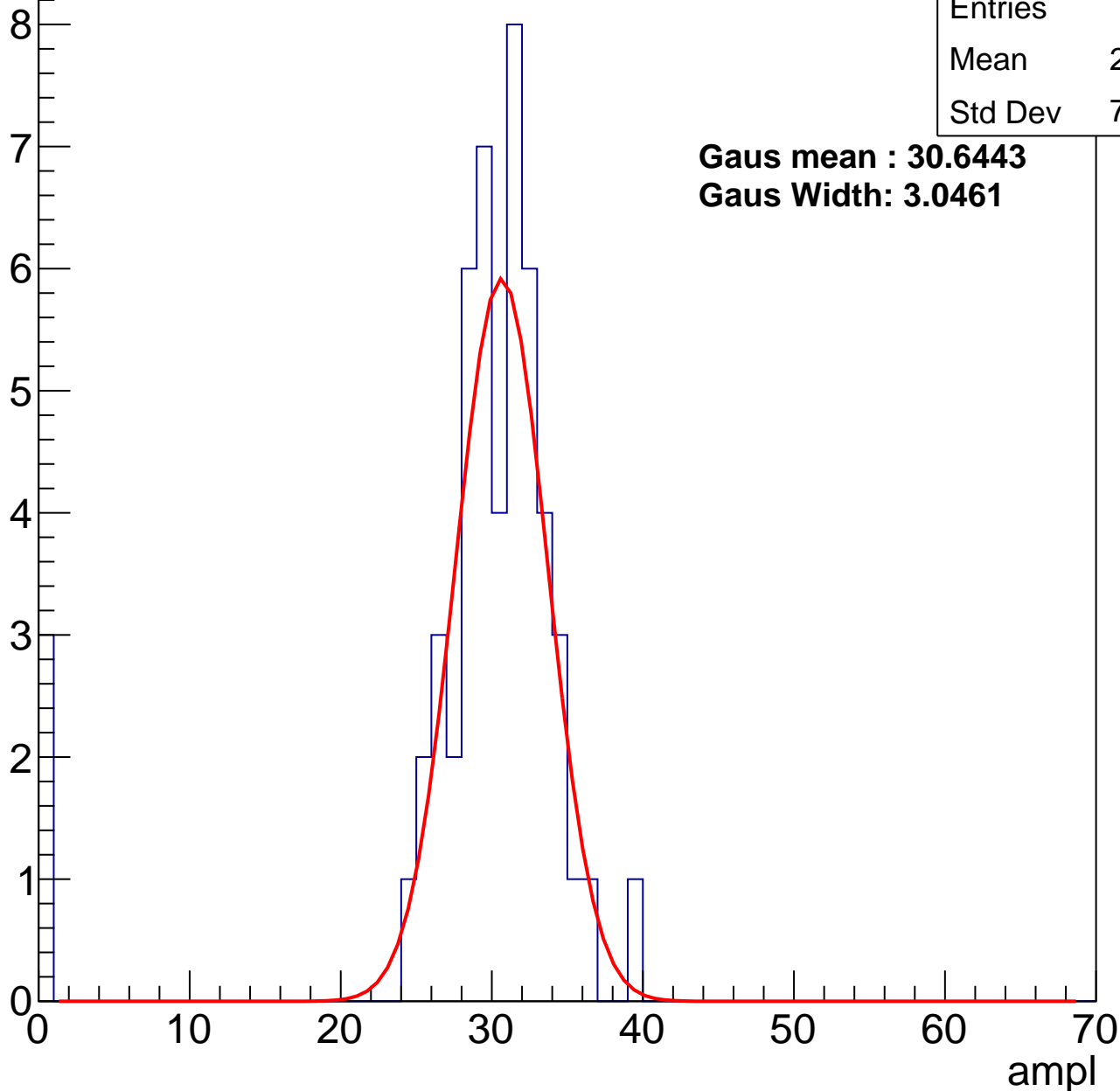
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	28.48
Std Dev	7.622

Gaus mean : 30.6443

Gaus Width: 3.0461



B0L001S, U15-ch68, adc1

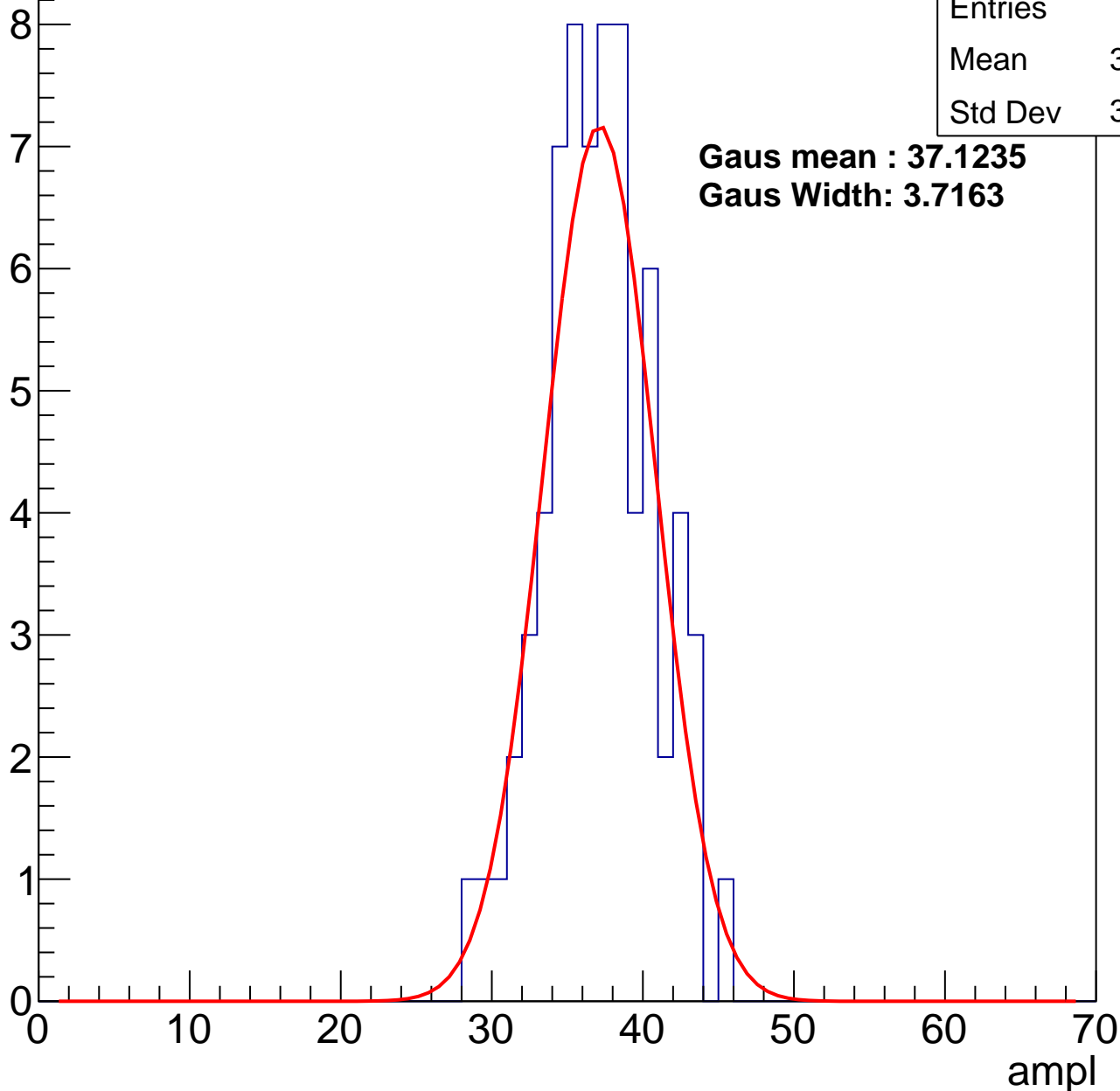
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.67
Std Dev	3.549

Gaus mean : 37.1235

Gaus Width: 3.7163



B0L001S, U15-ch68, adc2

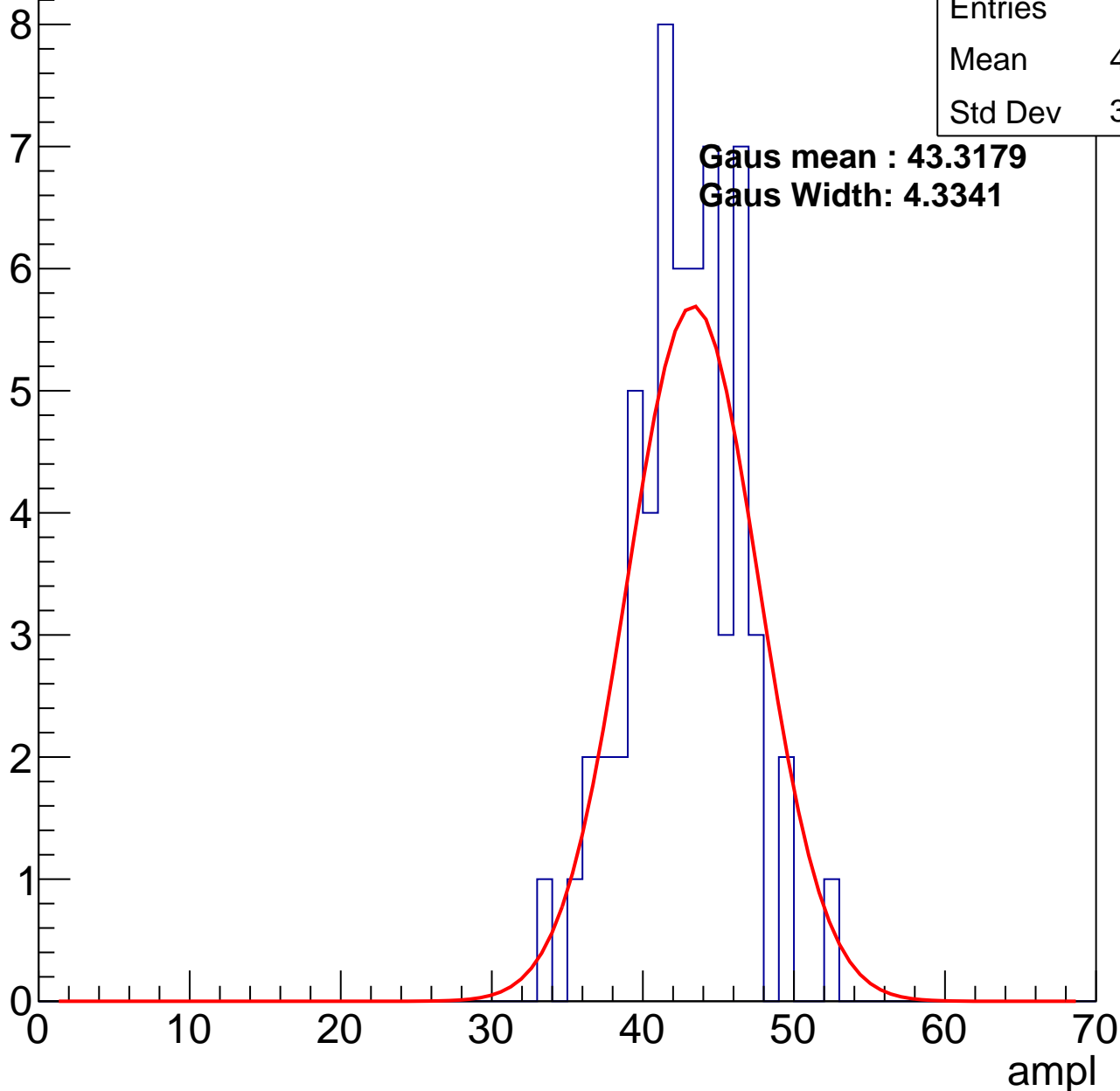
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	42.32
Std Dev	3.635

Gaus mean : 43.3179

Gaus Width: 4.3341



B0L001S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

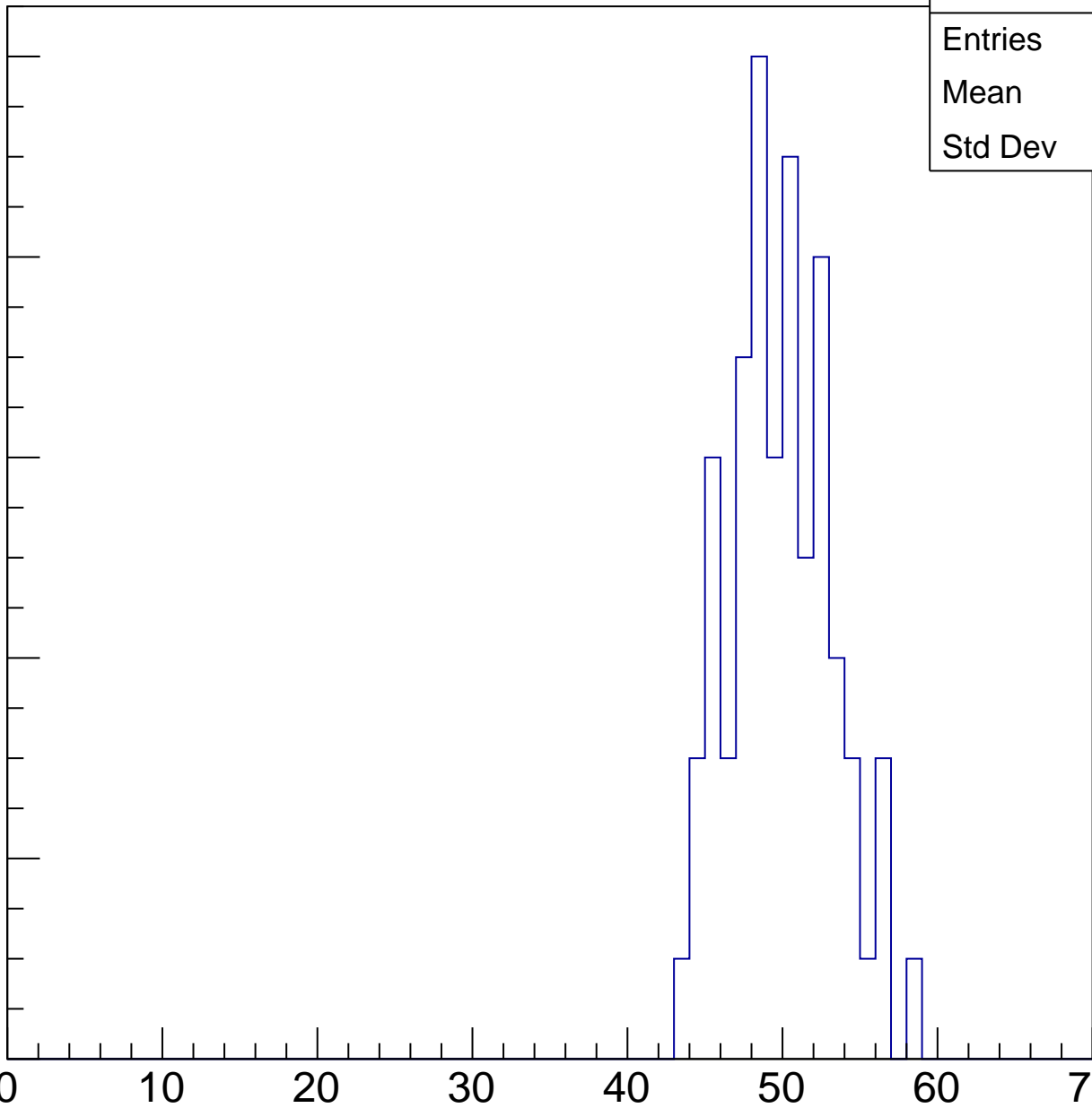
Entries	70
Mean	49.46
Std Dev	3.298

Entry

10
8
6
4
2
0

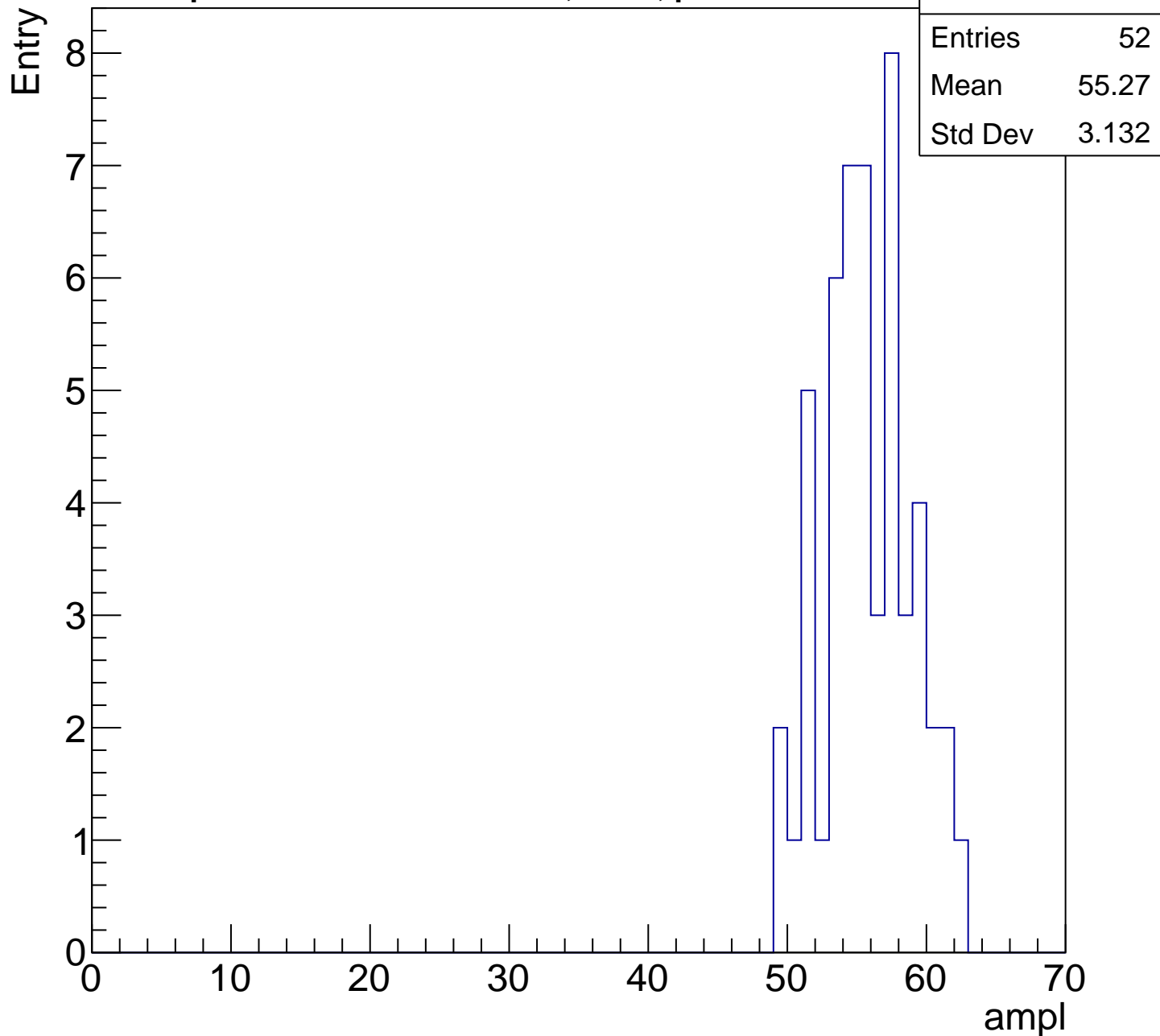
0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

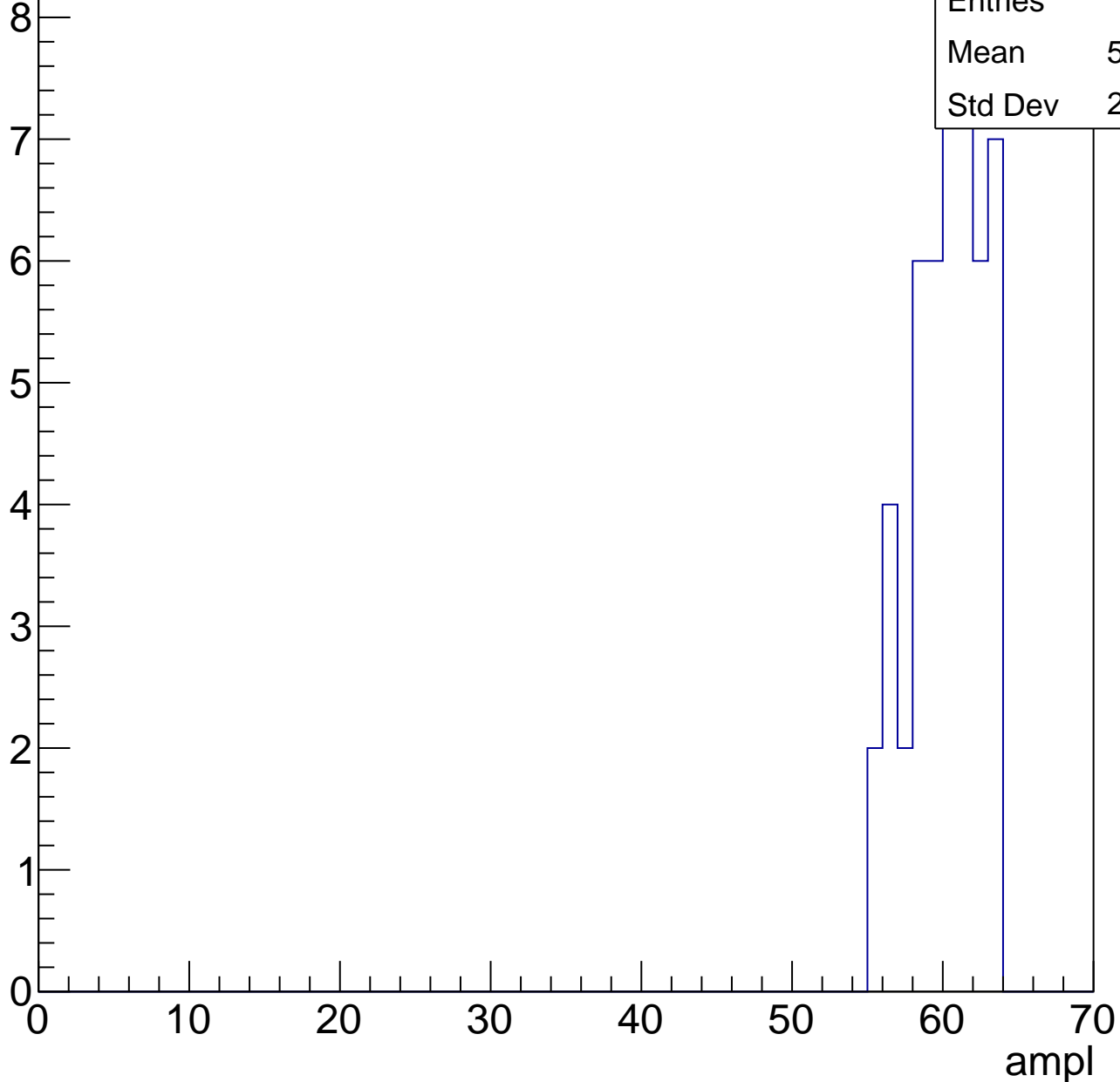


B0L001S, U15-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

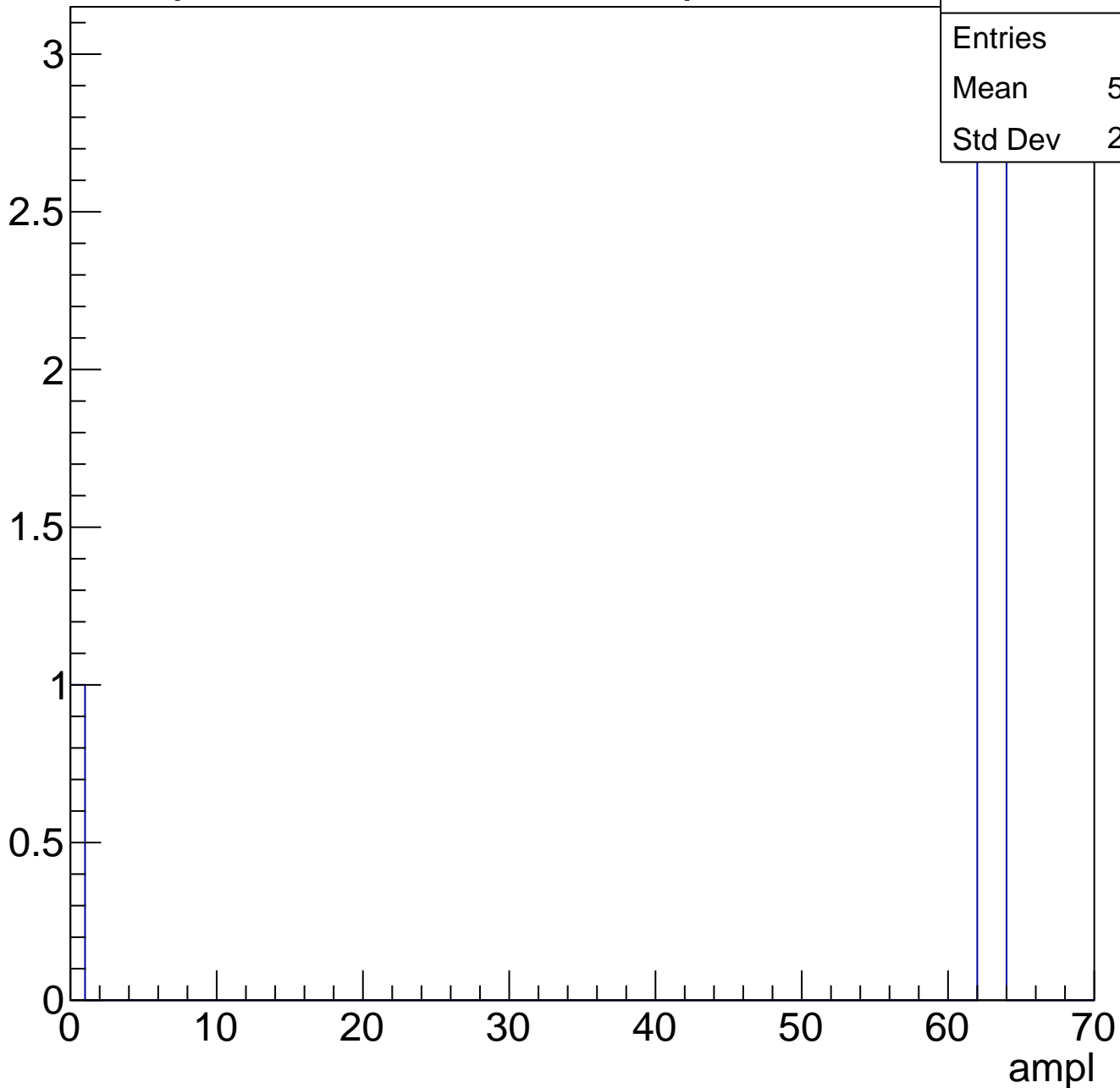
Entries	49
Mean	59.82
Std Dev	2.283



B0L001S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch69, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	29.55
Std Dev	5.351

Gaus mean : 30.8011

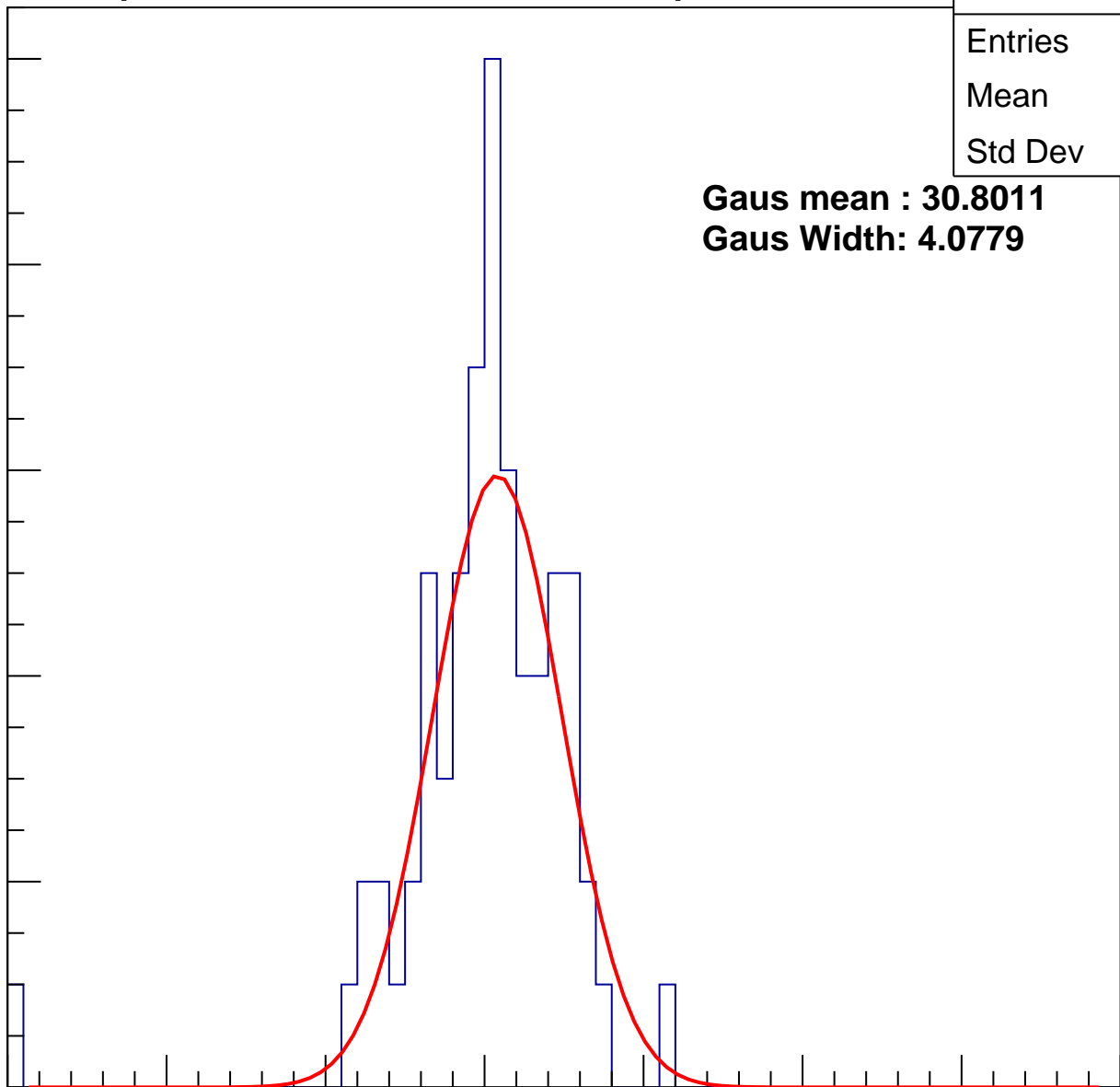
Gaus Width: 4.0779

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch69, adc1

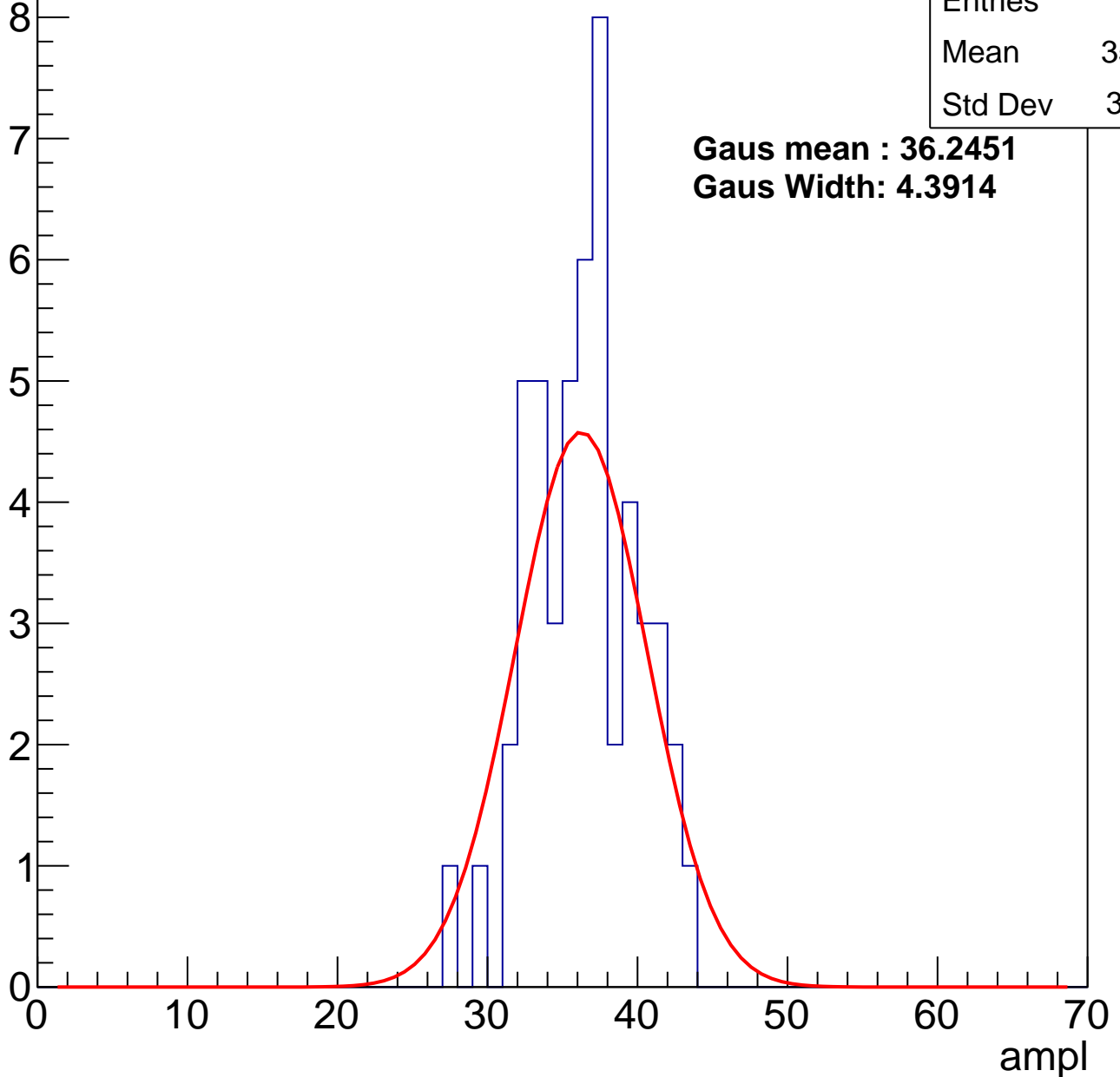
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	35.96
Std Dev	3.481

Gaus mean : 36.2451

Gaus Width: 4.3914



B0L001S, U15-ch69, adc2

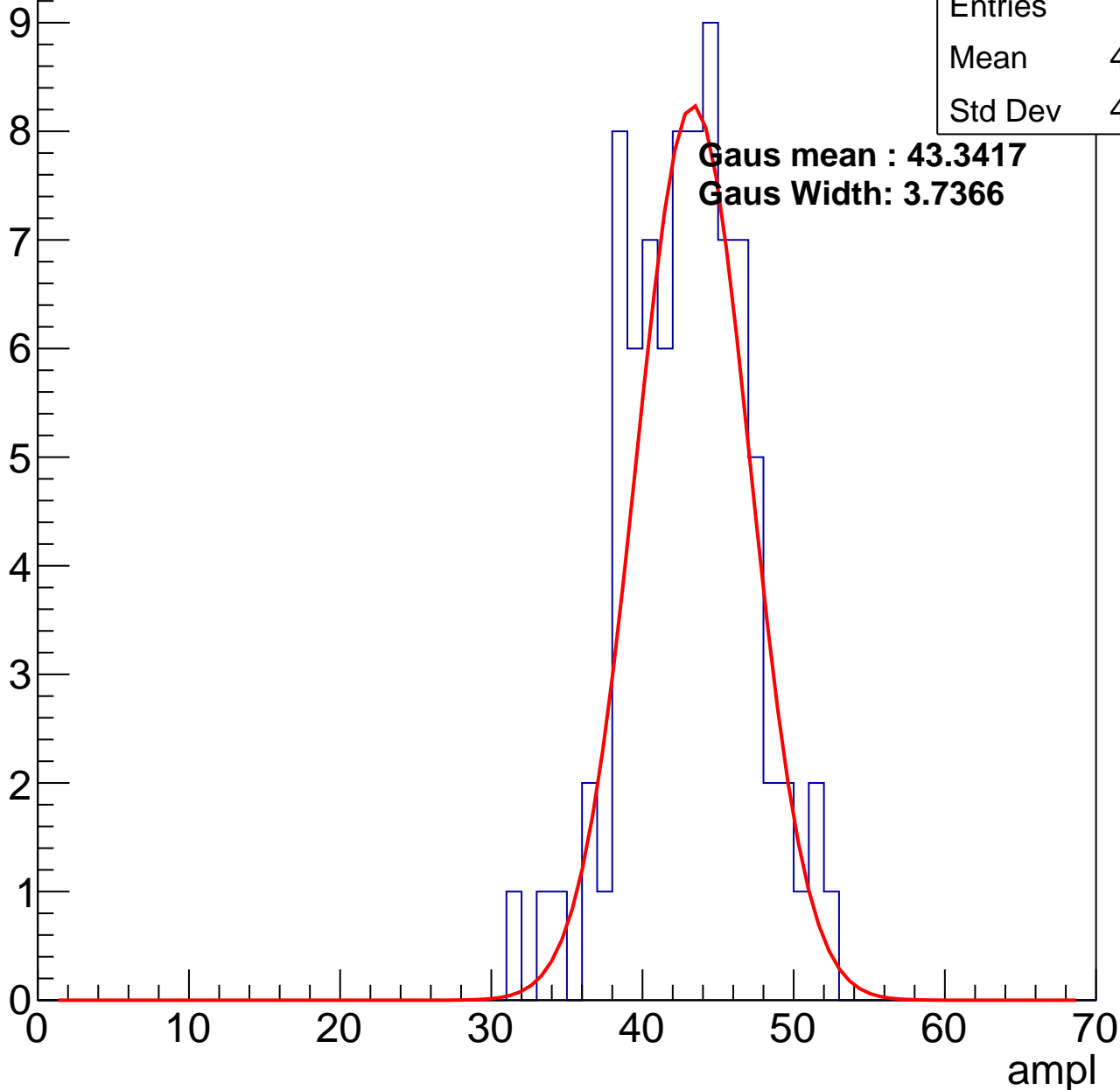
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	42.55
Std Dev	4.048

Gaus mean : 43.3417

Gaus Width: 3.7366

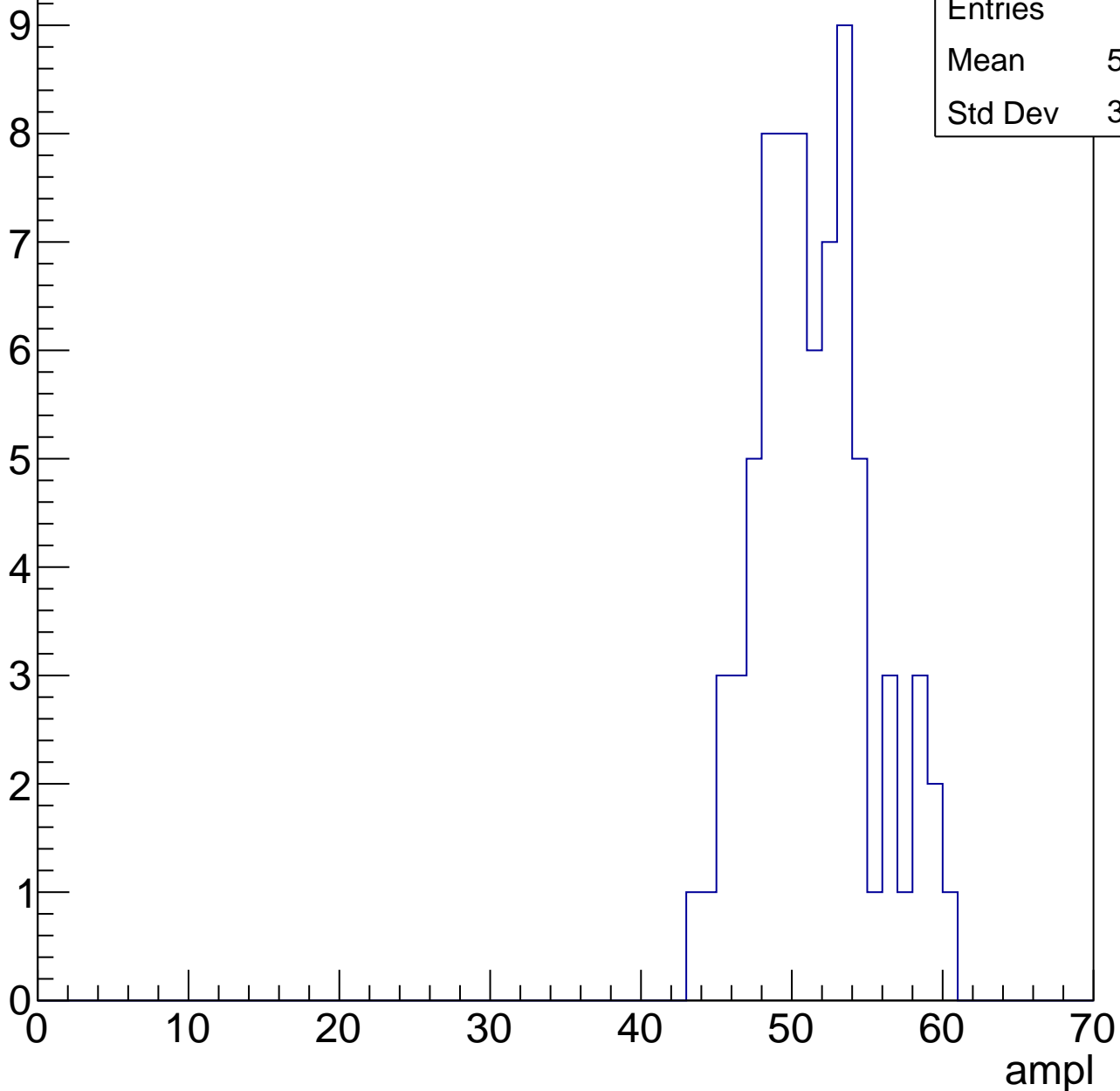


B0L001S, U15-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.93
Std Dev	3.757

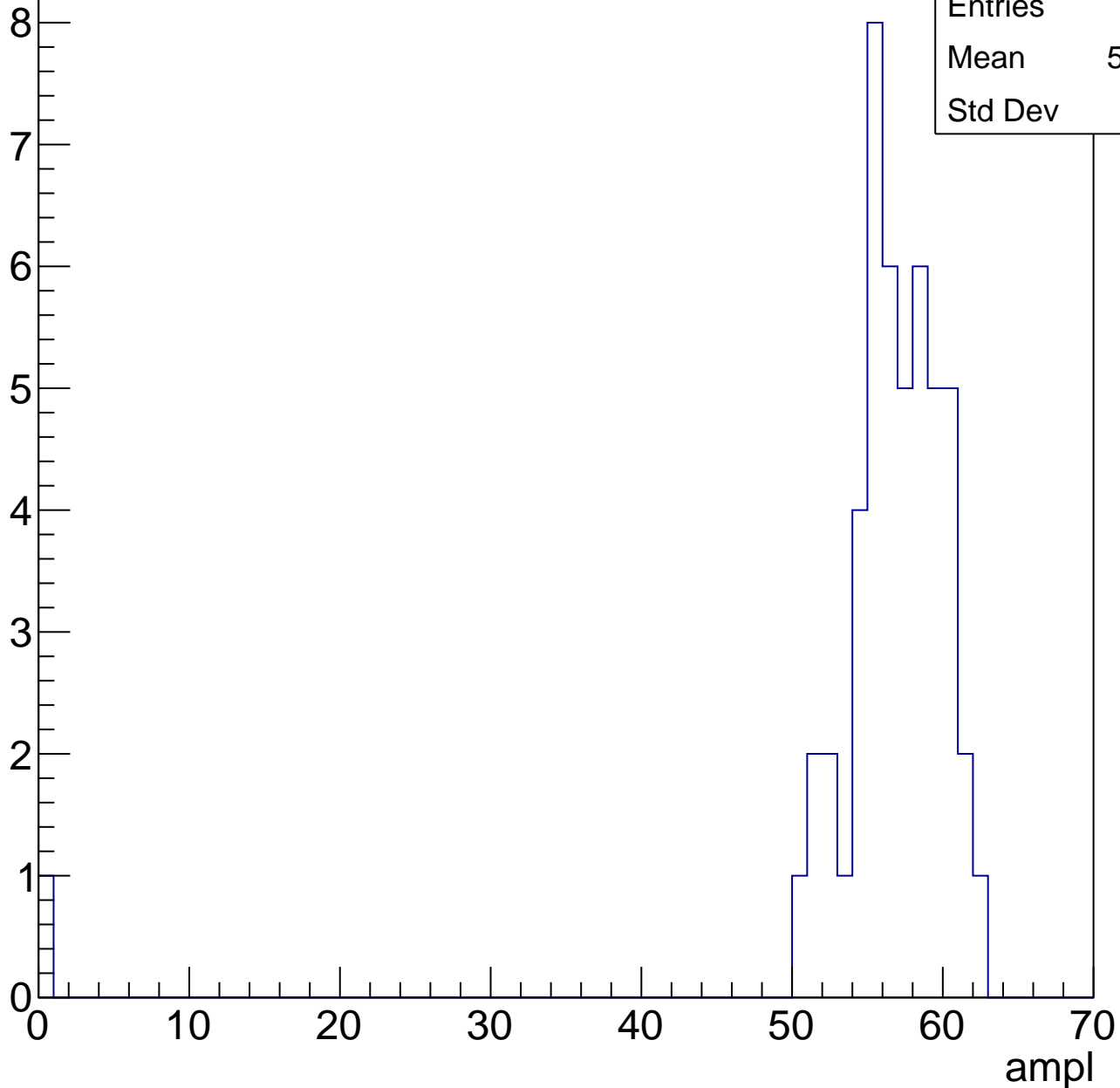


B0L001S, U15-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	55.37
Std Dev	8.46



B0L001S, U15-ch69, adc5

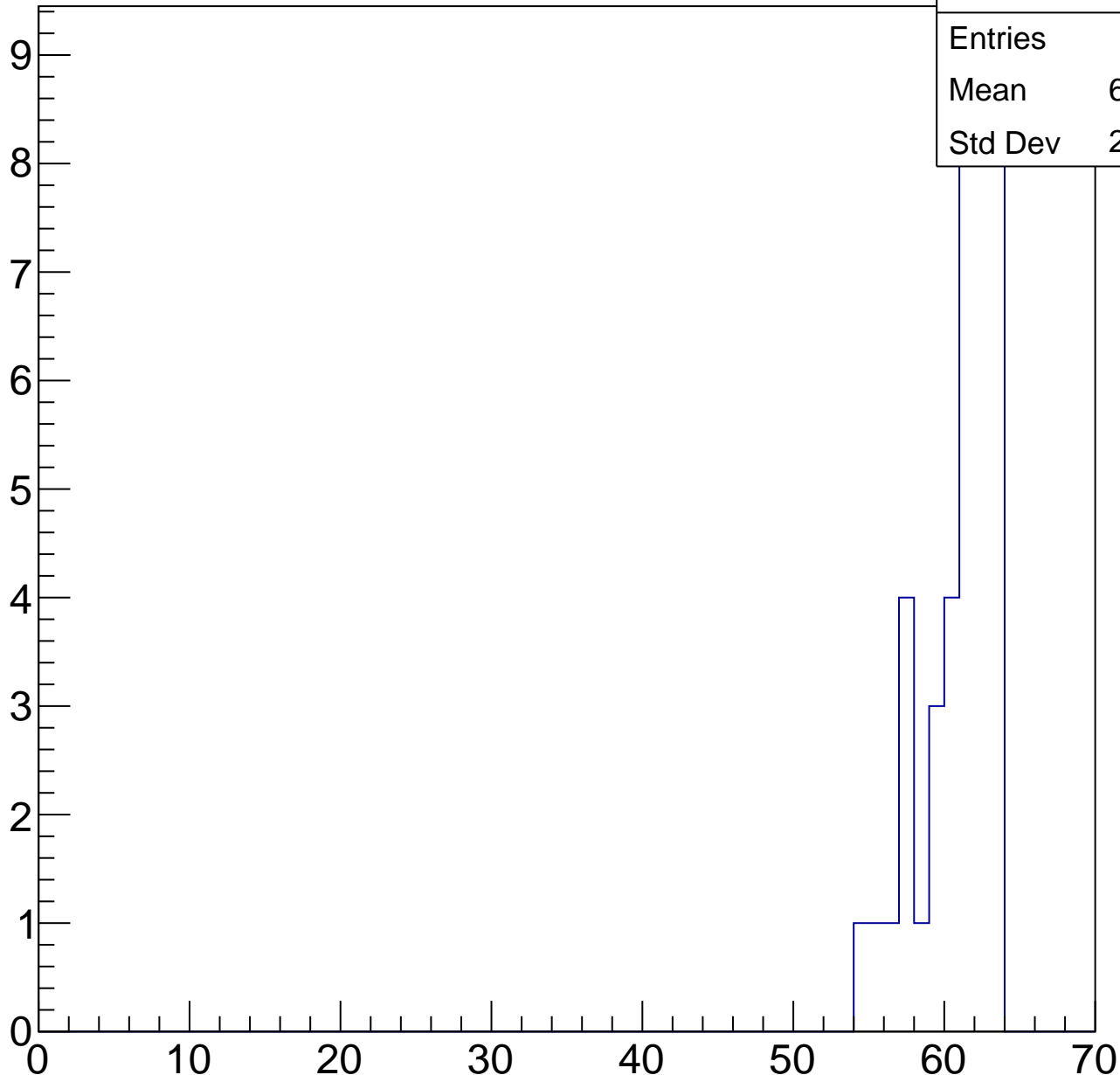
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.45
Std Dev	2.387

ampl



B0L001S, U15-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L001S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch70, adc0

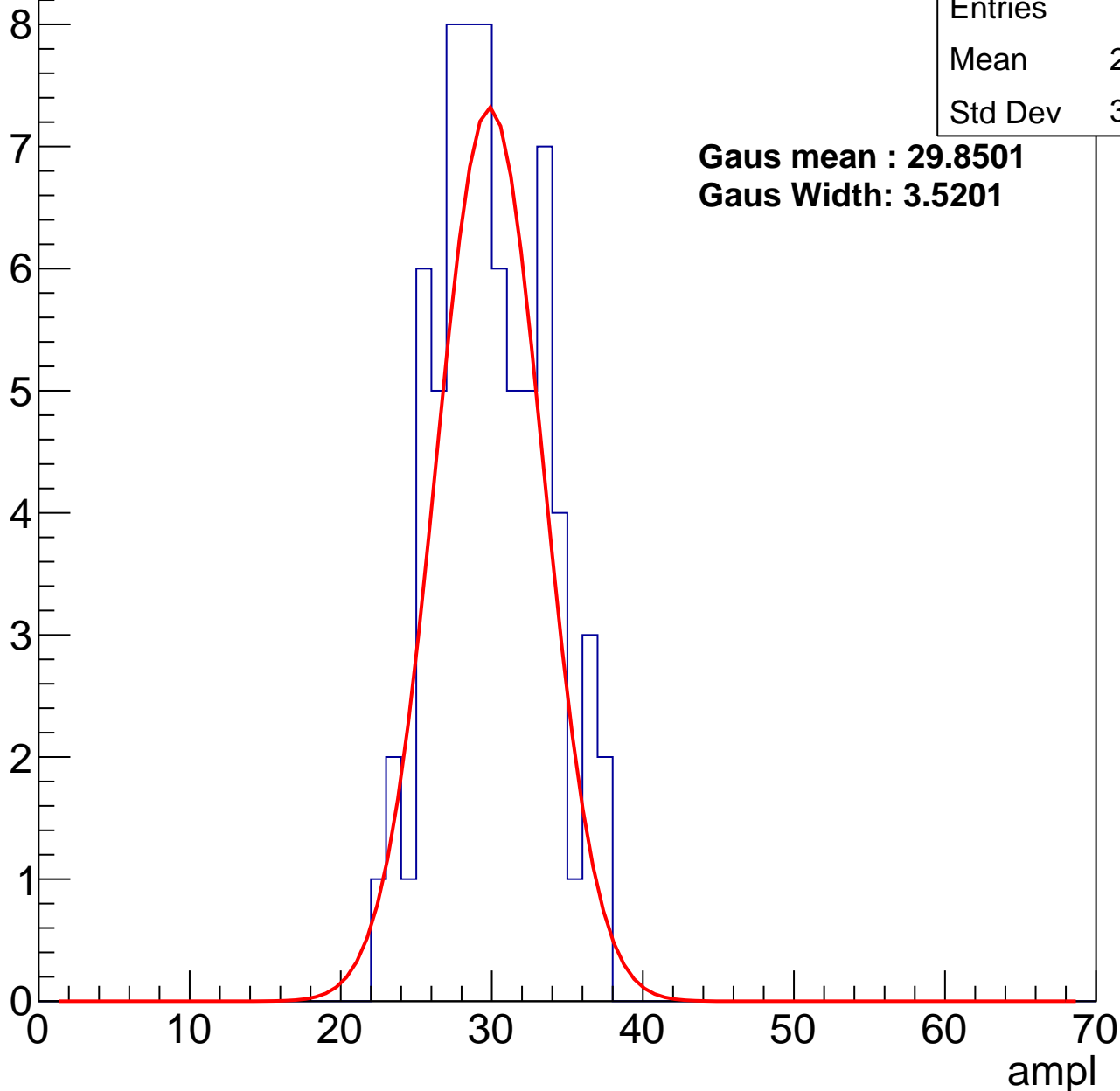
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.49
Std Dev	3.543

Gaus mean : 29.8501

Gaus Width: 3.5201



B0L001S, U15-ch70, adc1

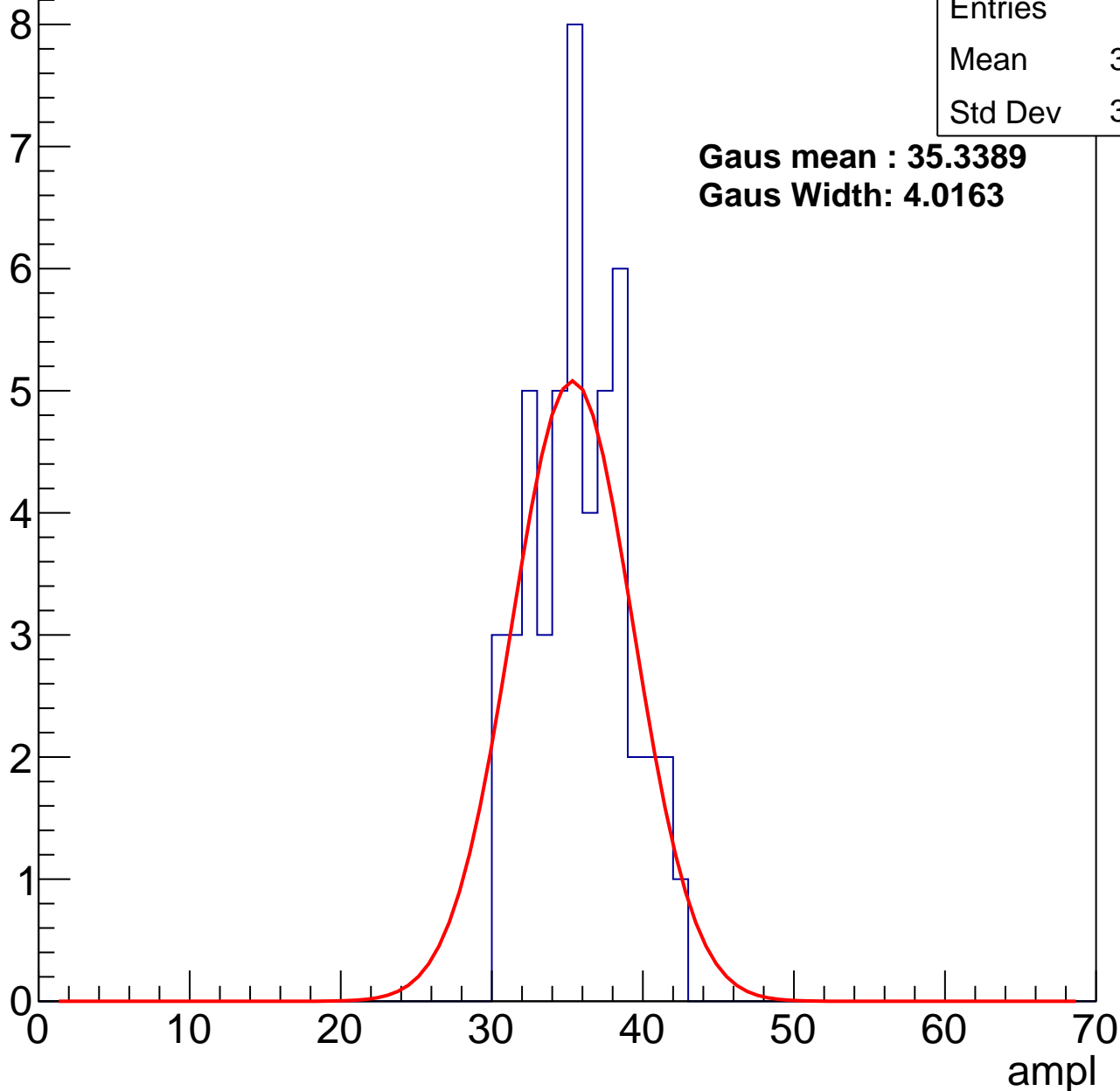
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	35.33
Std Dev	3.067

Gaus mean : 35.3389

Gaus Width: 4.0163



B0L001S, U15-ch70, adc2

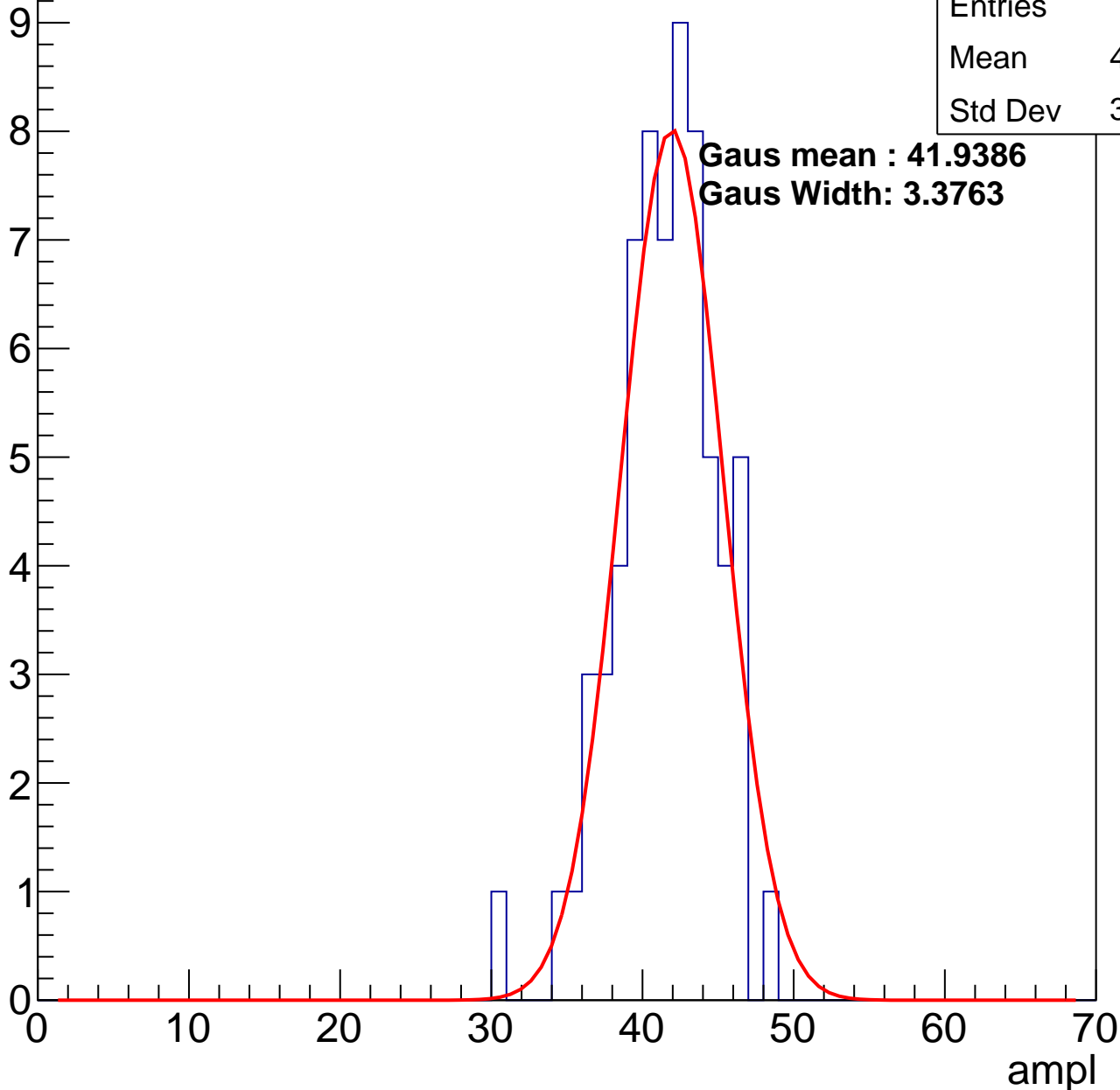
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	41.04
Std Dev	3.289

Gaus mean : 41.9386

Gaus Width: 3.3763

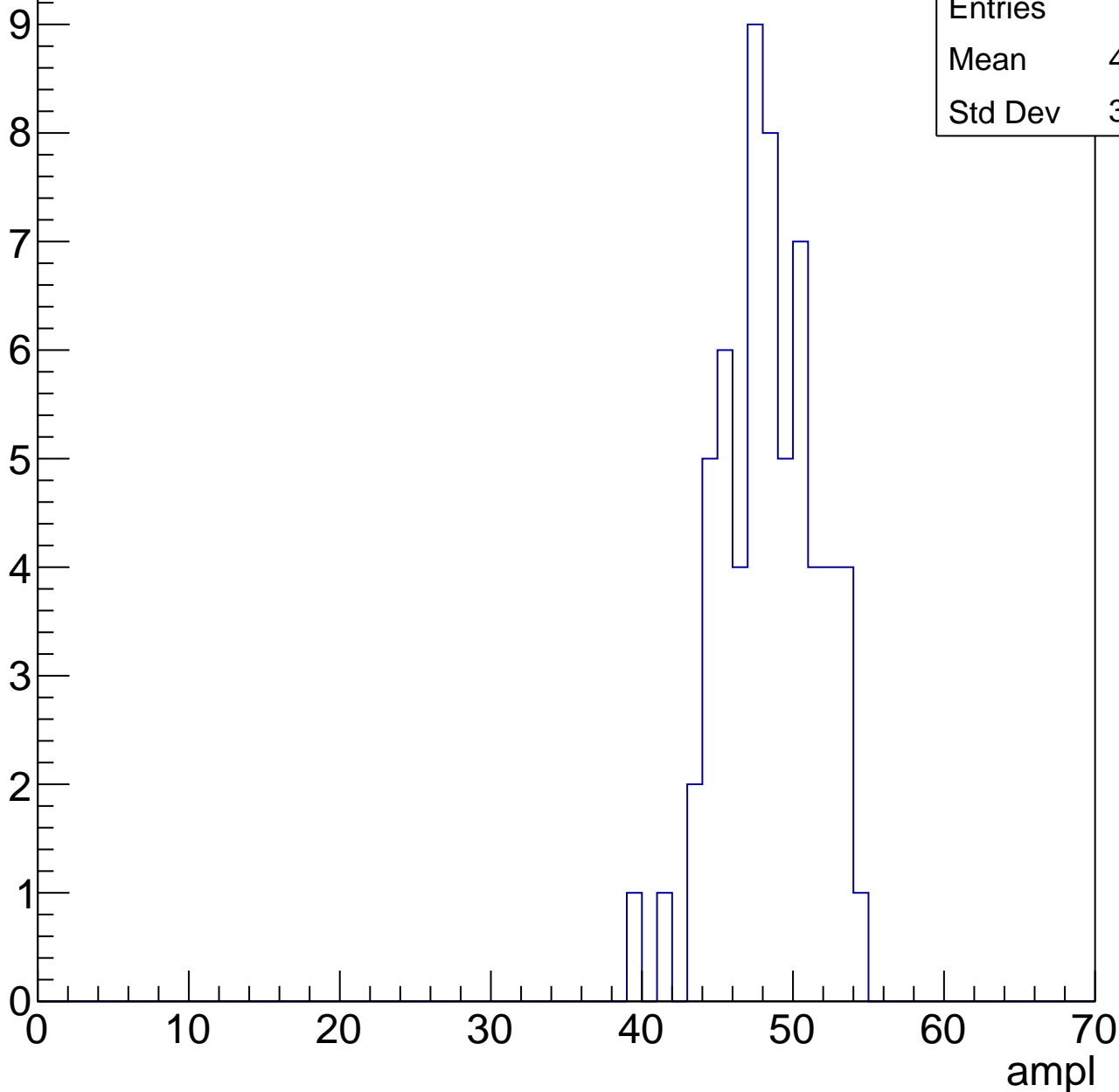


B0L001S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	47.87
Std Dev	3.154

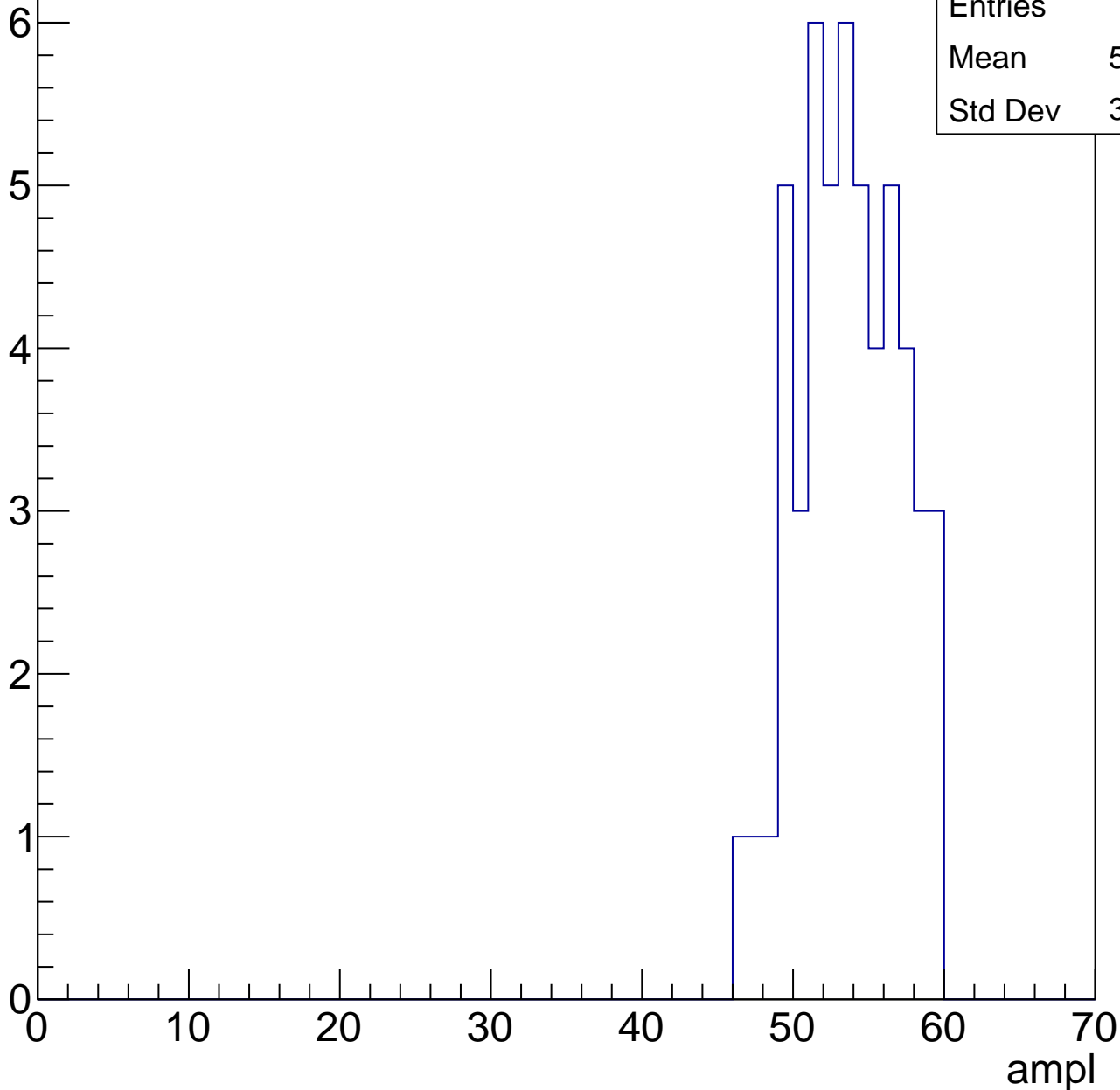


B0L001S, U15-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	53.25
Std Dev	3.269

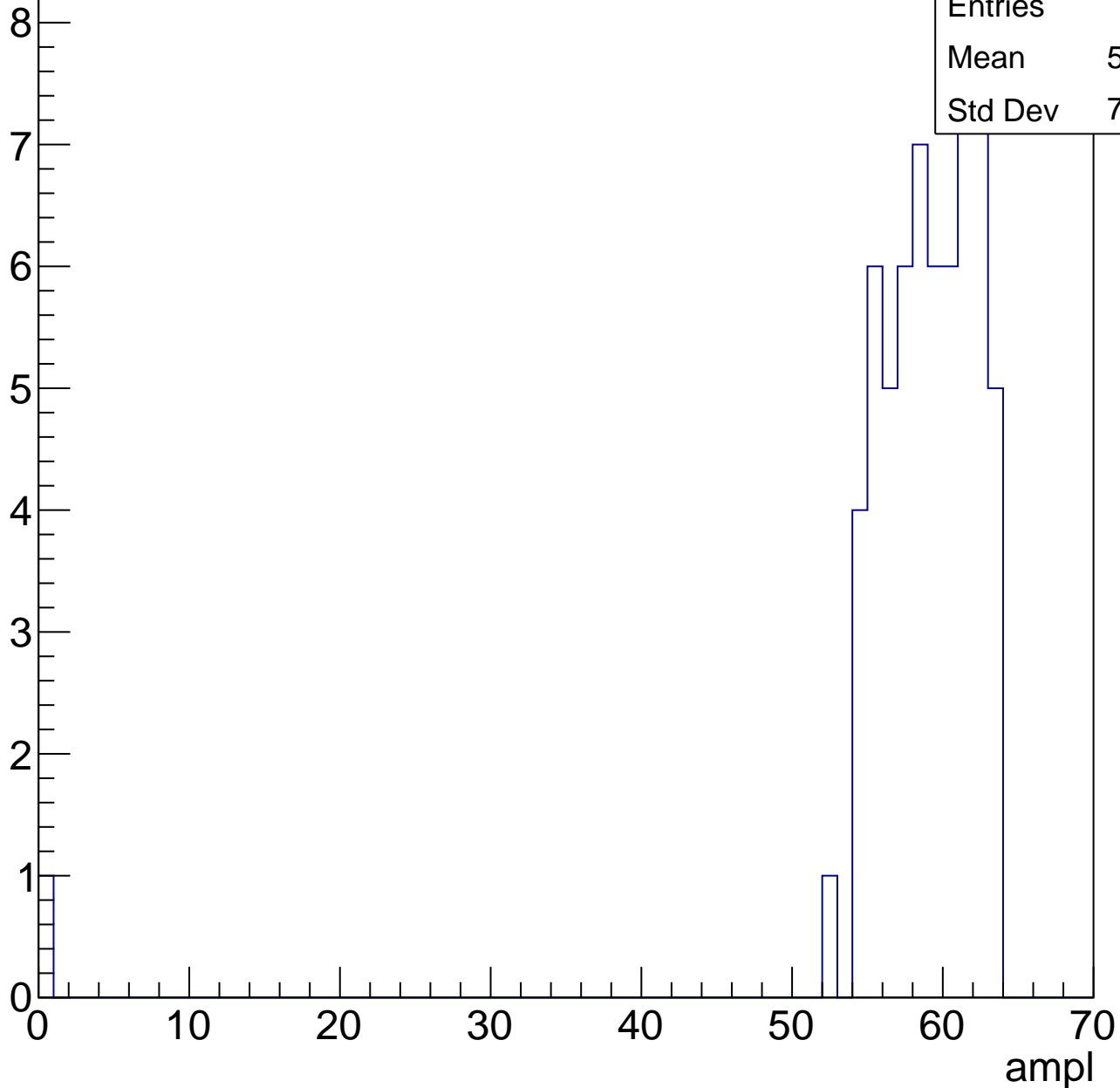


B0L001S, U15-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.76
Std Dev	7.863

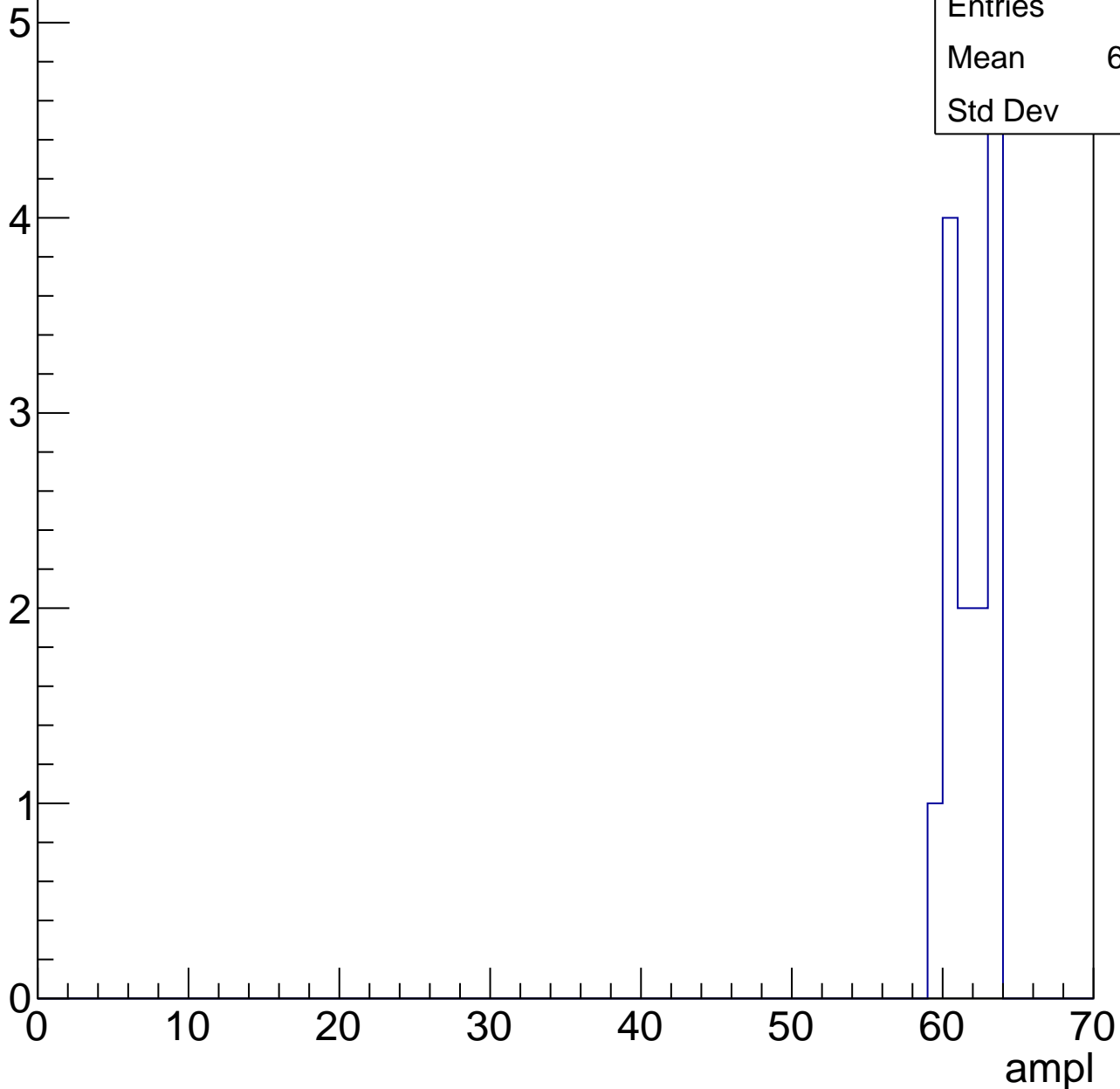


B0L001S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	61.43
Std Dev	1.4



B0L001S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch71, adc0

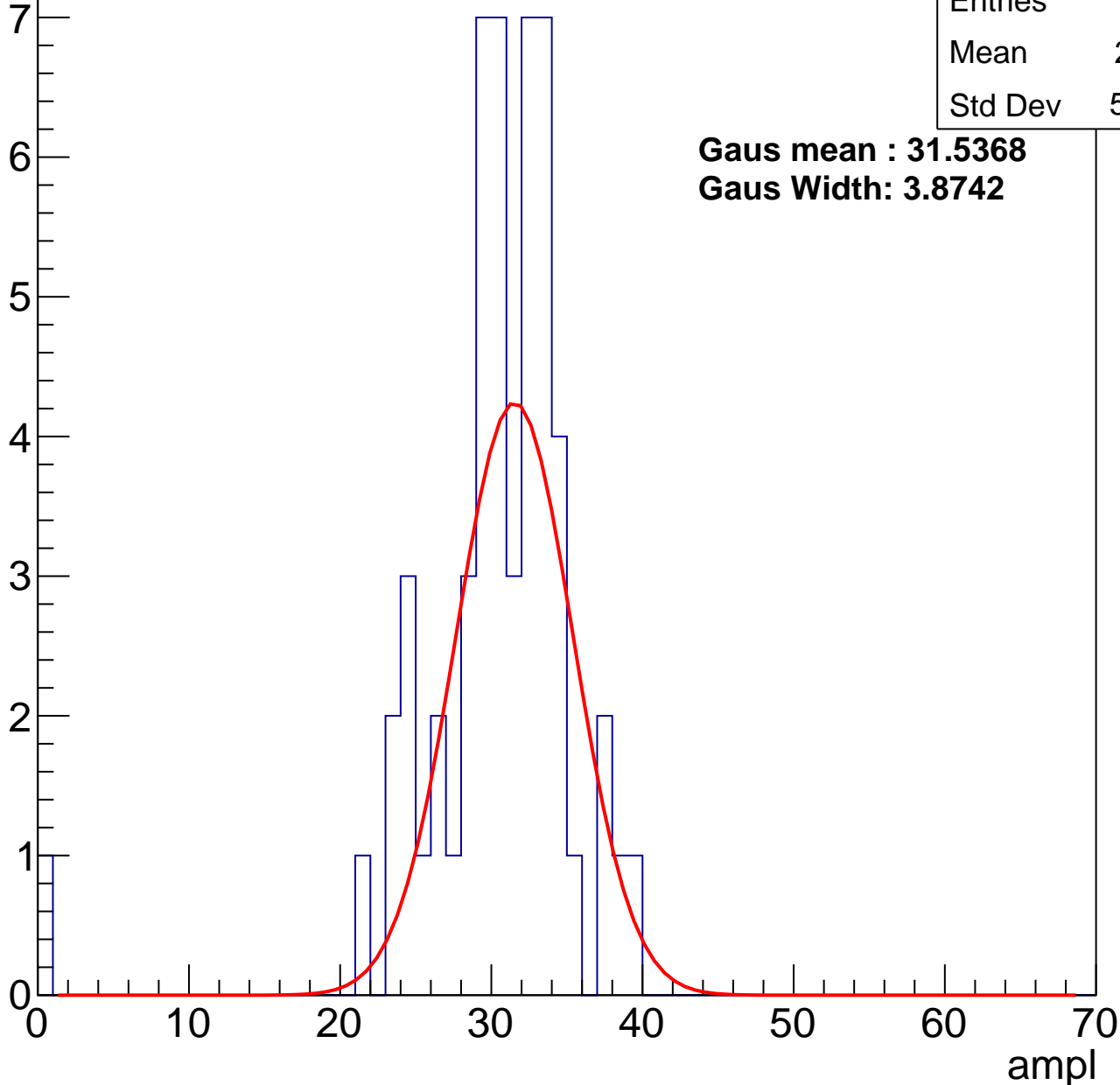
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.81
Std Dev	5.604

Gaus mean : 31.5368

Gaus Width: 3.8742



B0L001S, U15-ch71, adc1

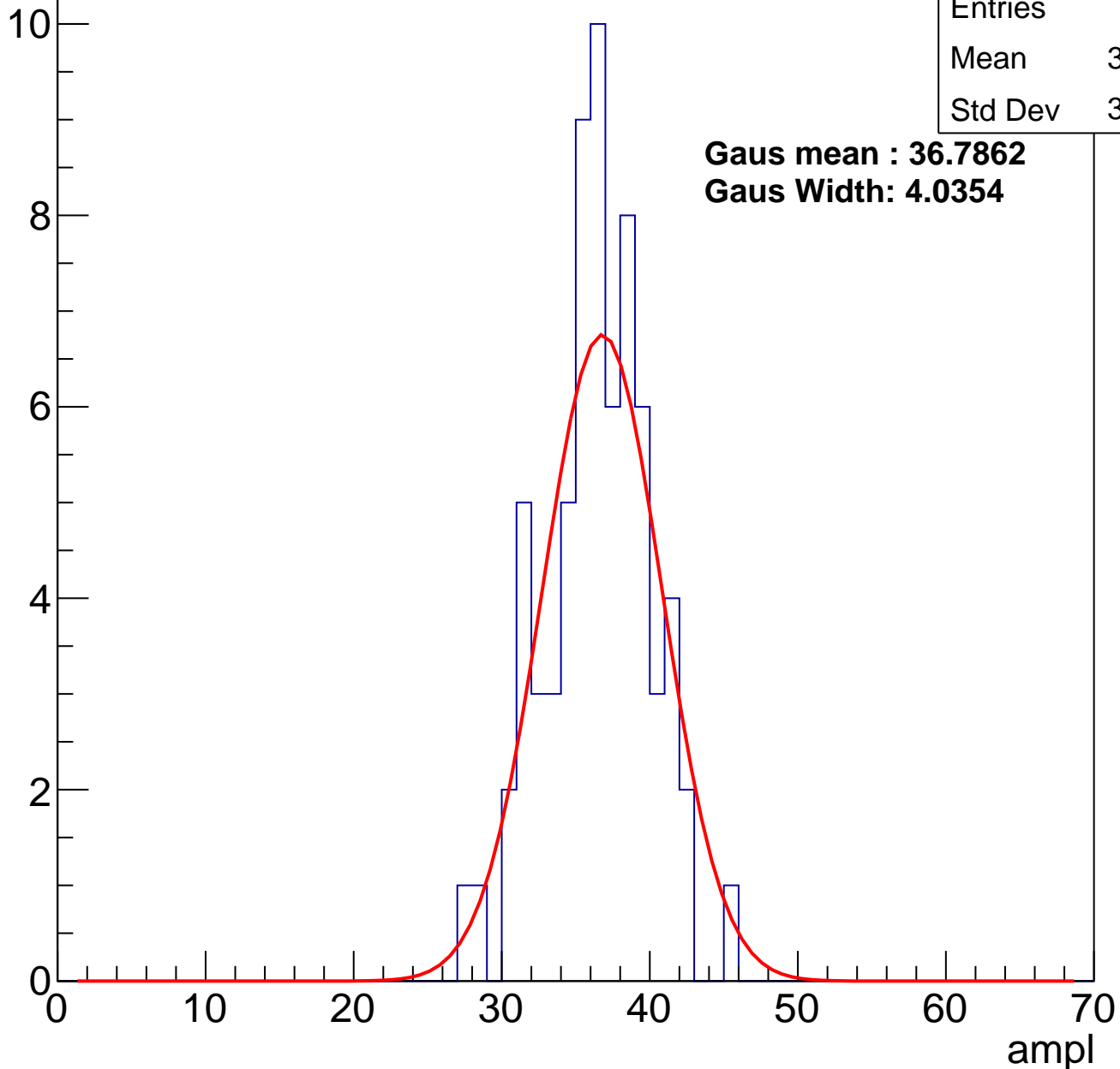
calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	35.99
Std Dev	3.487

Gaus mean : 36.7862

Gaus Width: 4.0354

Entry



B0L001S, U15-ch71, adc2

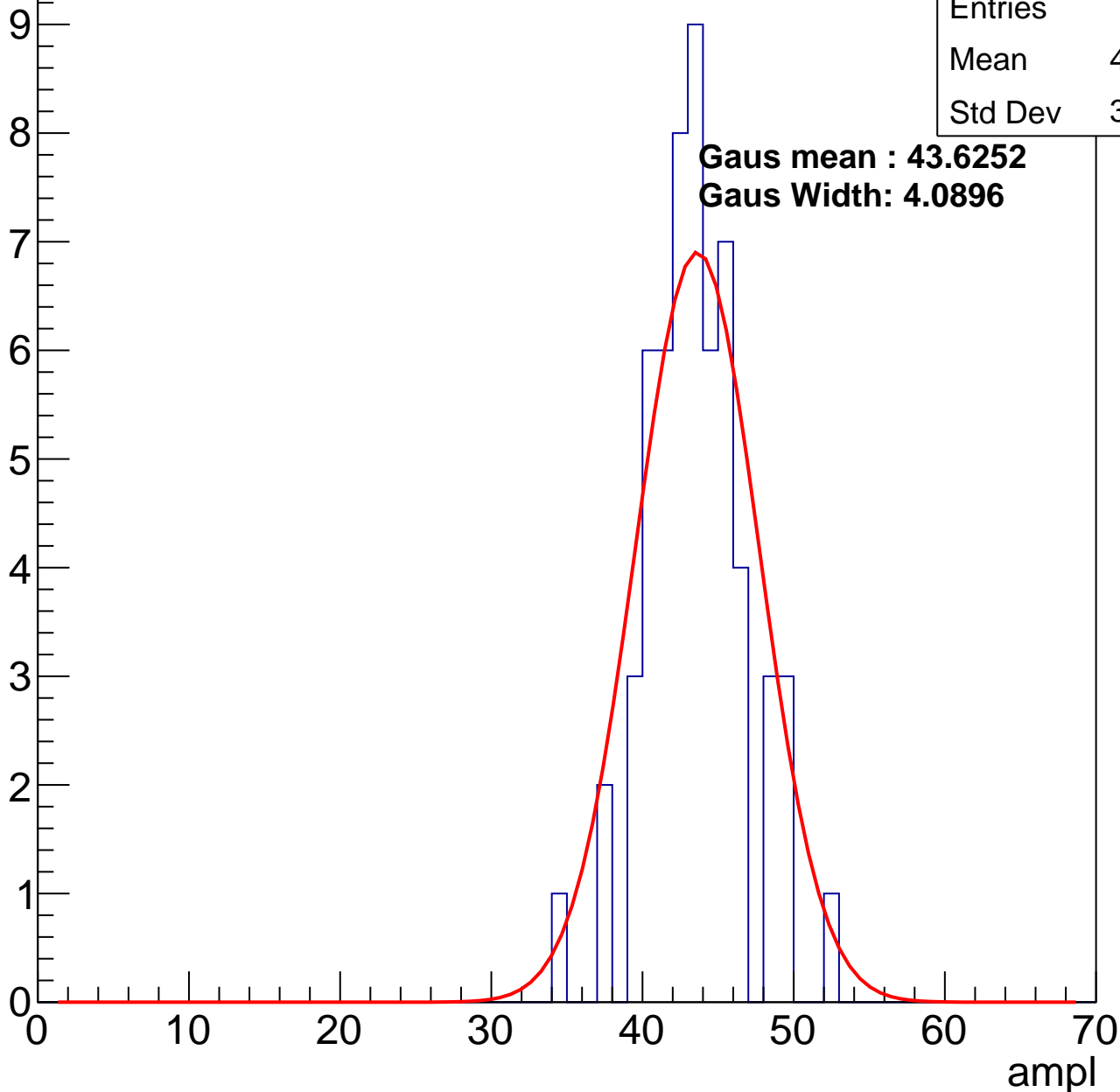
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.05
Std Dev	3.244

Gaus mean : 43.6252

Gaus Width: 4.0896

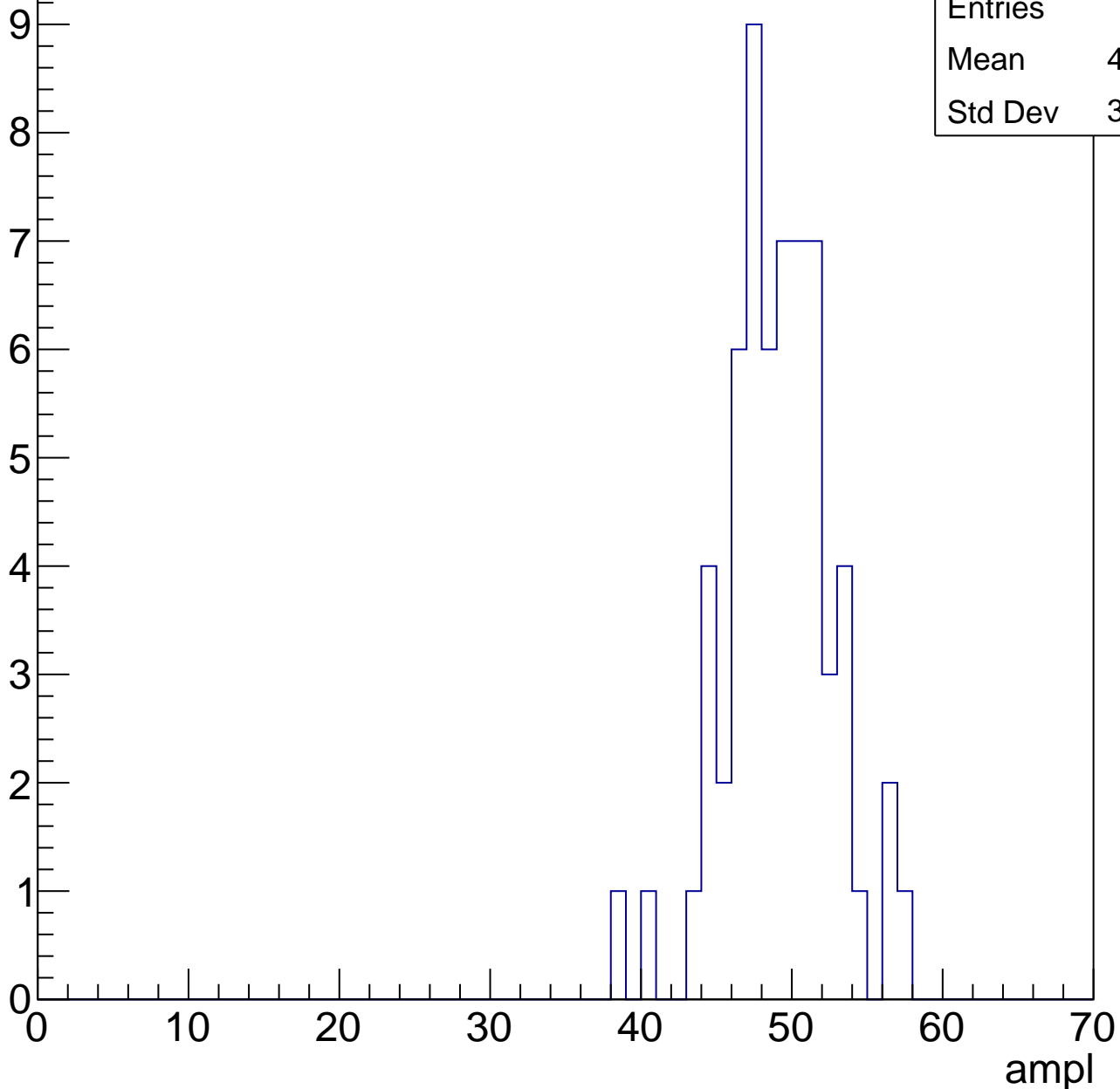


B0L001S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	48.63
Std Dev	3.525

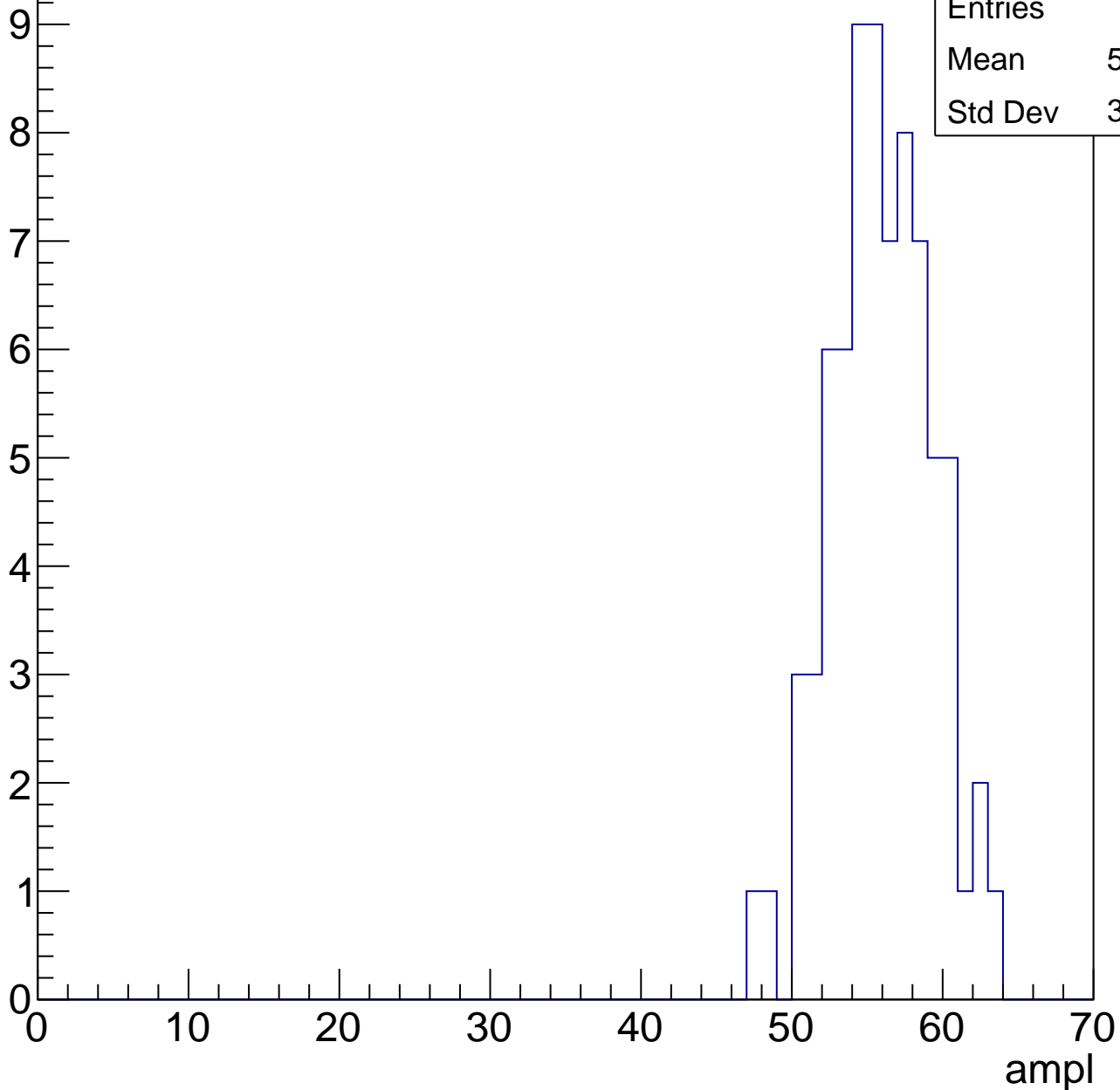


B0L001S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	55.49
Std Dev	3.309

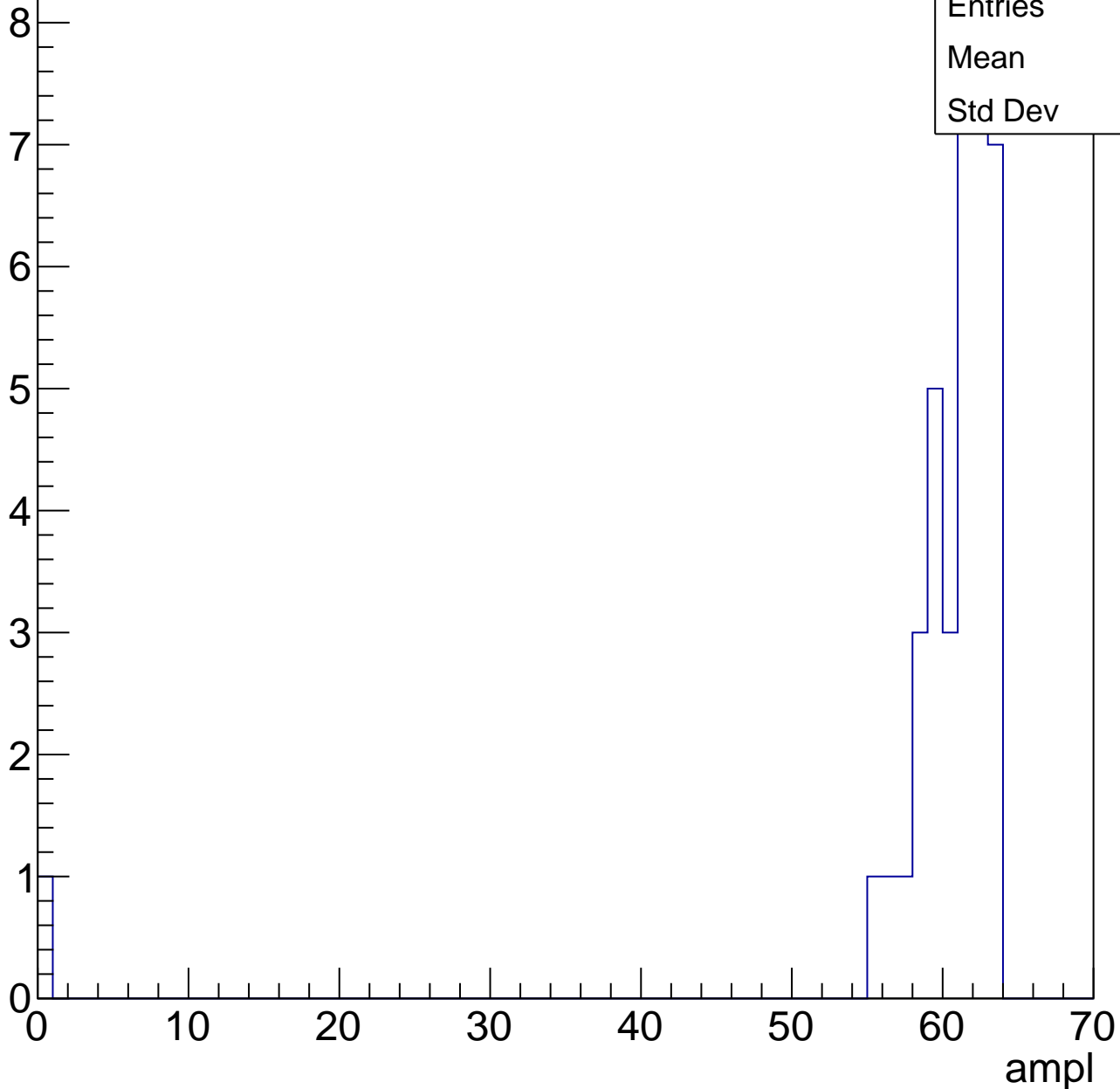


B0L001S, U15-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	59
Std Dev	9.91



B0L001S, U15-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch72, adc0

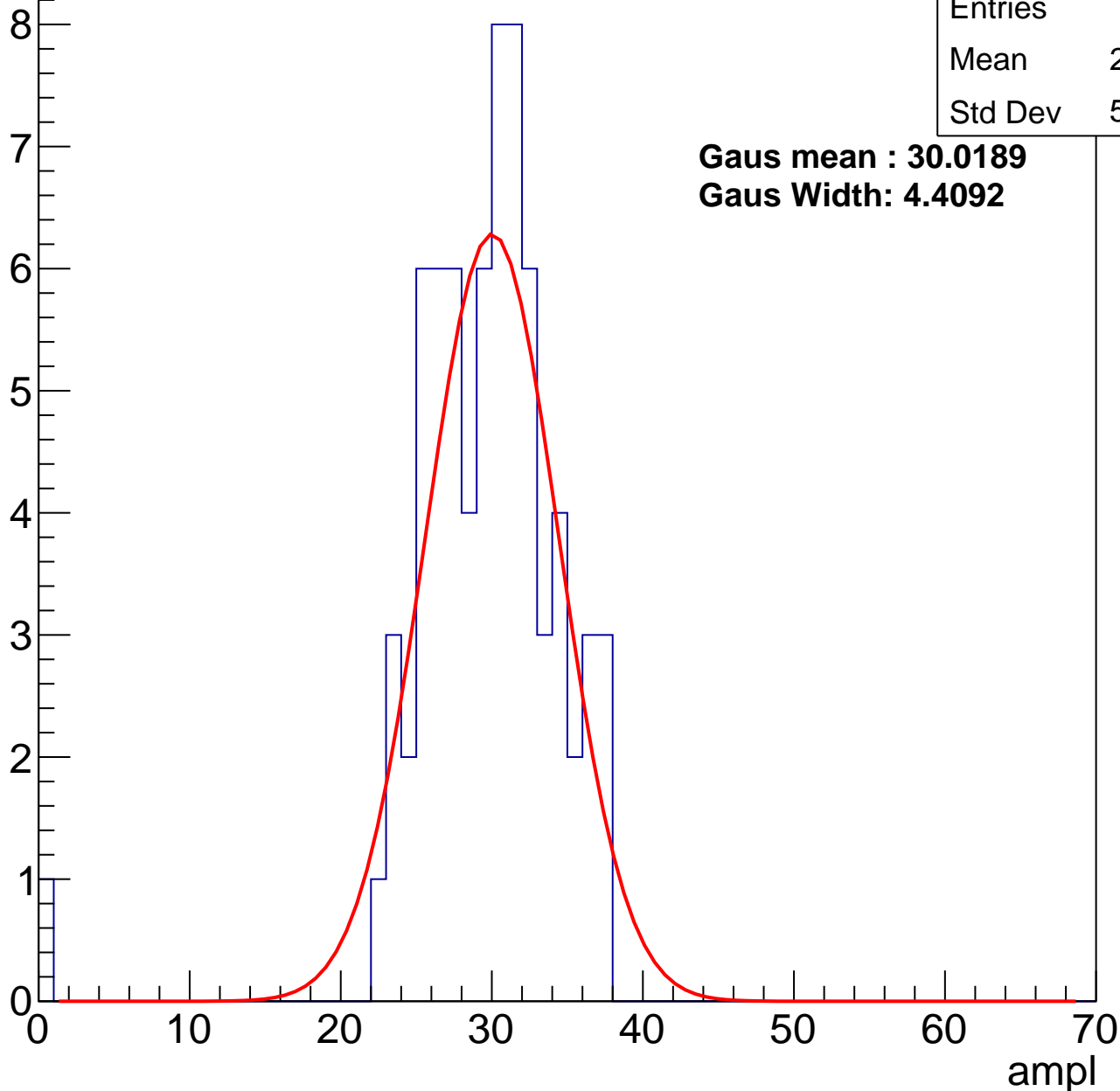
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.12
Std Dev	5.107

Gaus mean : 30.0189

Gaus Width: 4.4092



B0L001S, U15-ch72, adc1

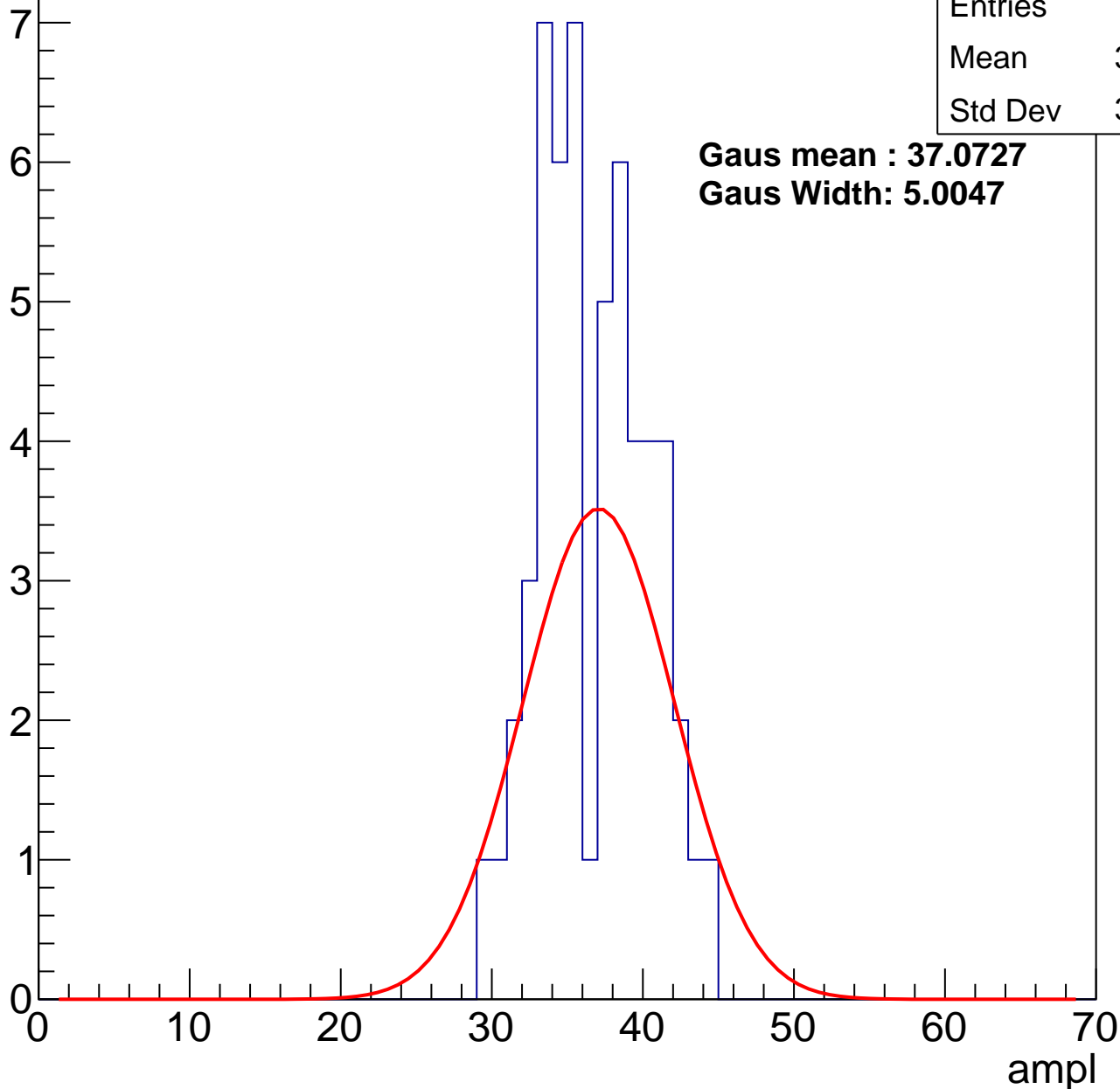
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	36.31
Std Dev	3.531

Gaus mean : 37.0727

Gaus Width: 5.0047



B0L001S, U15-ch72, adc2

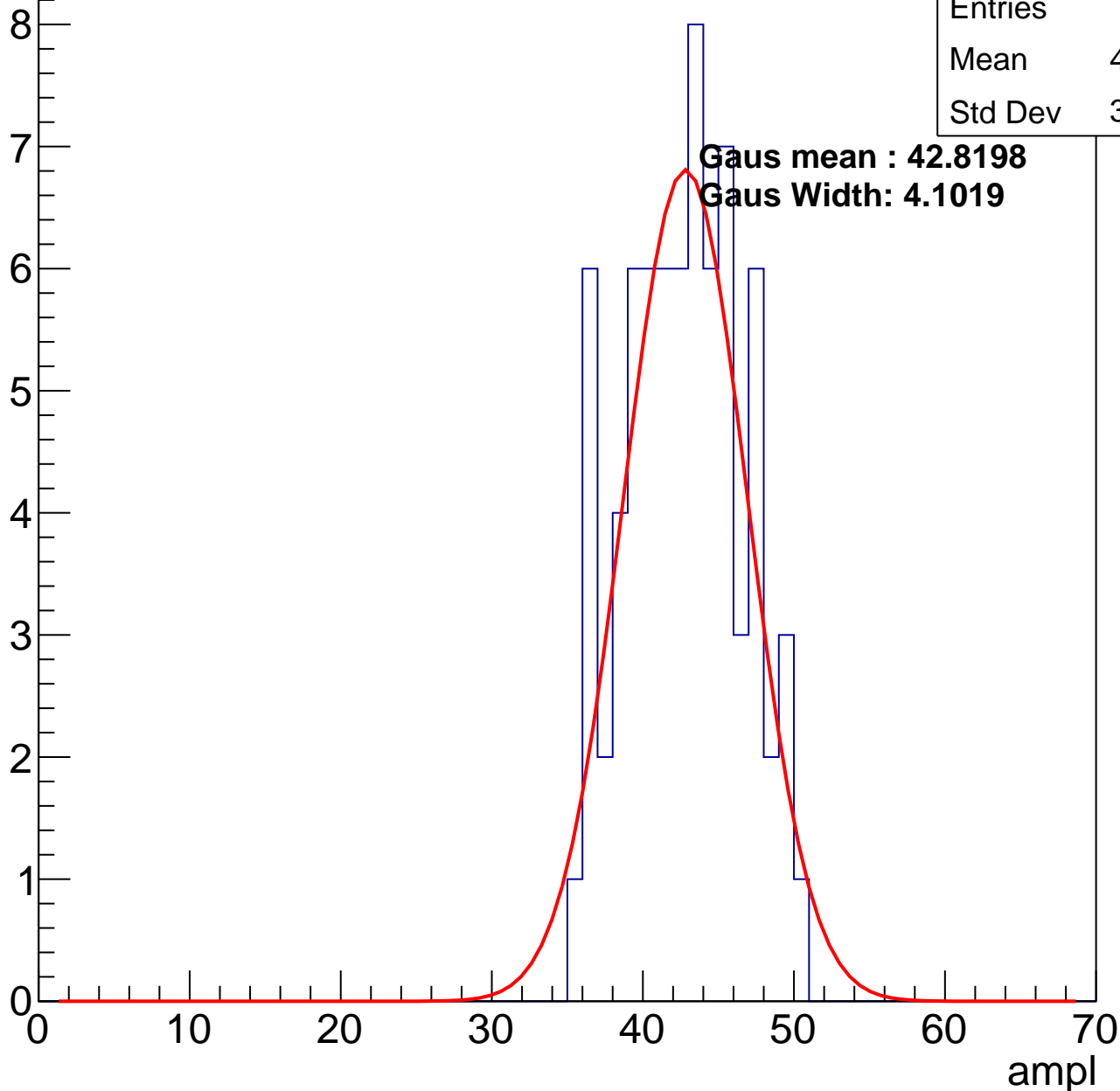
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	42.26
Std Dev	3.764

Gaus mean : 42.8198

Gaus Width: 4.1019

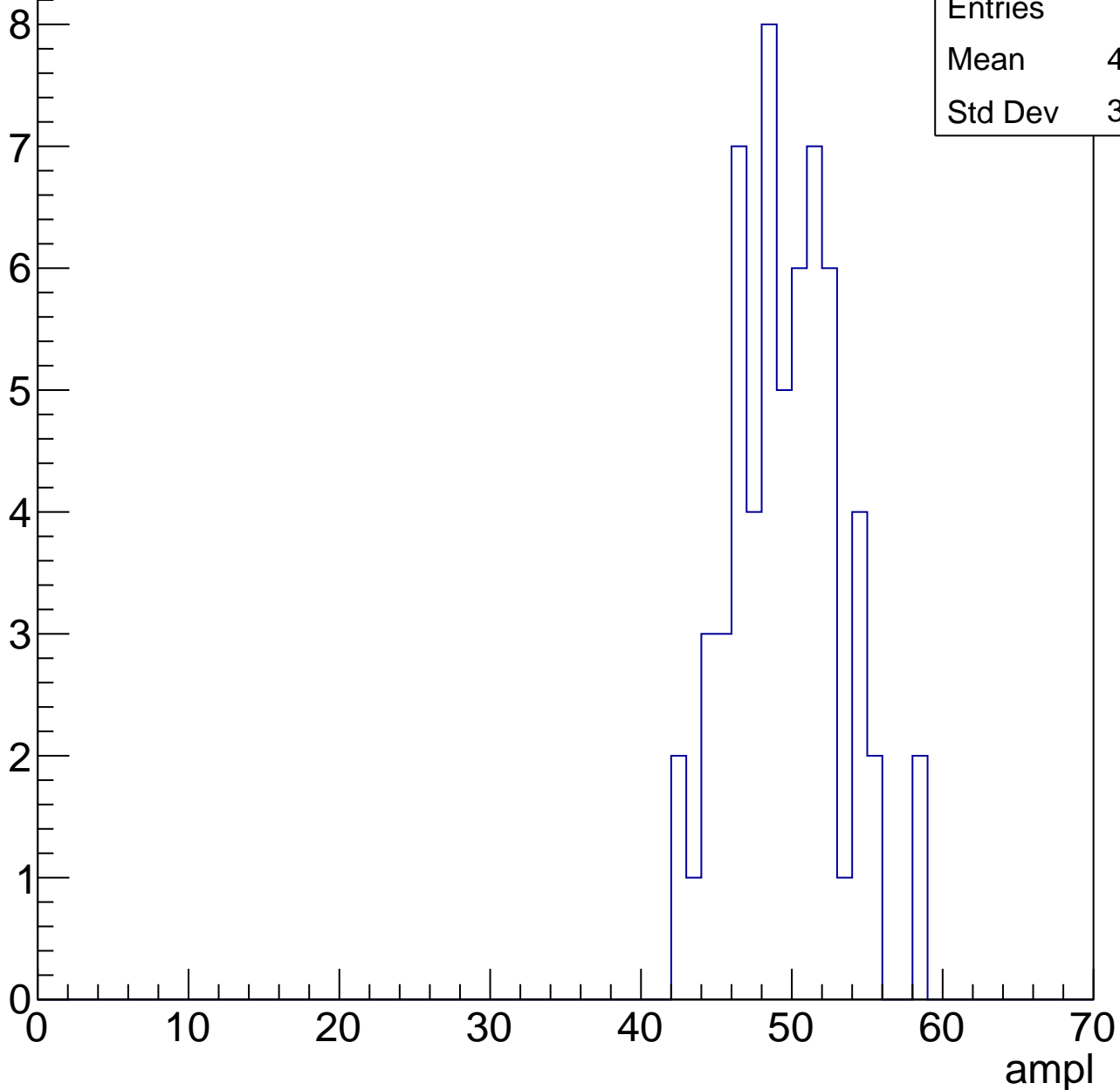


B0L001S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

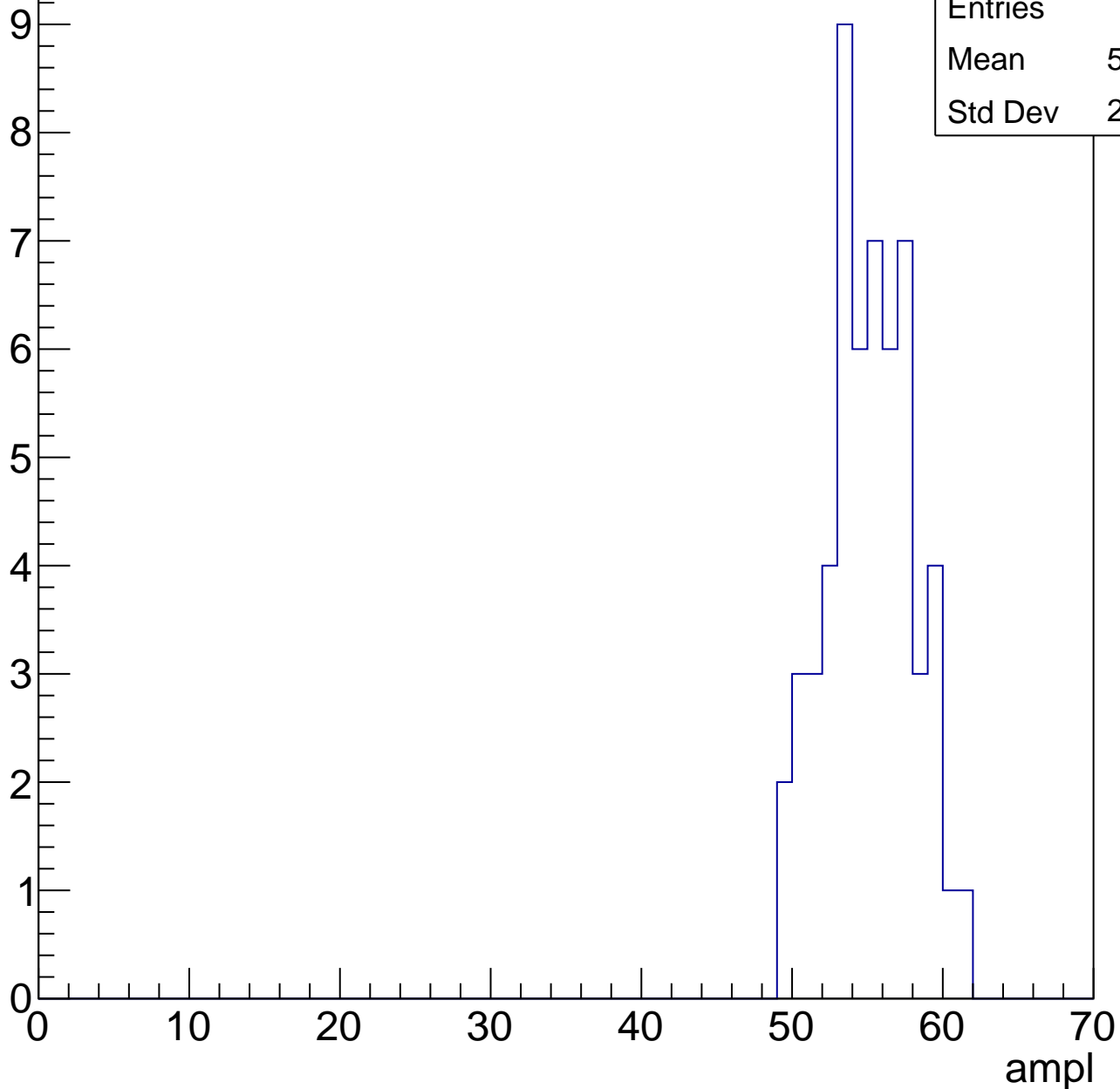
Entries	61
Mean	49.13
Std Dev	3.578



B0L001S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



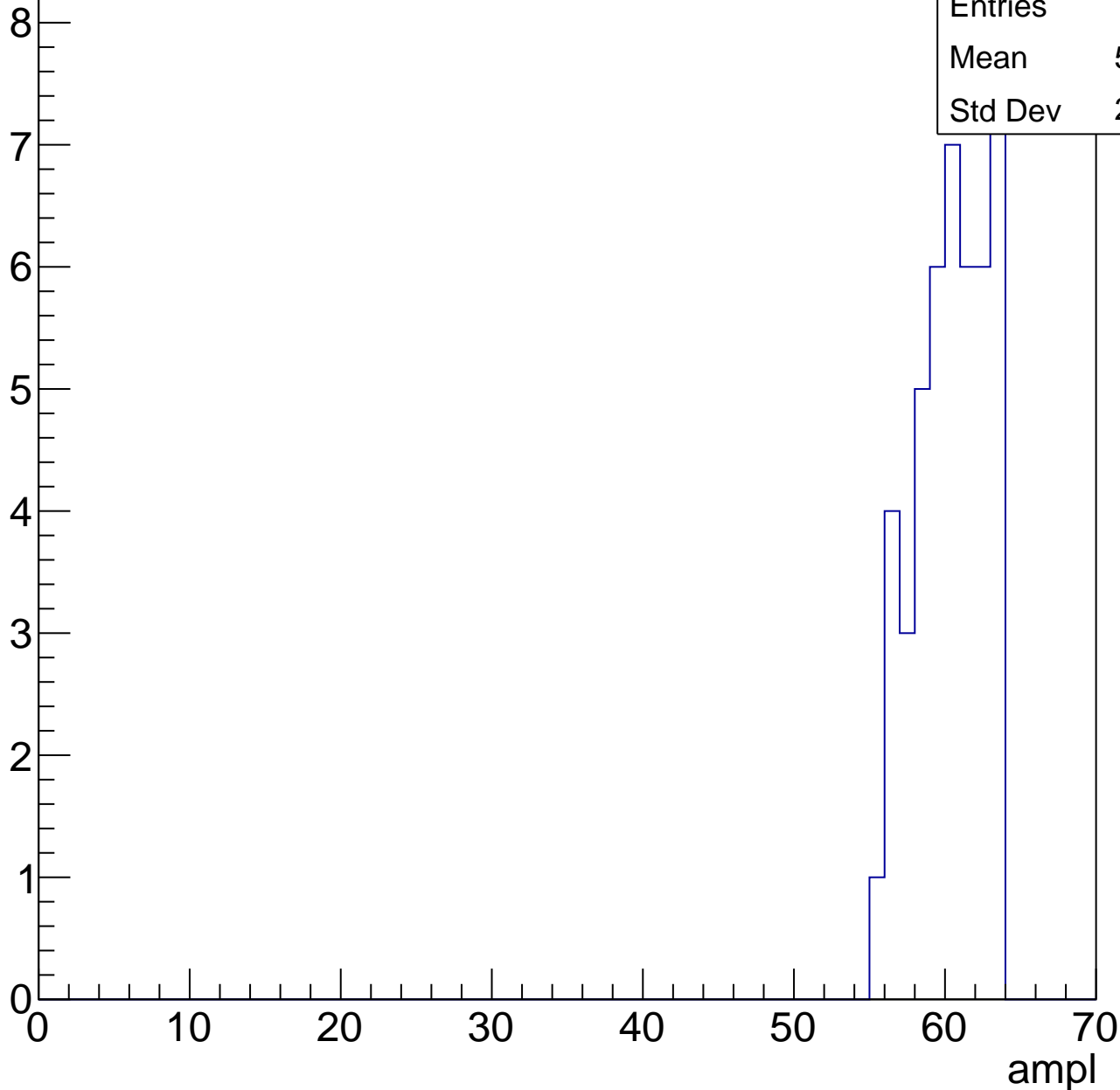
Entries	56
Mean	54.66
Std Dev	2.843

B0L001S, U15-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

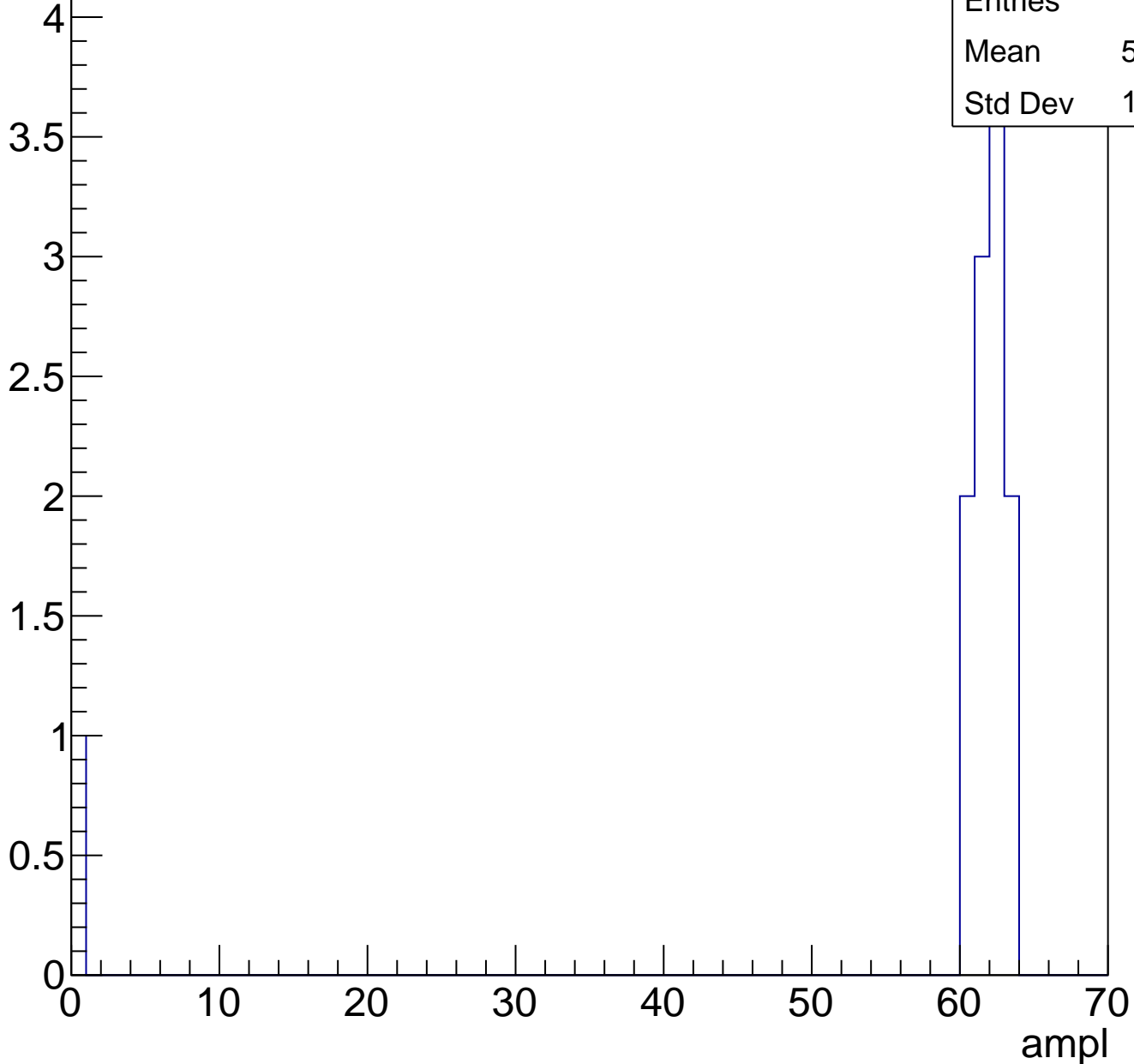
Entries	46
Mean	59.91
Std Dev	2.301



B0L001S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	12
Mean	56.42
Std Dev	17.04

B0L001S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch73, adc0

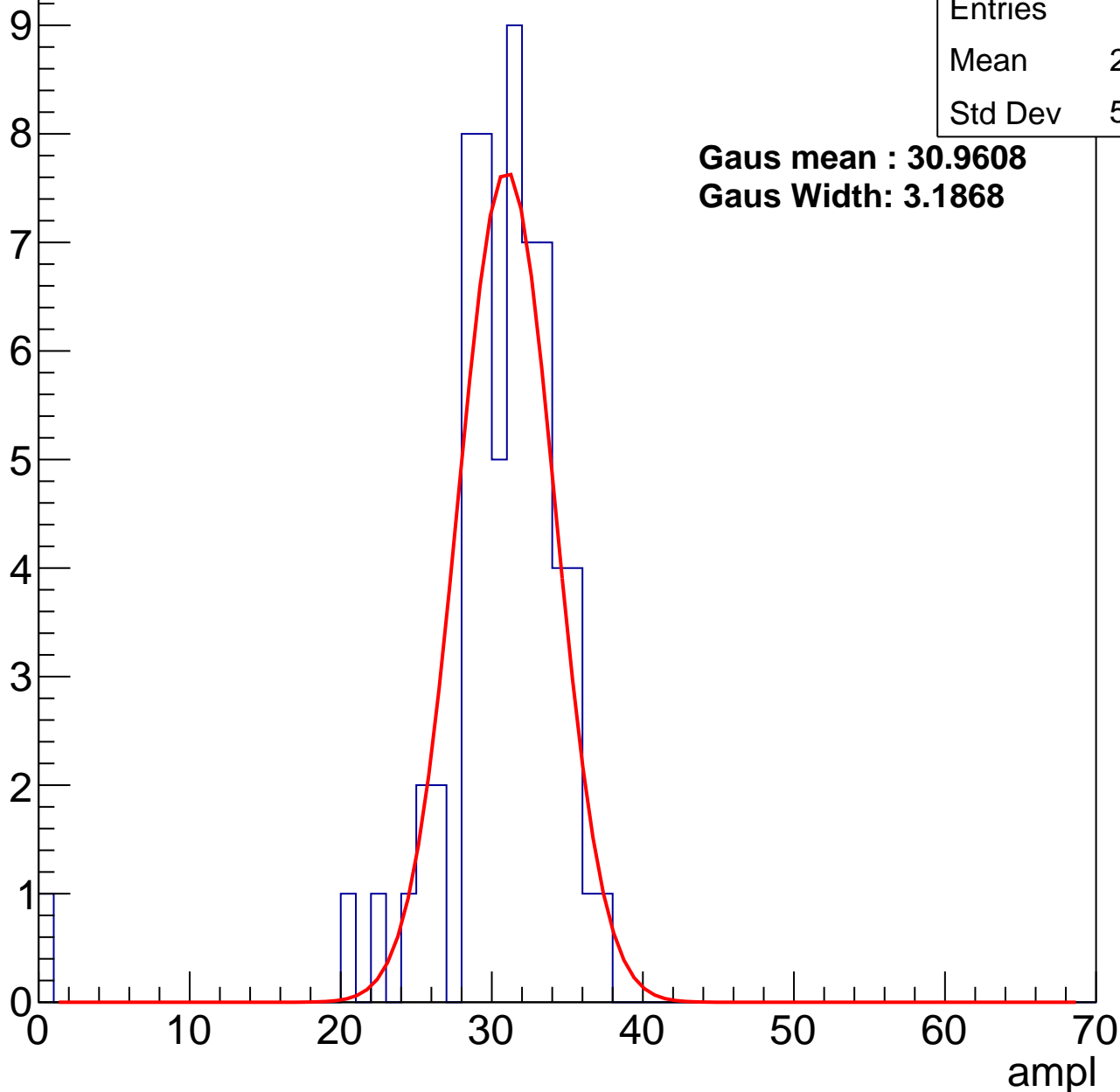
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.95
Std Dev	5.034

Gaus mean : 30.9608

Gaus Width: 3.1868



B0L001S, U15-ch73, adc1

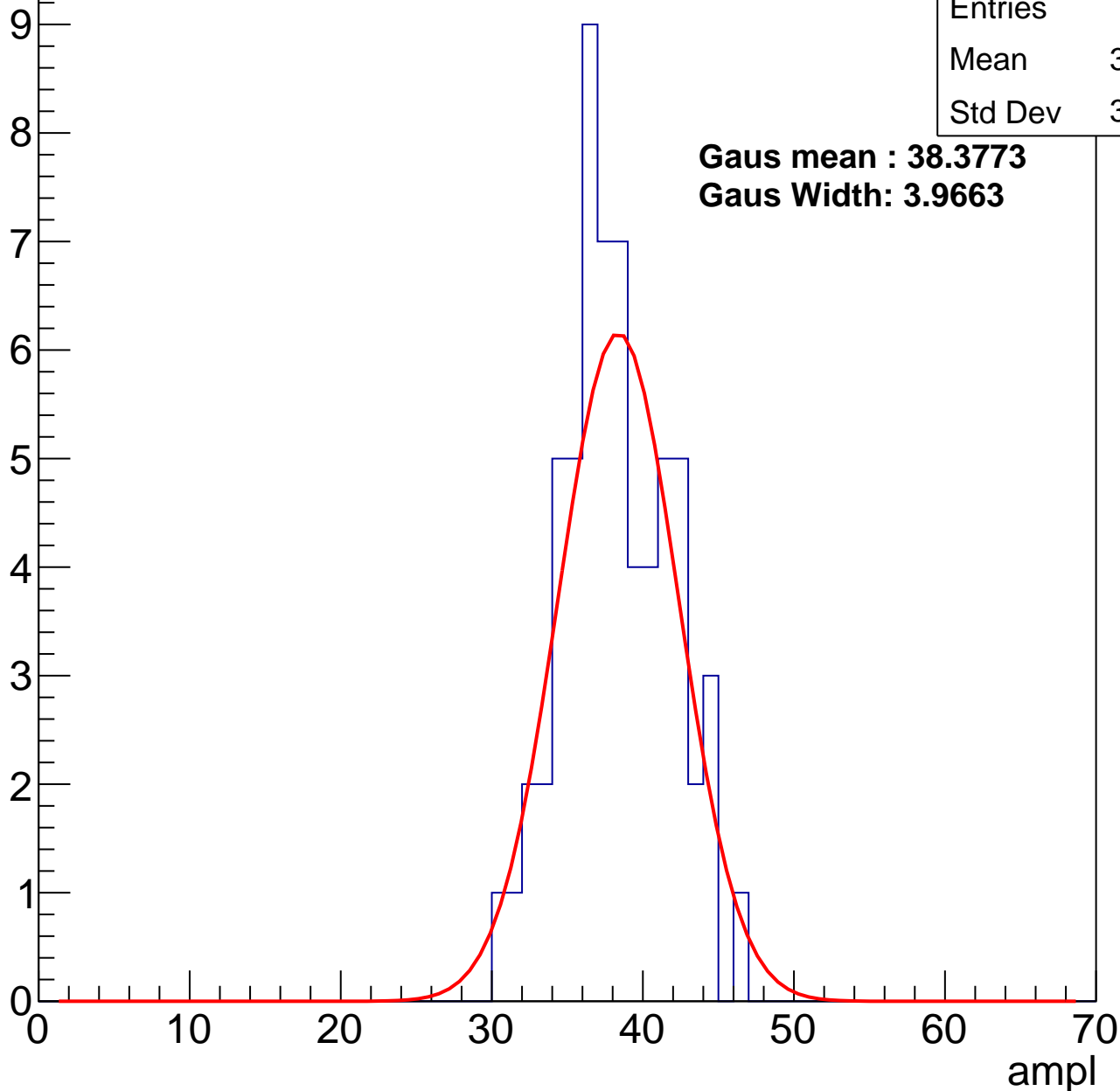
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.78
Std Dev	3.489

Gaus mean : 38.3773

Gaus Width: 3.9663



B0L001S, U15-ch73, adc2

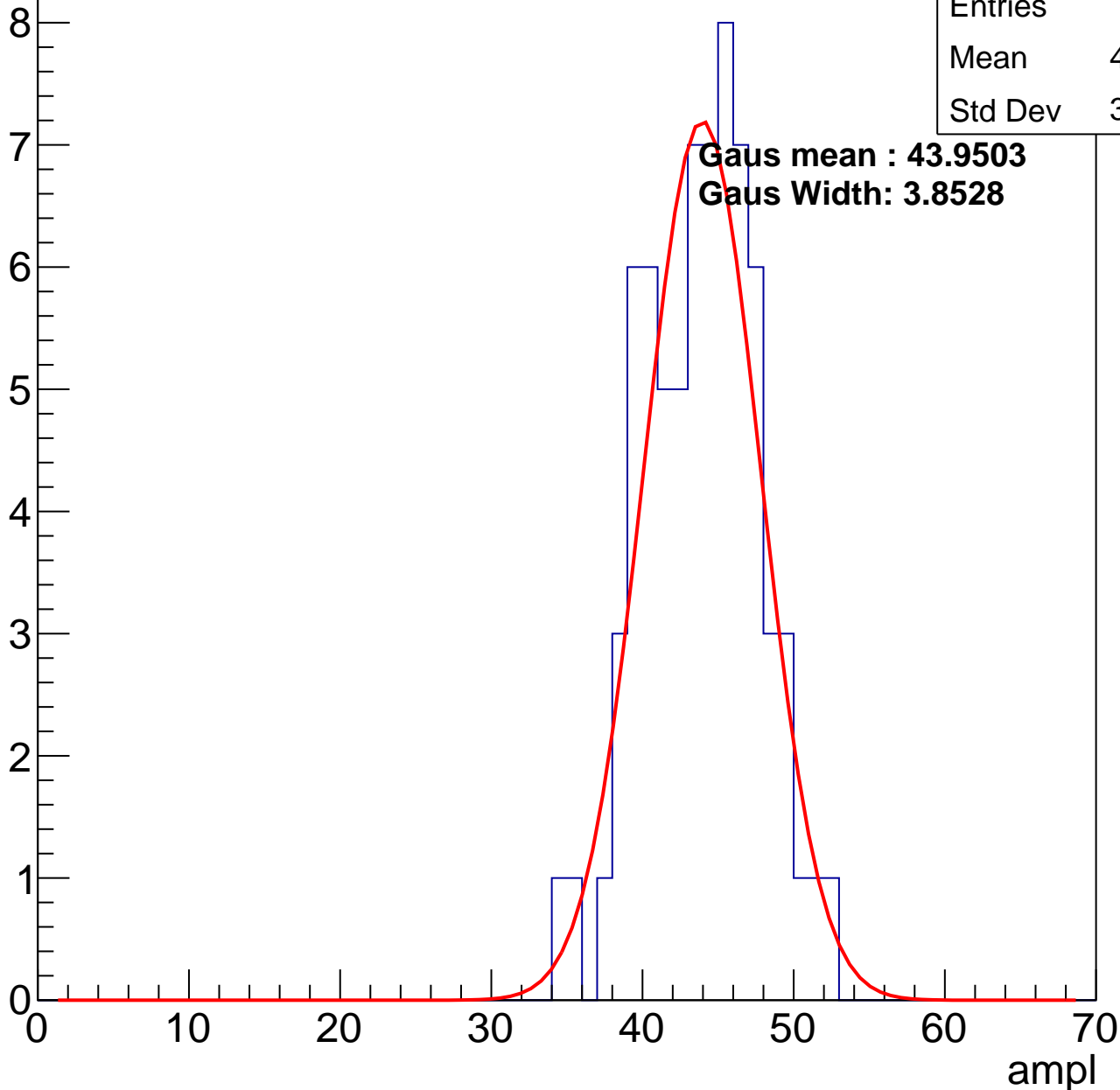
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.42
Std Dev	3.715

Gaus mean : 43.9503

Gaus Width: 3.8528

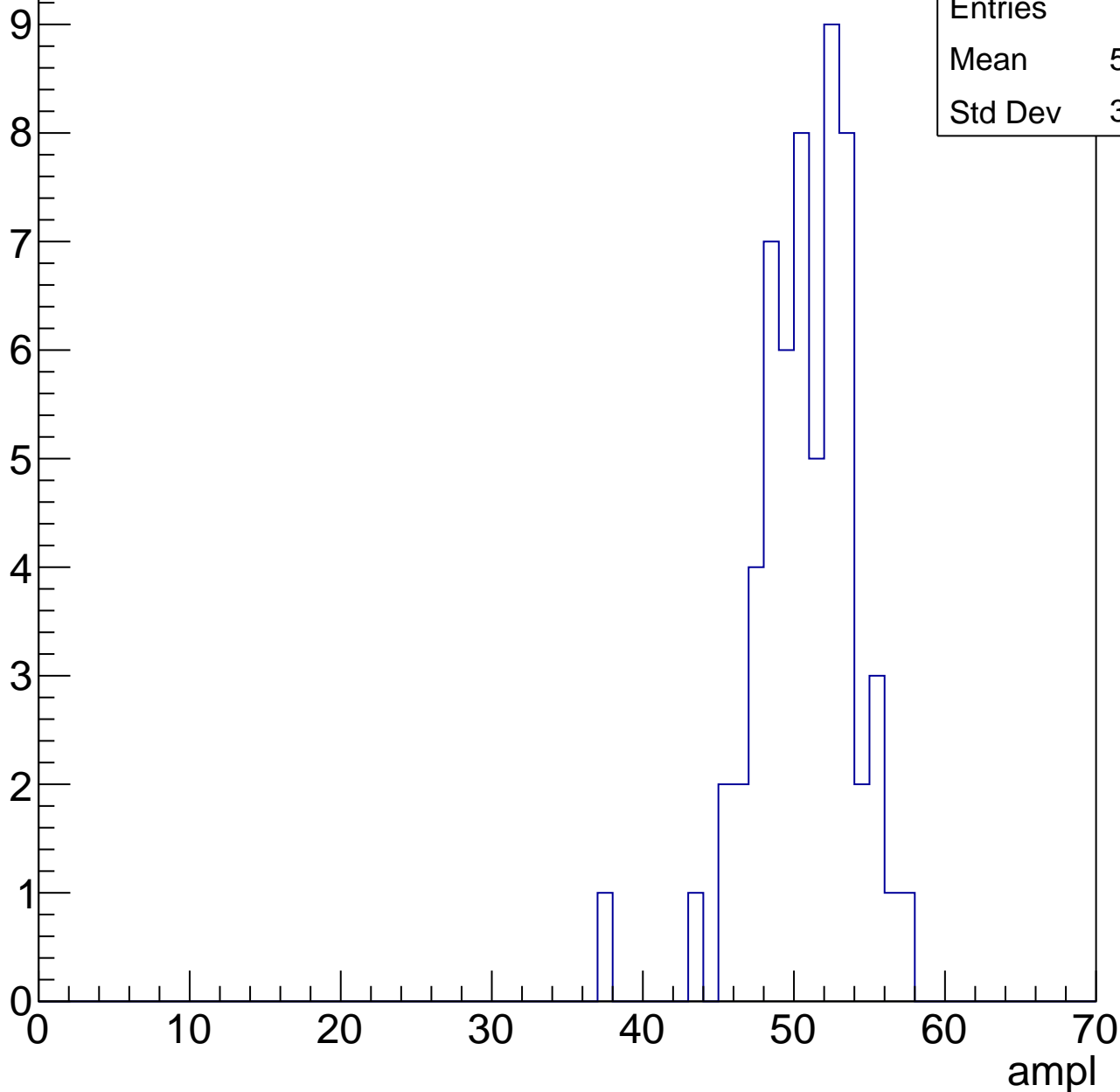


B0L001S, U15-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.22
Std Dev	3.347

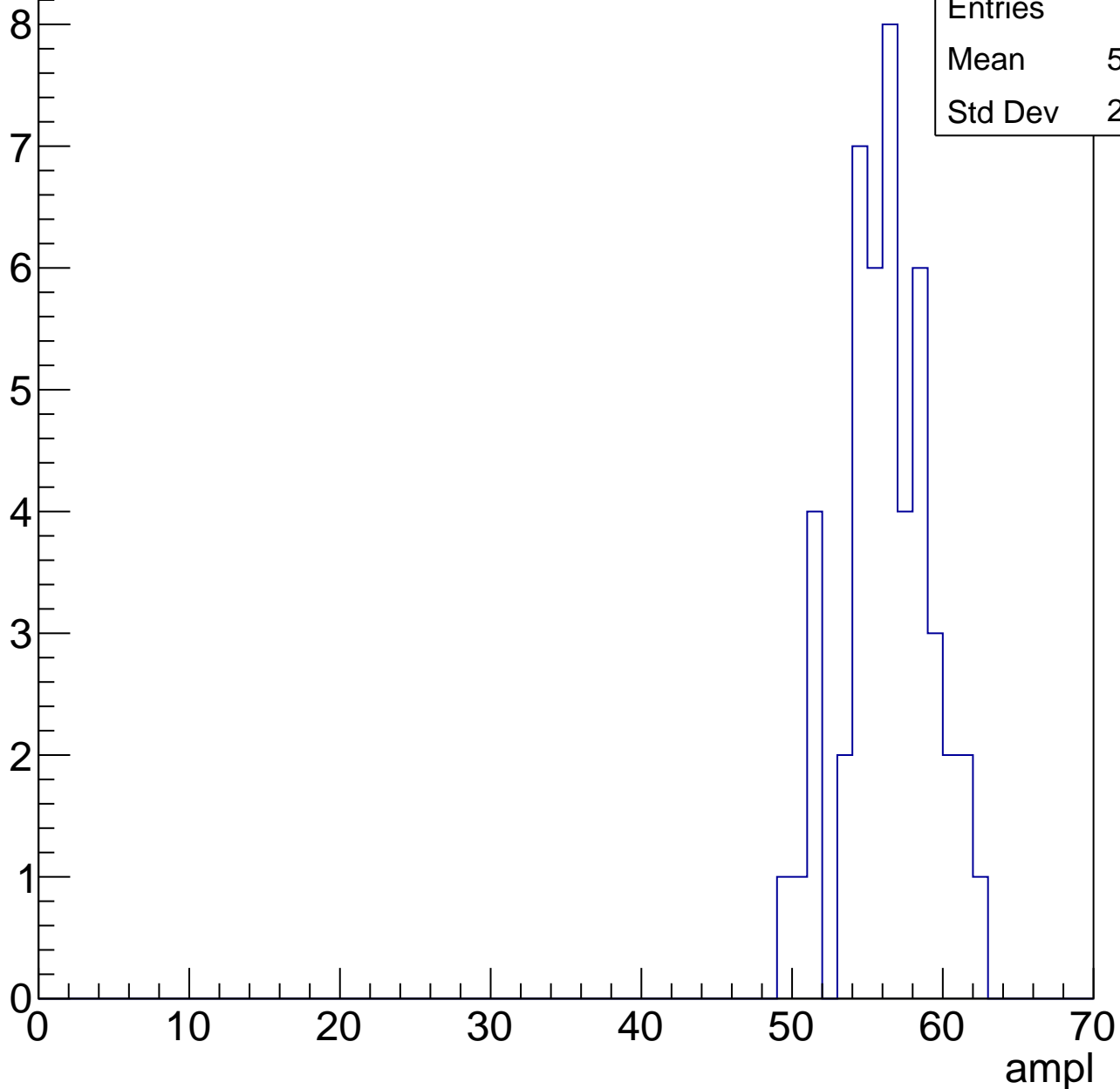


B0L001S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

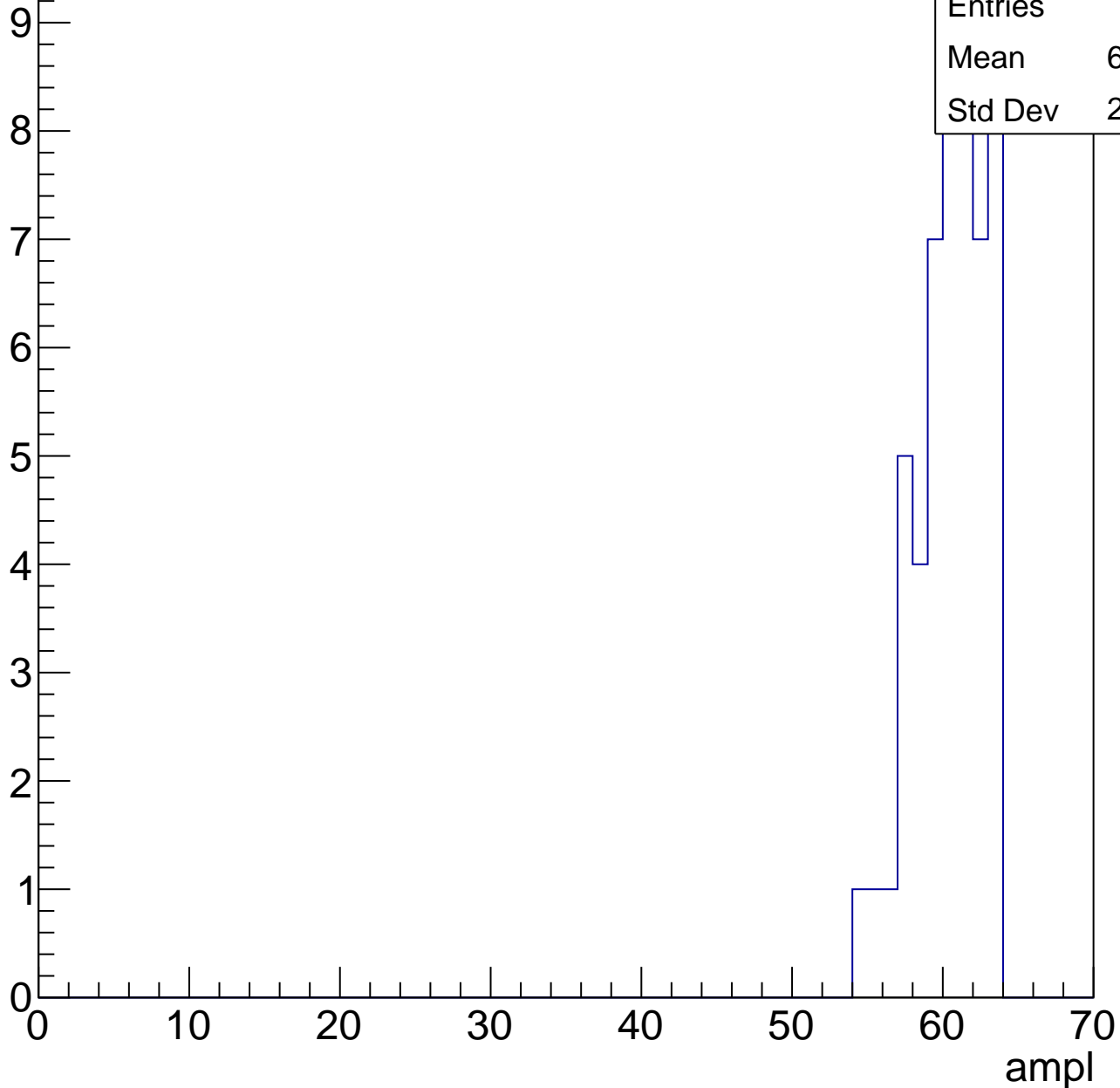
Entries	47
Mean	55.79
Std Dev	2.946



B0L001S, U15-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	51
Mean	60.08
Std Dev	2.265

B0L001S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B0L001S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch74, adc0

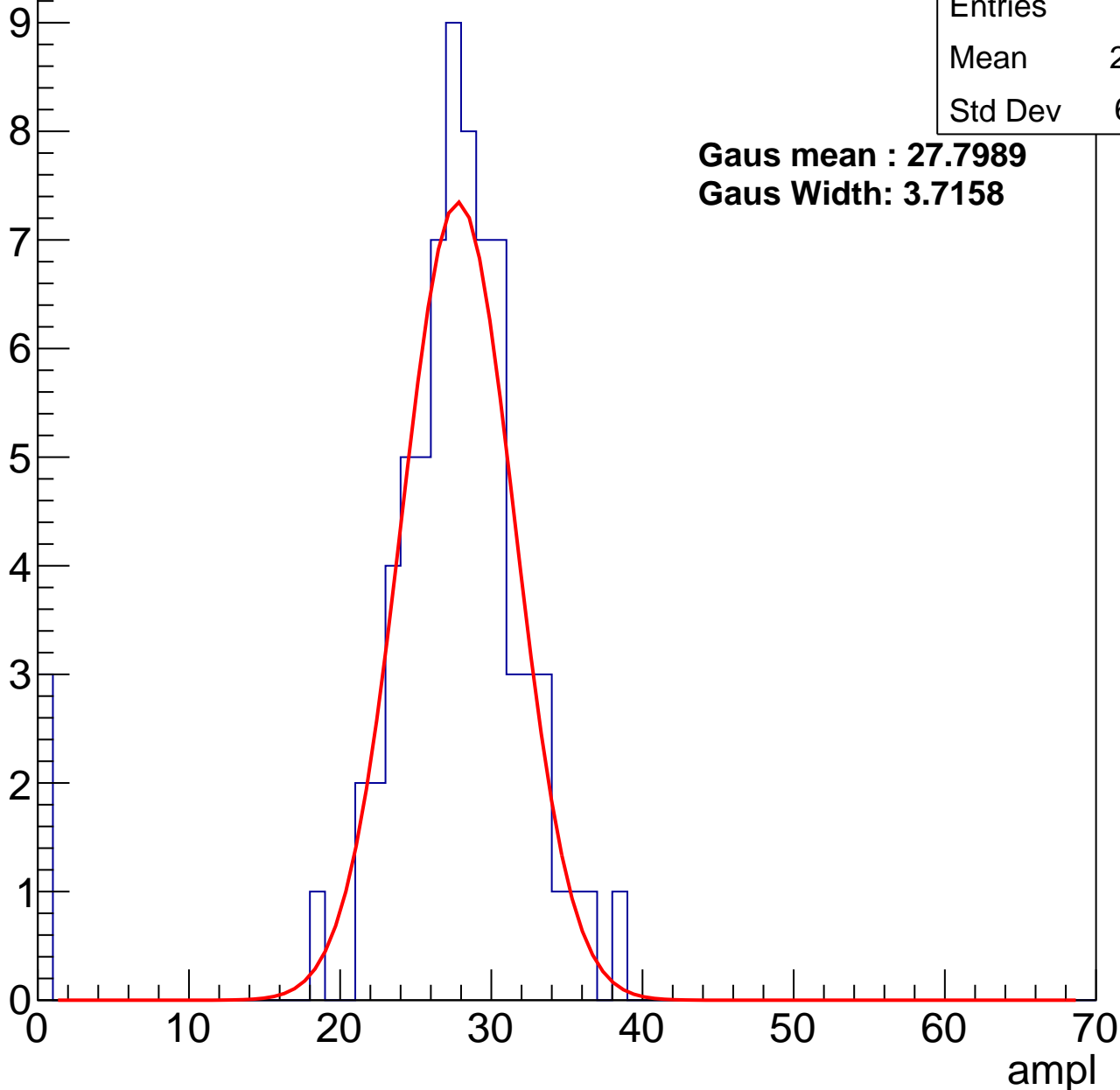
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	26.49
Std Dev	6.571

Gaus mean : 27.7989

Gaus Width: 3.7158



B0L001S, U15-ch74, adc1

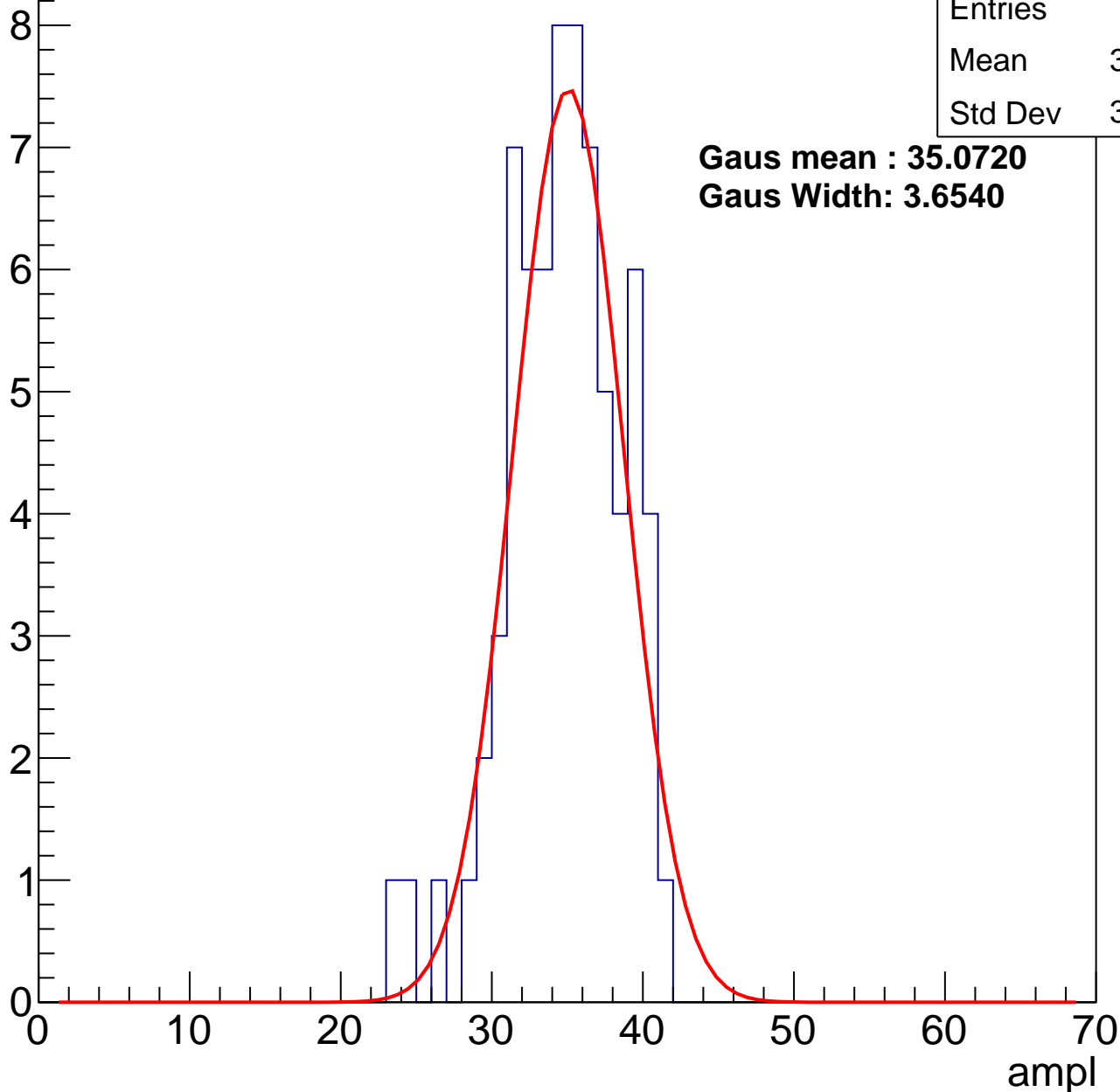
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	34.25
Std Dev	3.744

Gaus mean : 35.0720

Gaus Width: 3.6540



B0L001S, U15-ch74, adc2

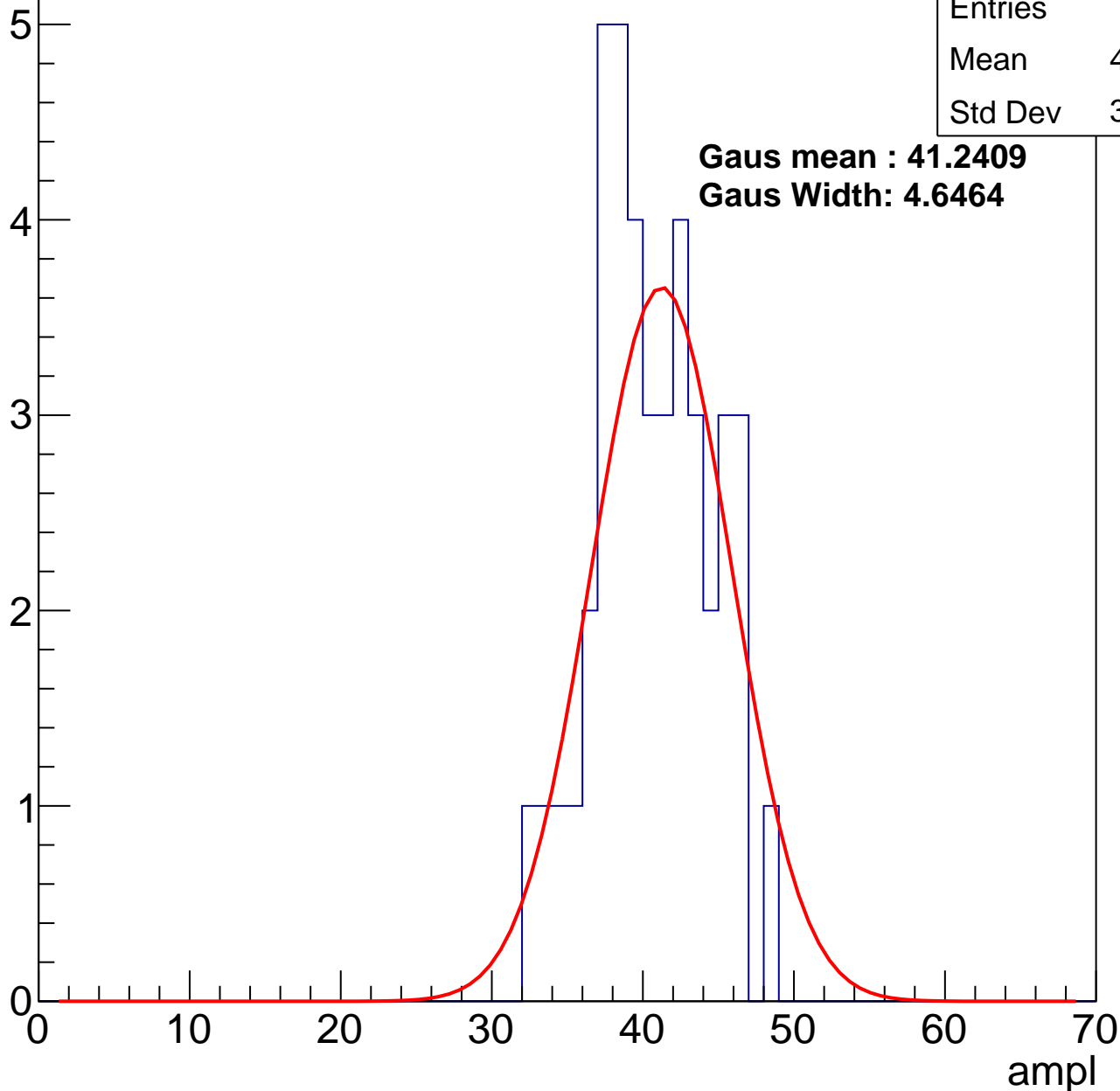
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	40.14
Std Dev	3.777

Gaus mean : 41.2409

Gaus Width: 4.6464

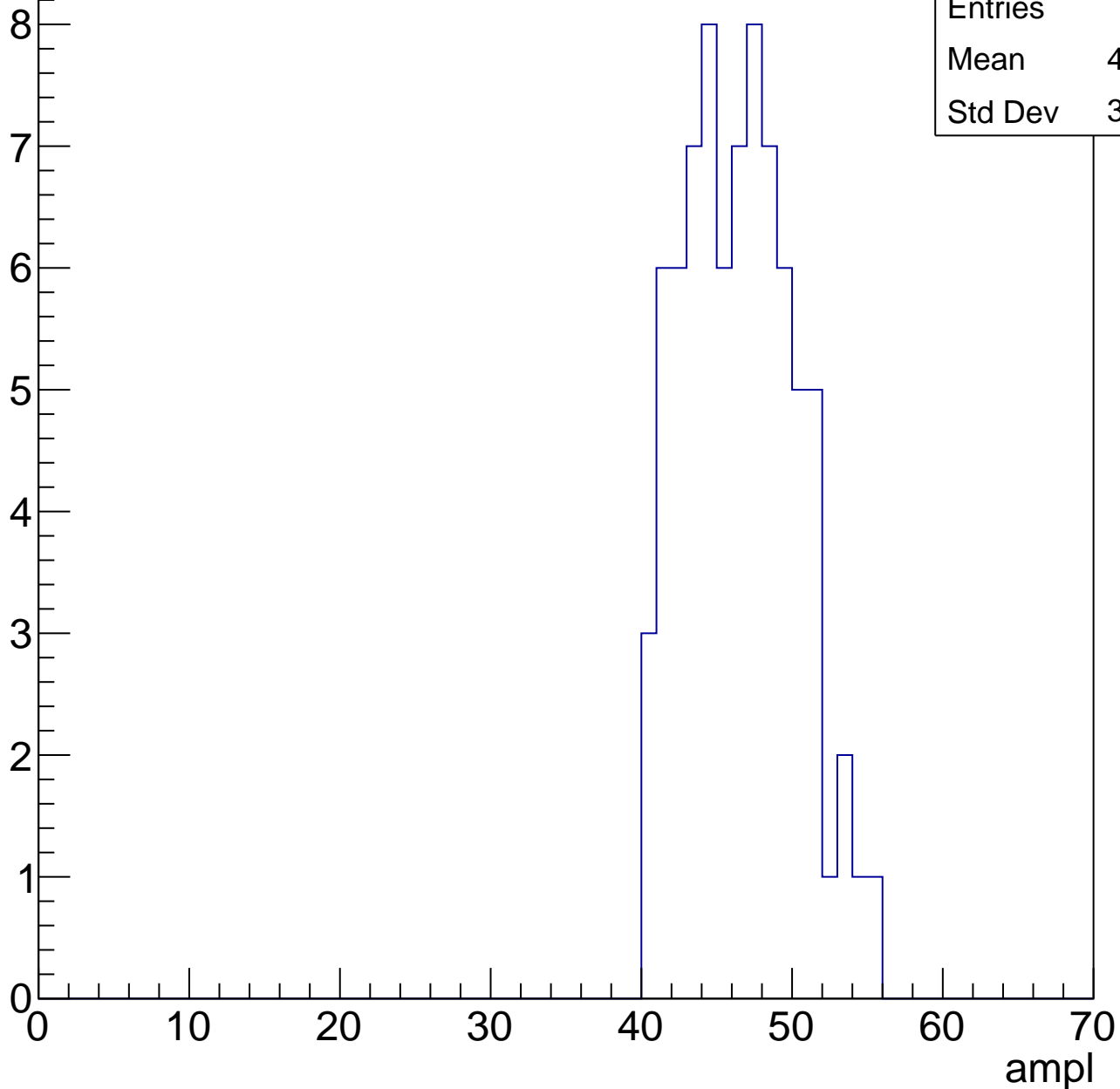


B0L001S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	46.09
Std Dev	3.608

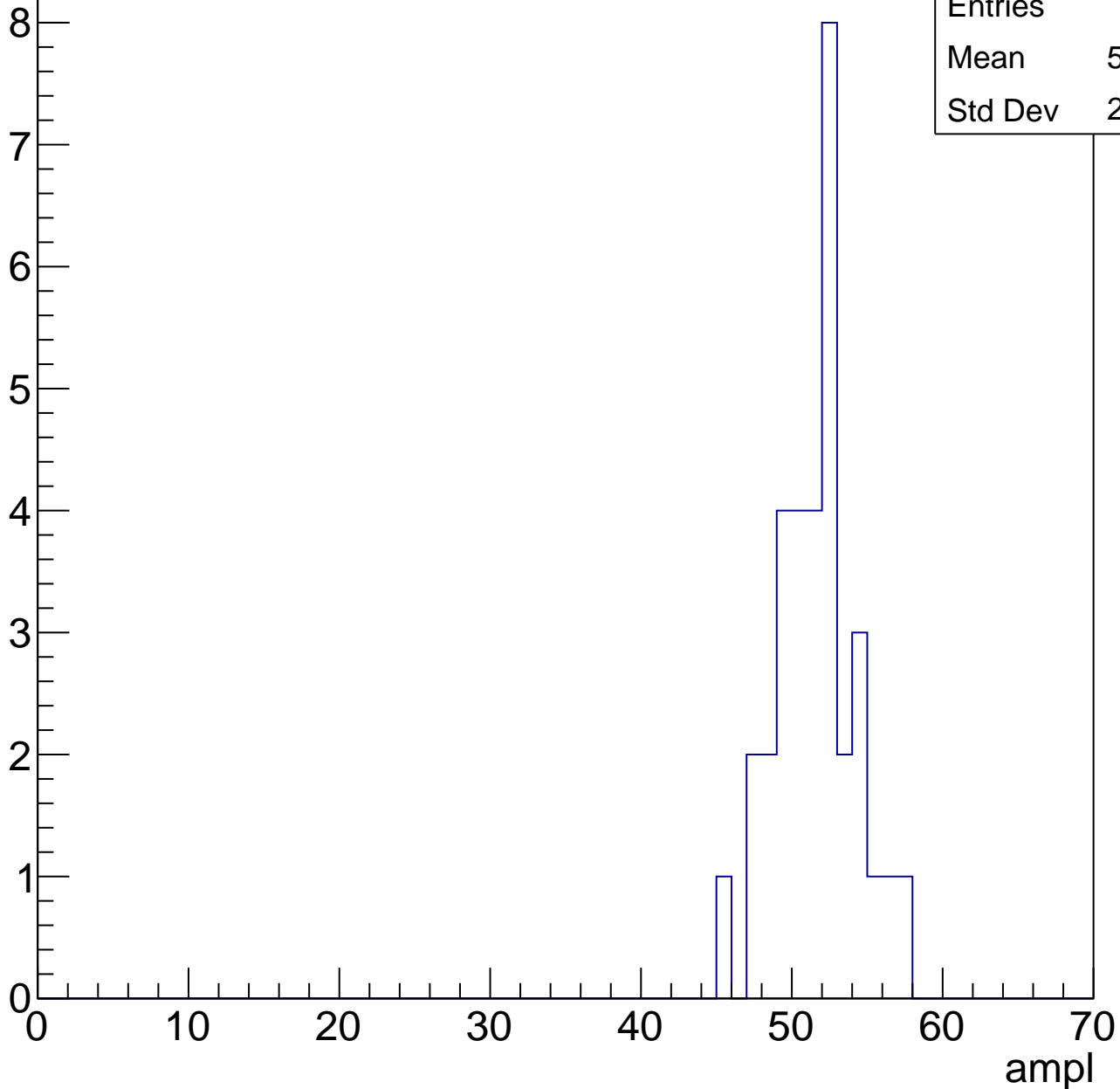


B0L001S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	51.12
Std Dev	2.614

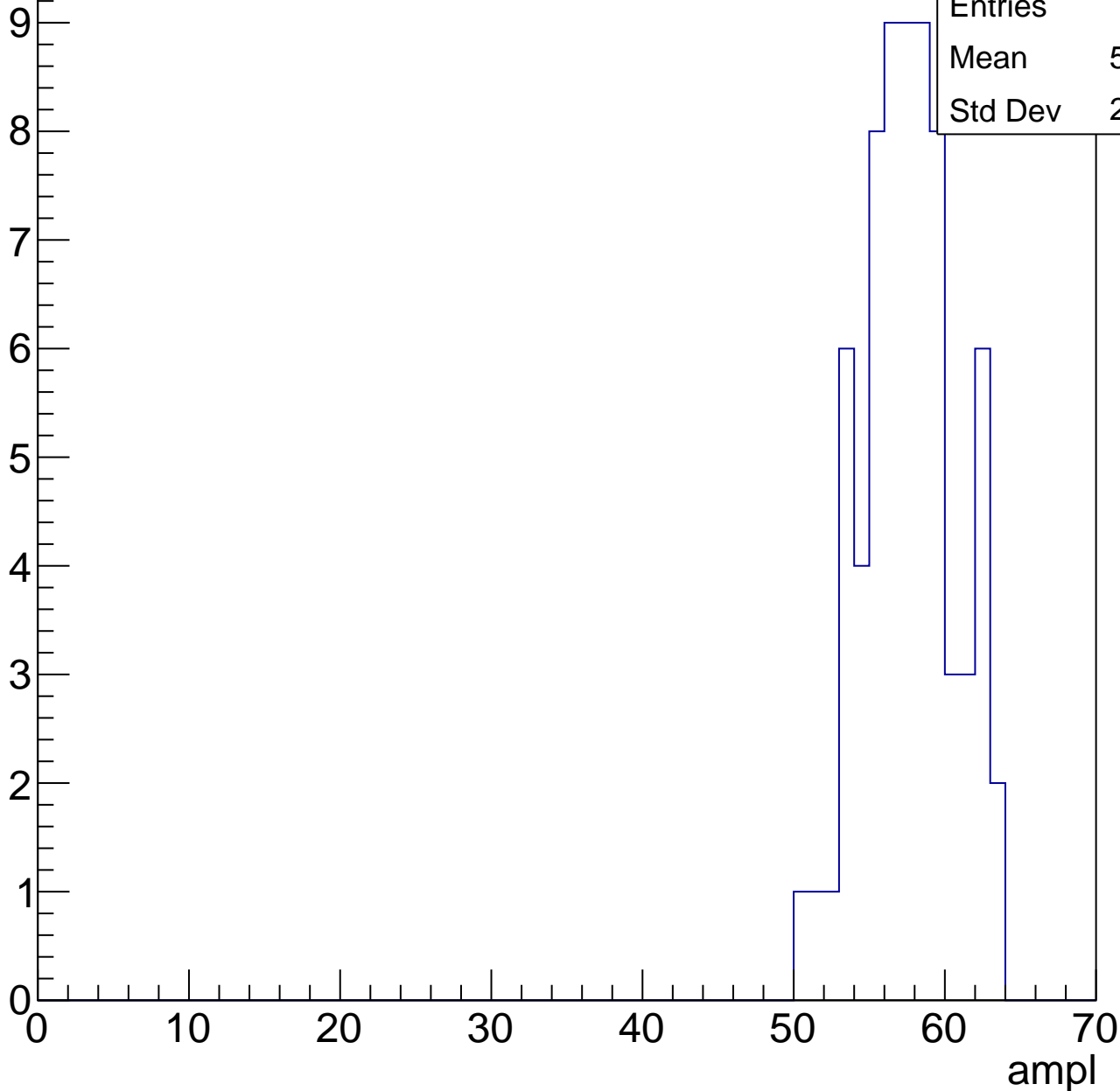


B0L001S, U15-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	57.13
Std Dev	2.976

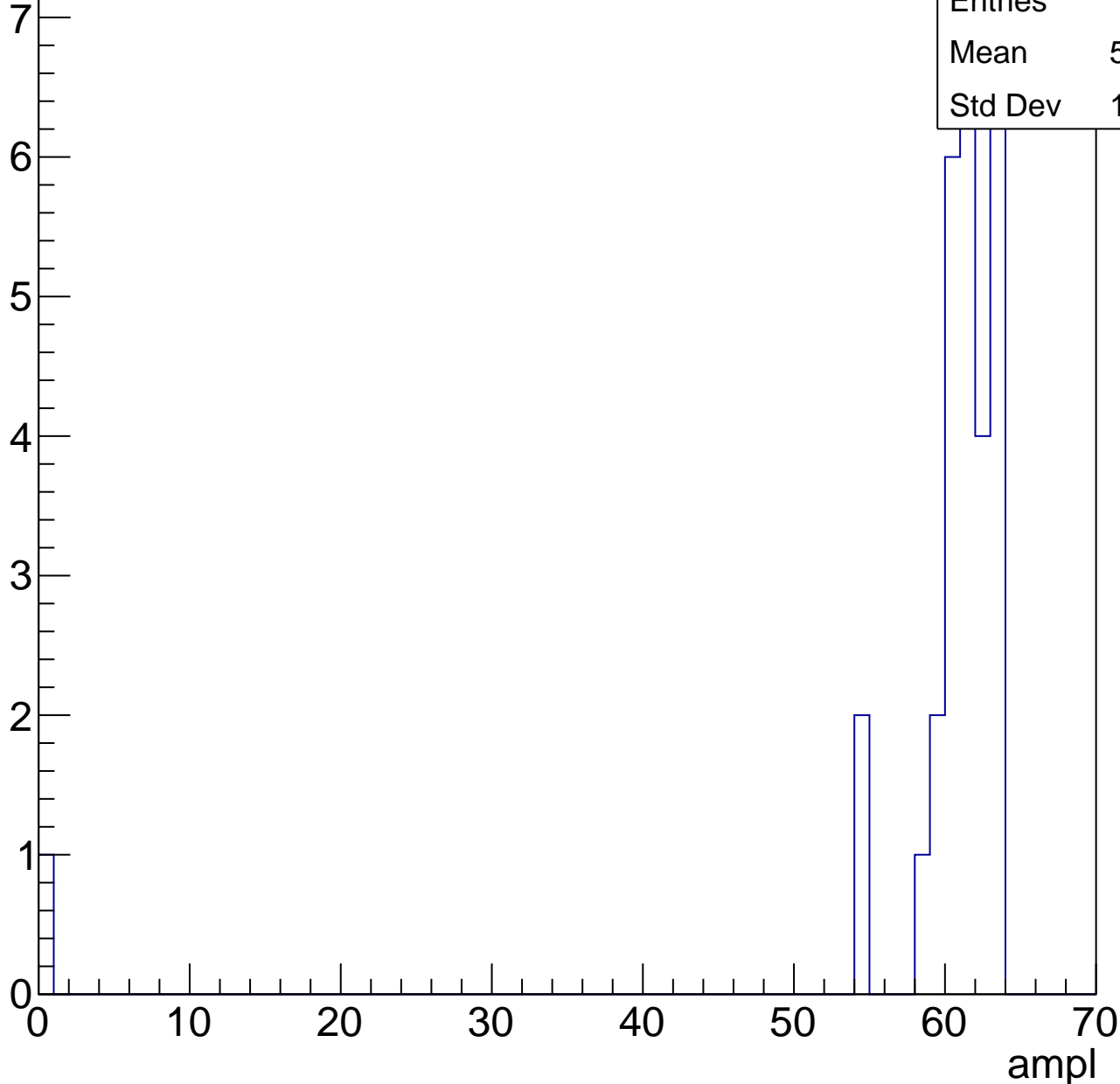


B0L001S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

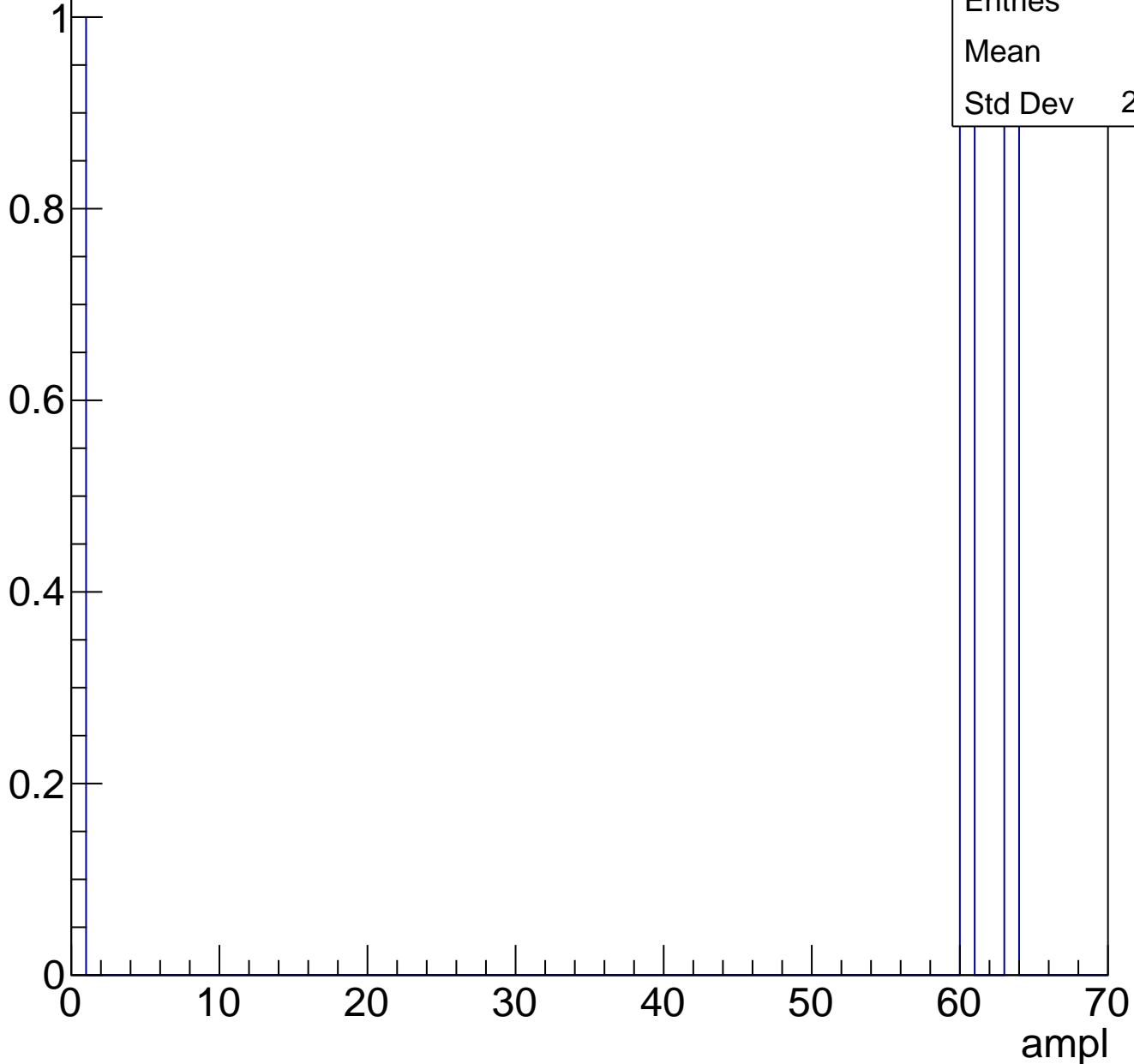
Entries	30
Mean	58.67
Std Dev	11.12



B0L001S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch75, adc0

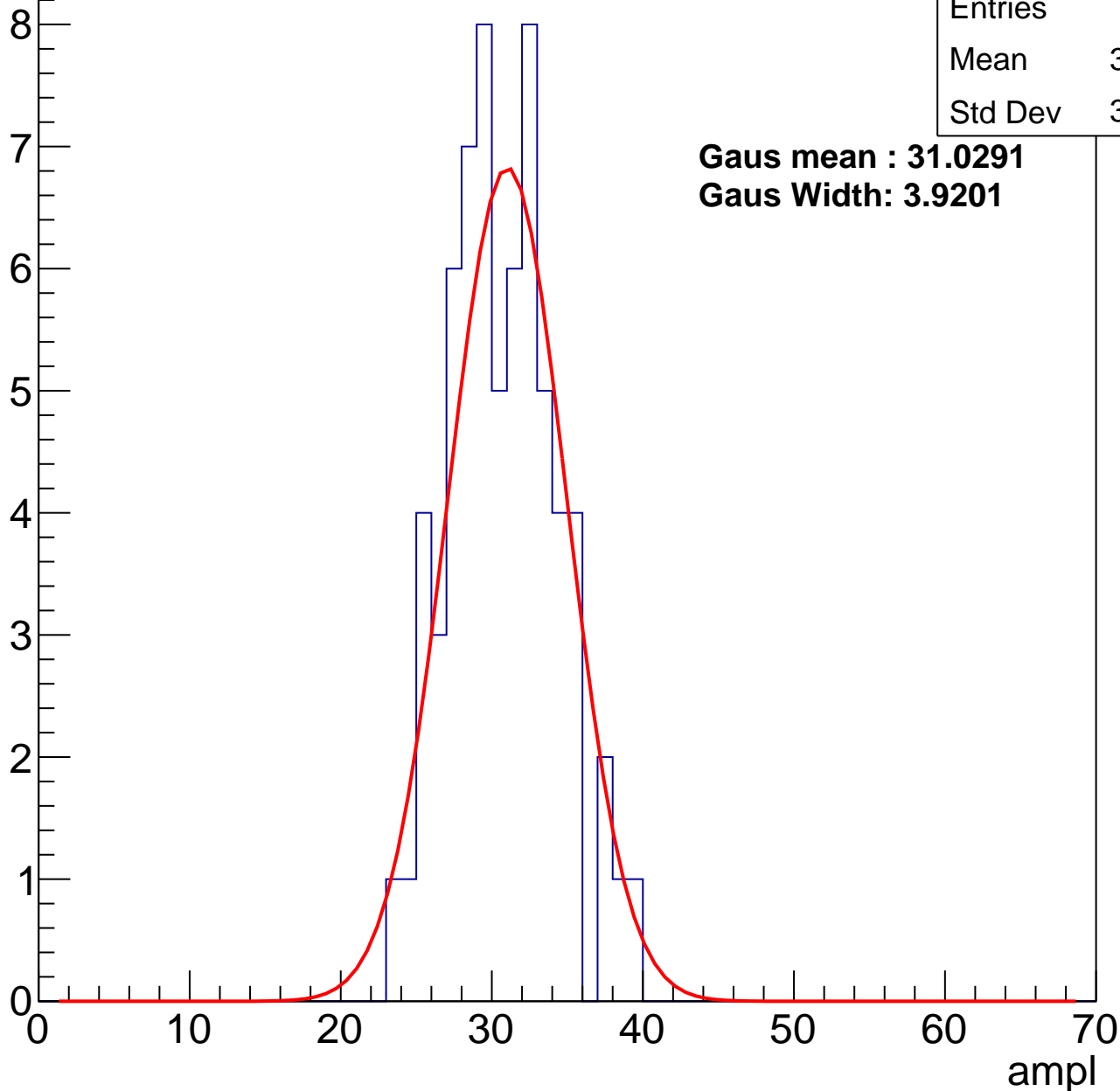
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.29
Std Dev	3.498

Gaus mean : 31.0291

Gaus Width: 3.9201



B0L001S, U15-ch75, adc1

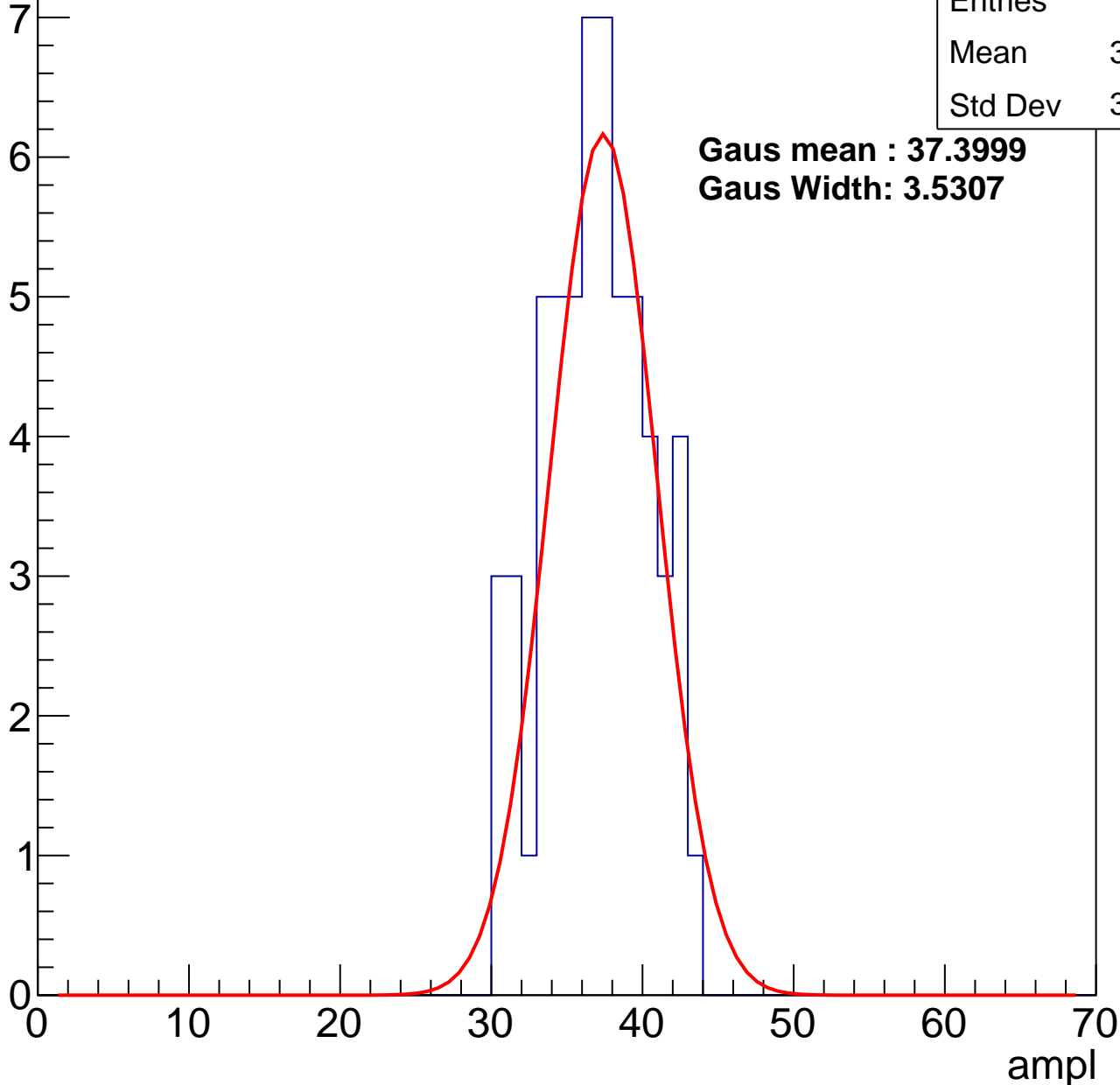
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	36.47
Std Dev	3.374

Gaus mean : 37.3999

Gaus Width: 3.5307



B0L001S, U15-ch75, adc2

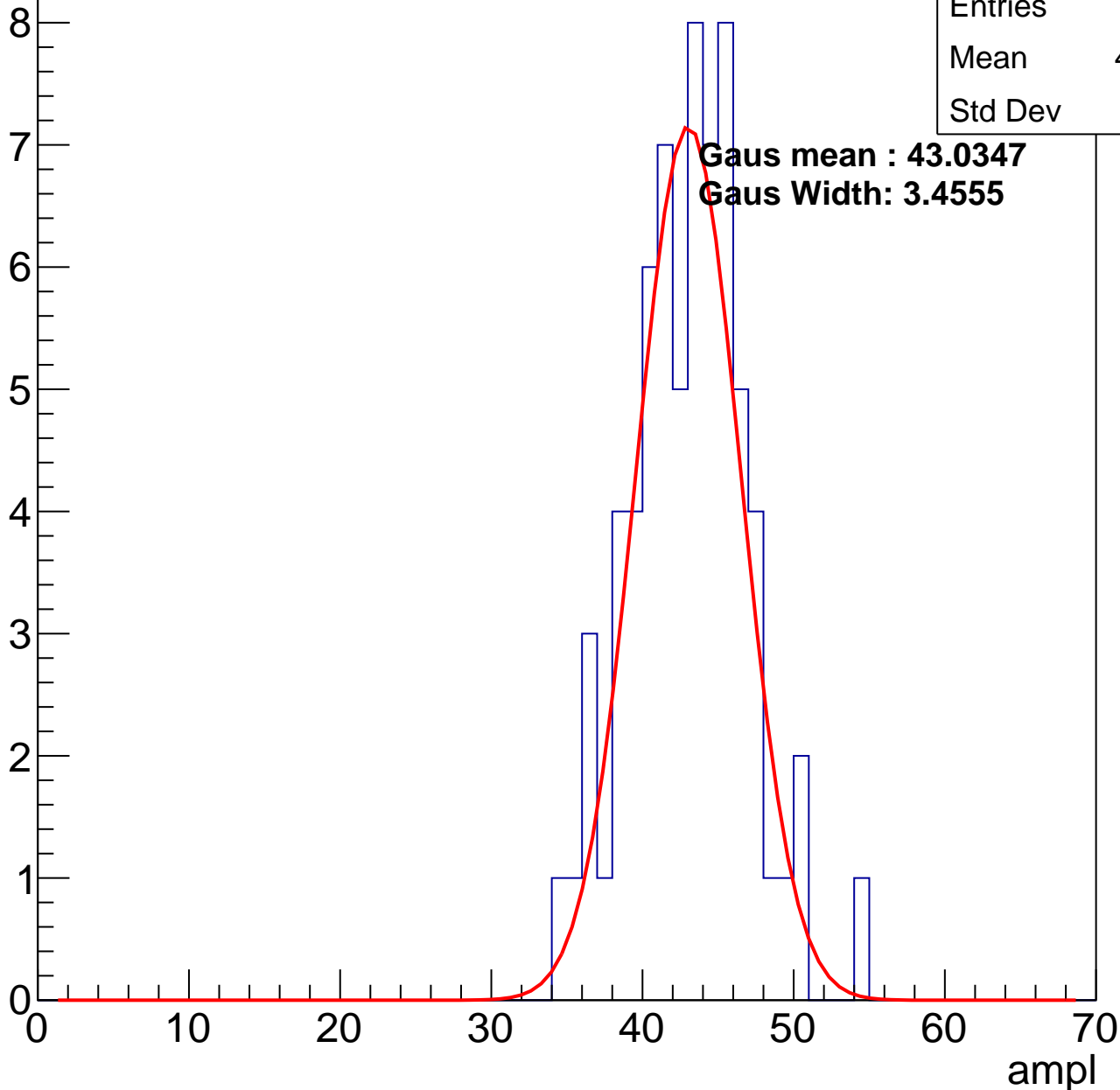
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.61
Std Dev	3.8

Gaus mean : 43.0347

Gaus Width: 3.4555

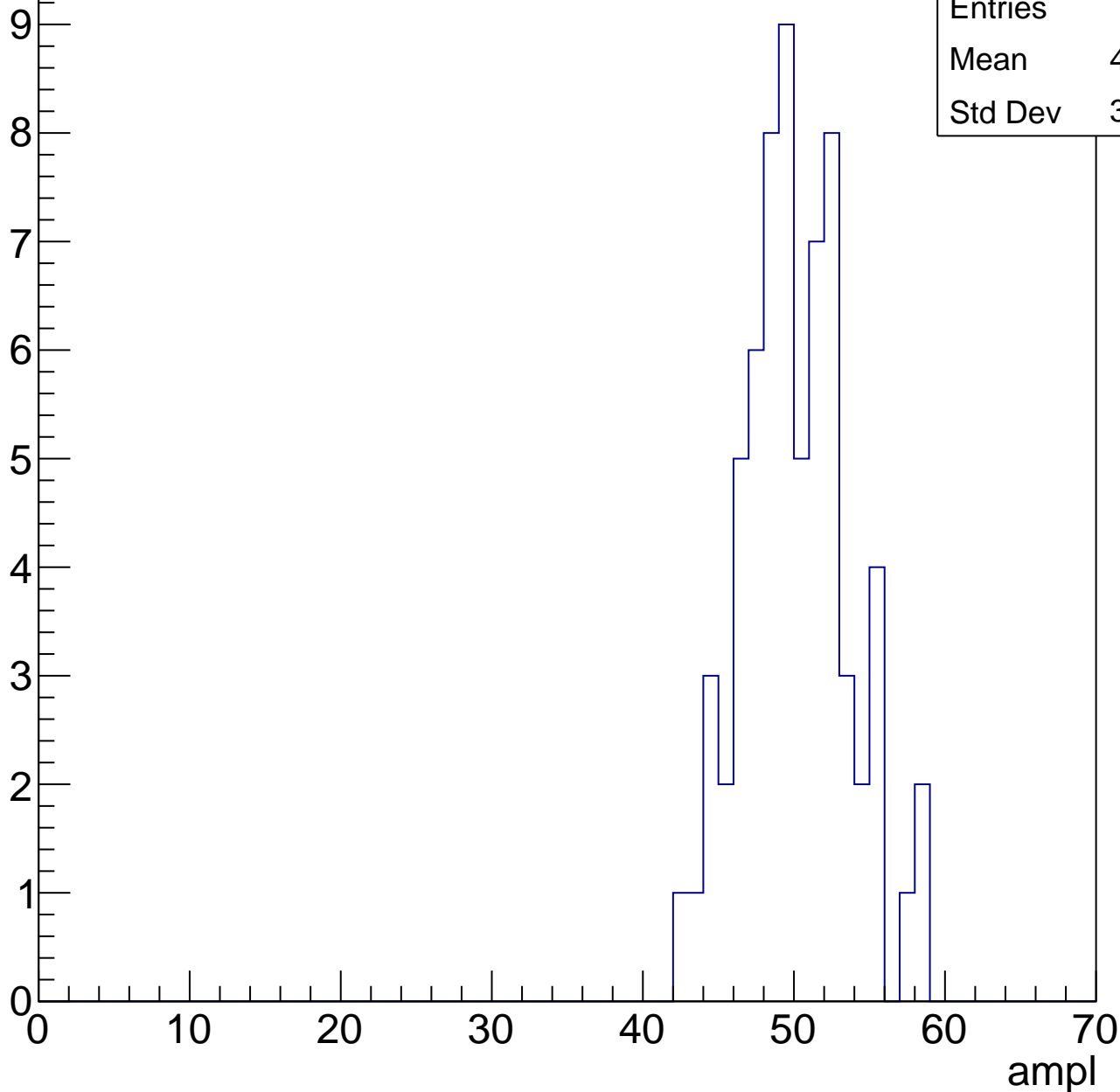


B0L001S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

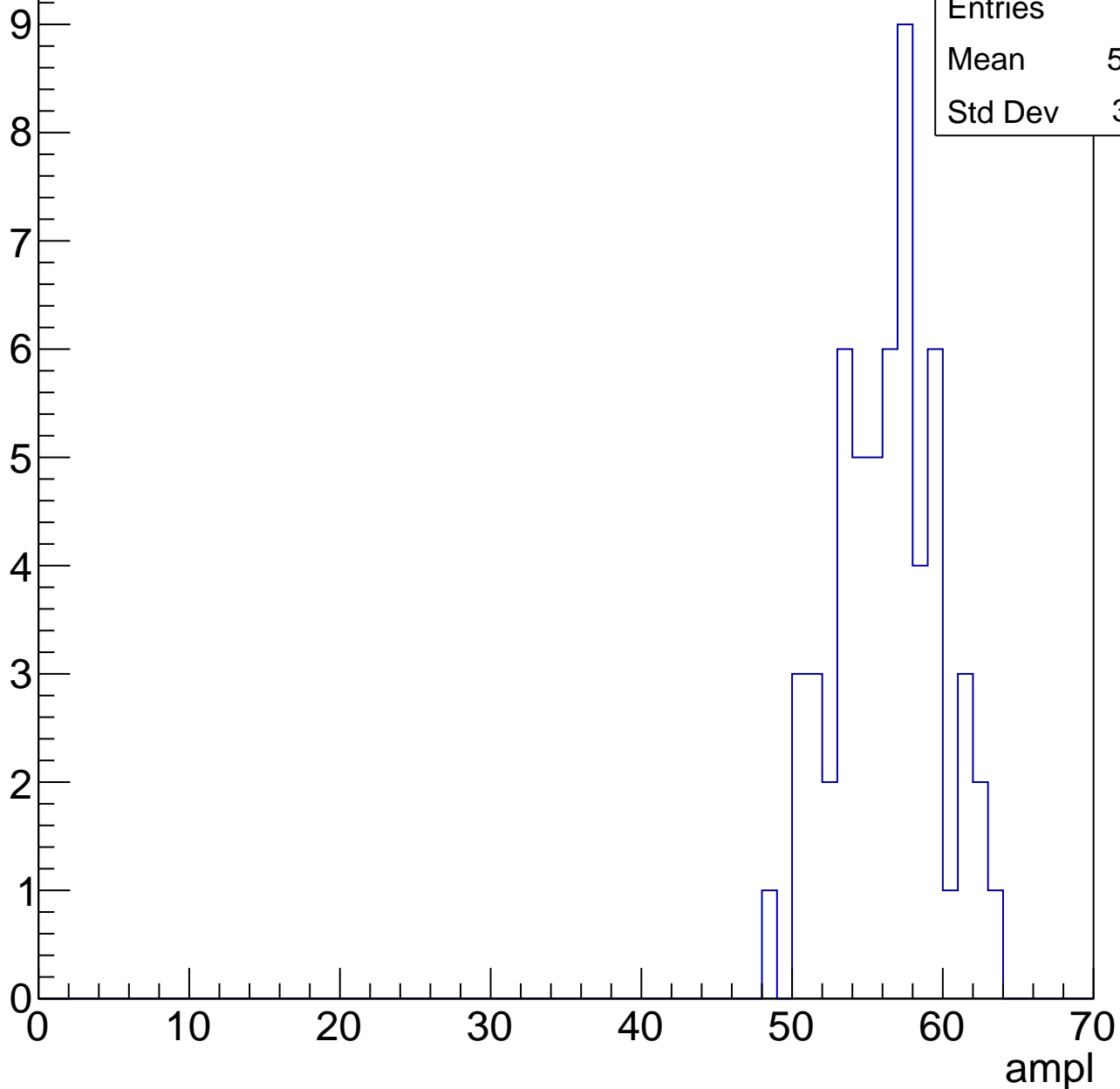
Entries	67
Mean	49.66
Std Dev	3.484



B0L001S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

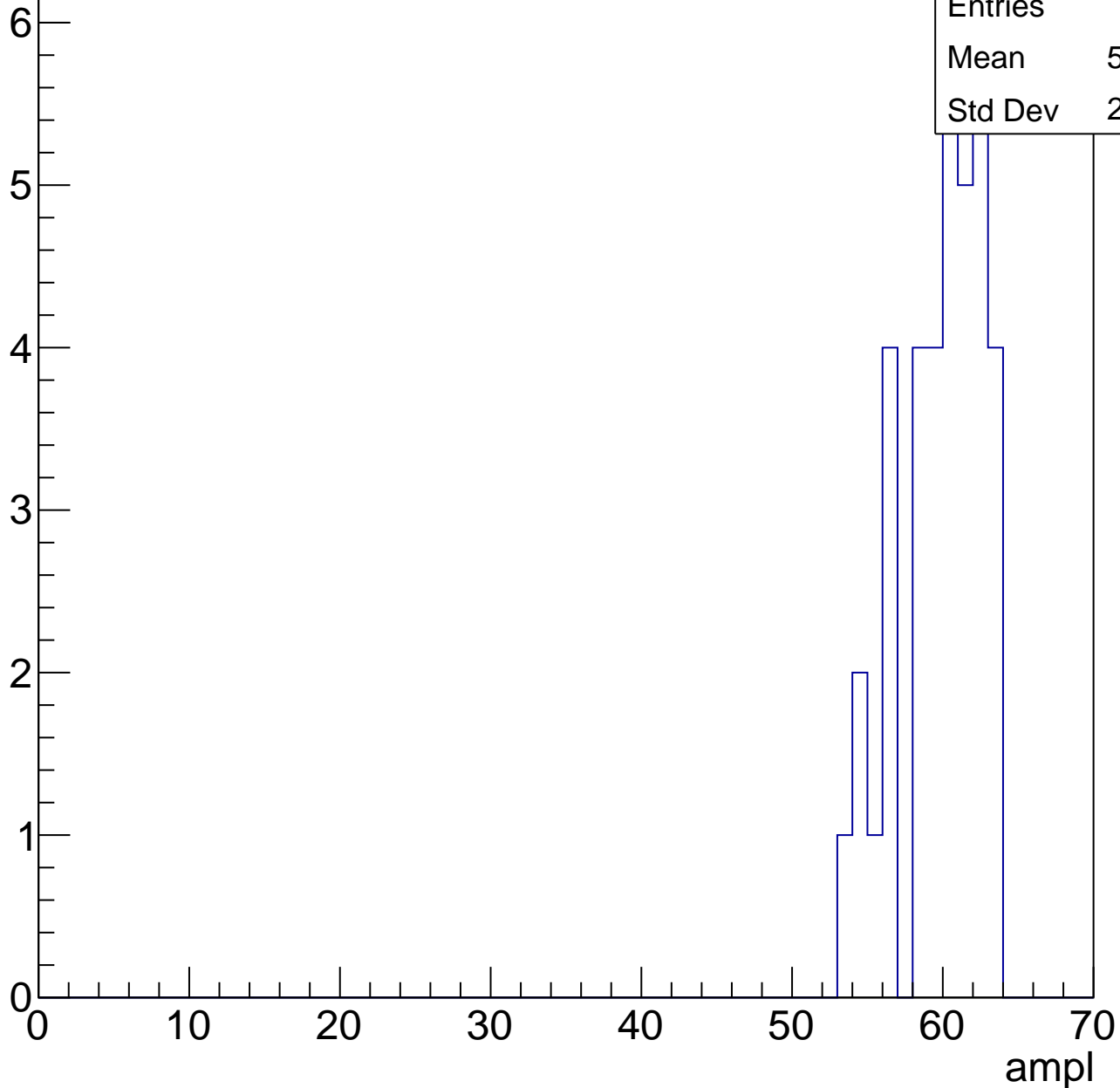
Entry



B0L001S, U15-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

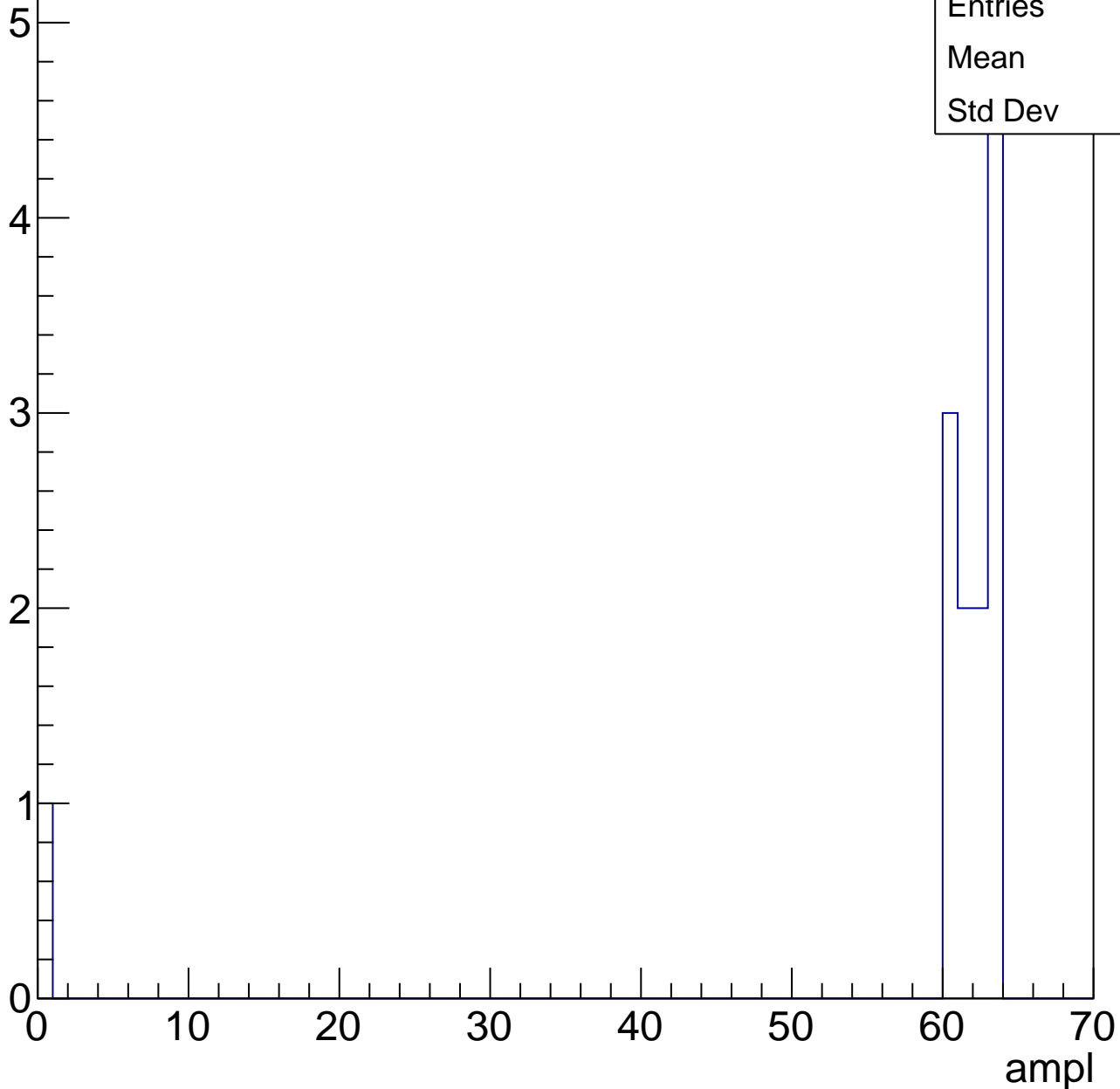


B0L001S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	13
Mean	57
Std Dev	16.5



B0L001S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch76, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	29.92
Std Dev	4.918

Gaus mean : 31.3303

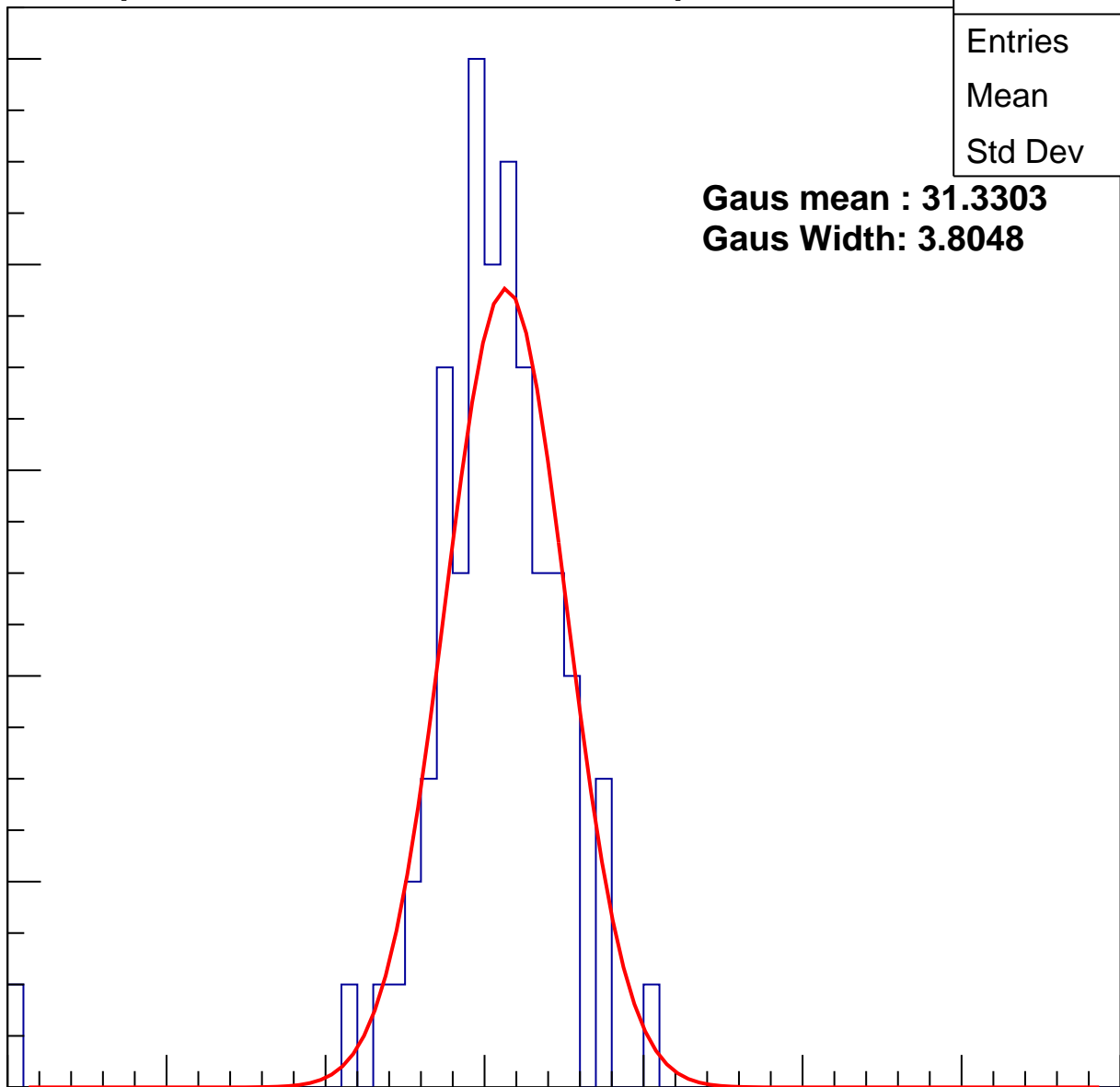
Gaus Width: 3.8048

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch76, adc1

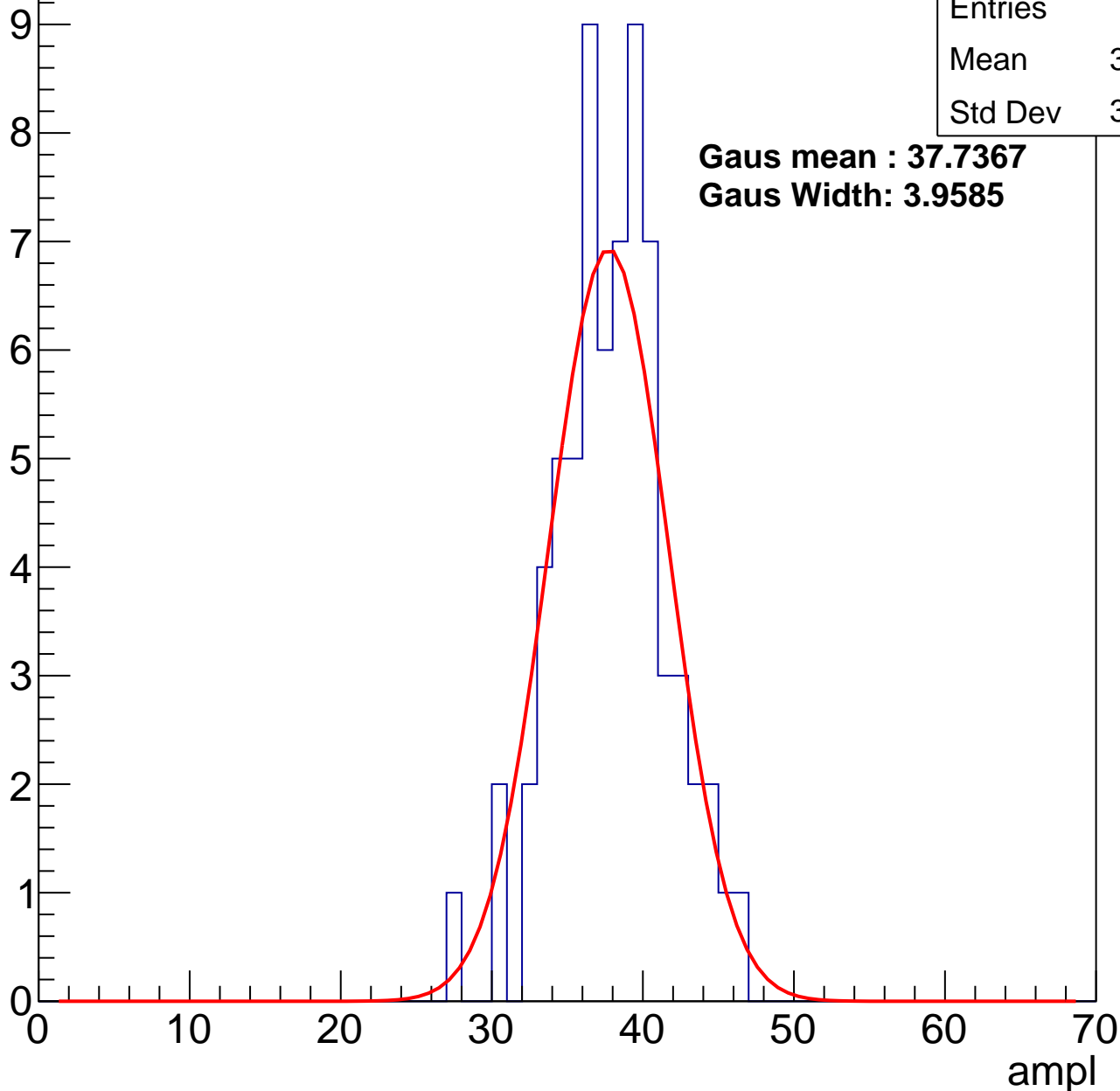
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.46
Std Dev	3.642

Gaus mean : 37.7367

Gaus Width: 3.9585



B0L001S, U15-ch76, adc2

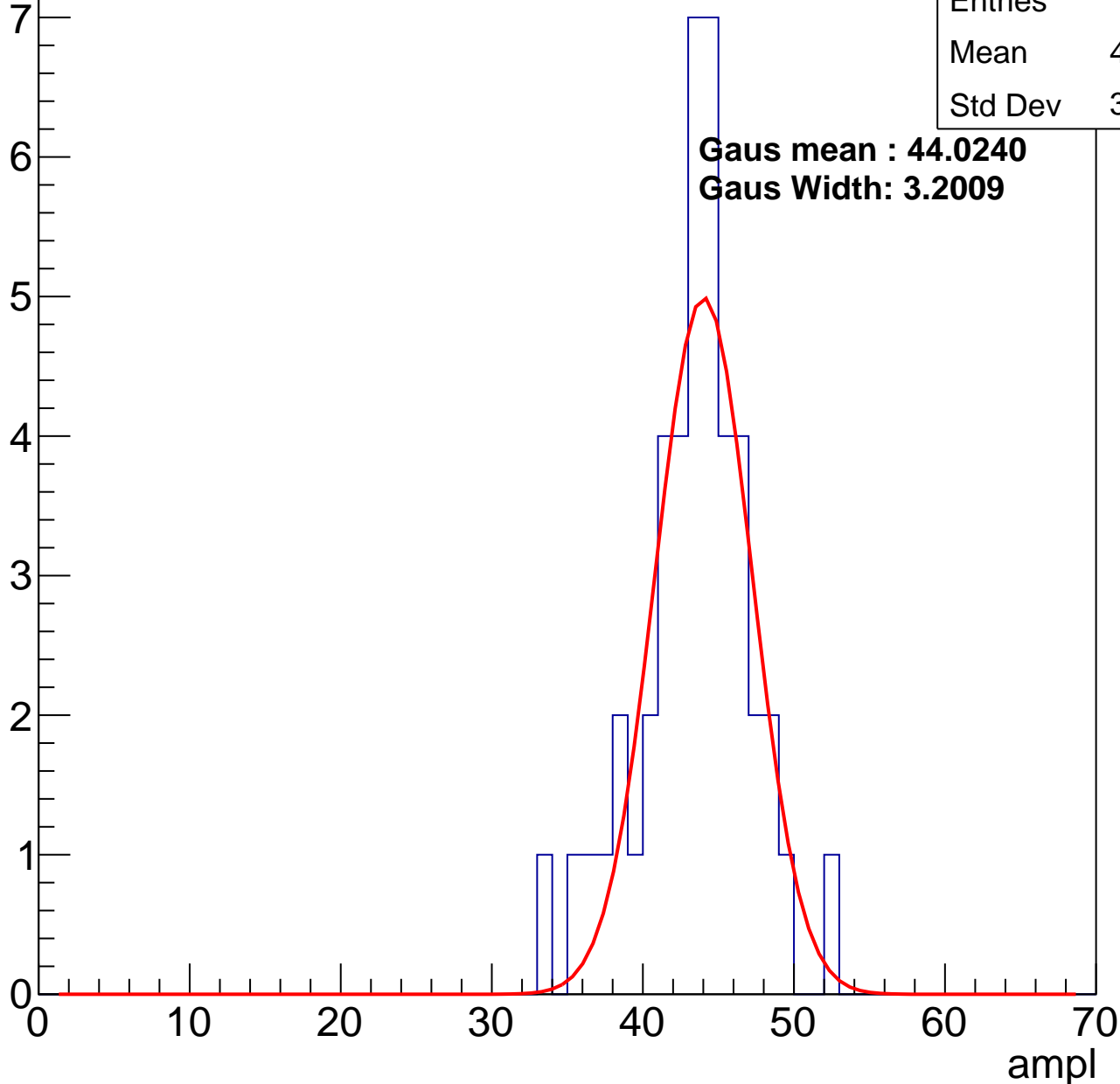
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	42.93
Std Dev	3.684

Gaus mean : 44.0240

Gaus Width: 3.2009

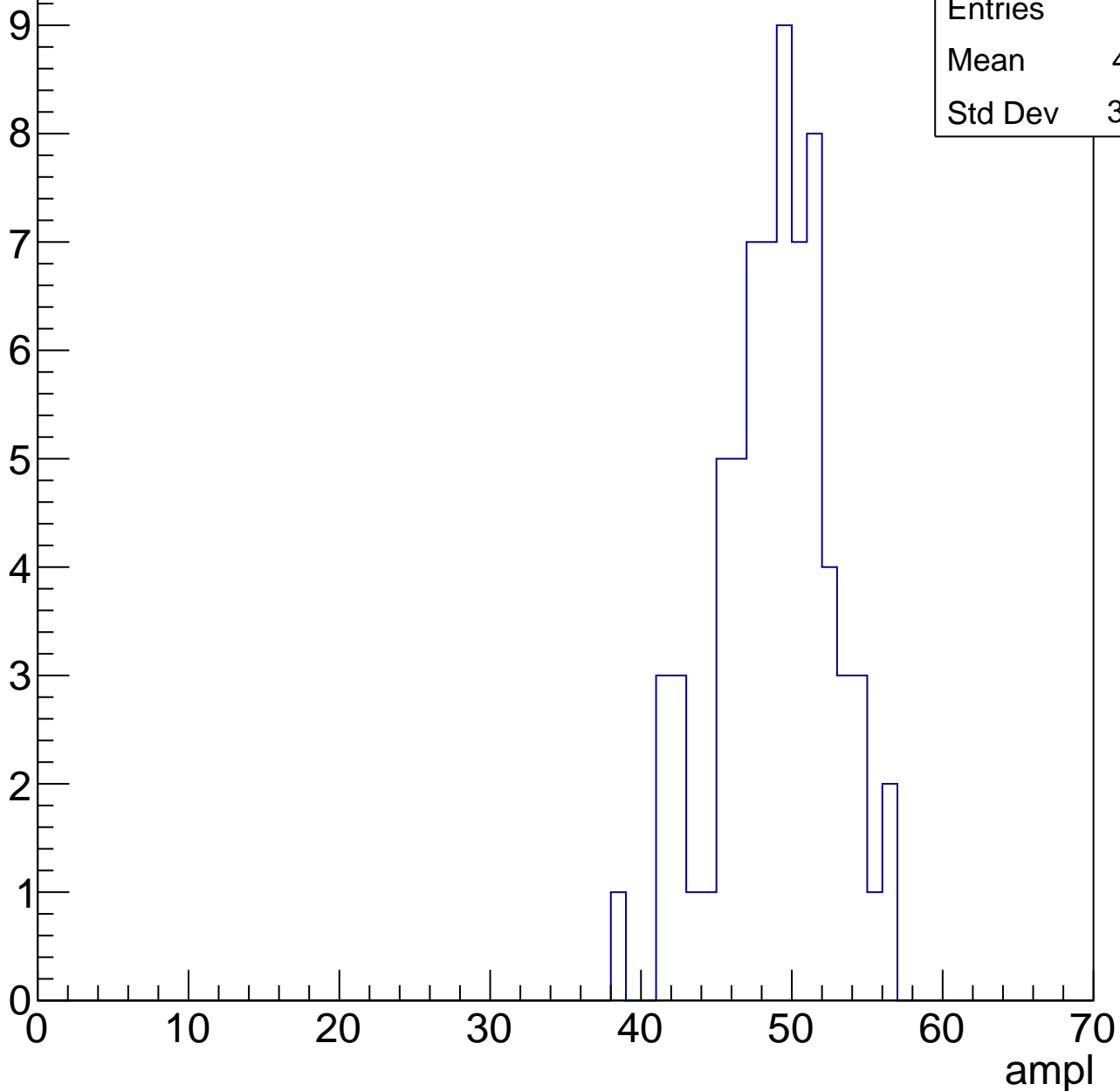


B0L001S, U15-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	48.41
Std Dev	3.759

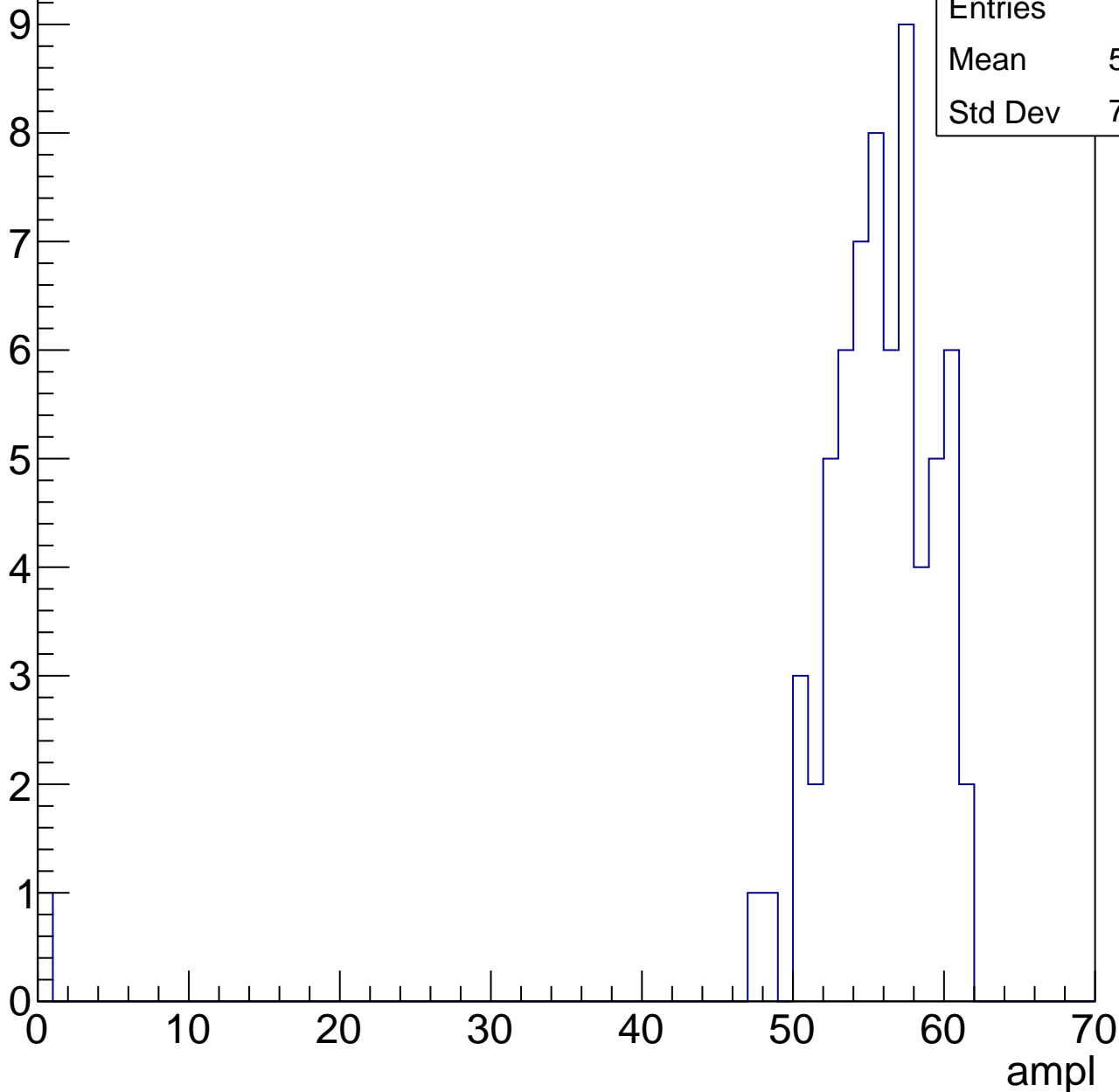


B0L001S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	54.56
Std Dev	7.475

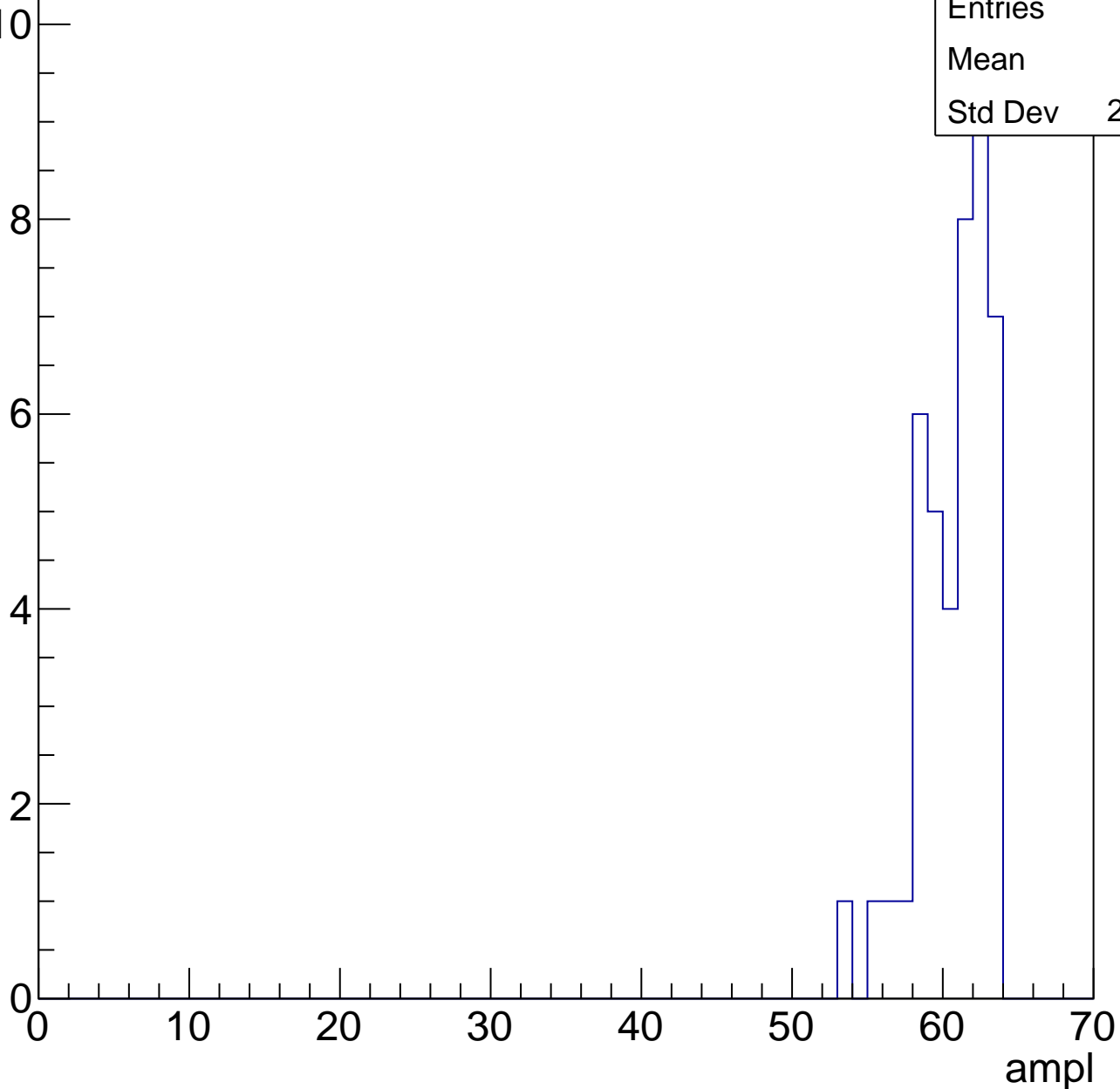


B0L001S, U15-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

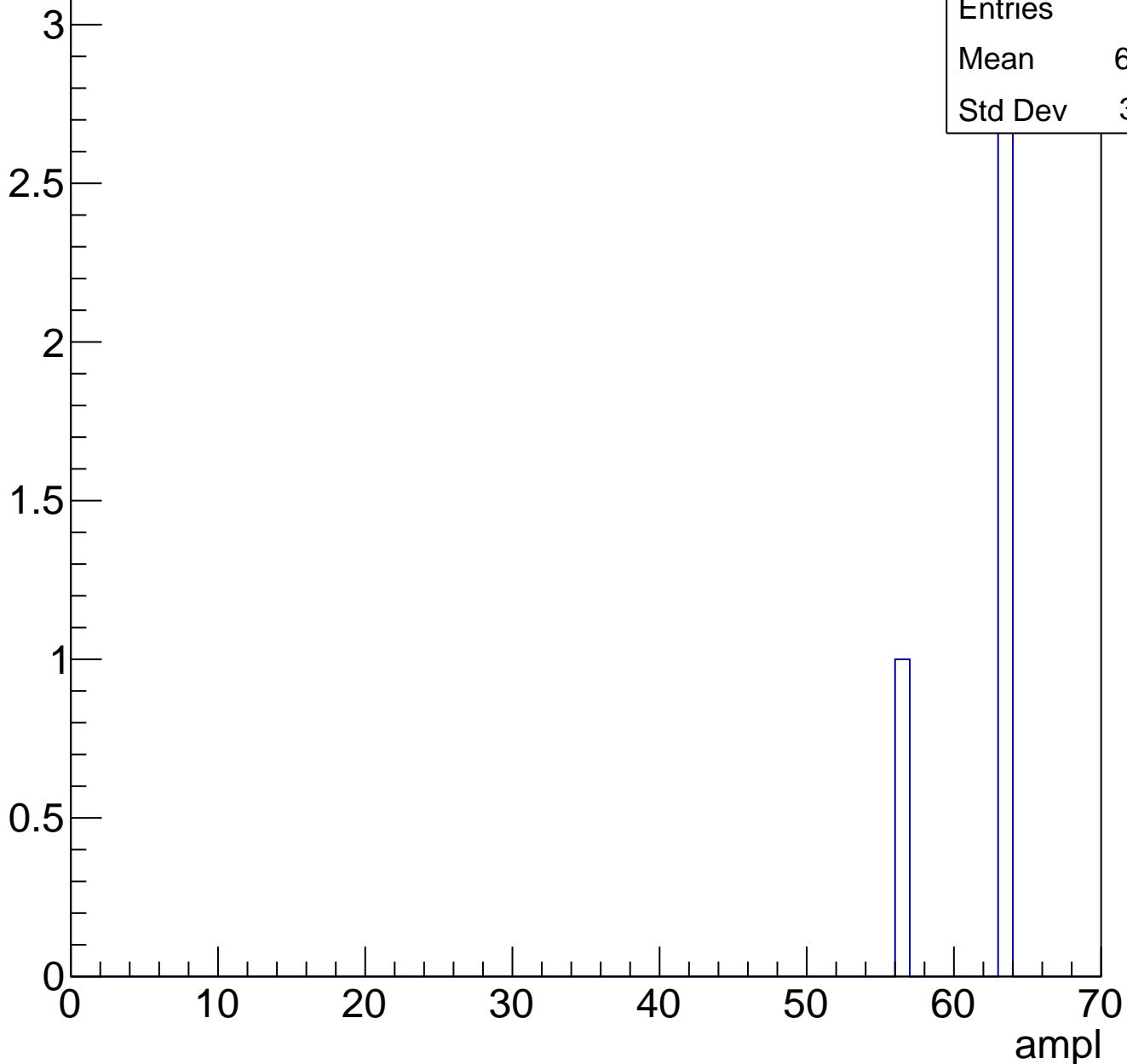
Entries	44
Mean	60.3
Std Dev	2.312



B0L001S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch77, adc0

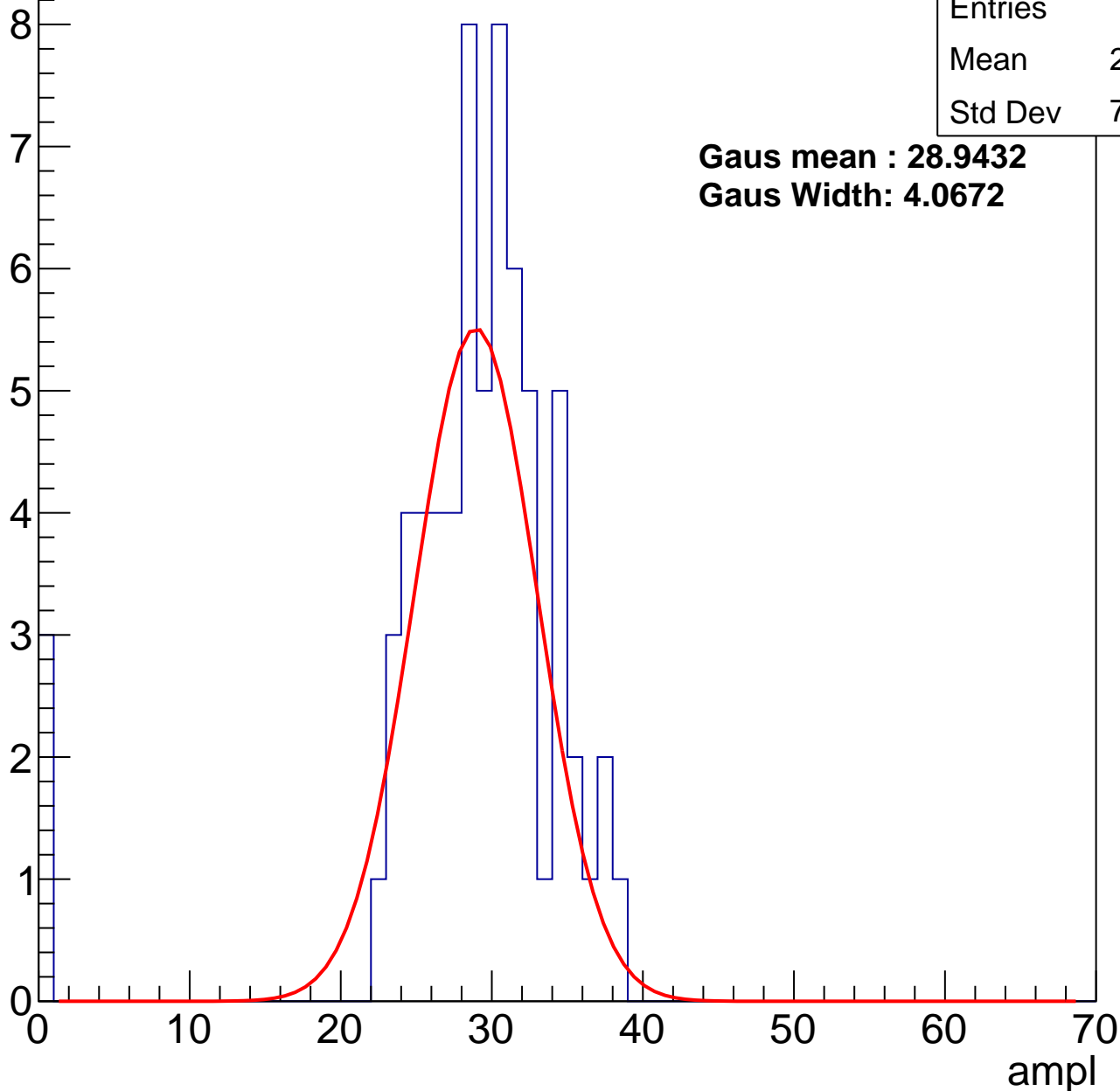
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	27.99
Std Dev	7.106

Gaus mean : 28.9432

Gaus Width: 4.0672



B0L001S, U15-ch77, adc1

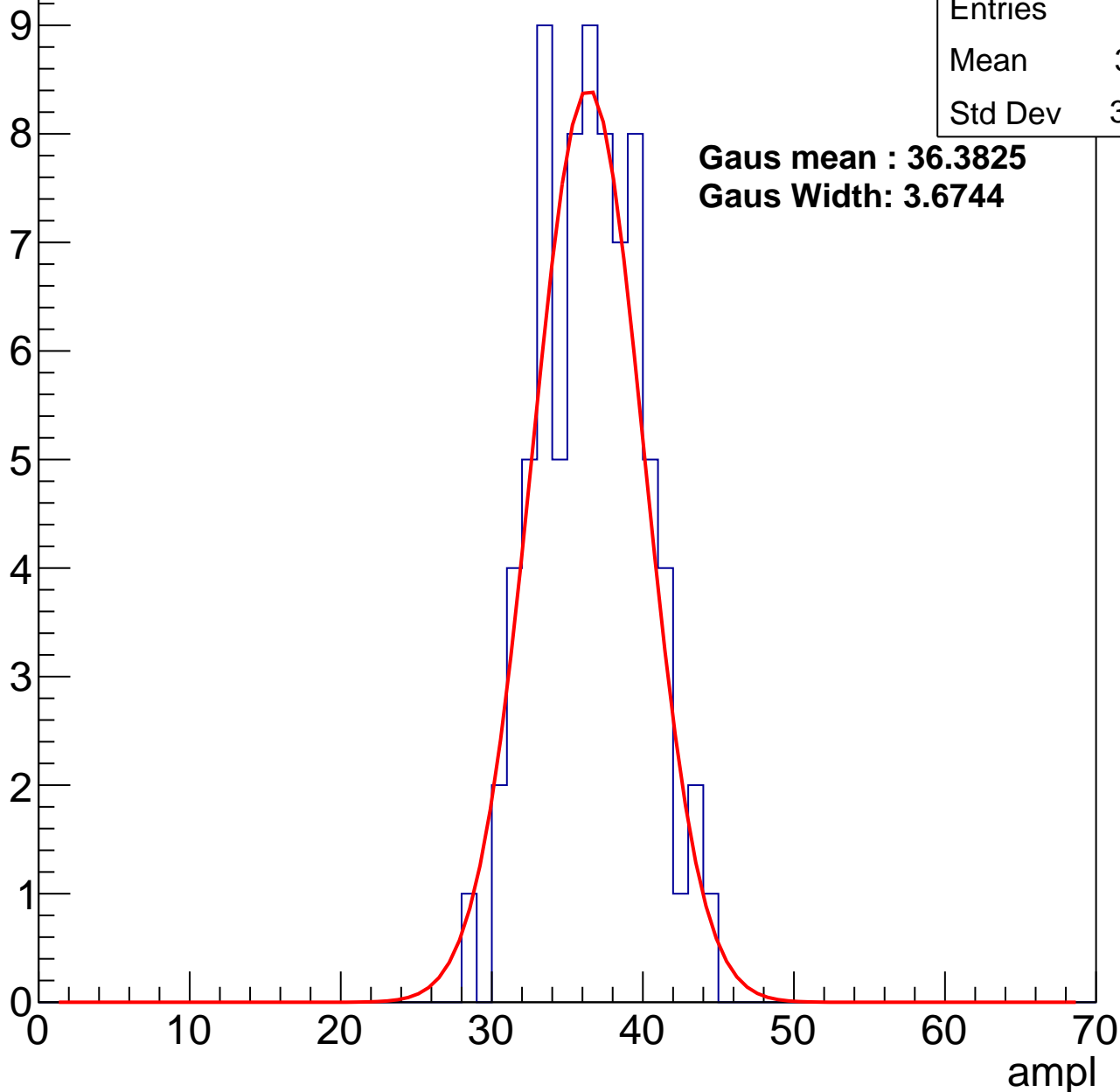
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.11
Std Dev	3.394

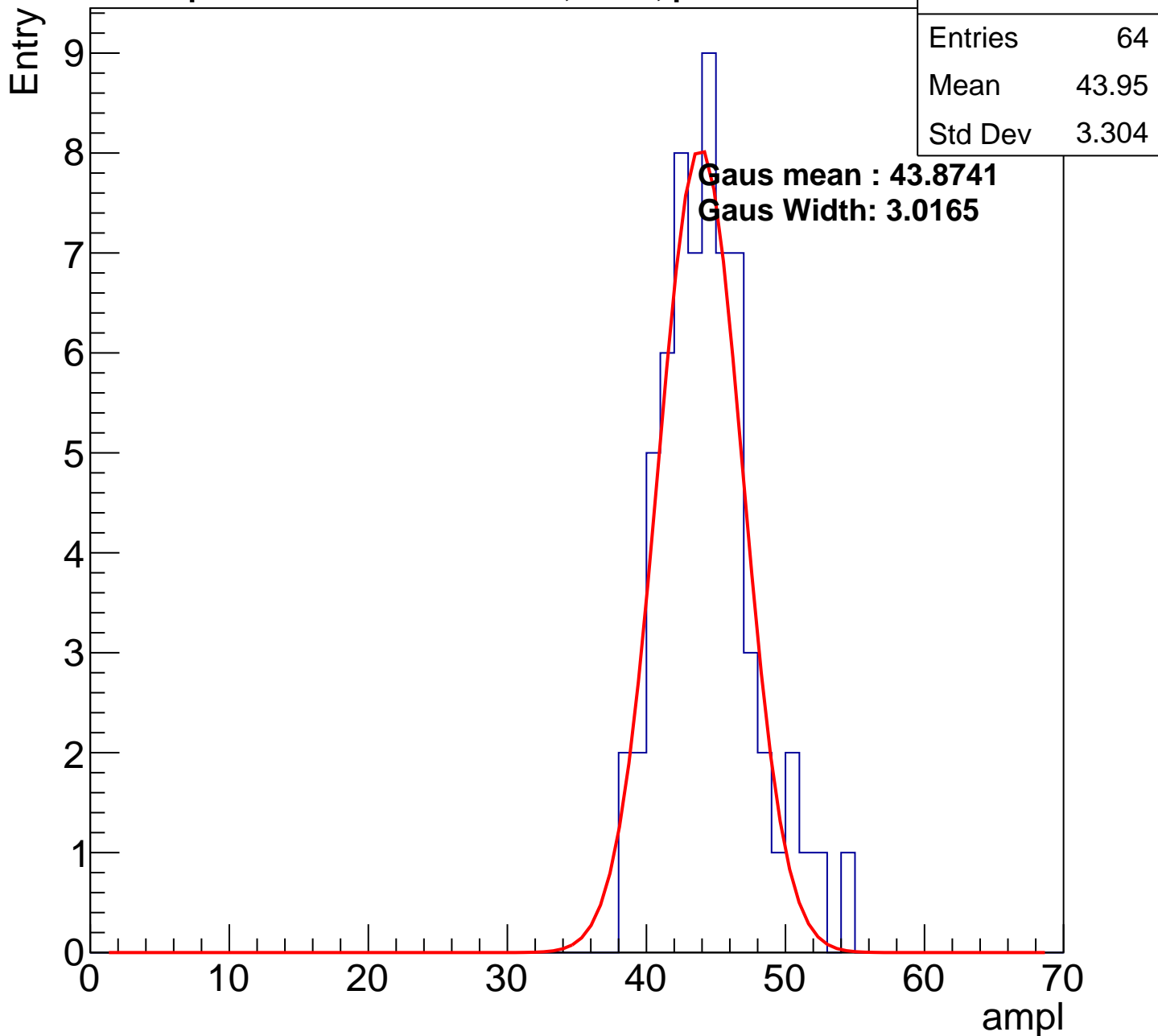
Gaus mean : 36.3825

Gaus Width: 3.6744



B0L001S, U15-ch77, adc2

calib_packv5_042523_0143.root, FC#9, port A1

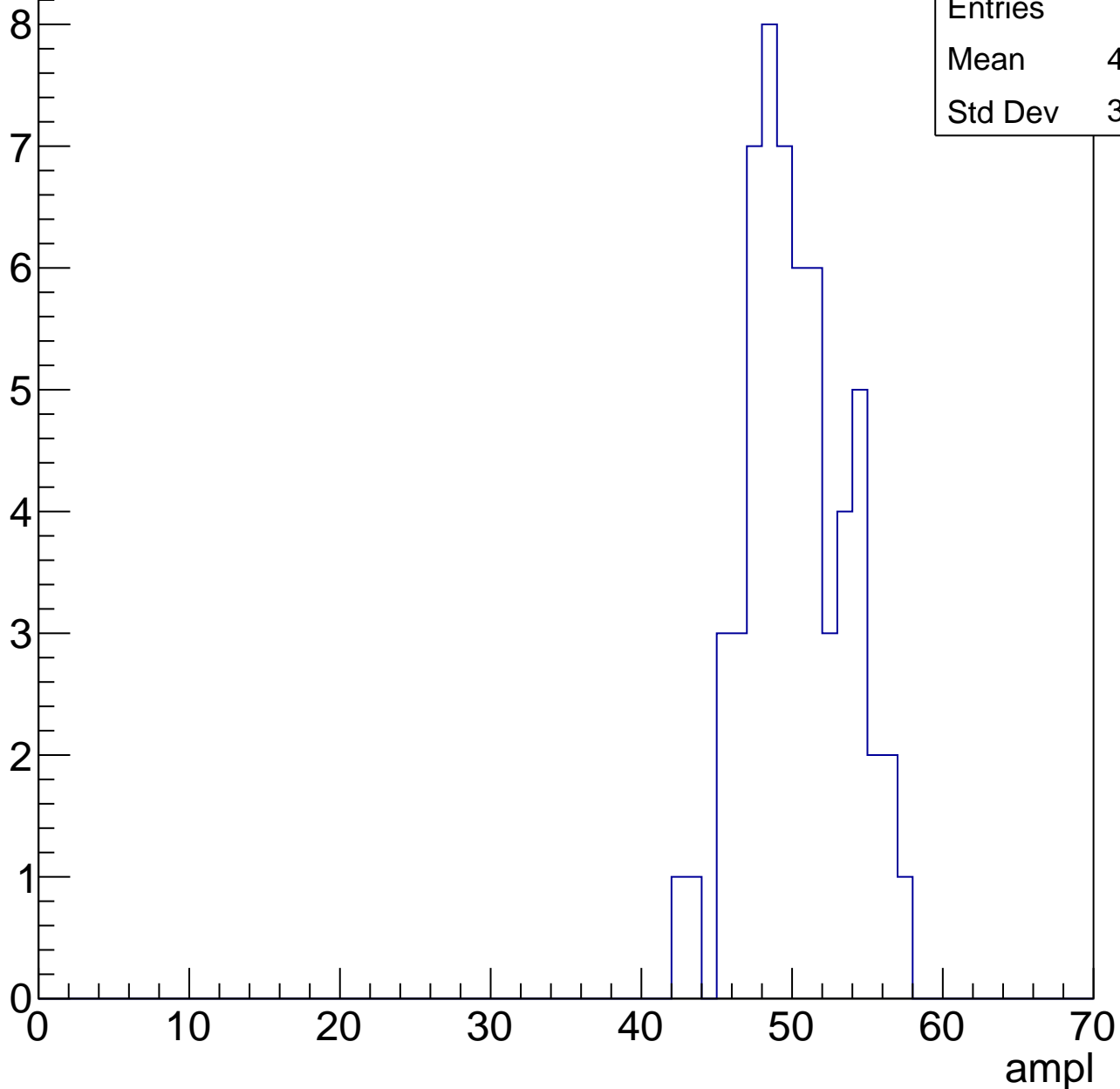


B0L001S, U15-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.78
Std Dev	3.294

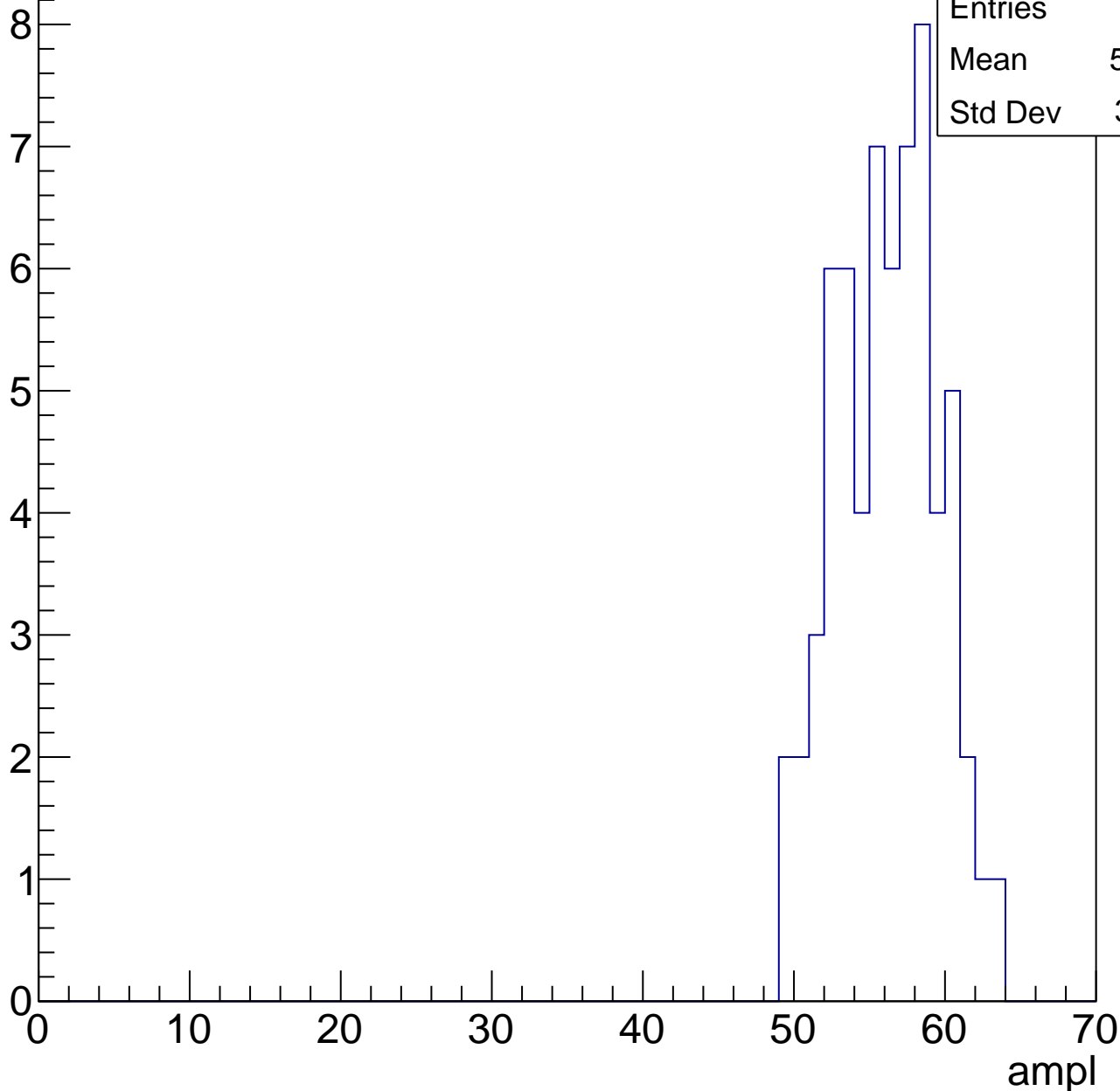


B0L001S, U15-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.69
Std Dev	3.311

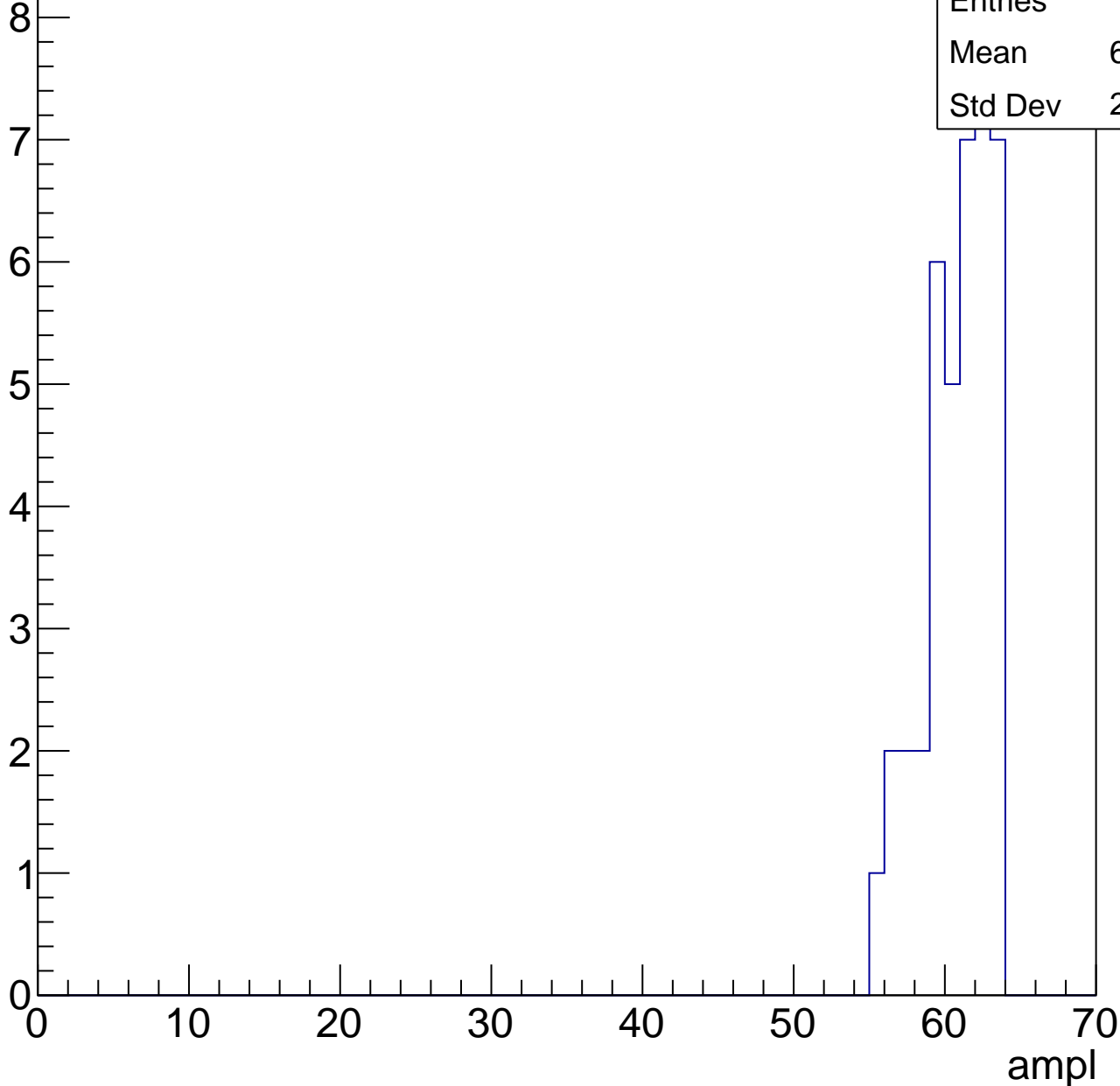


B0L001S, U15-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

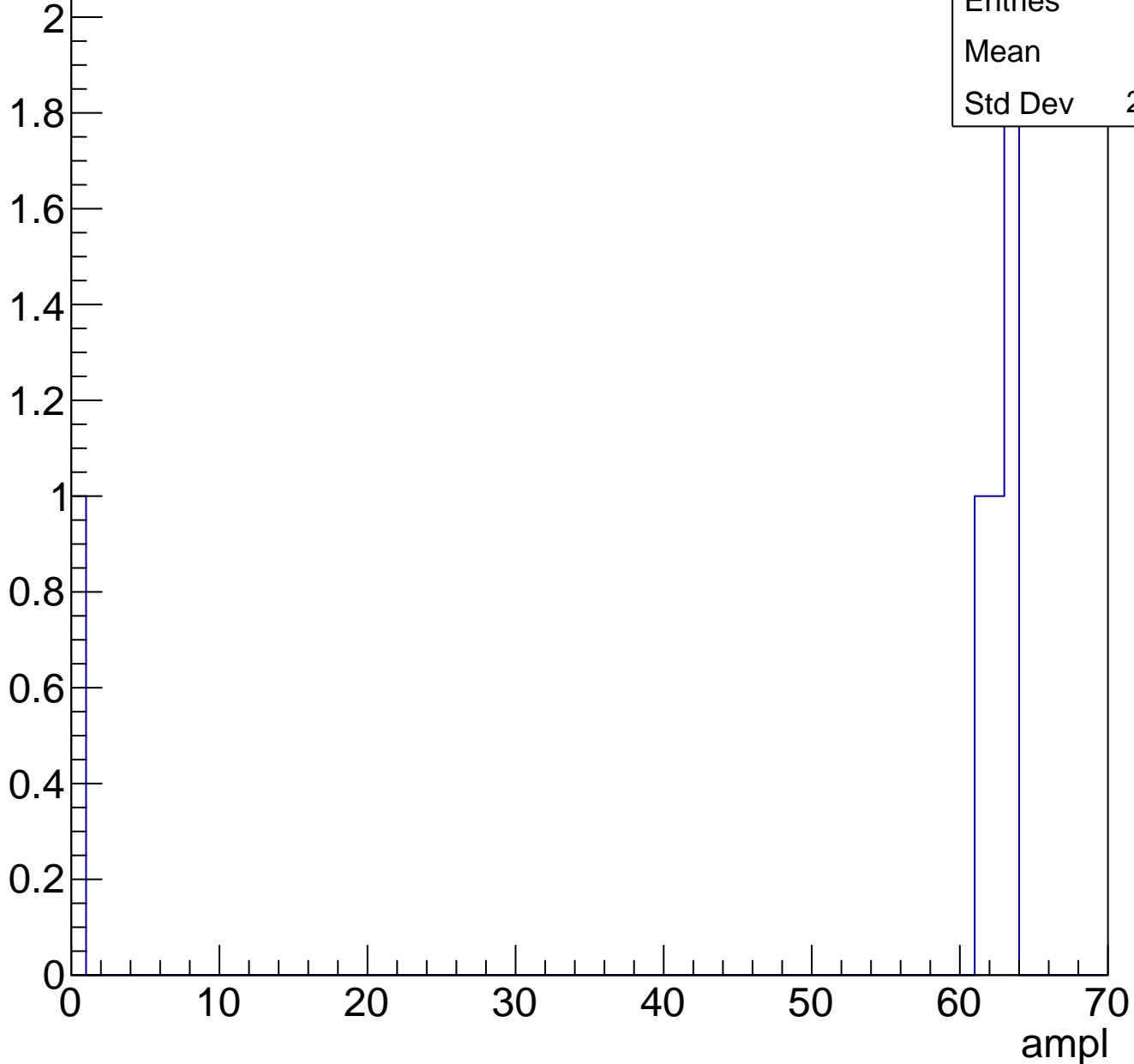
Entries	40
Mean	60.38
Std Dev	2.153



B0L001S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch78, adc0

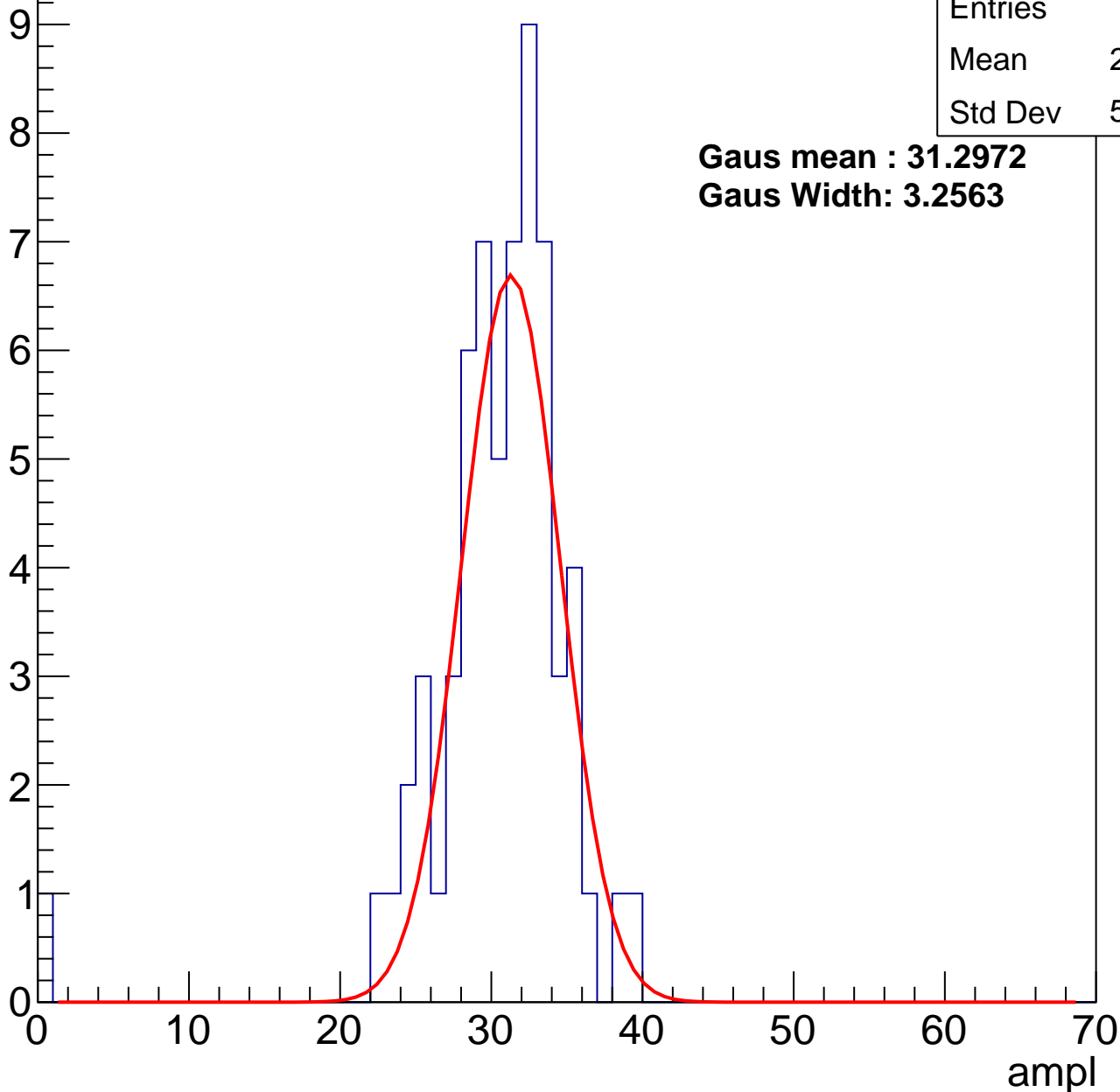
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.95
Std Dev	5.147

Gaus mean : 31.2972

Gaus Width: 3.2563



B0L001S, U15-ch78, adc1

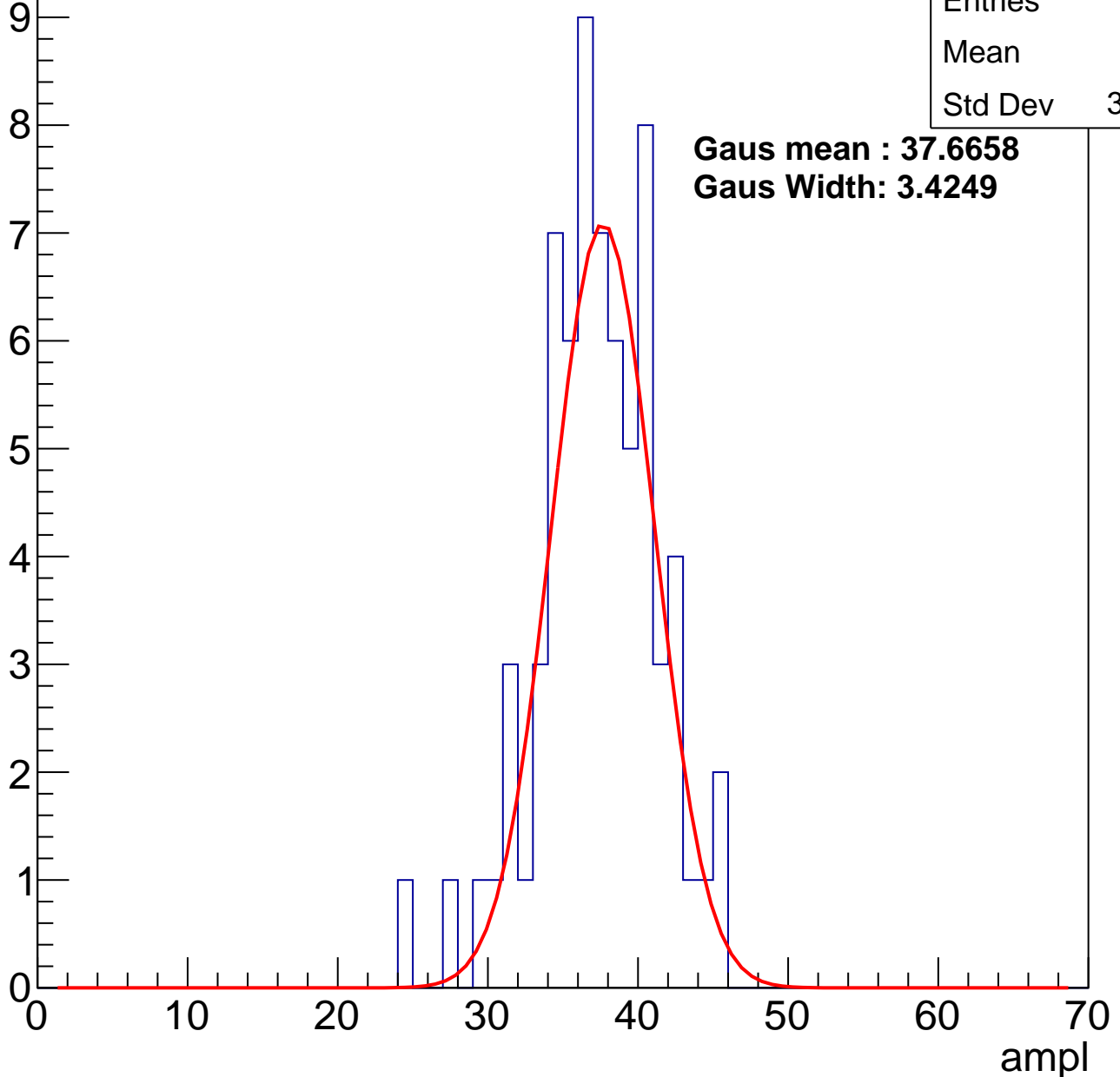
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.8
Std Dev	3.991

Gaus mean : 37.6658

Gaus Width: 3.4249



B0L001S, U15-ch78, adc2

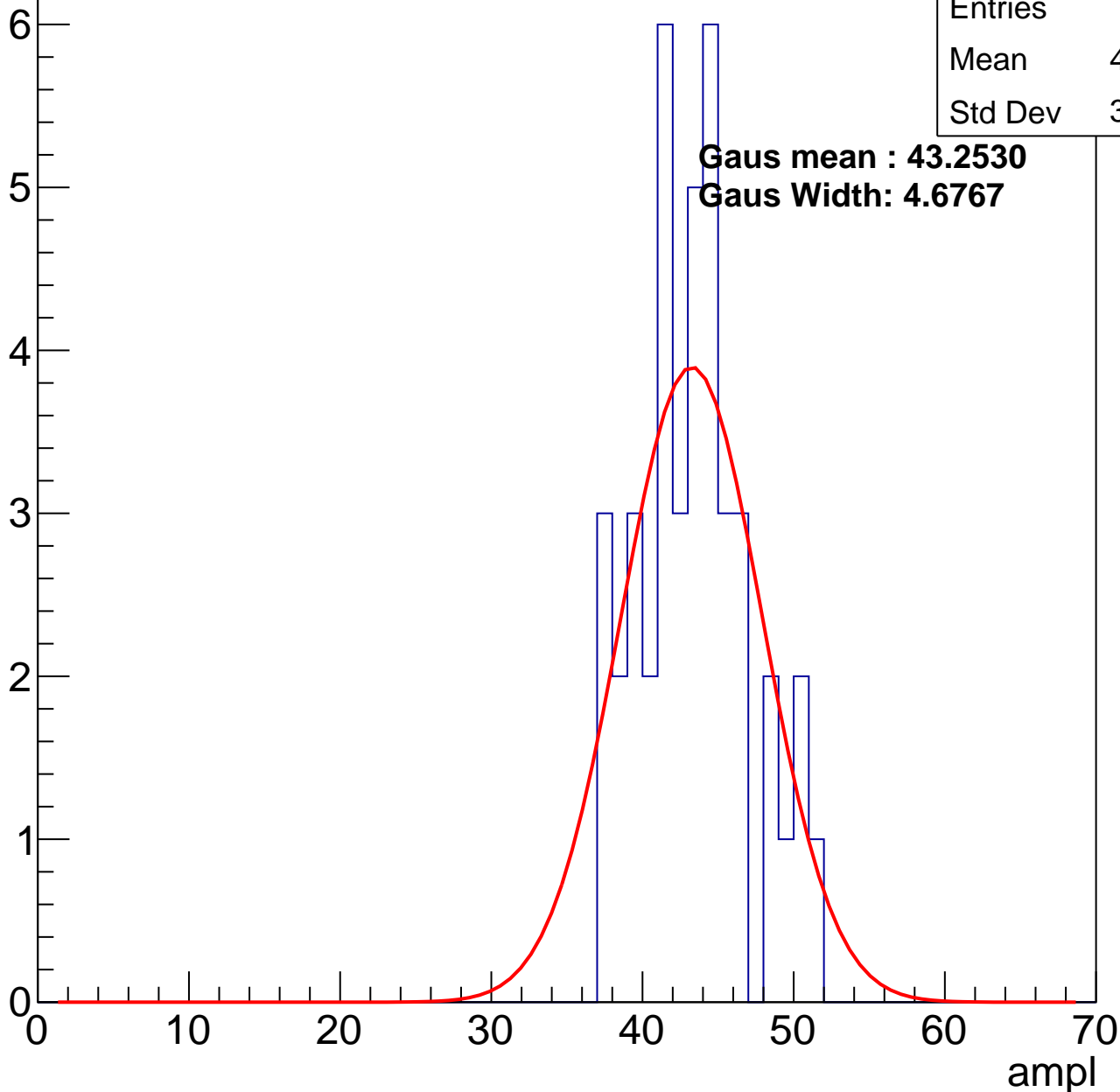
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	42.95
Std Dev	3.599

Gaus mean : 43.2530

Gaus Width: 4.6767



B0L001S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

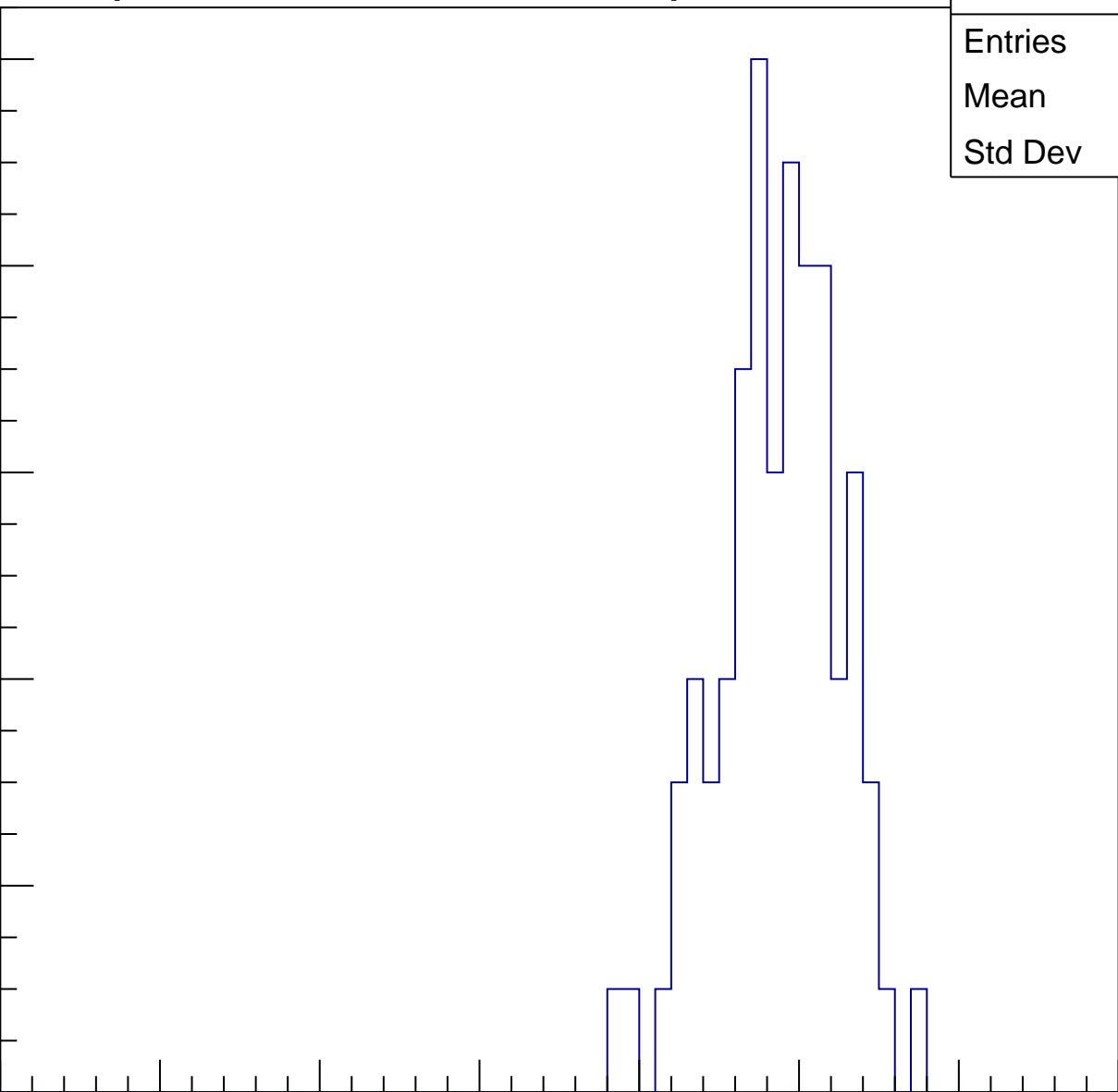
Entries	80
Mean	48.21
Std Dev	3.744

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

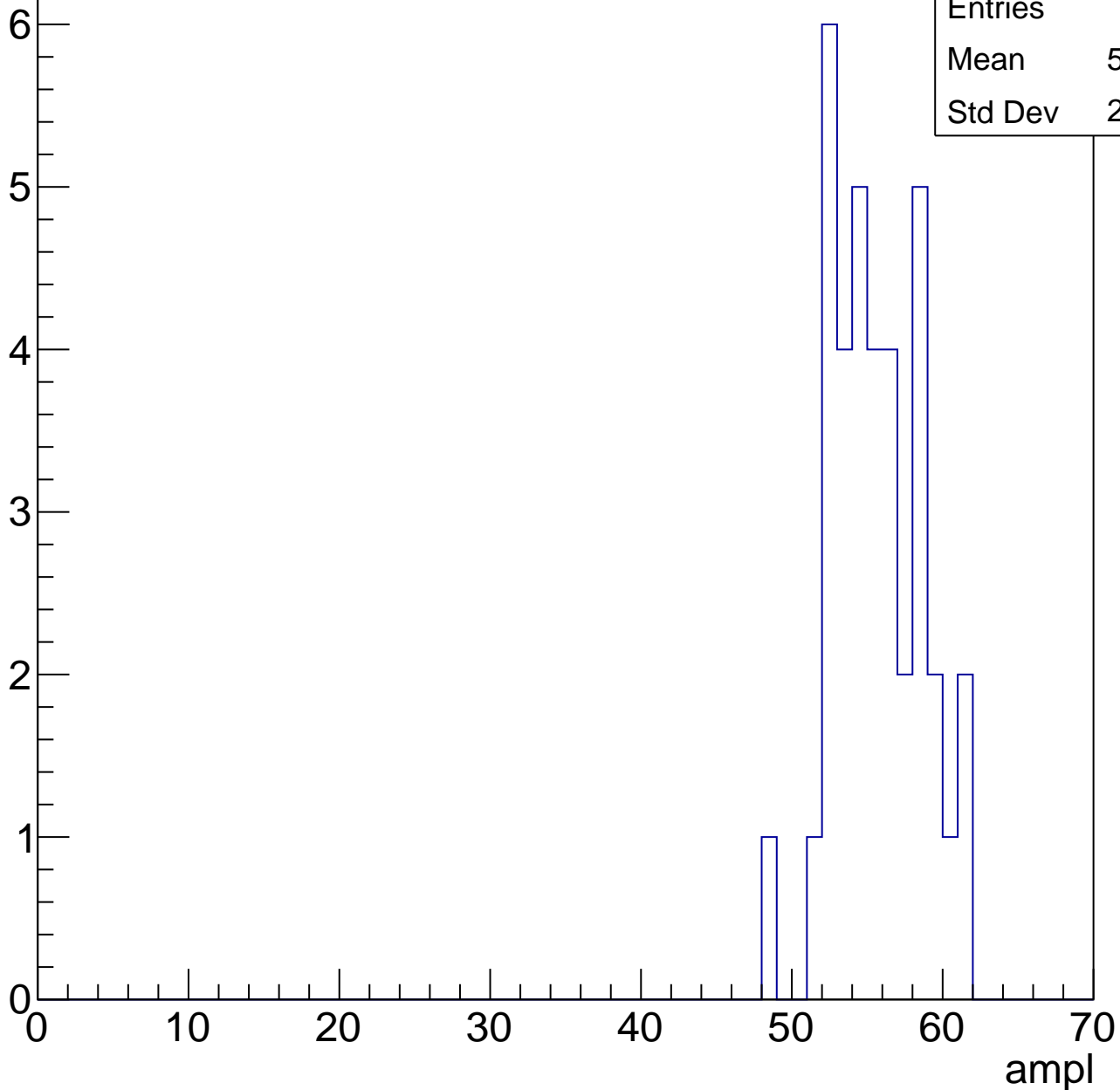


B0L001S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	55.16
Std Dev	2.964

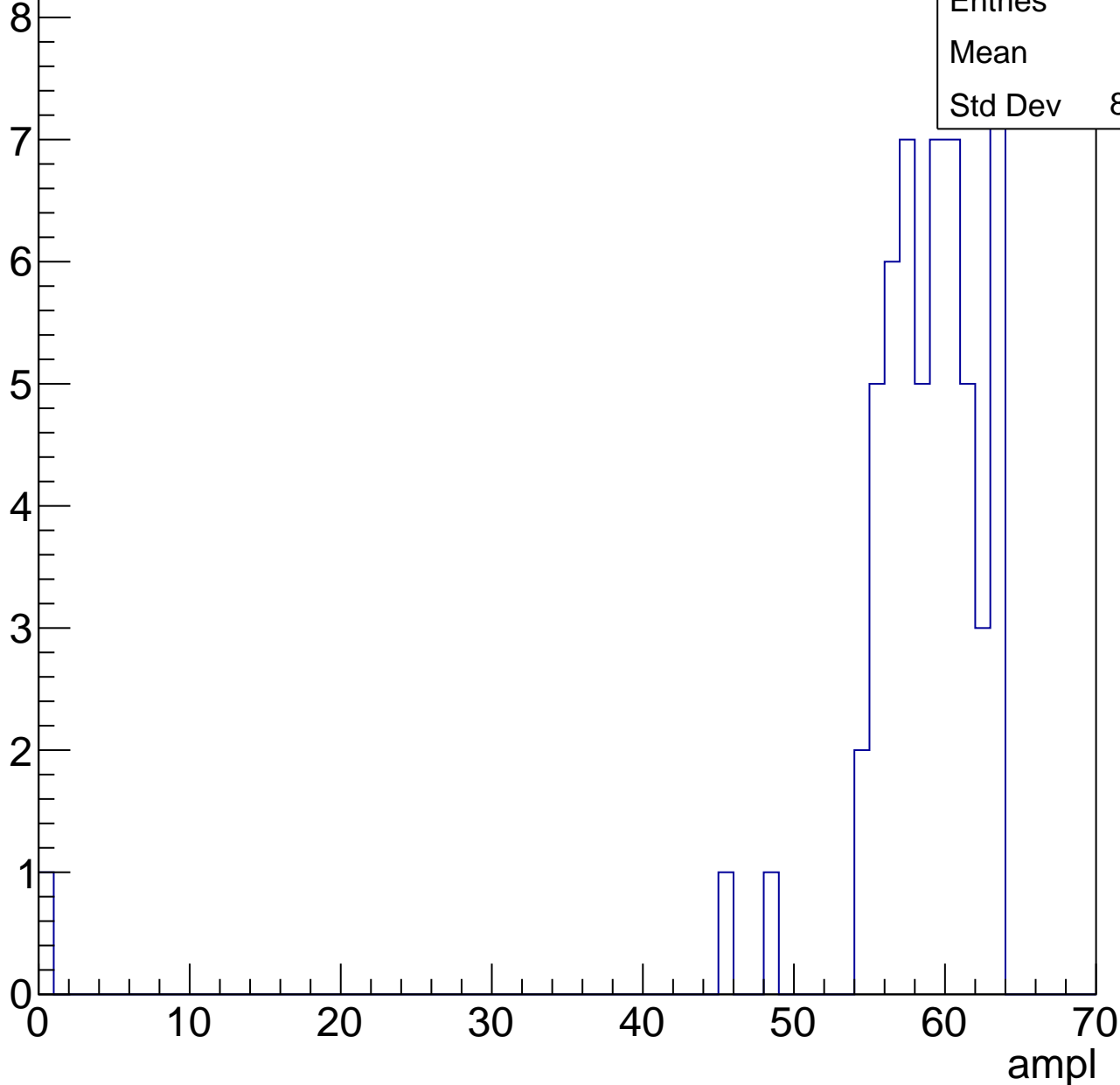


B0L001S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.4
Std Dev	8.354

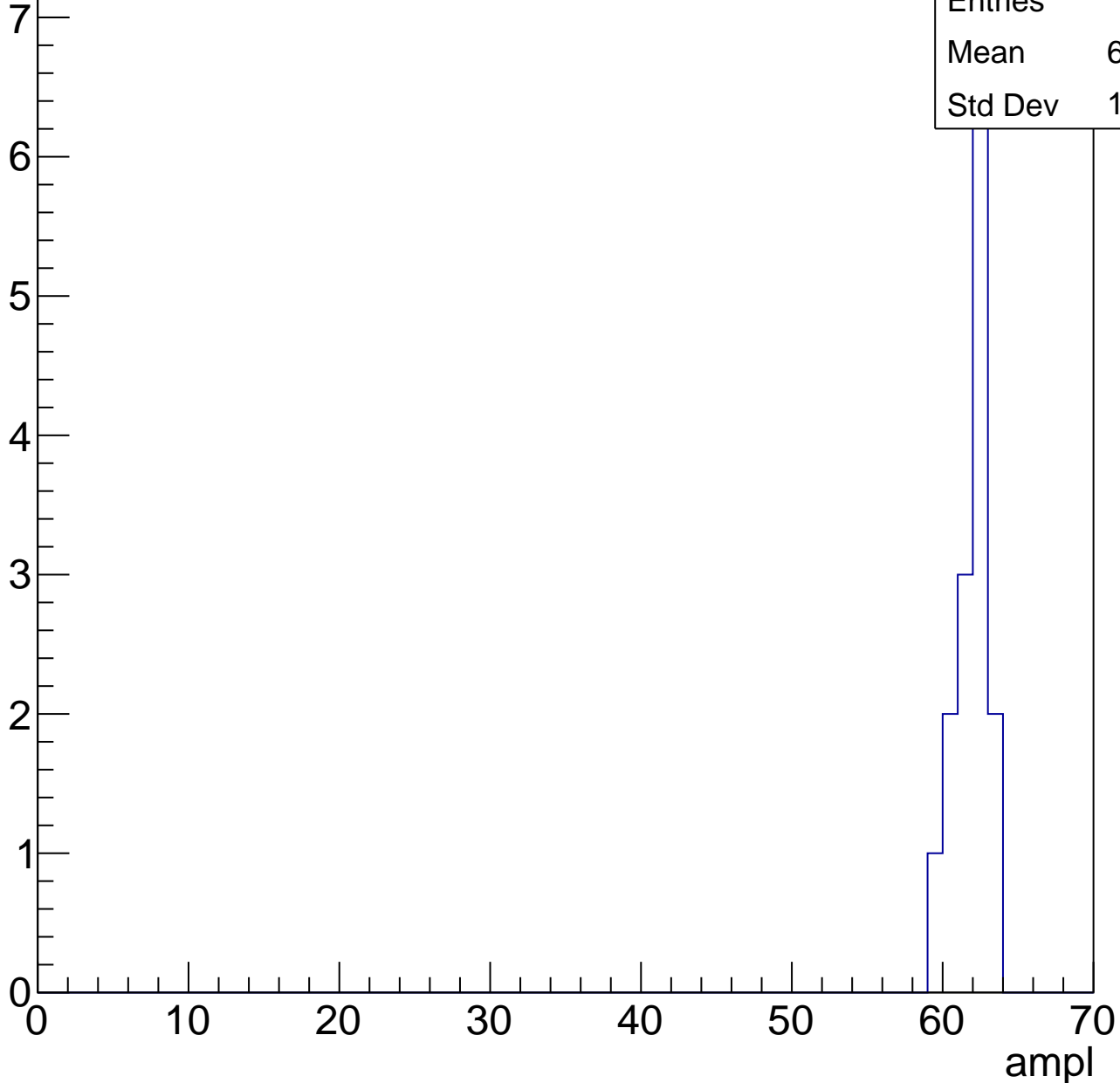


B0L001S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.47
Std Dev	1.087



B0L001S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch79, adc0

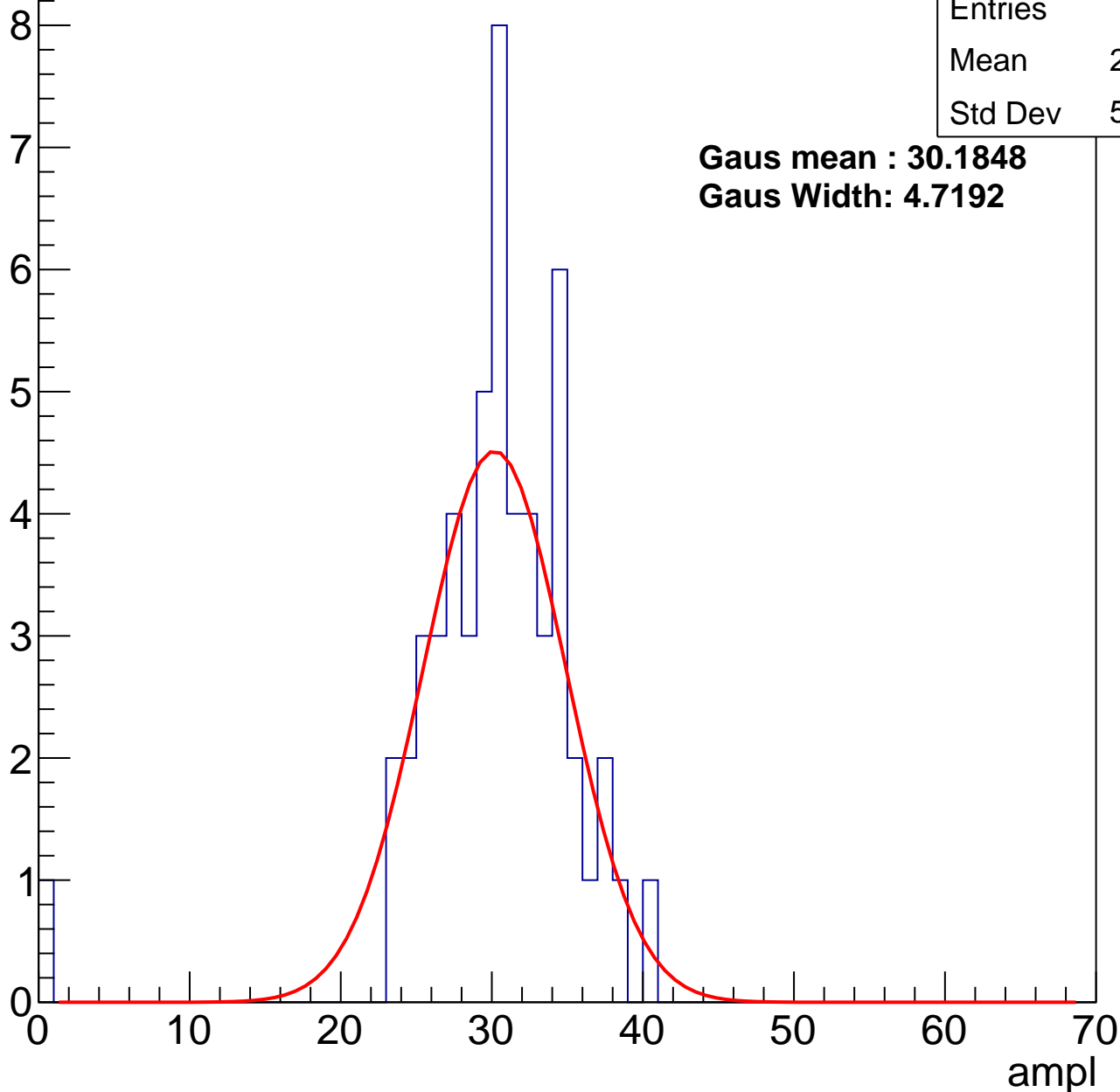
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.76
Std Dev	5.615

Gaus mean : 30.1848

Gaus Width: 4.7192



B0L001S, U15-ch79, adc1

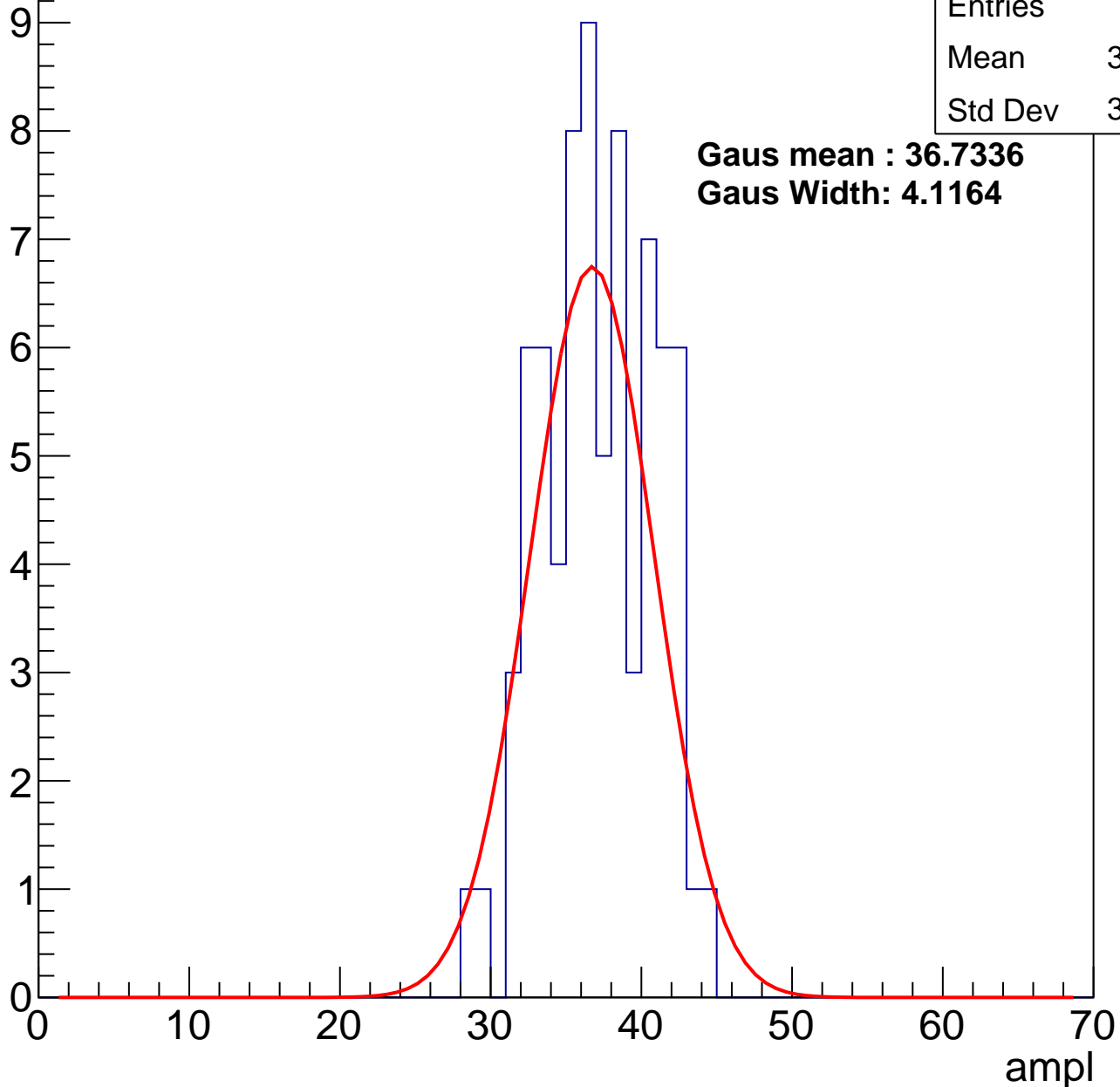
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.68
Std Dev	3.619

Gaus mean : 36.7336

Gaus Width: 4.1164



B0L001S, U15-ch79, adc2

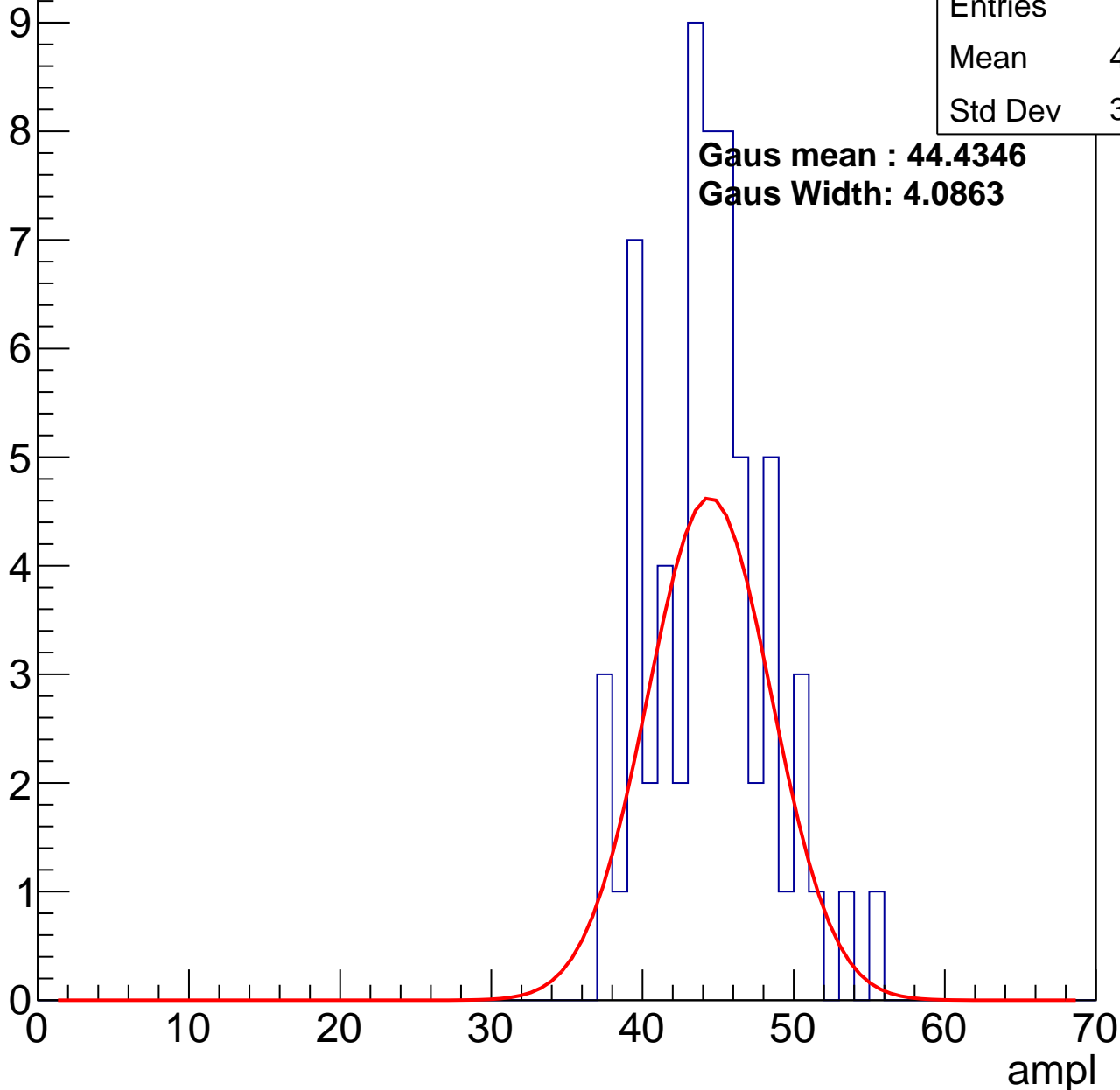
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.98
Std Dev	3.889

Gaus mean : 44.4346

Gaus Width: 4.0863

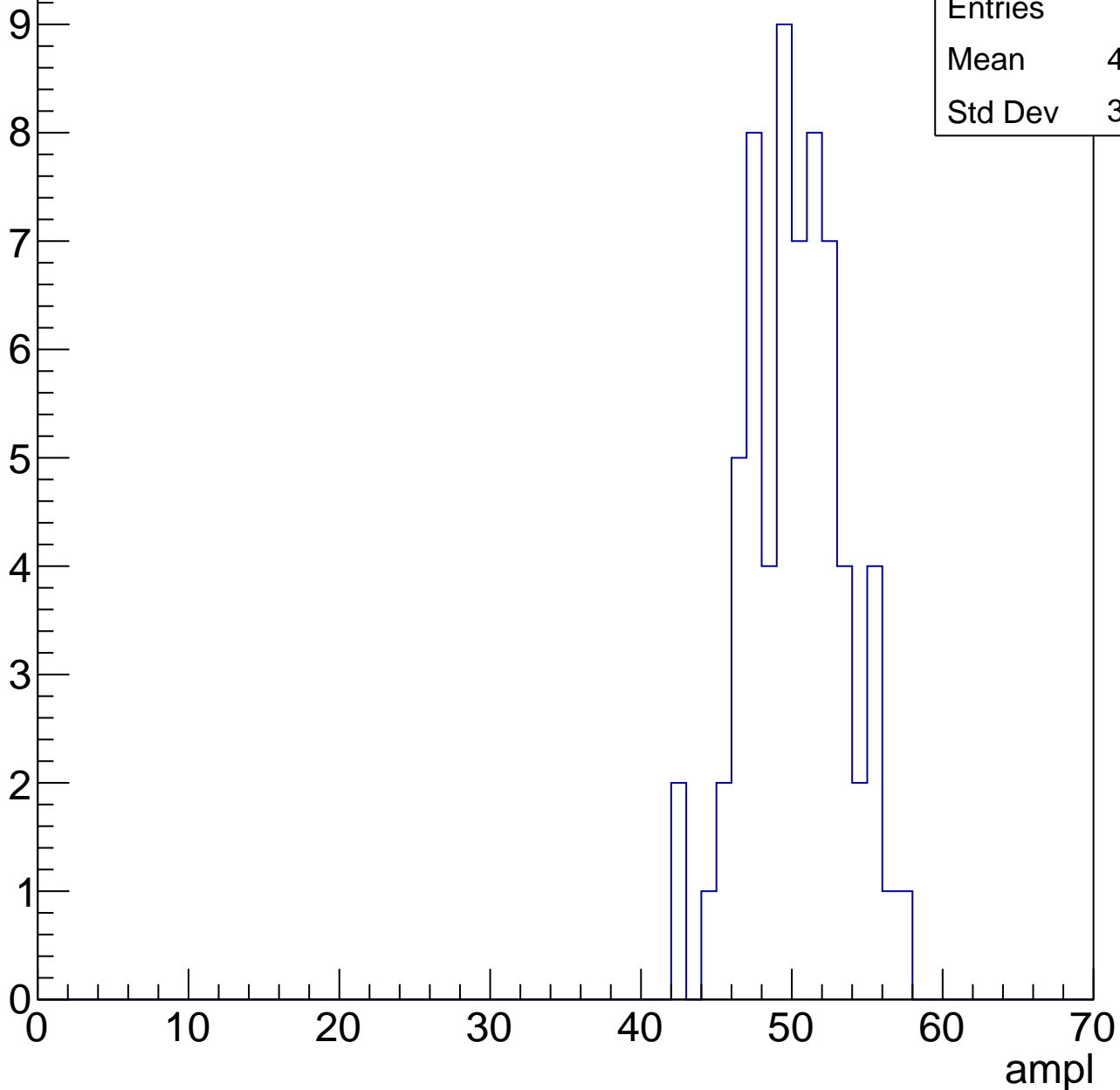


B0L001S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.72
Std Dev	3.223

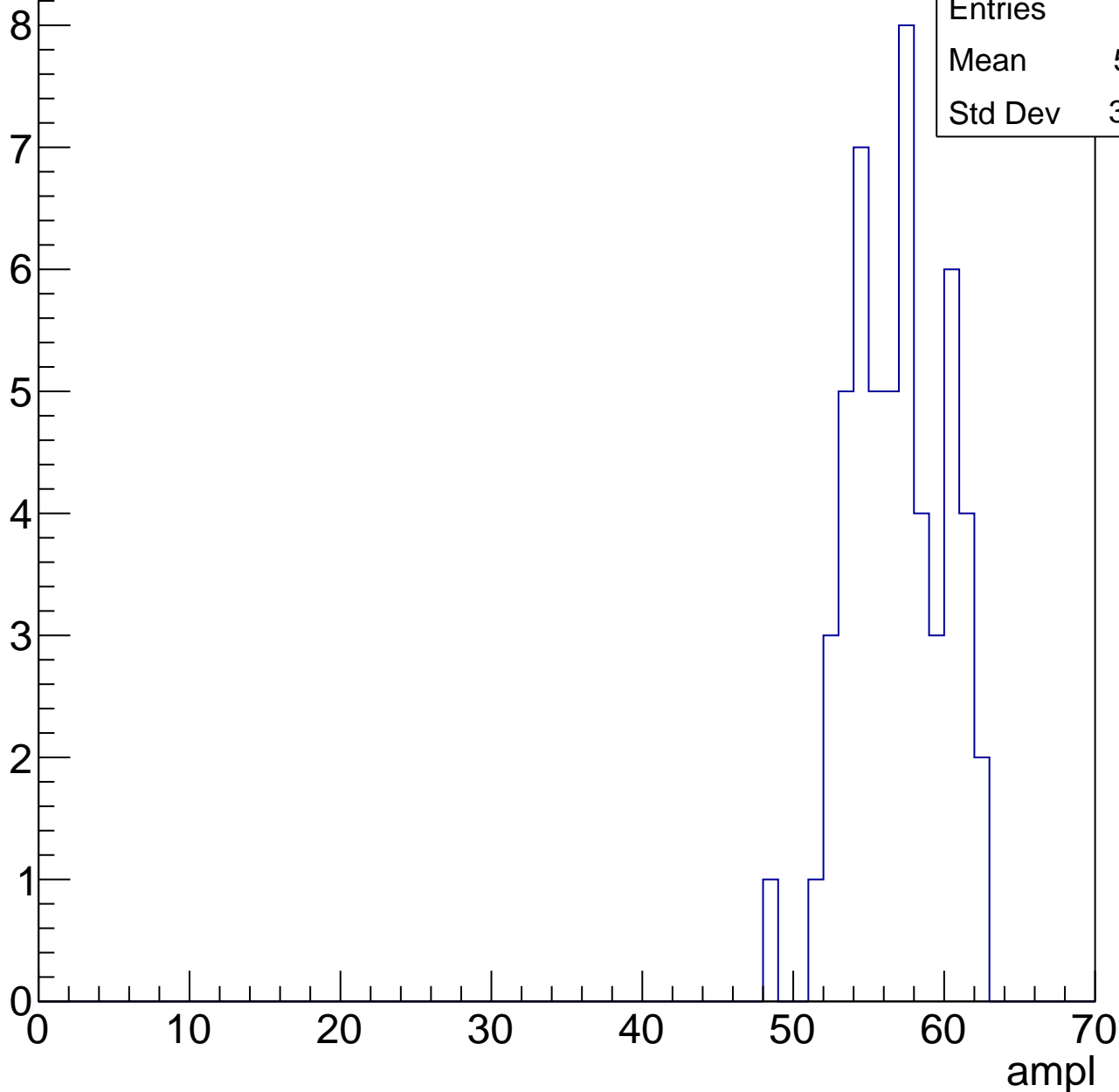


B0L001S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.41
Std Dev	3.118

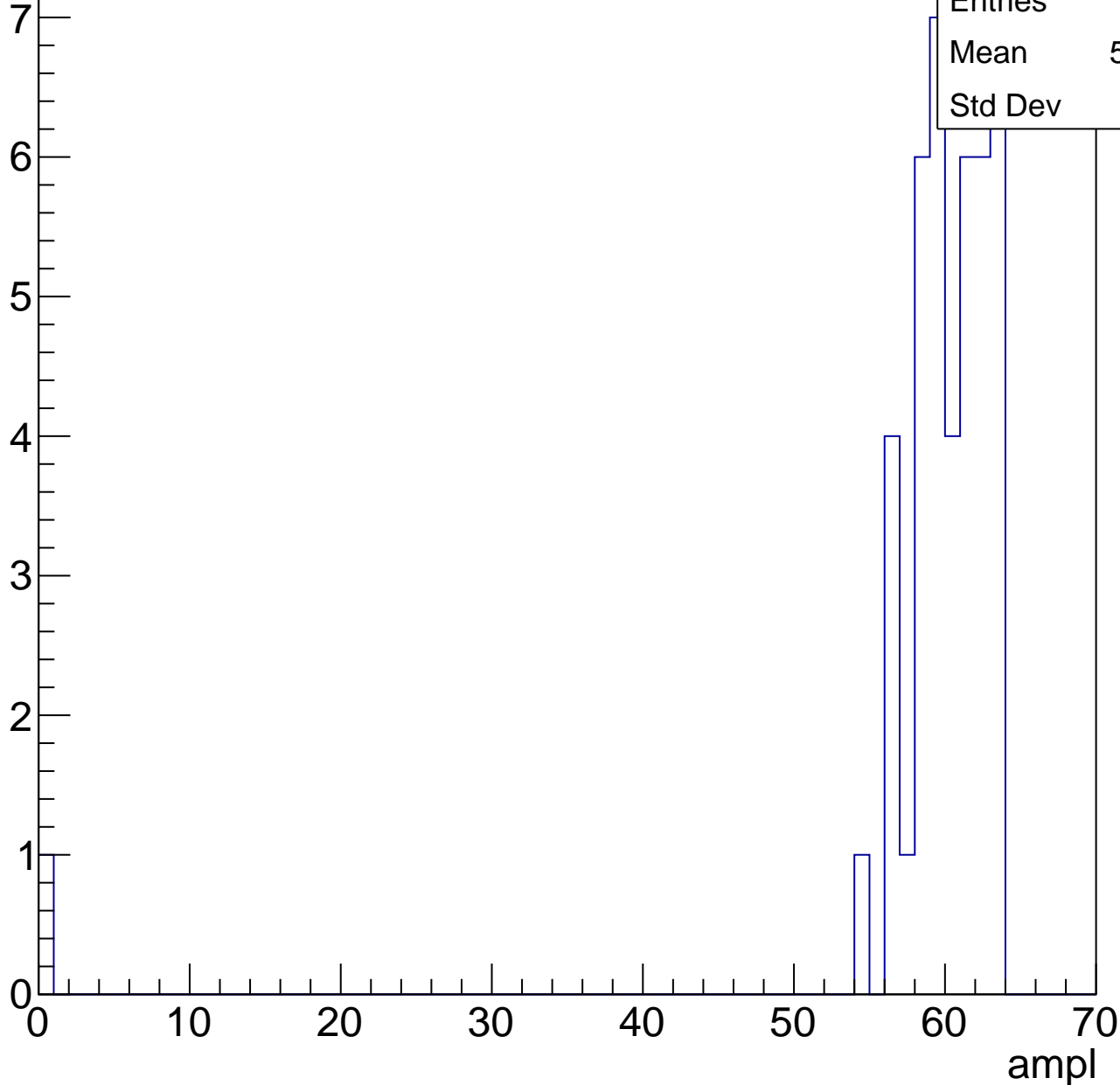


B0L001S, U15-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.49
Std Dev	9.32



B0L001S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch80, adc0

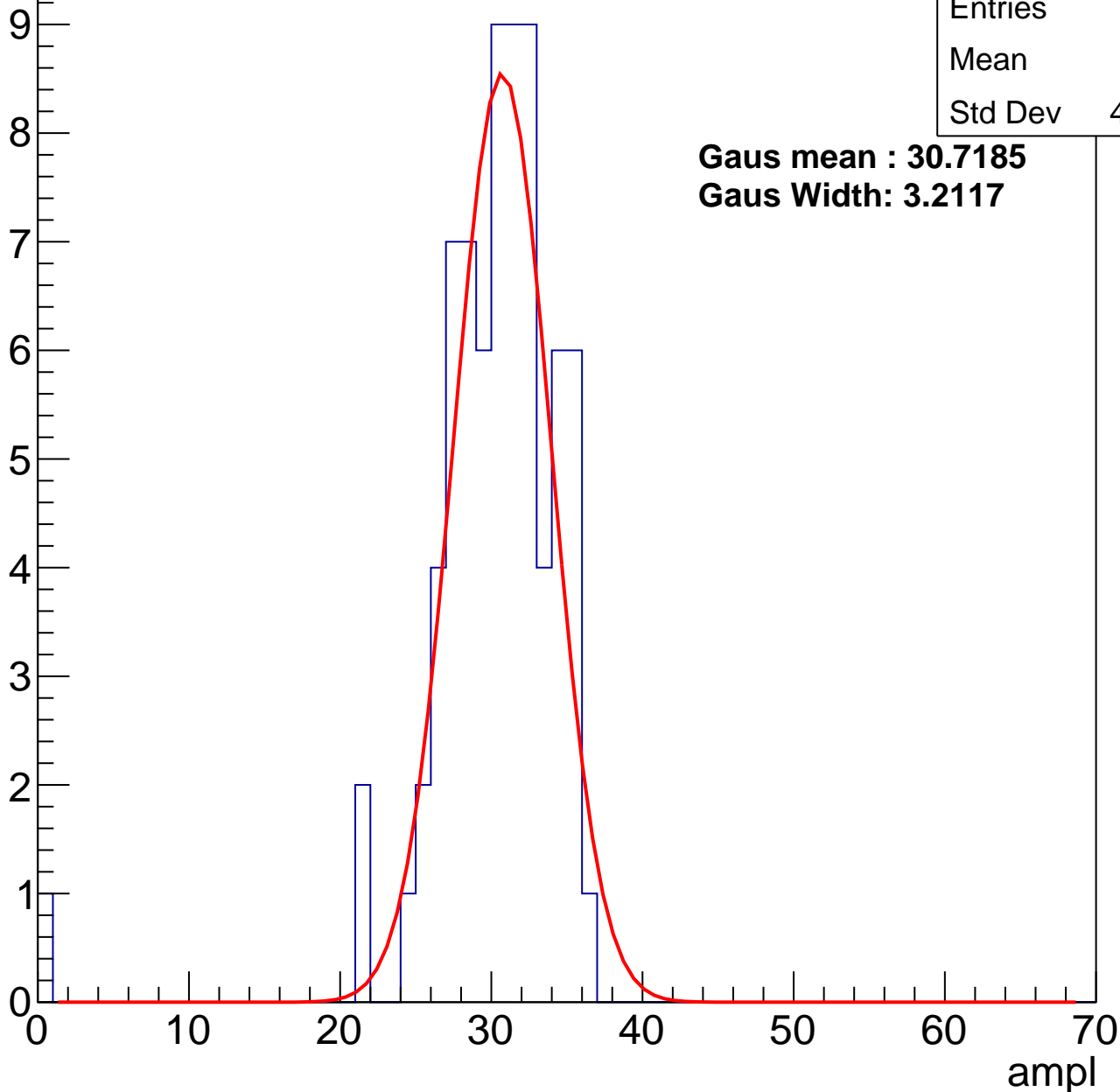
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.7
Std Dev	4.744

Gaus mean : 30.7185

Gaus Width: 3.2117



B0L001S, U15-ch80, adc1

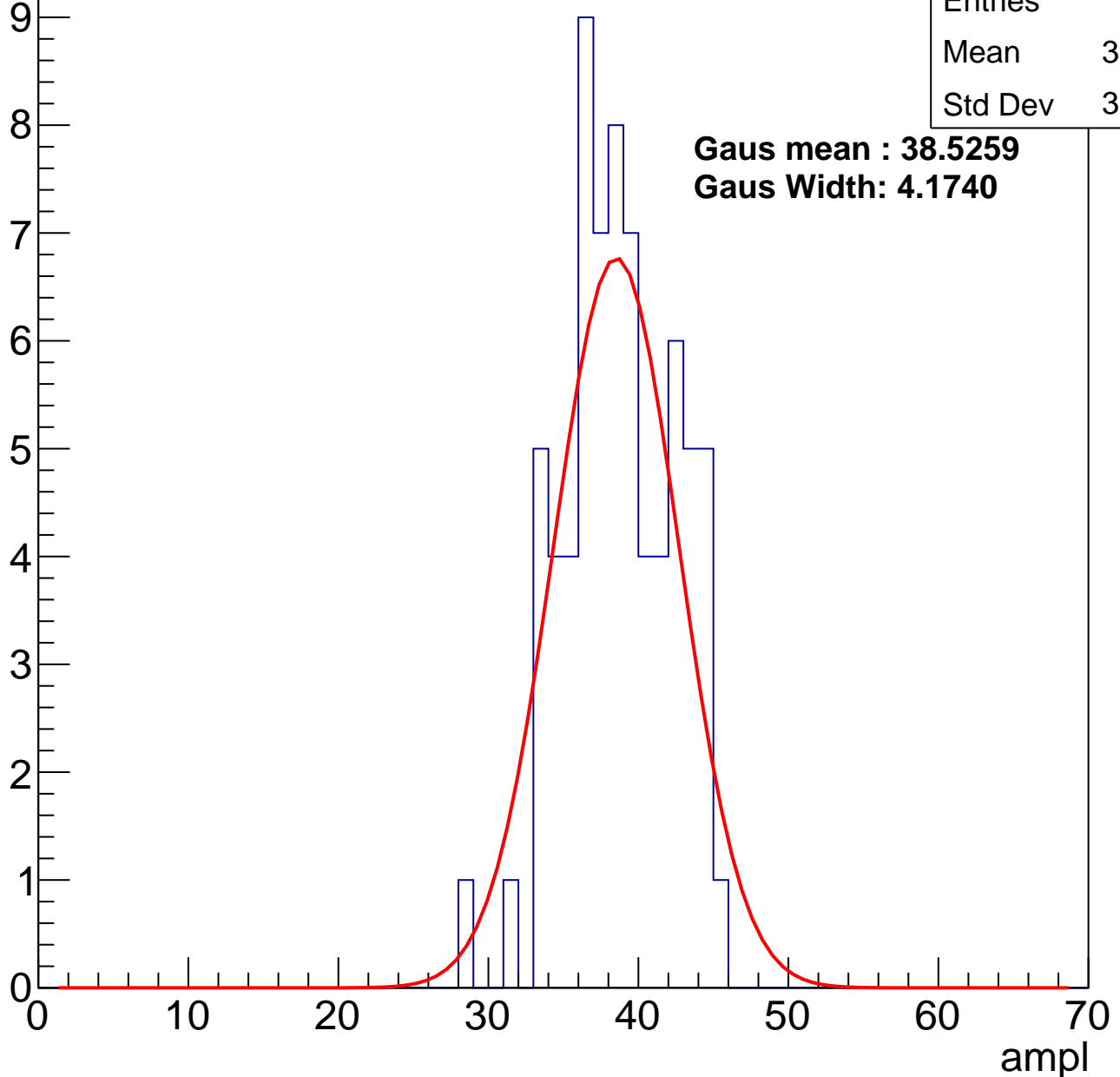
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.25
Std Dev	3.602

Gaus mean : 38.5259

Gaus Width: 4.1740



B0L001S, U15-ch80, adc2

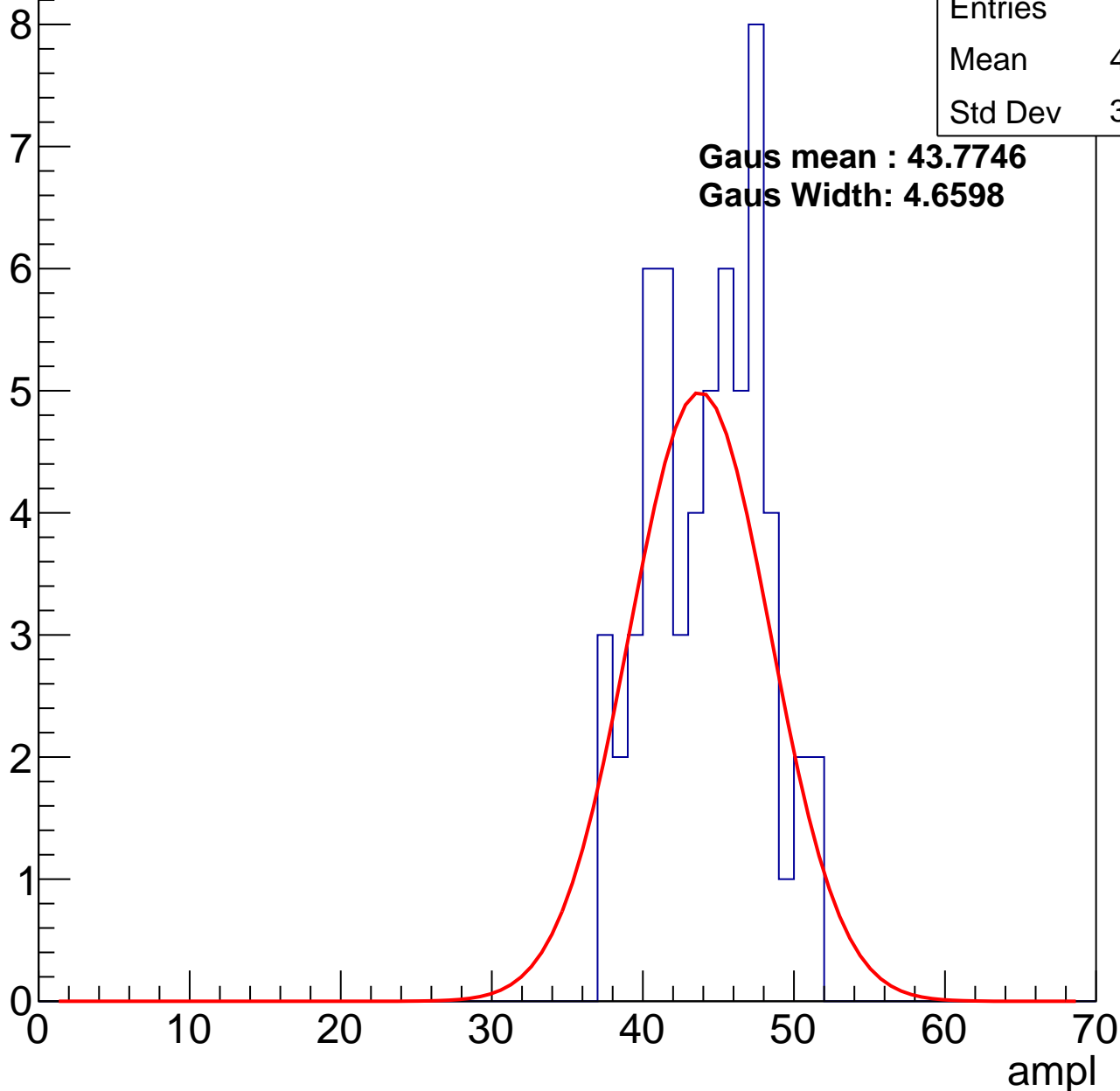
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.78
Std Dev	3.684

Gaus mean : 43.7746

Gaus Width: 4.6598

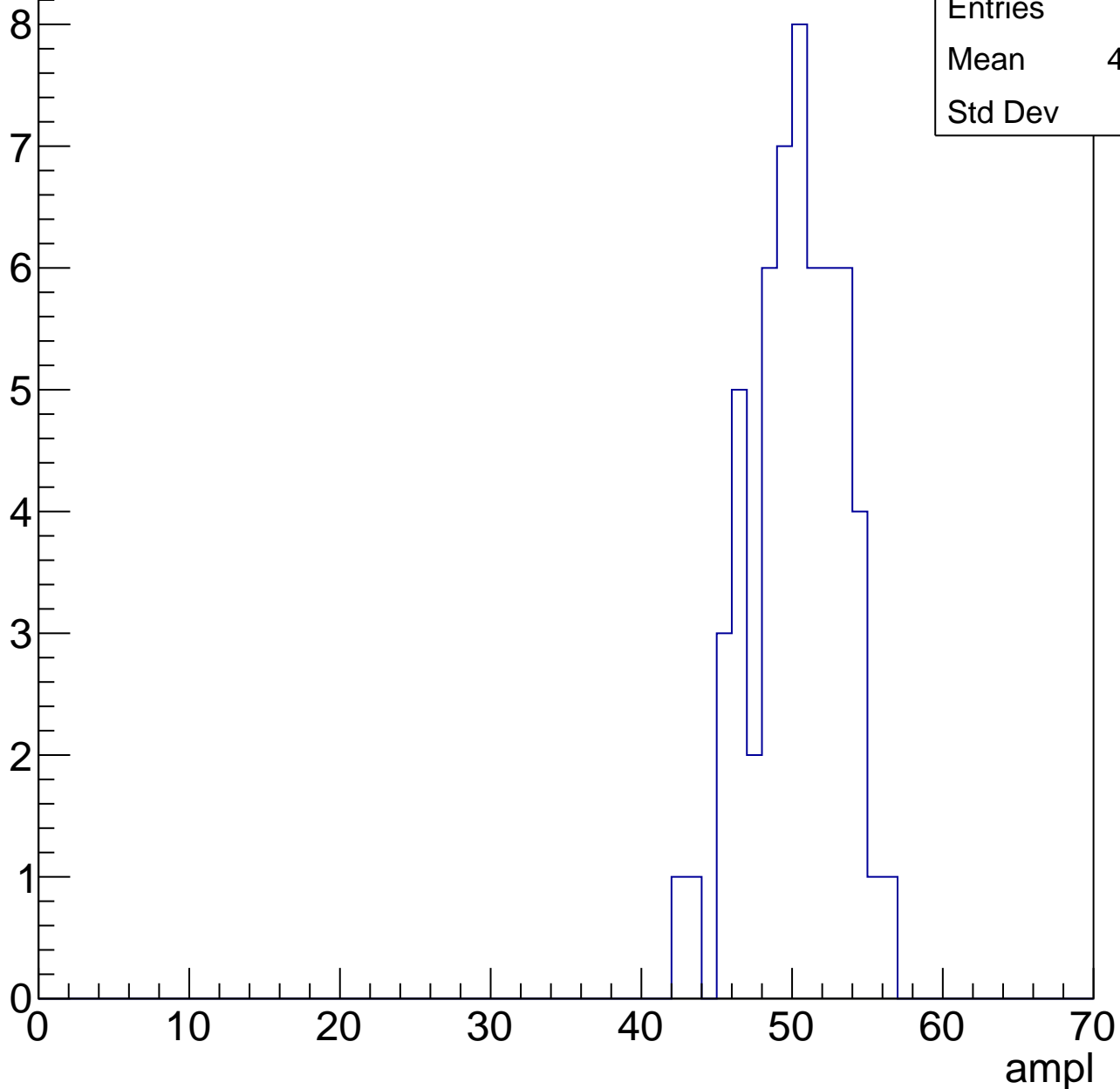


B0L001S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

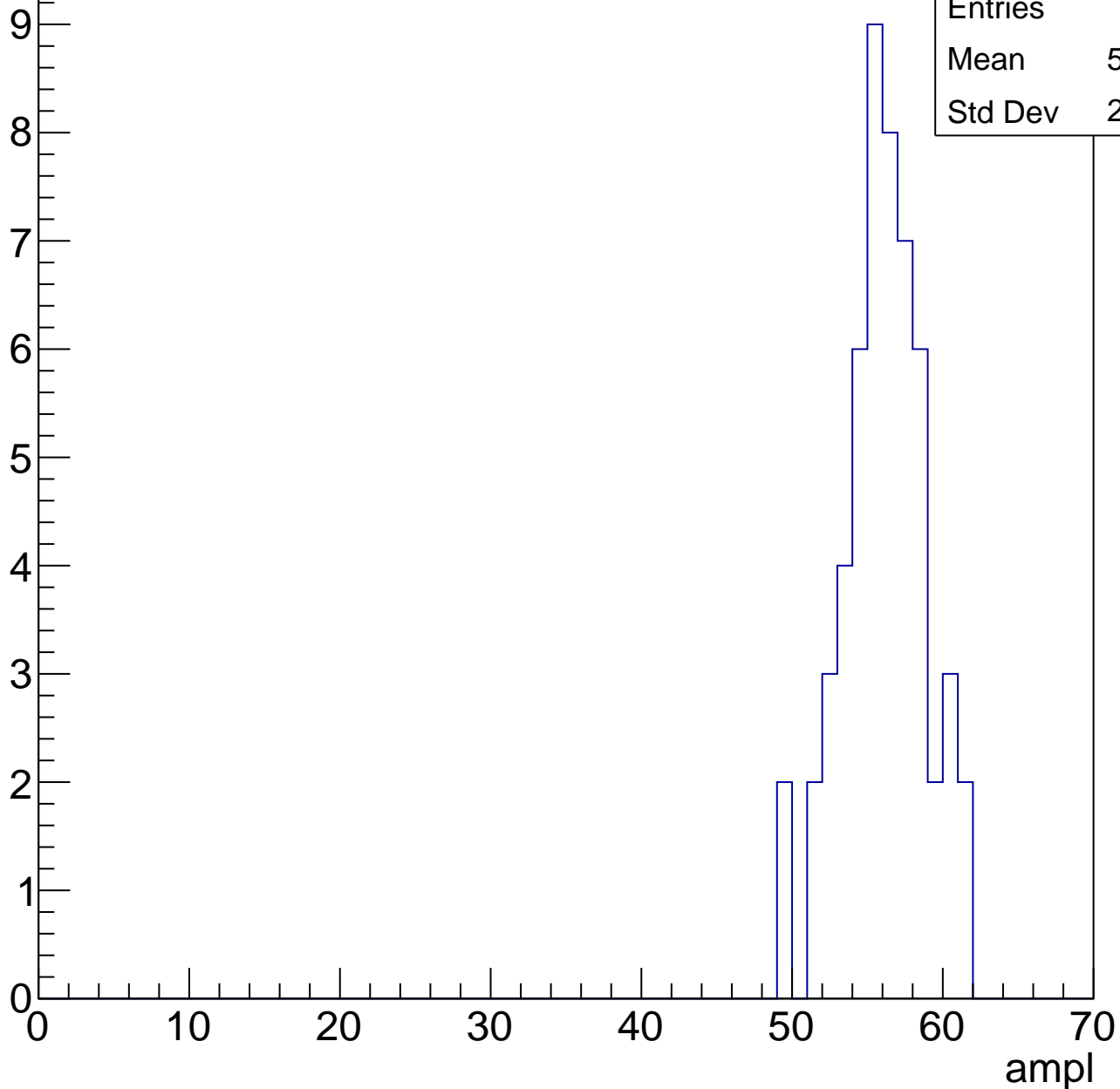
Entries	57
Mean	49.79
Std Dev	3.03



B0L001S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

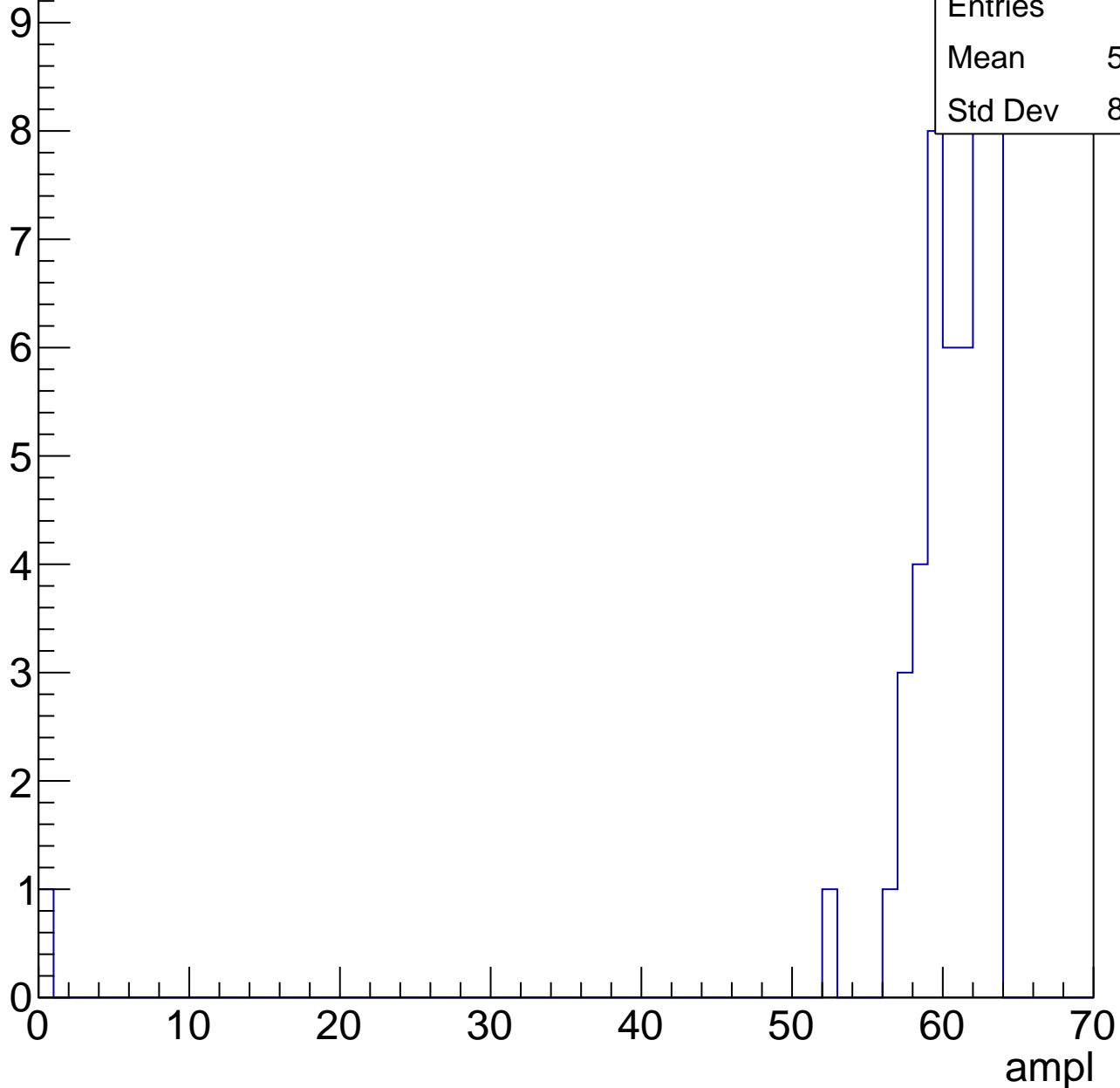


Entries	54
Mean	55.59
Std Dev	2.732

B0L001S, U15-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch81, adc0

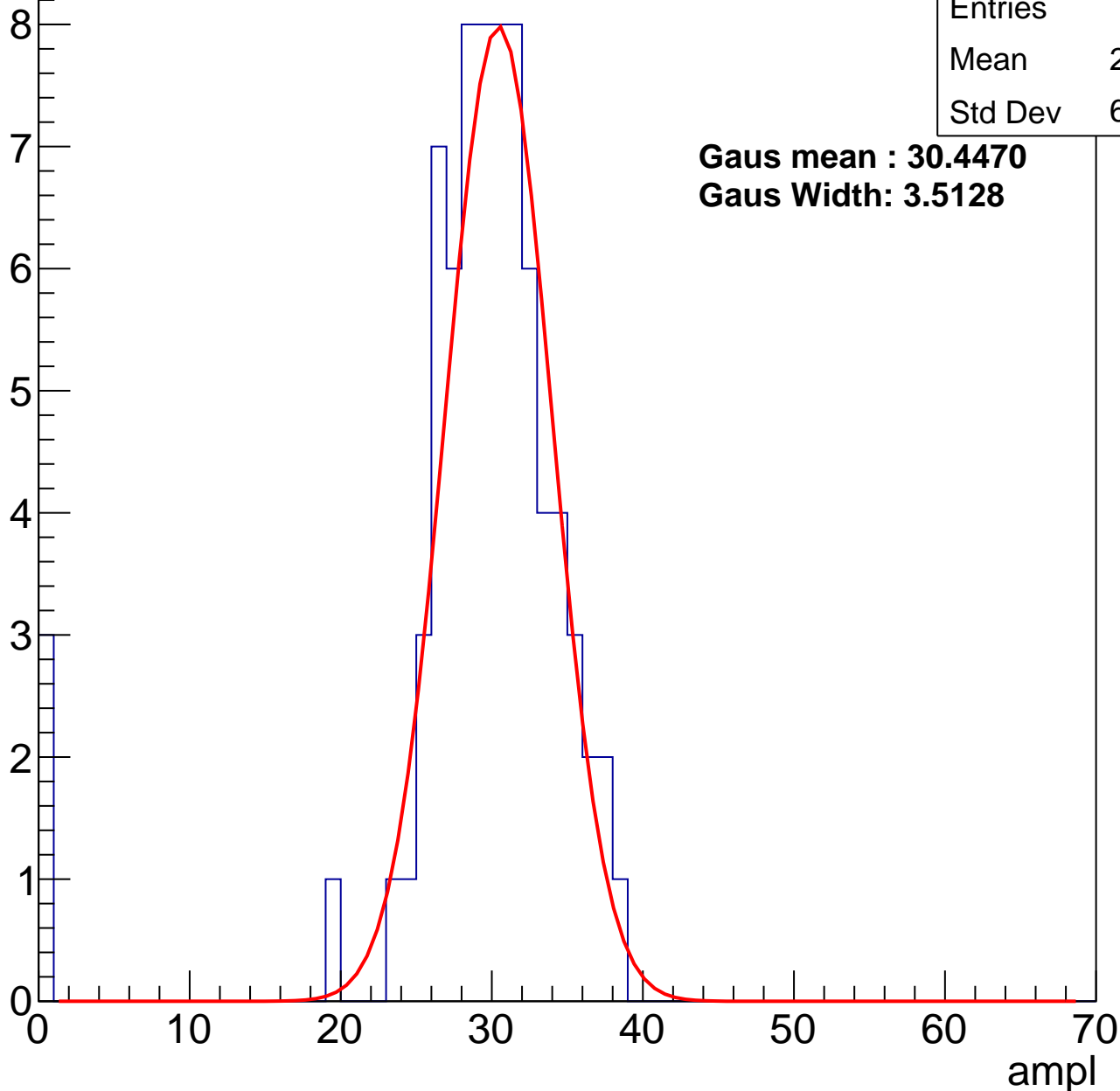
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	28.66
Std Dev	6.778

Gaus mean : 30.4470

Gaus Width: 3.5128



B0L001S, U15-ch81, adc1

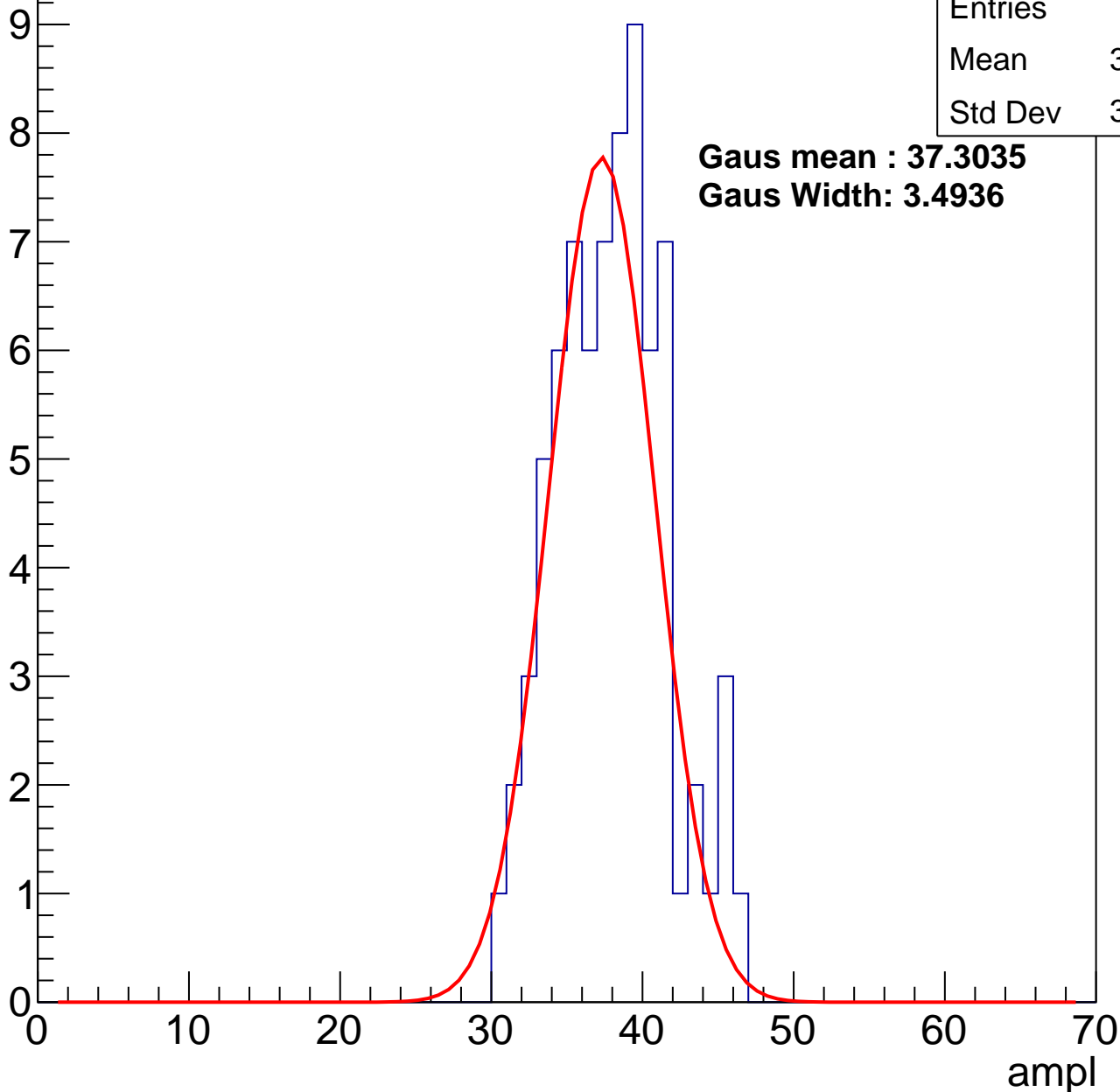
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	37.49
Std Dev	3.605

Gaus mean : 37.3035

Gaus Width: 3.4936



B0L001S, U15-ch81, adc2

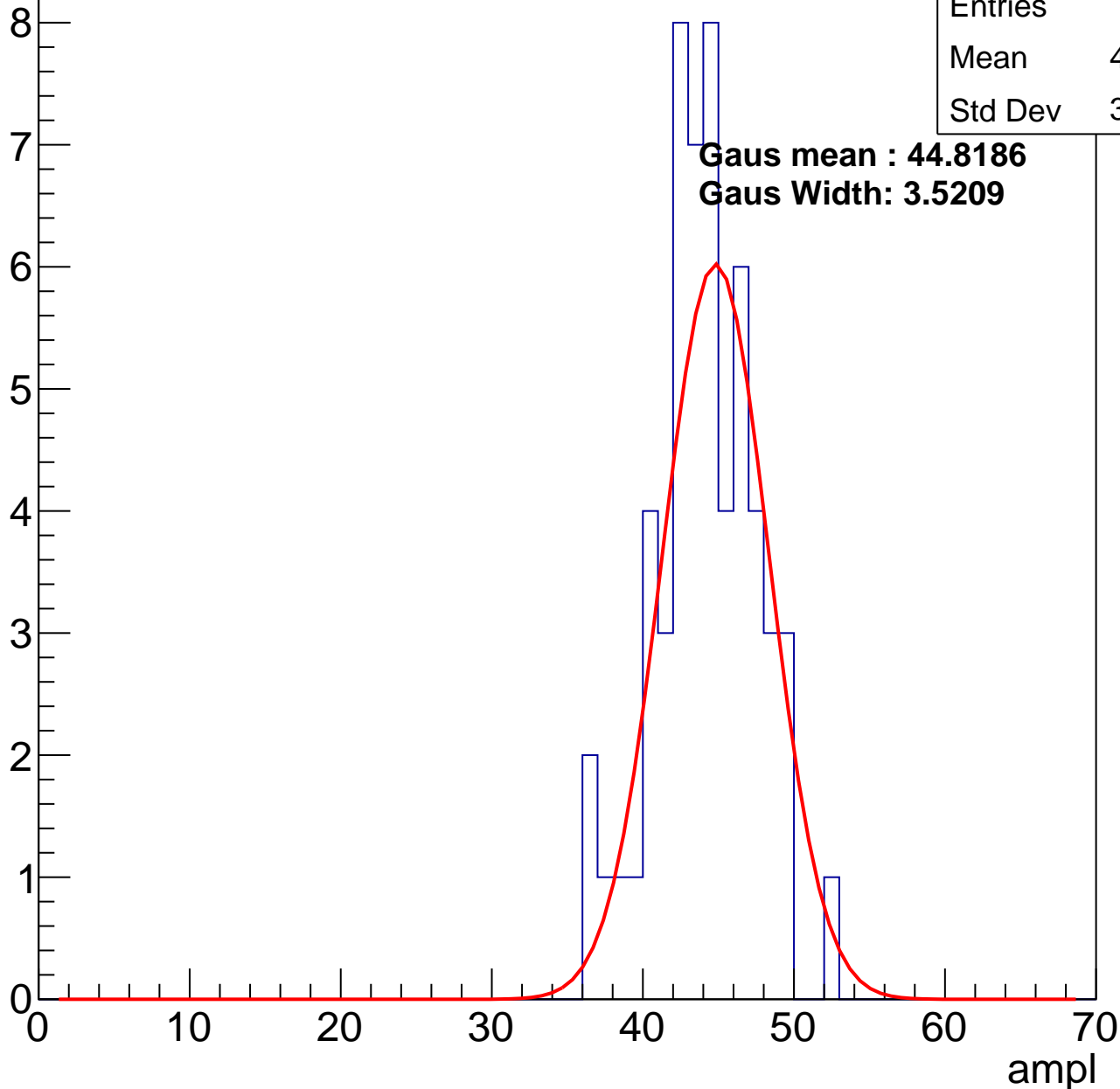
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.66
Std Dev	3.307

Gaus mean : 44.8186

Gaus Width: 3.5209

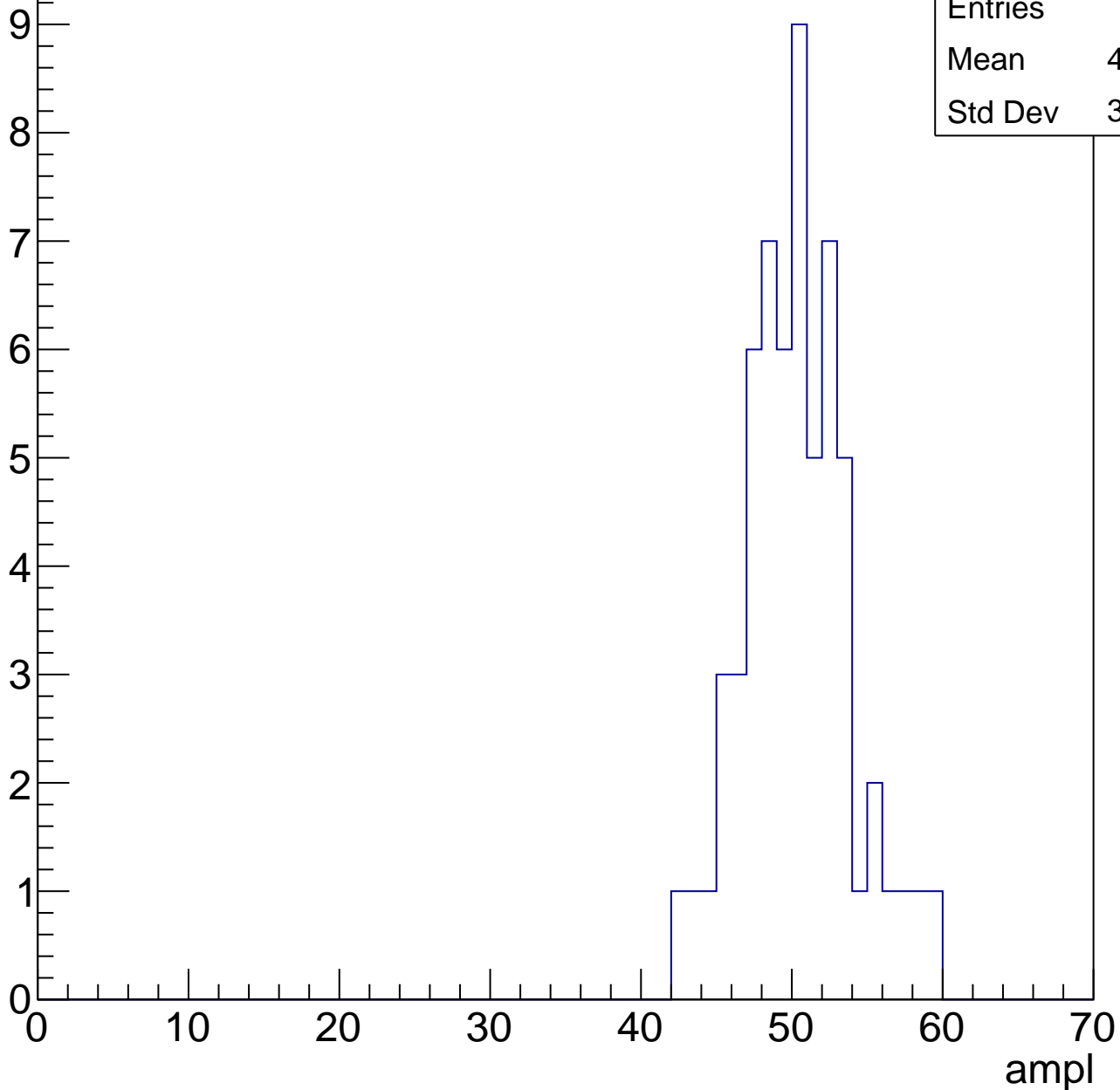


B0L001S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	49.87
Std Dev	3.466

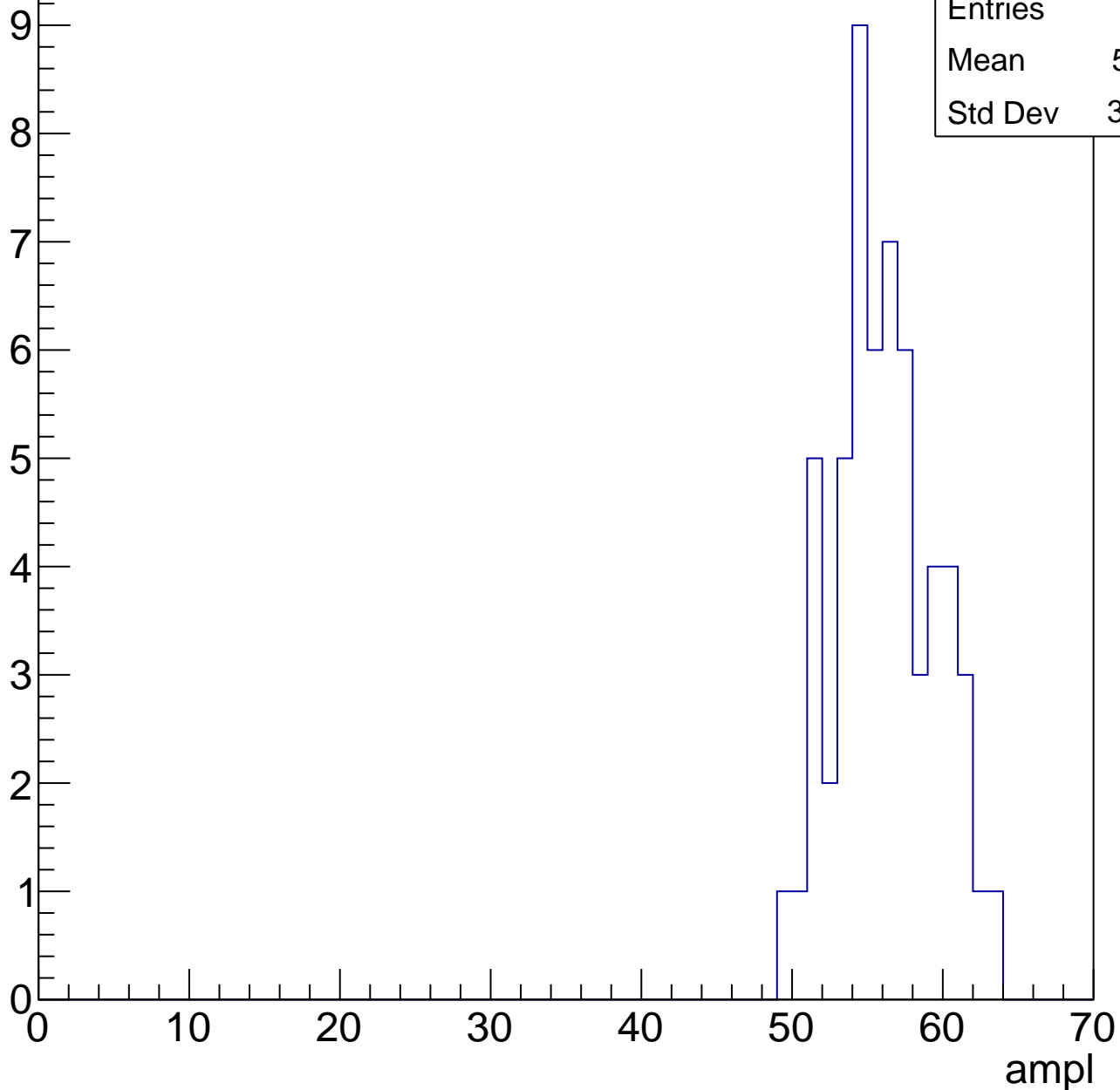


B0L001S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	55.71
Std Dev	3.222

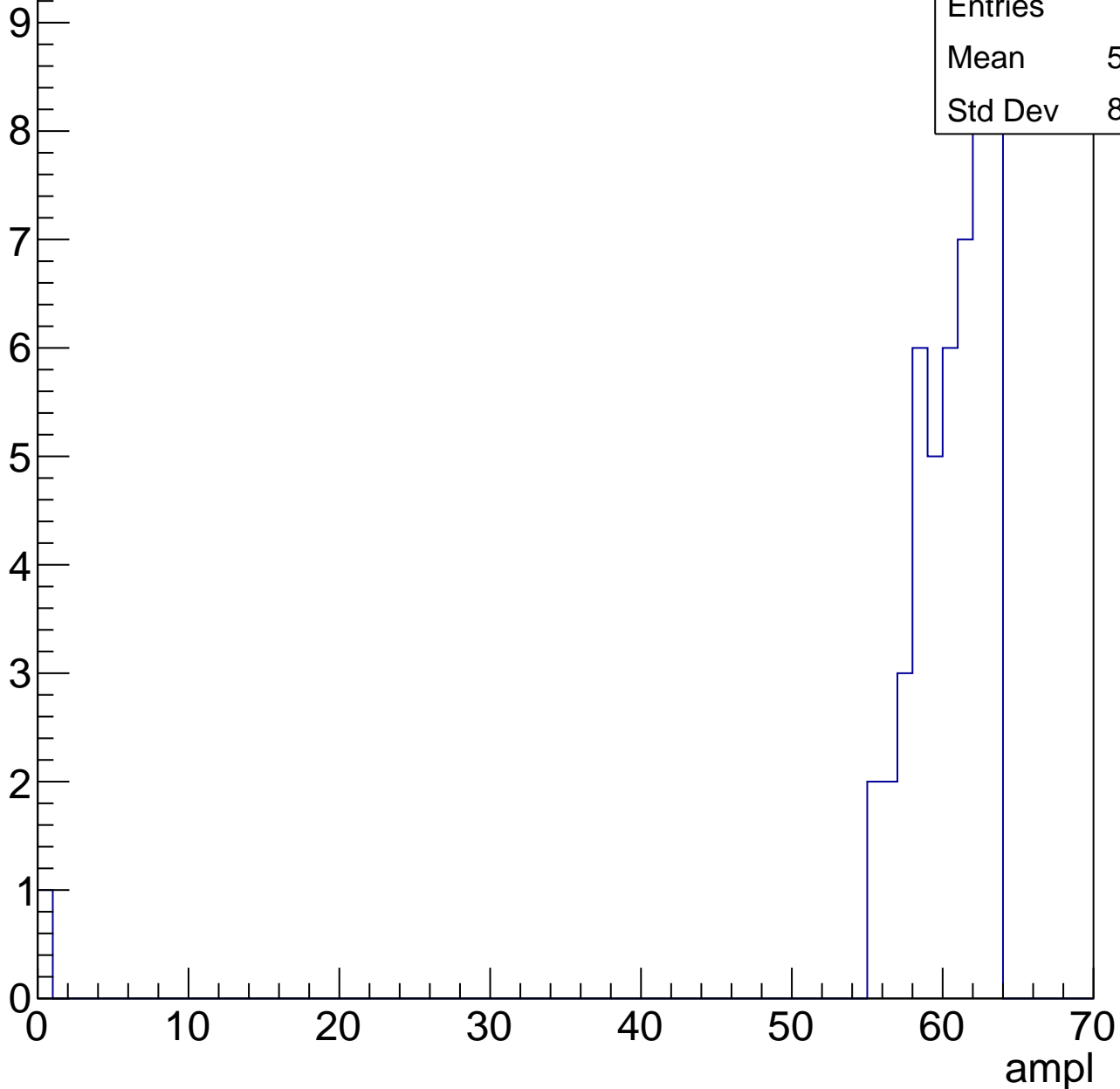


B0L001S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.88
Std Dev	8.796



B0L001S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch82, adc0

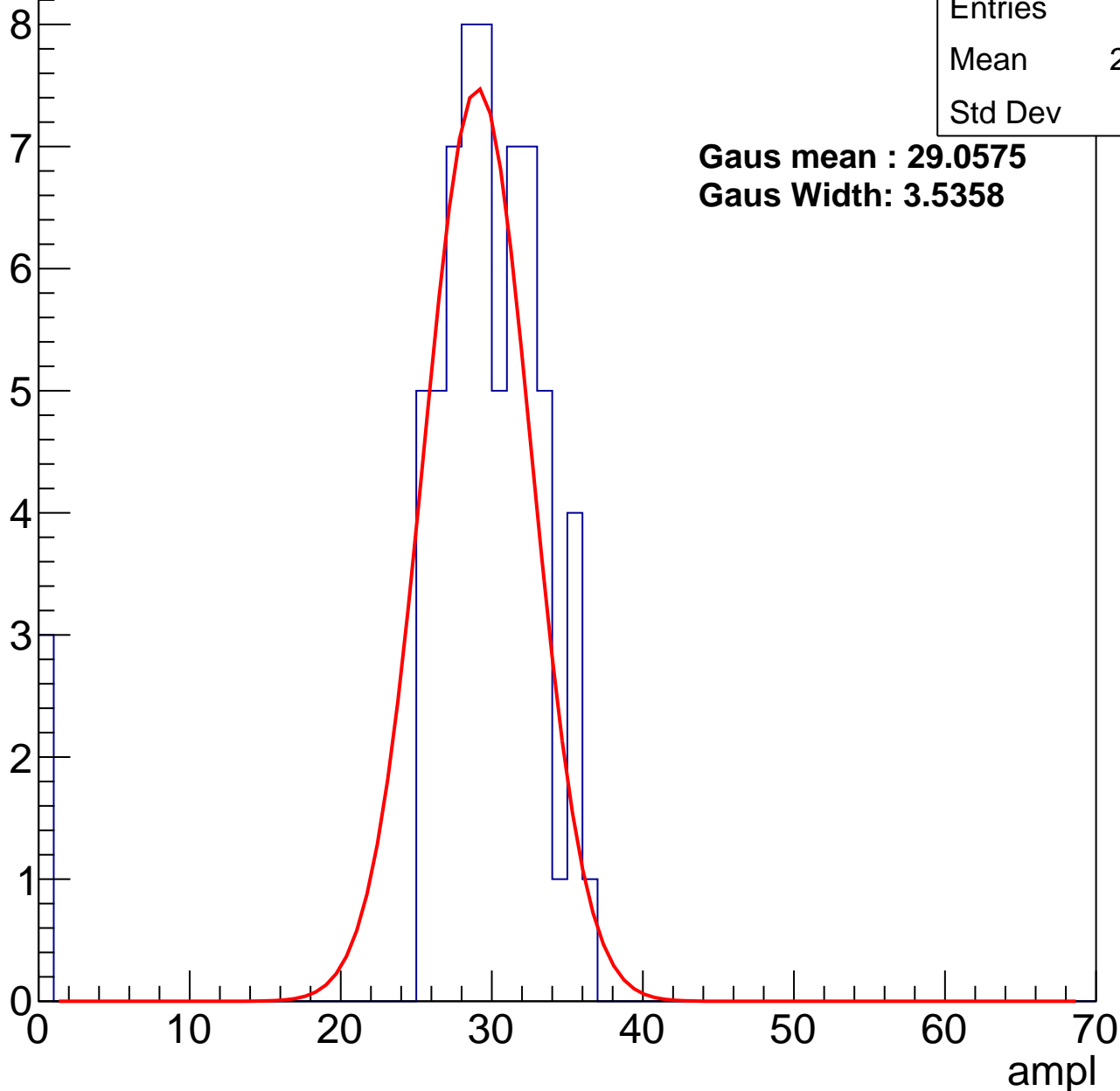
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	28.27
Std Dev	6.79

Gaus mean : 29.0575

Gaus Width: 3.5358



B0L001S, U15-ch82, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	36.15
Std Dev	3.607

Gaus mean : 36.0977

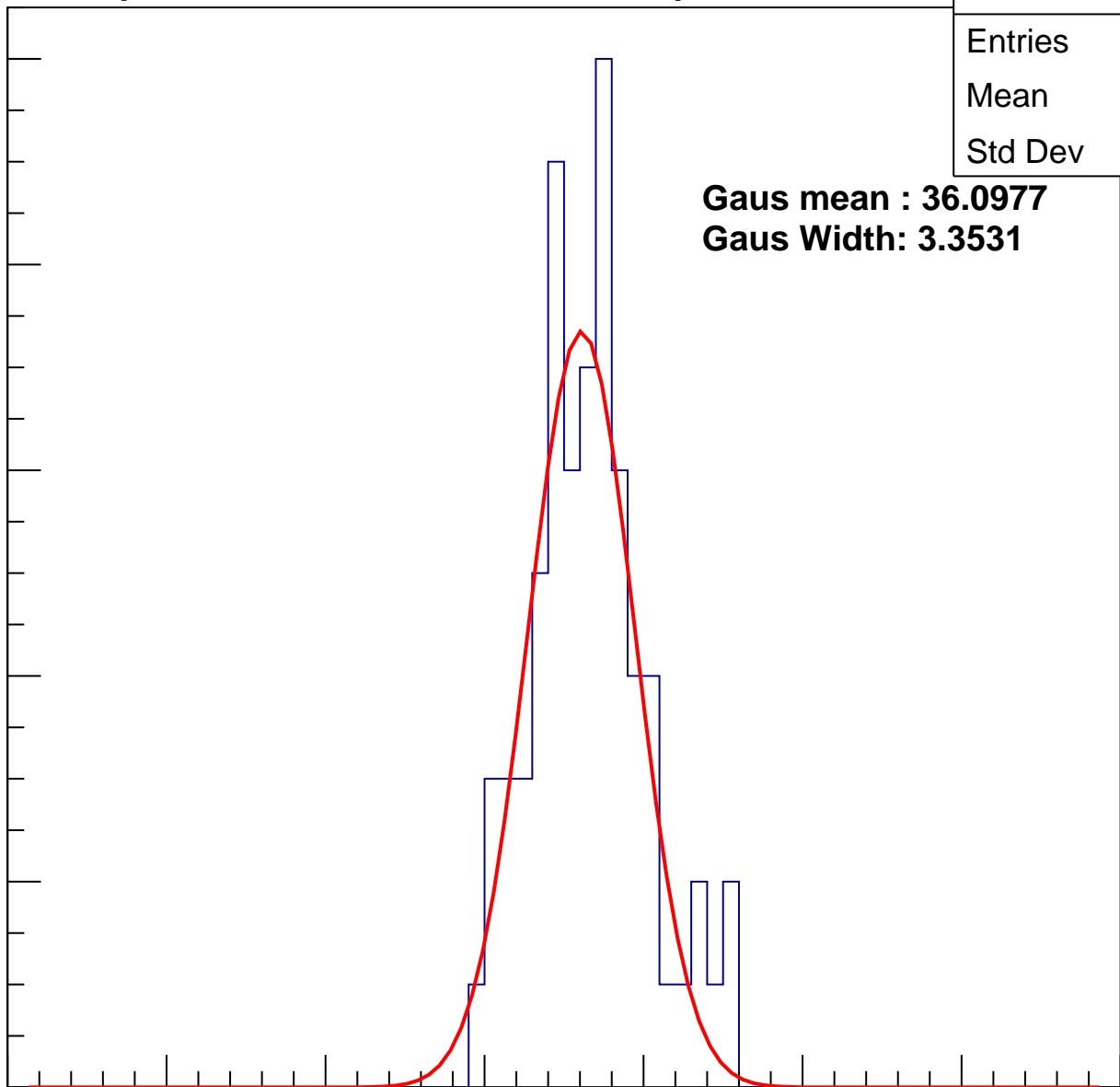
Gaus Width: 3.3531

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch82, adc2

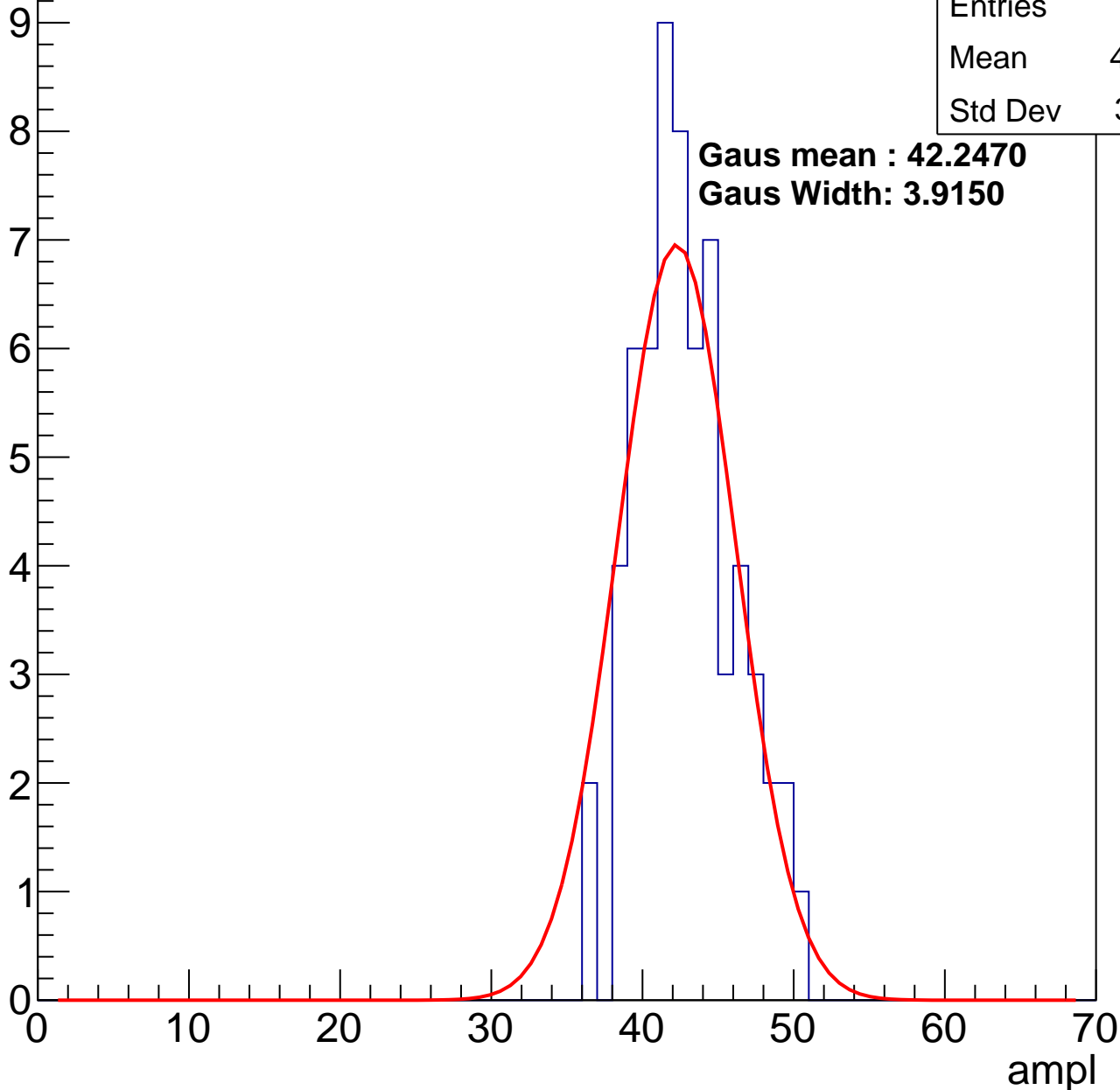
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.43
Std Dev	3.201

Gaus mean : 42.2470

Gaus Width: 3.9150

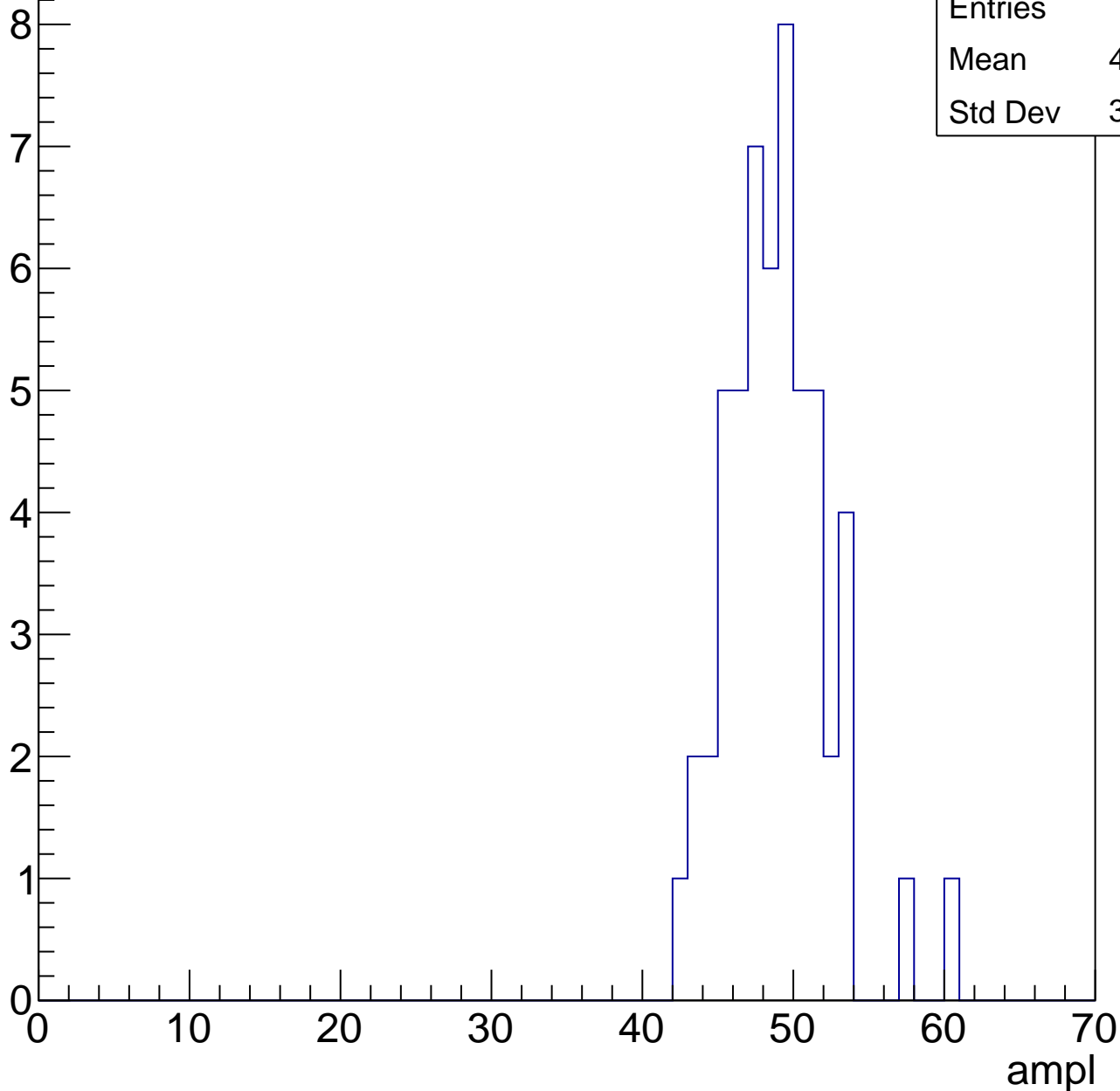


B0L001S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	48.48
Std Dev	3.354

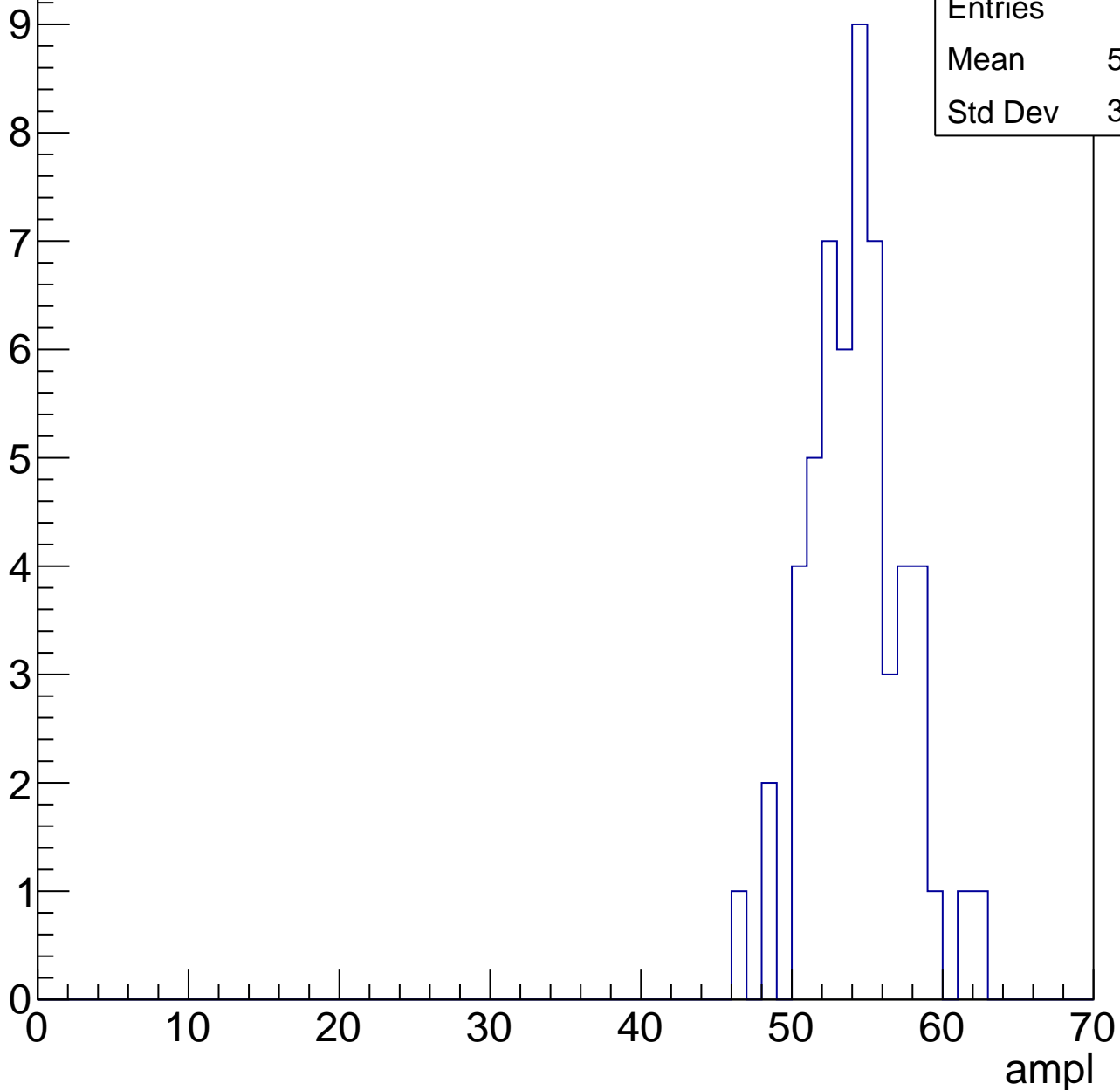


B0L001S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	53.82
Std Dev	3.116

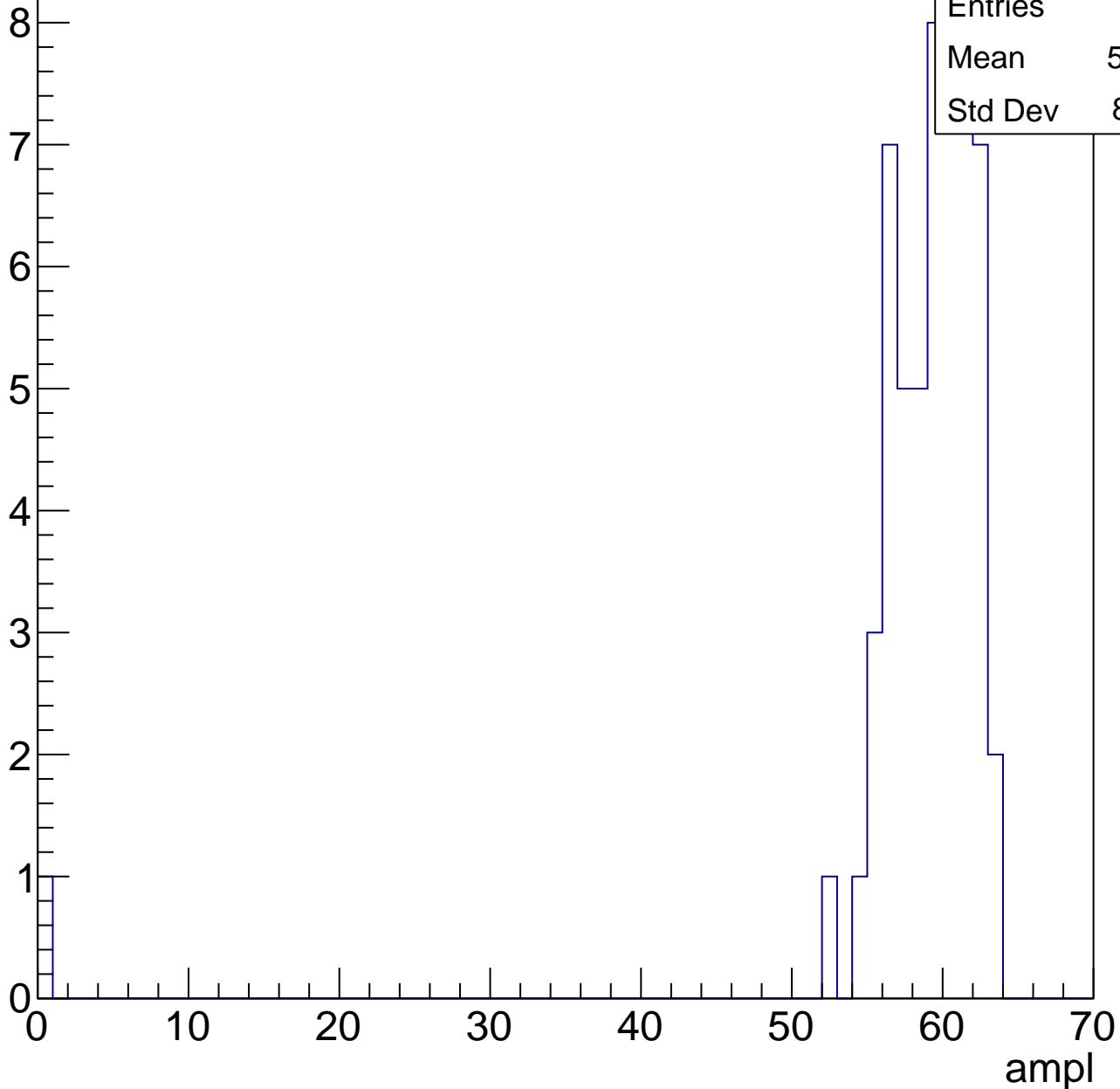


B0L001S, U15-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.82
Std Dev	8.181

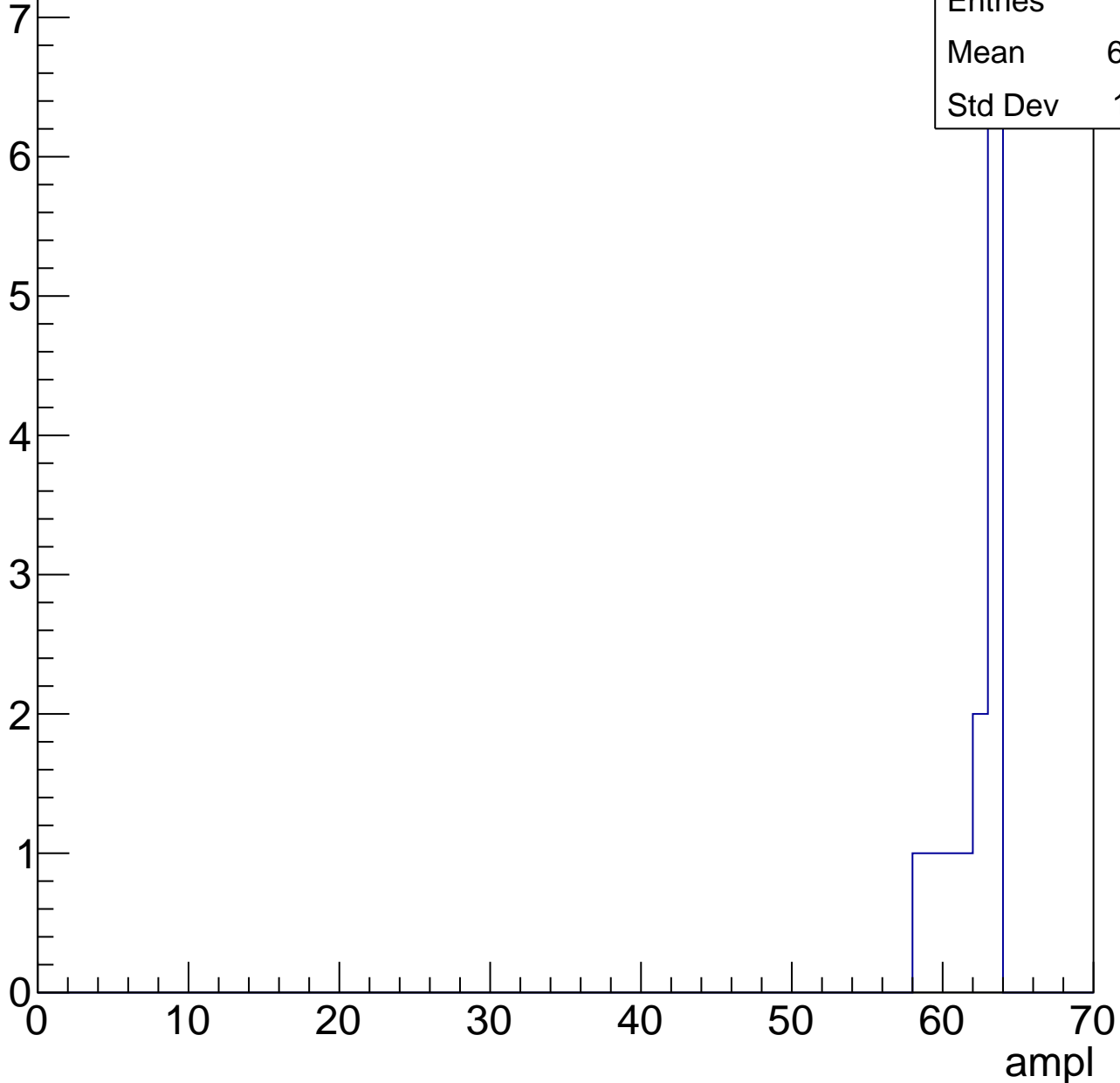


B0L001S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

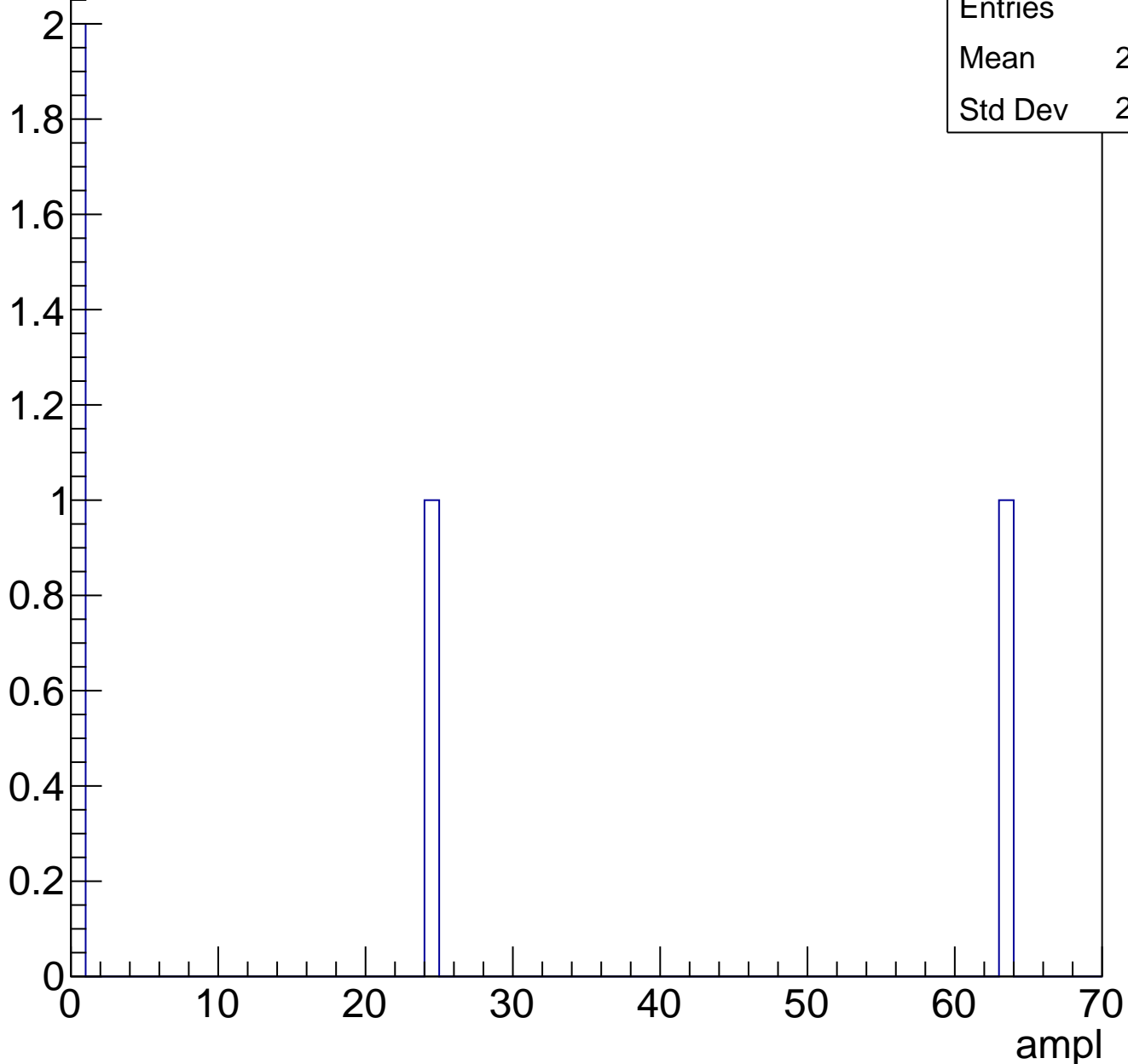
Entries	13
Mean	61.77
Std Dev	1.671



B0L001S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	21.75
Std Dev	25.75

B0L001S, U15-ch83, adc0

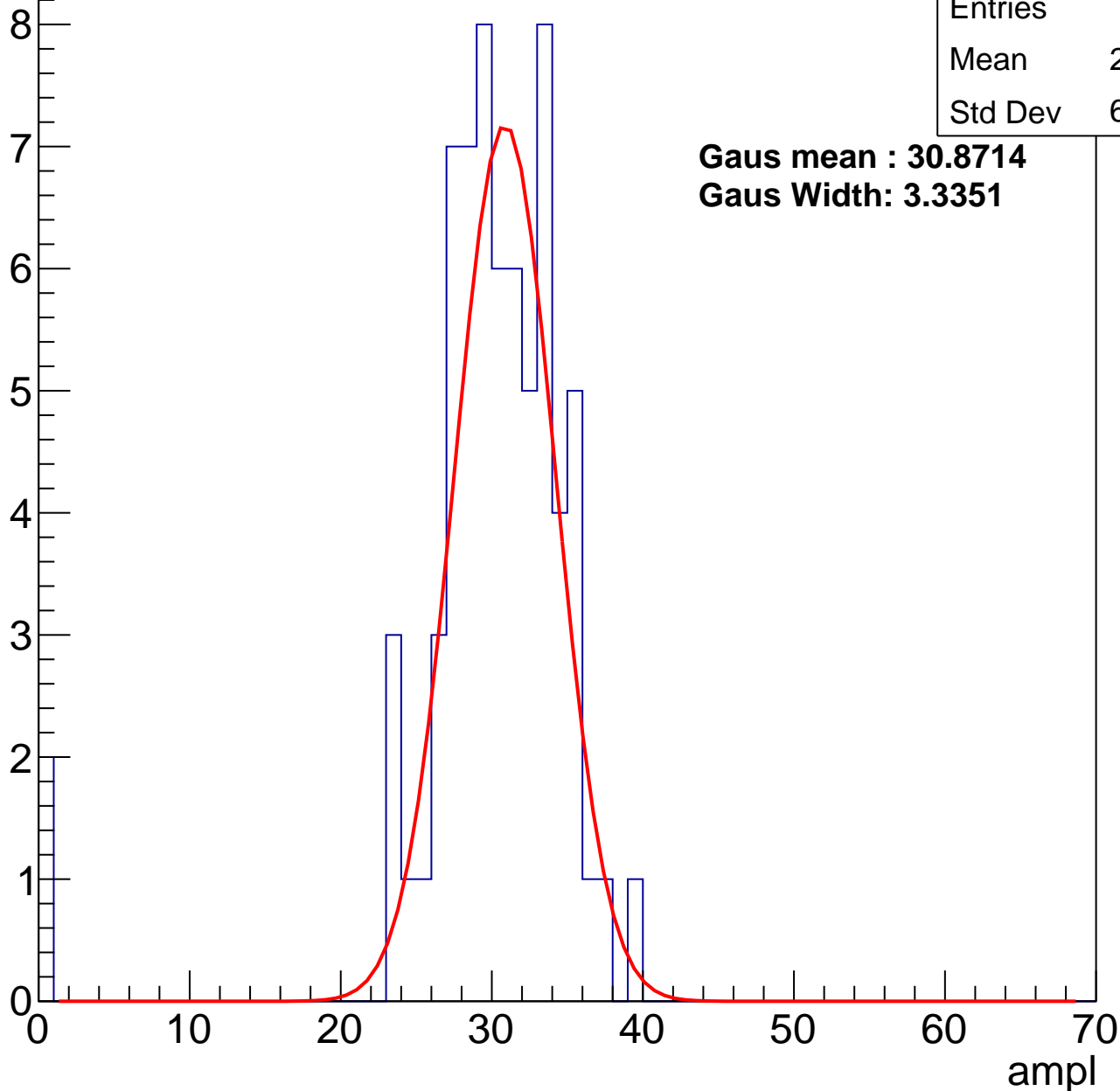
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	29.36
Std Dev	6.115

Gaus mean : 30.8714

Gaus Width: 3.3351



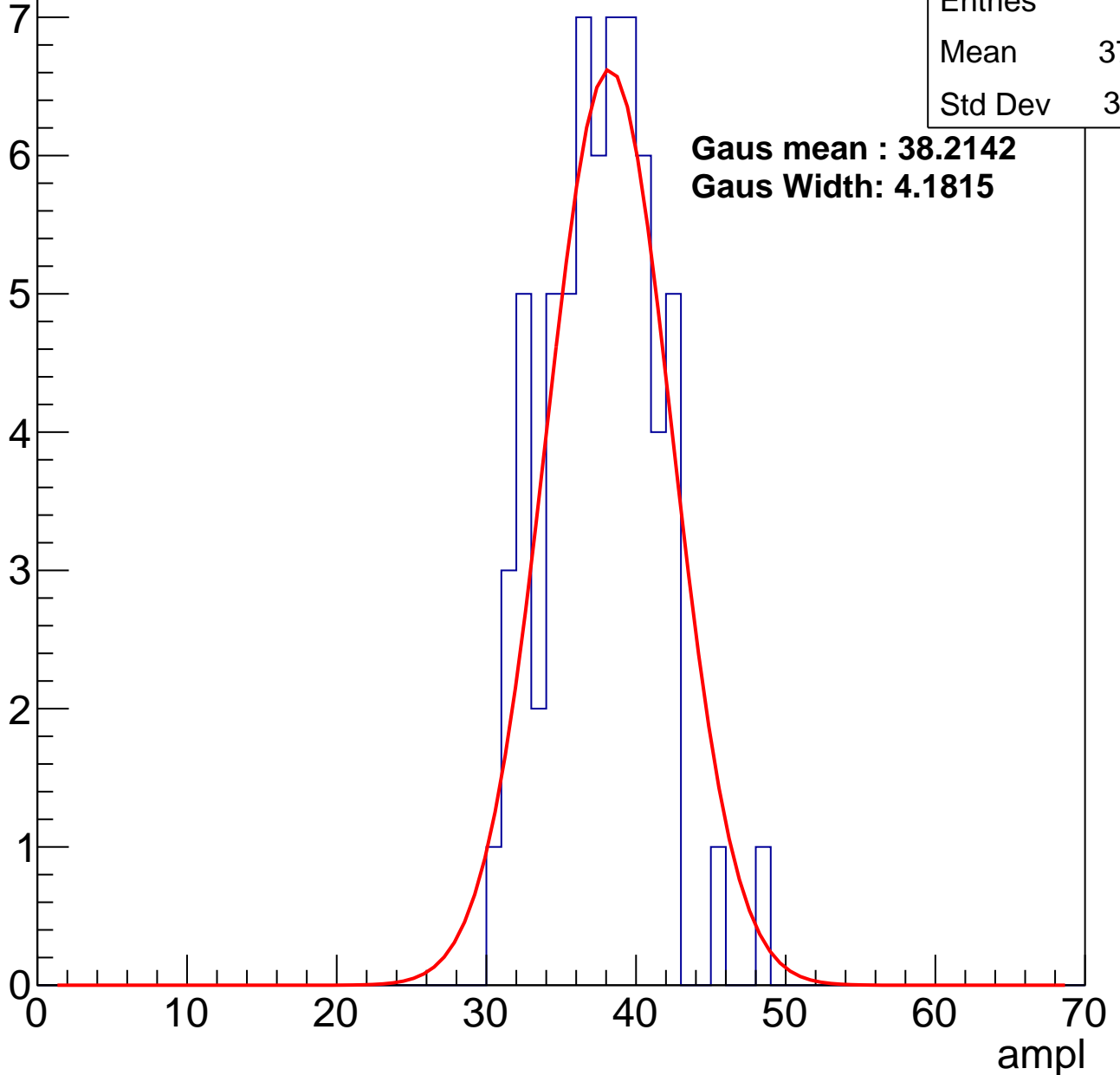
B0L001S, U15-ch83, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.14
Std Dev	3.611

Gaus mean : 38.2142
Gaus Width: 4.1815



B0L001S, U15-ch83, adc2

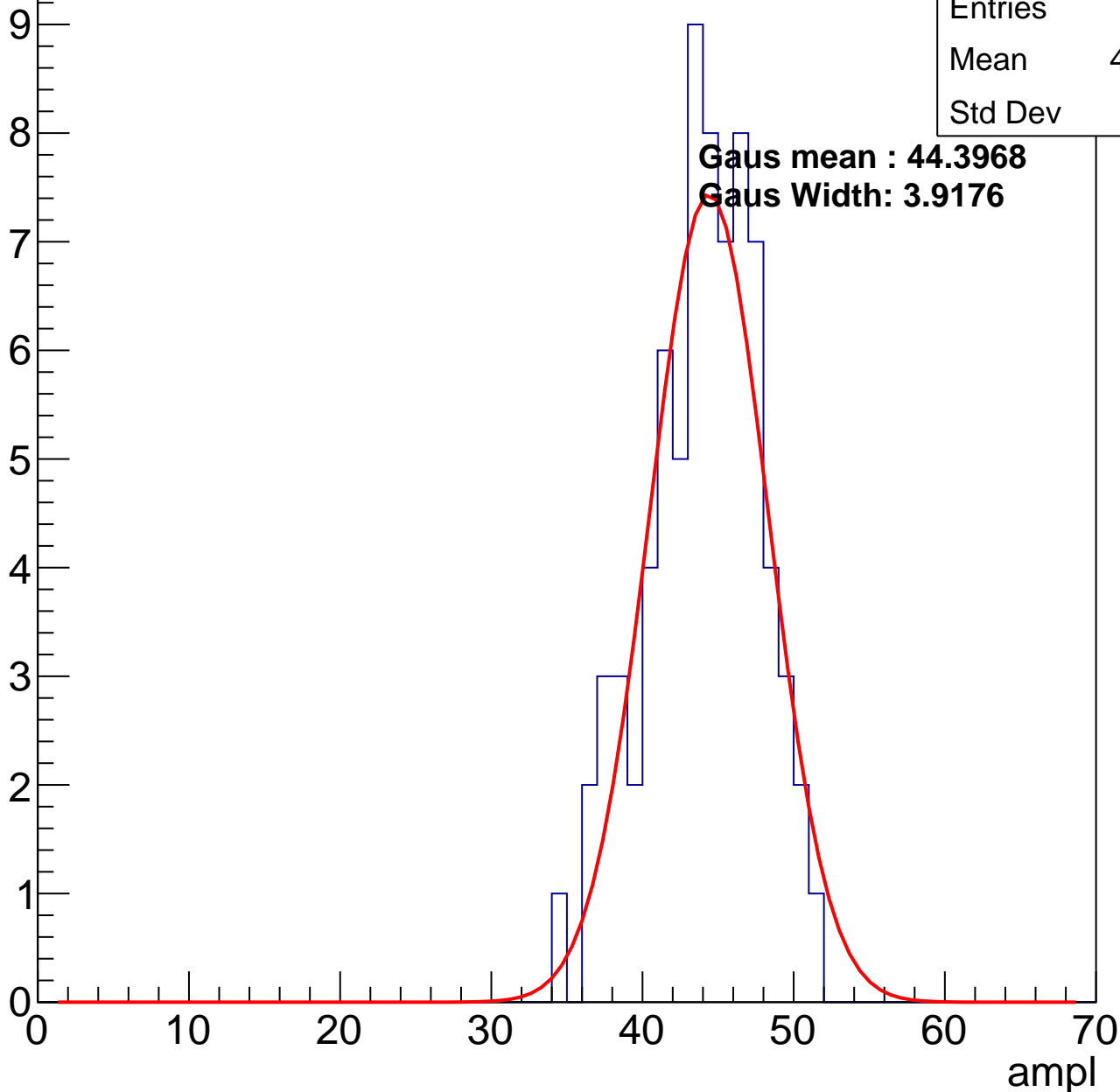
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.55
Std Dev	3.7

Gaus mean : 44.3968

Gaus Width: 3.9176

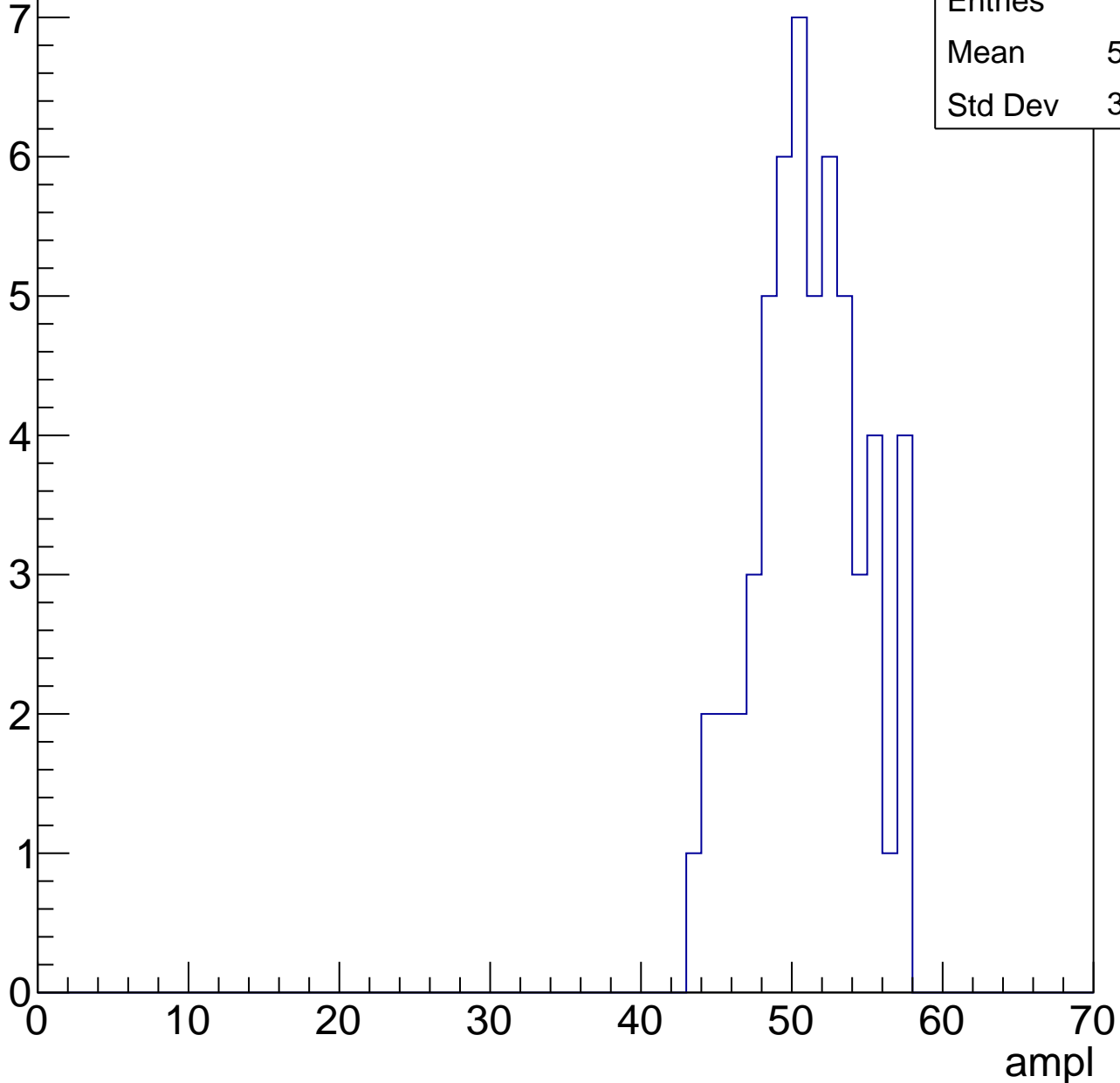


B0L001S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

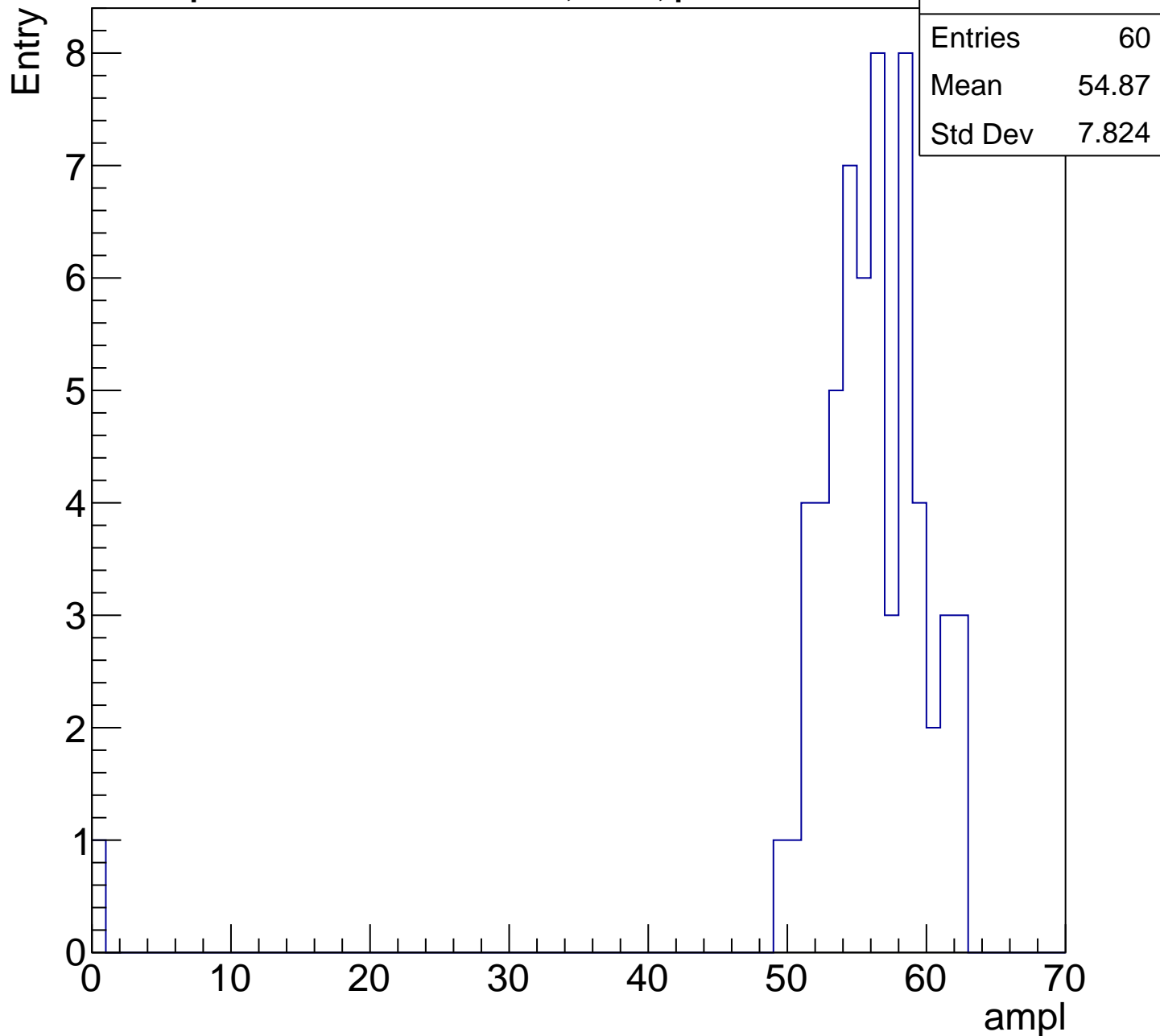
Entry

Entries	56
Mean	50.64
Std Dev	3.502



B0L001S, U15-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

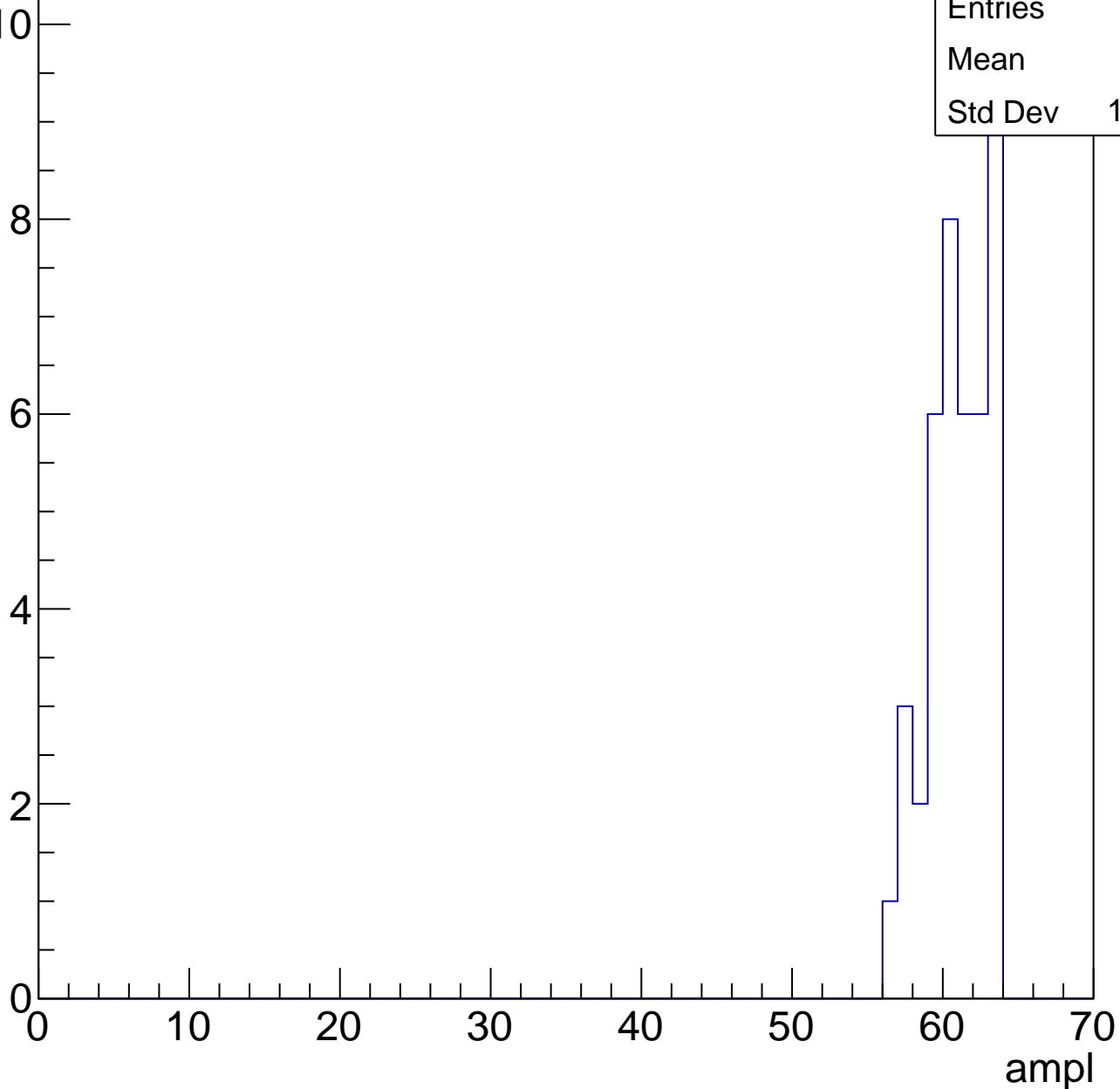


B0L001S, U15-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	60.6
Std Dev	1.965



B0L001S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U15-ch84, adc0

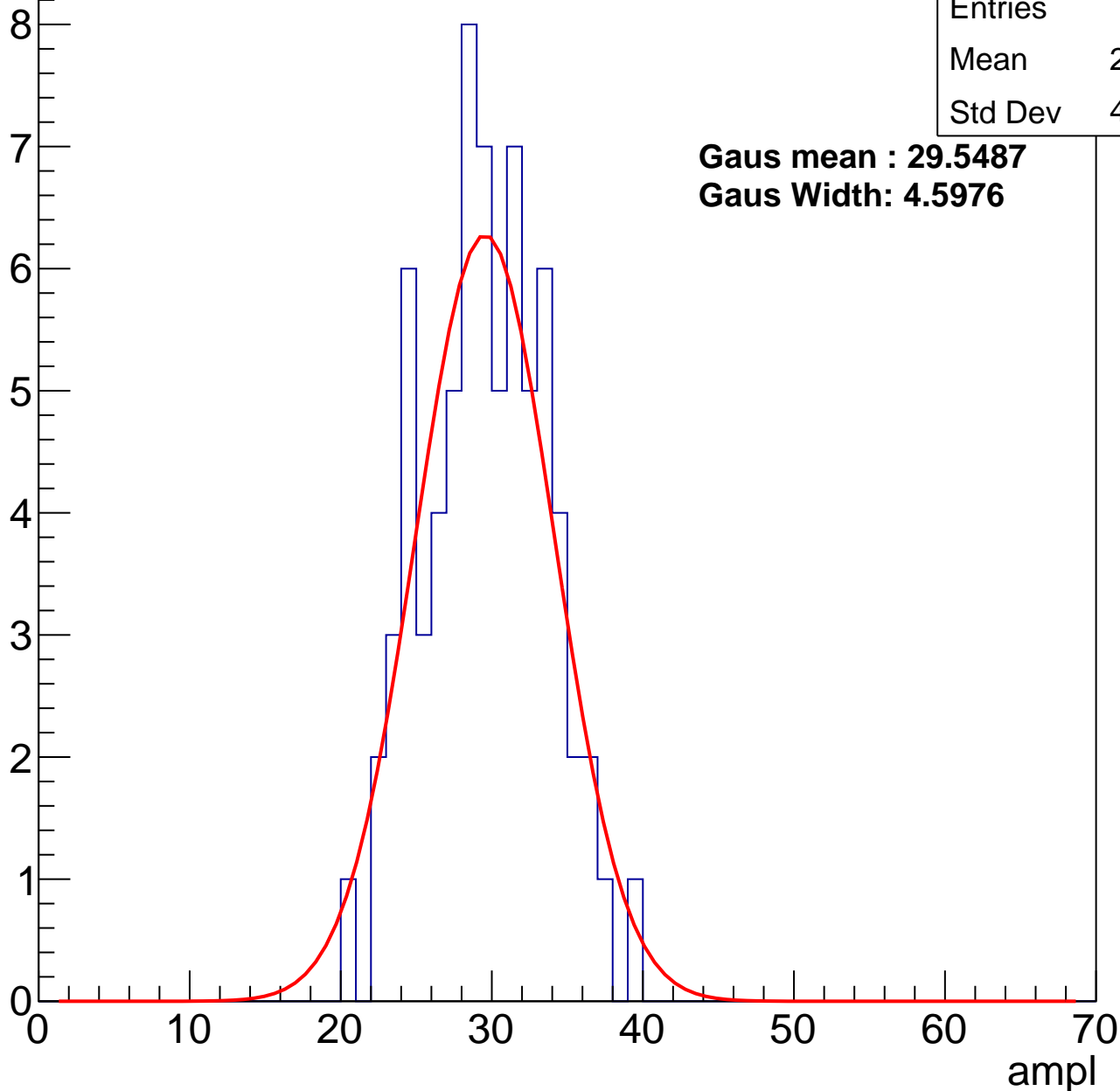
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.12
Std Dev	4.007

Gaus mean : 29.5487

Gaus Width: 4.5976



B0L001S, U15-ch84, adc1

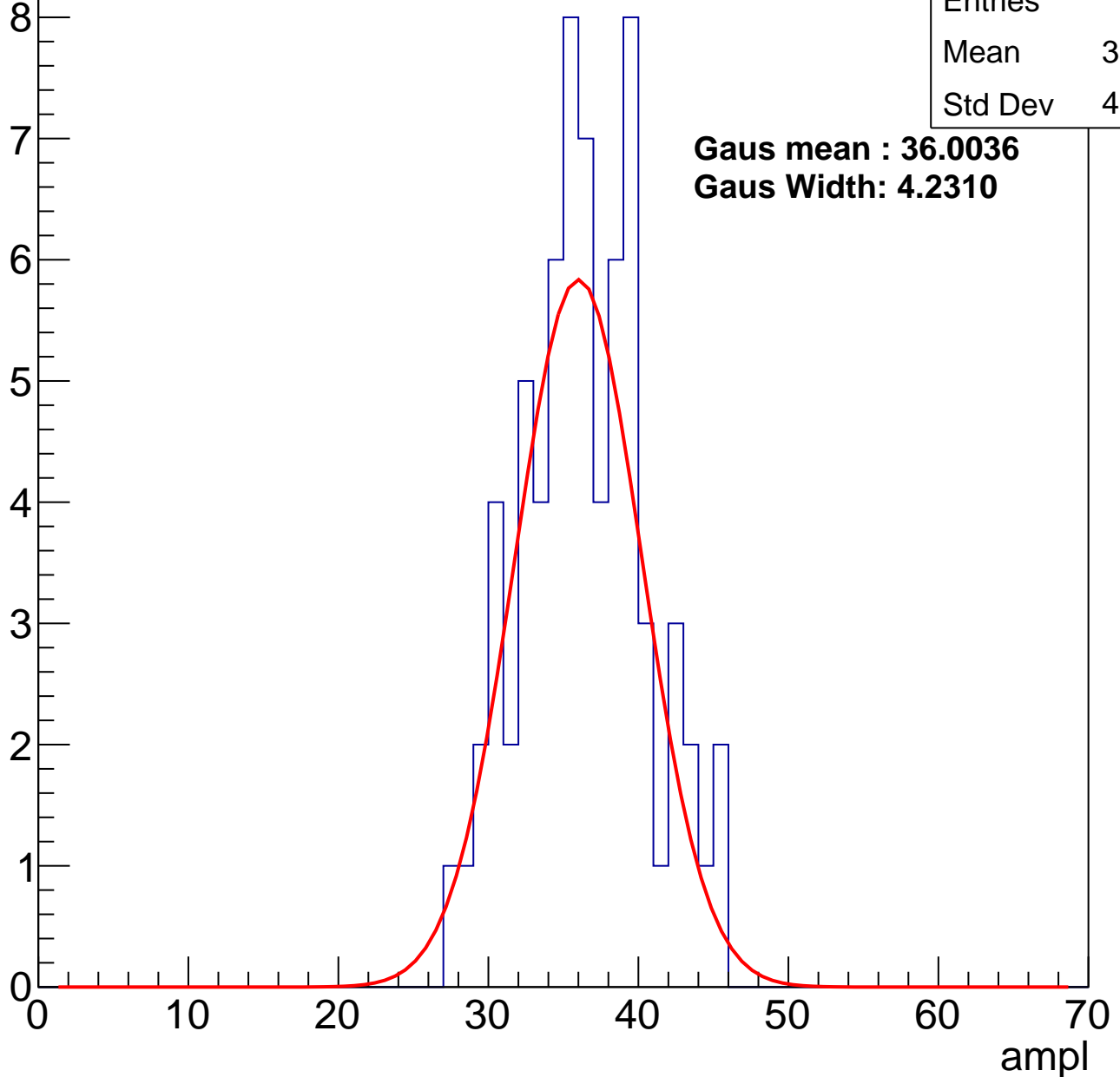
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	35.97
Std Dev	4.123

Gaus mean : 36.0036

Gaus Width: 4.2310



B0L001S, U15-ch84, adc2

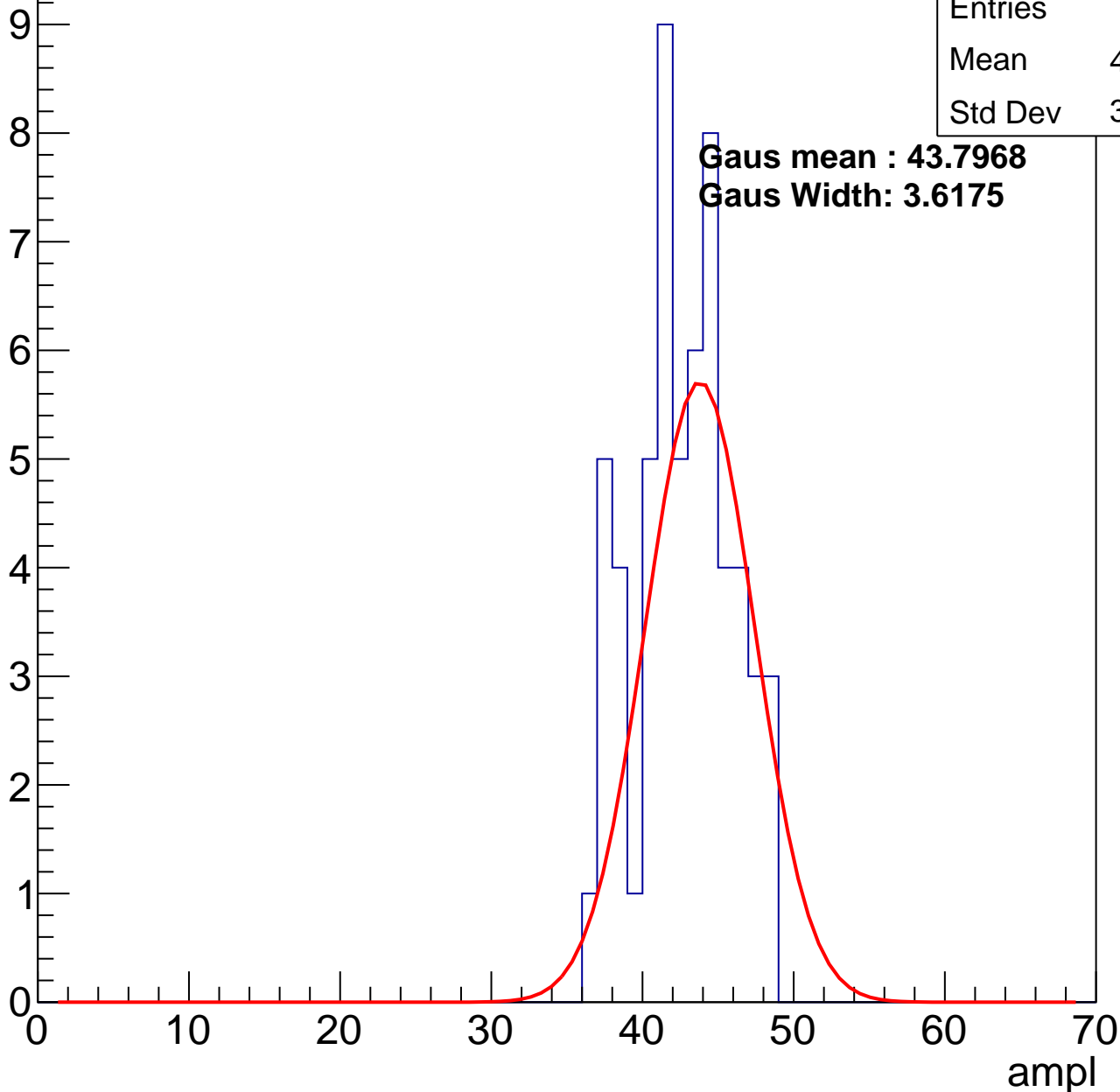
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	42.24
Std Dev	3.164

Gaus mean : 43.7968

Gaus Width: 3.6175

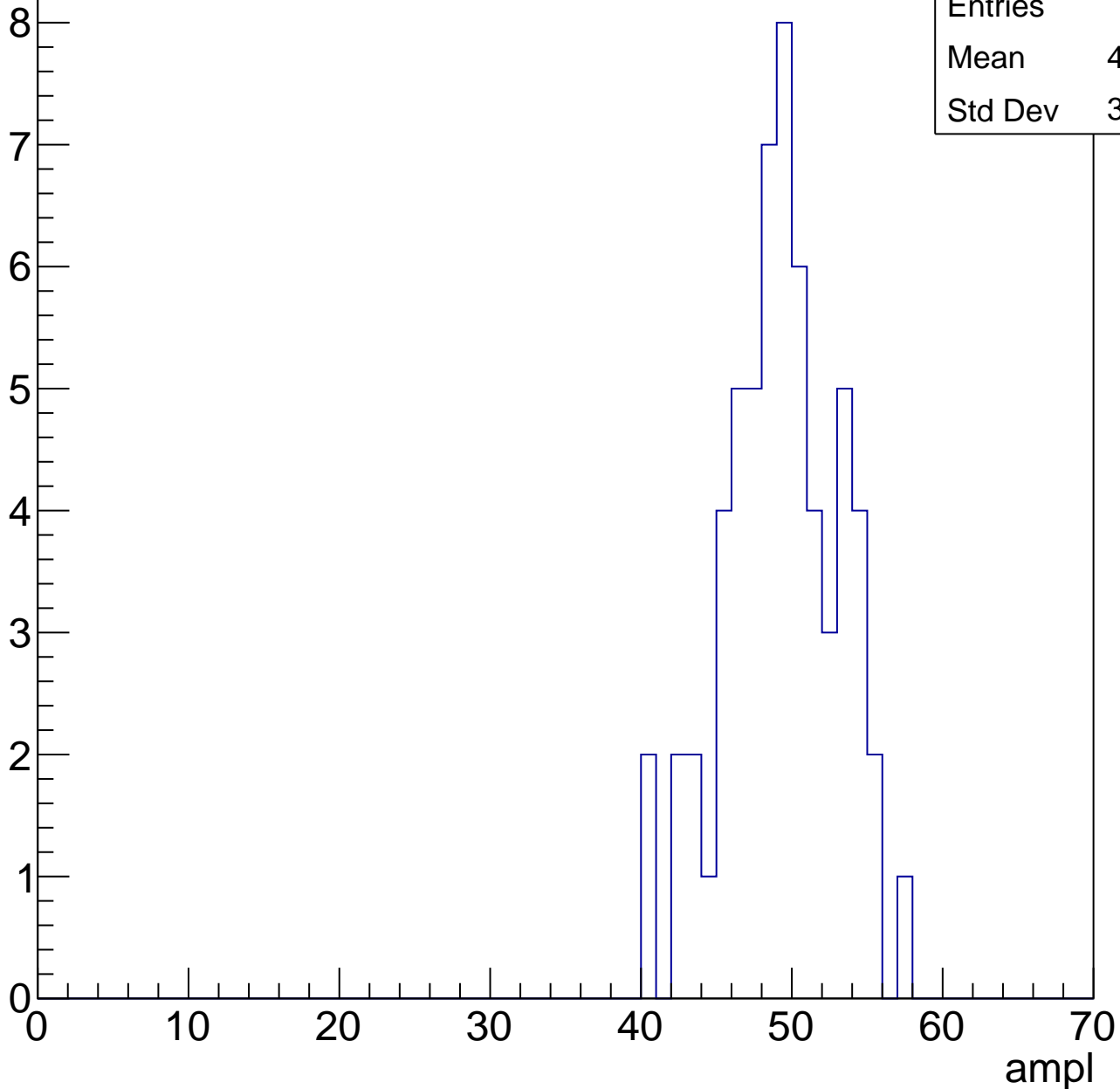


B0L001S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	48.77
Std Dev	3.743

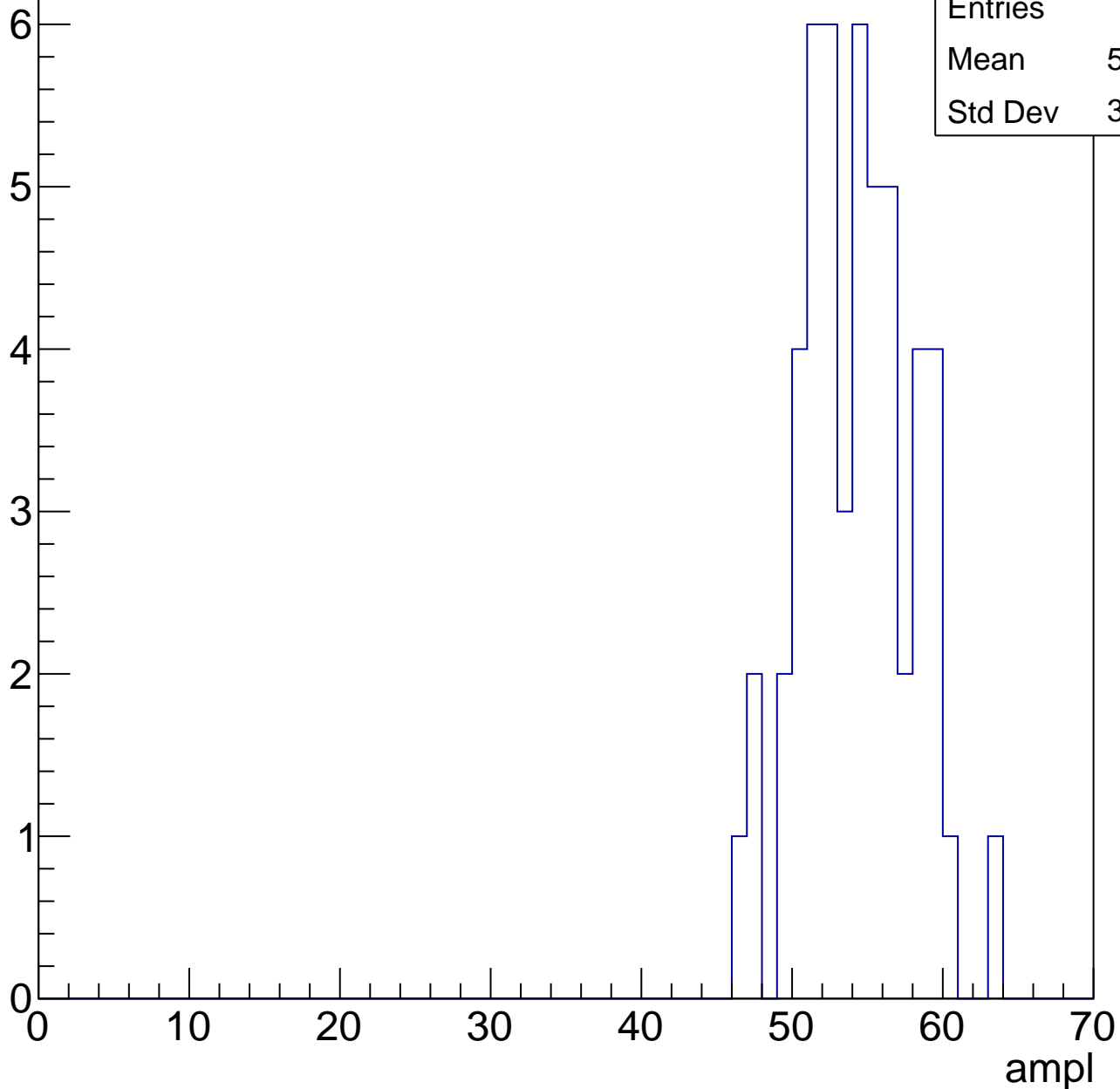


B0L001S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

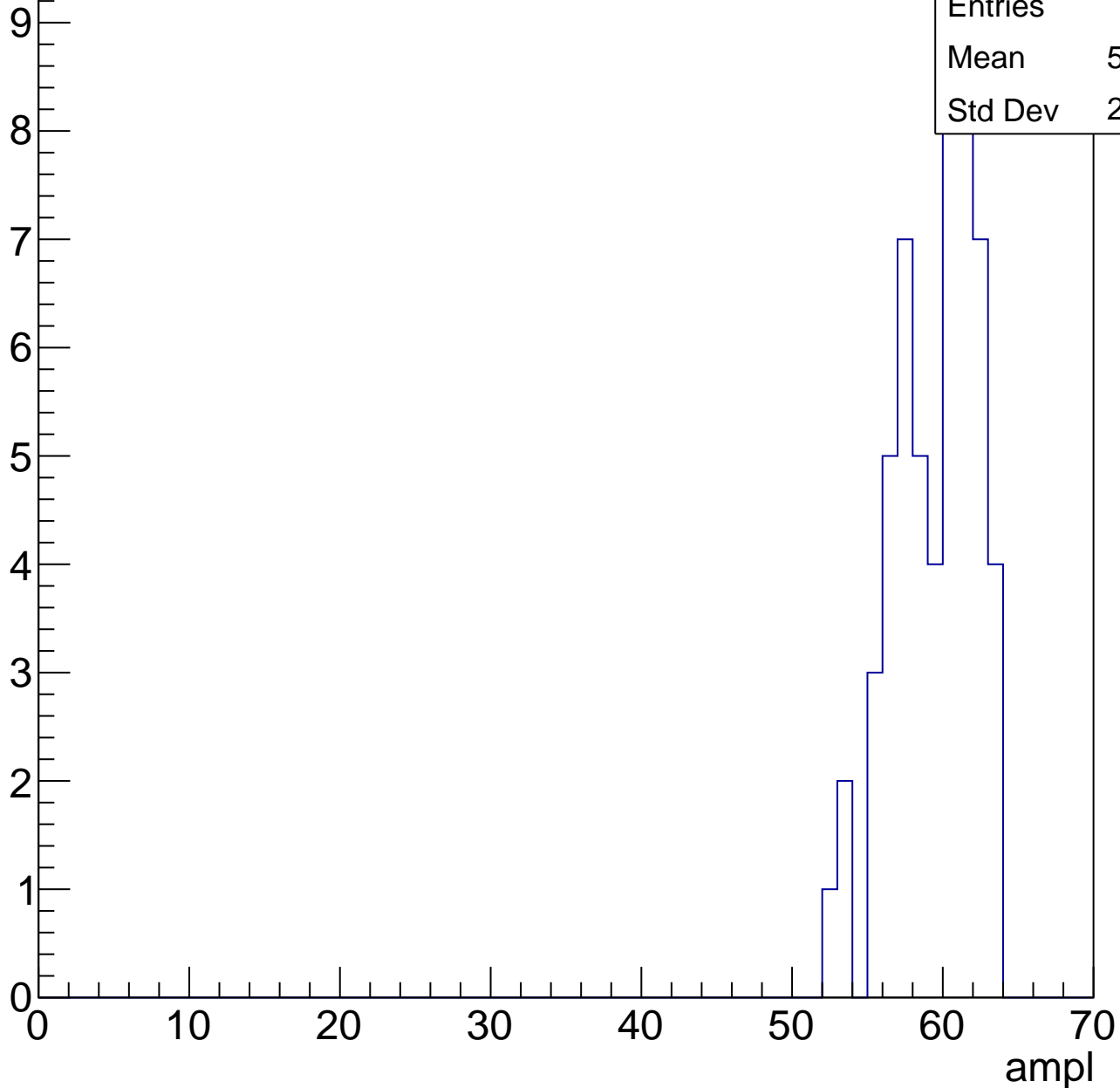
Entries	52
Mean	53.83
Std Dev	3.615



B0L001S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

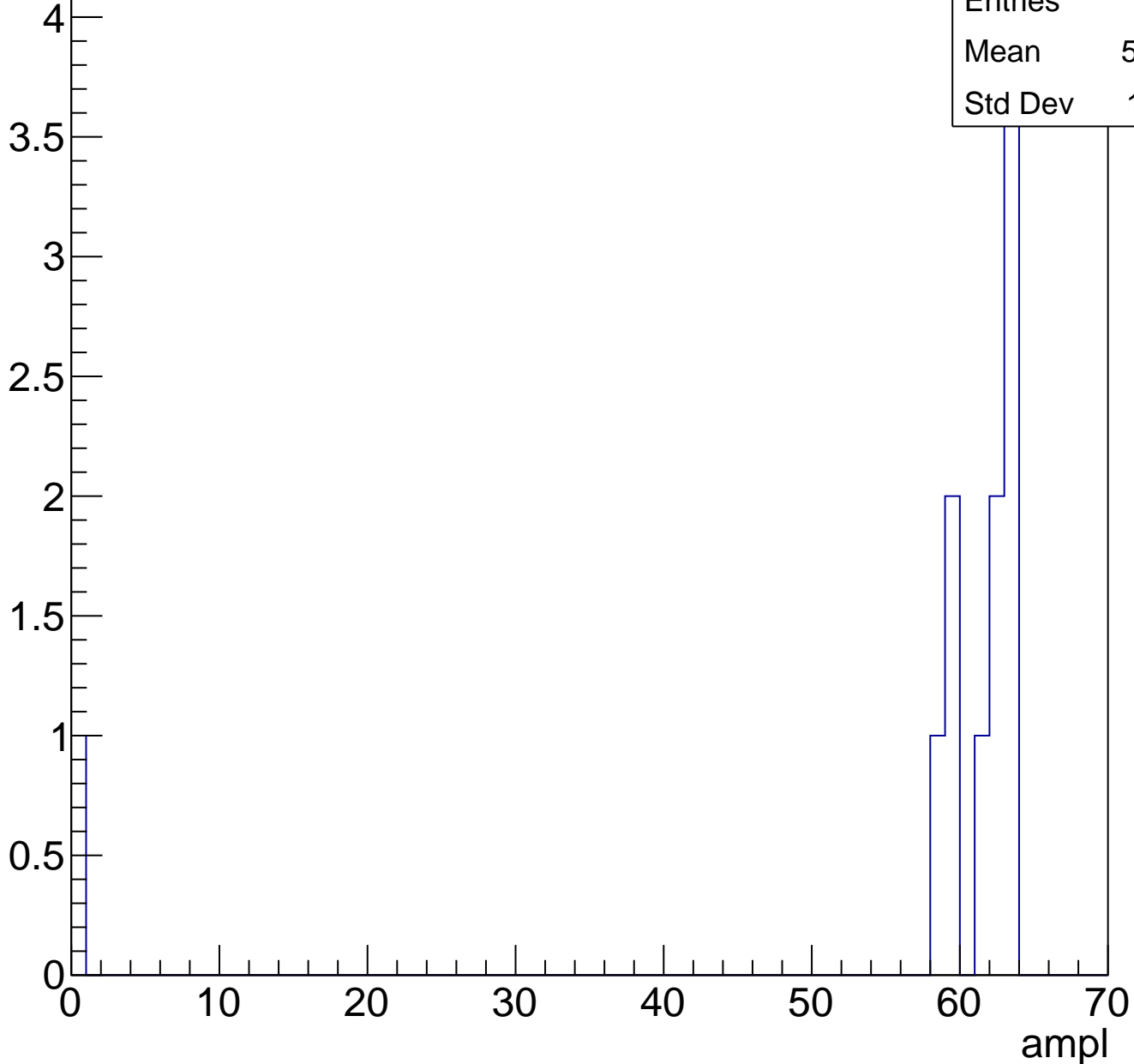
Entry



B0L001S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch85, adc0

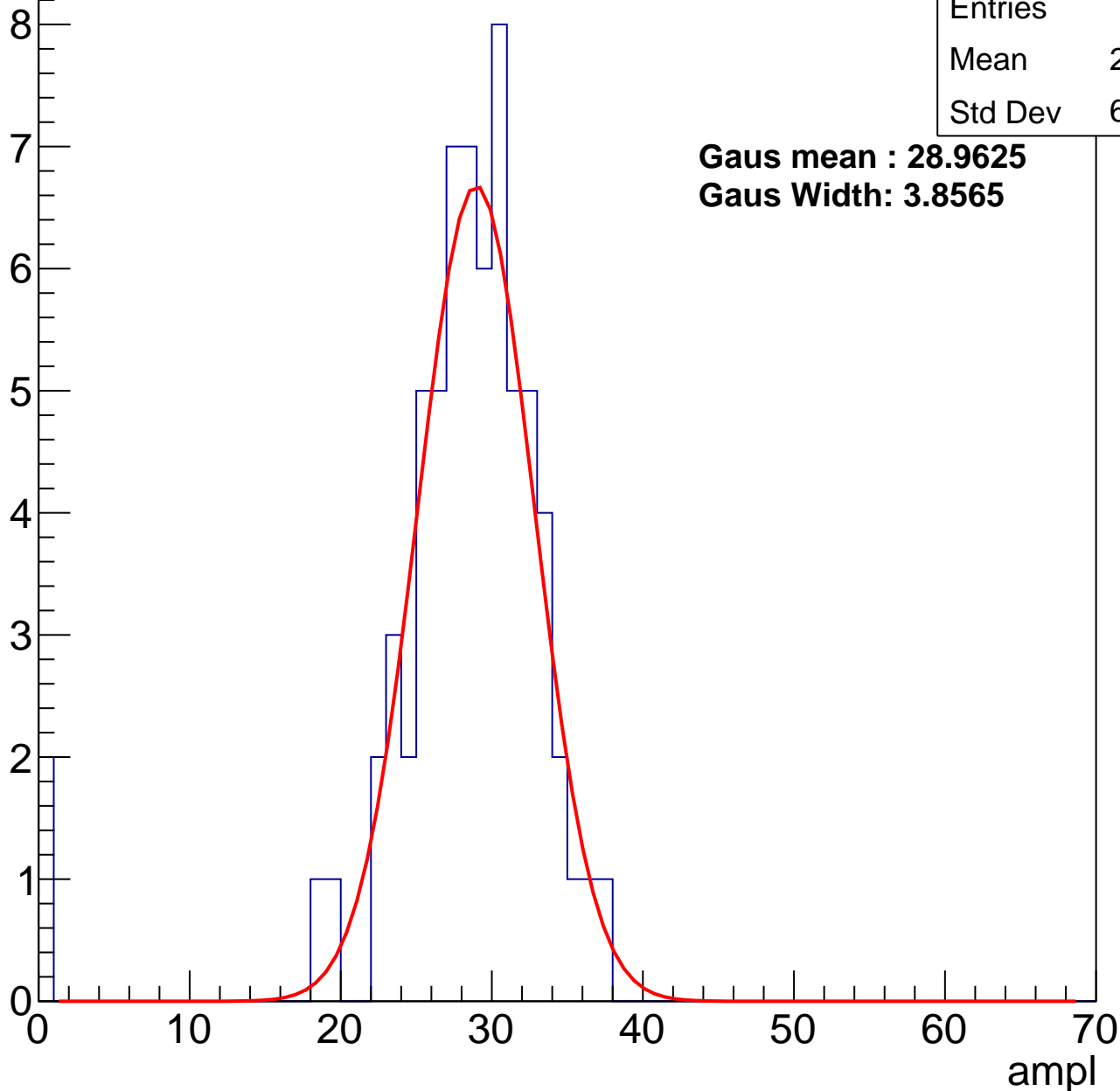
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	27.57
Std Dev	6.084

Gaus mean : 28.9625

Gaus Width: 3.8565



B0L001S, U15-ch85, adc1

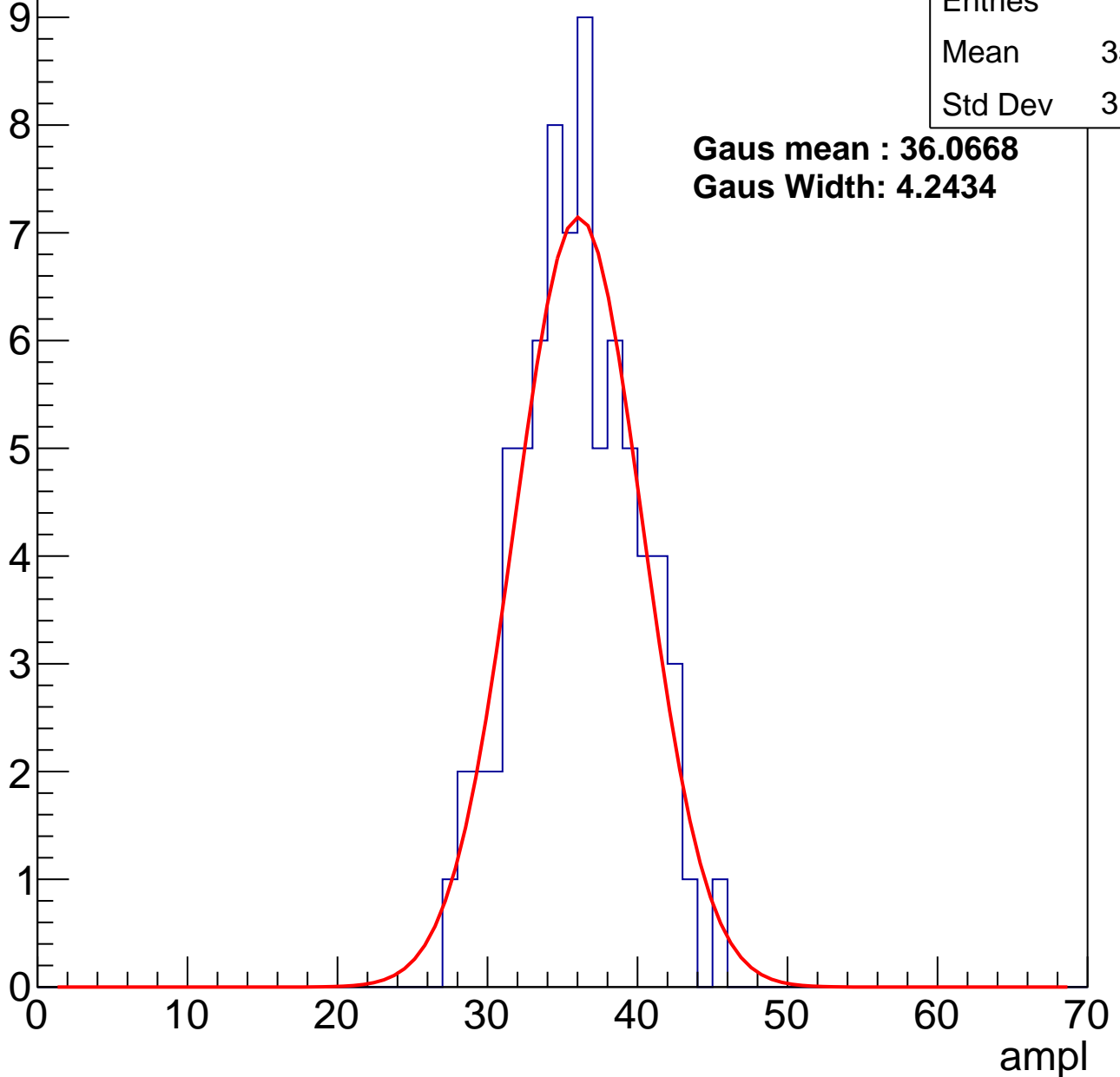
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	35.54
Std Dev	3.868

Gaus mean : 36.0668

Gaus Width: 4.2434



B0L001S, U15-ch85, adc2

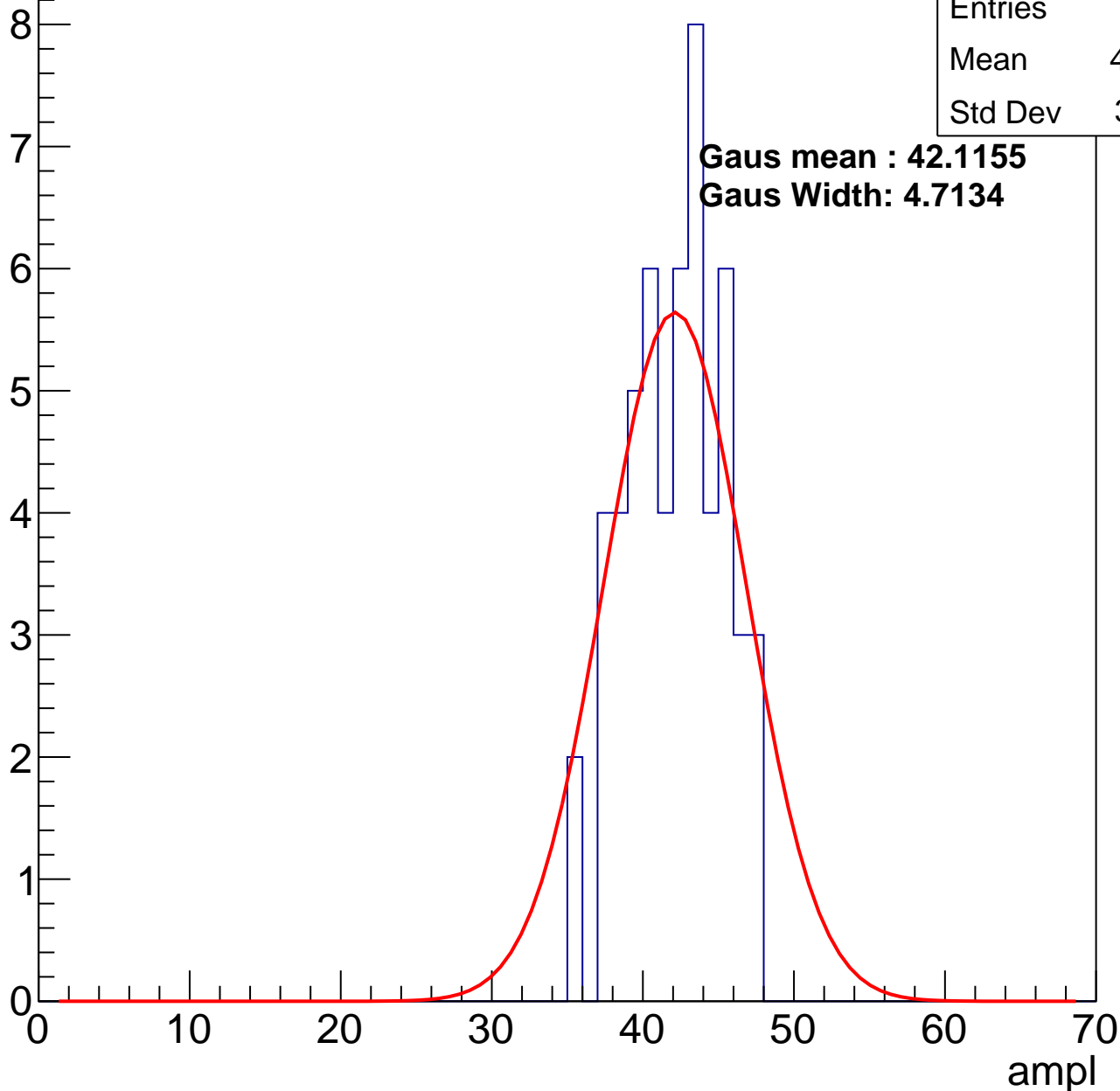
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	41.64
Std Dev	3.101

Gaus mean : 42.1155

Gaus Width: 4.7134

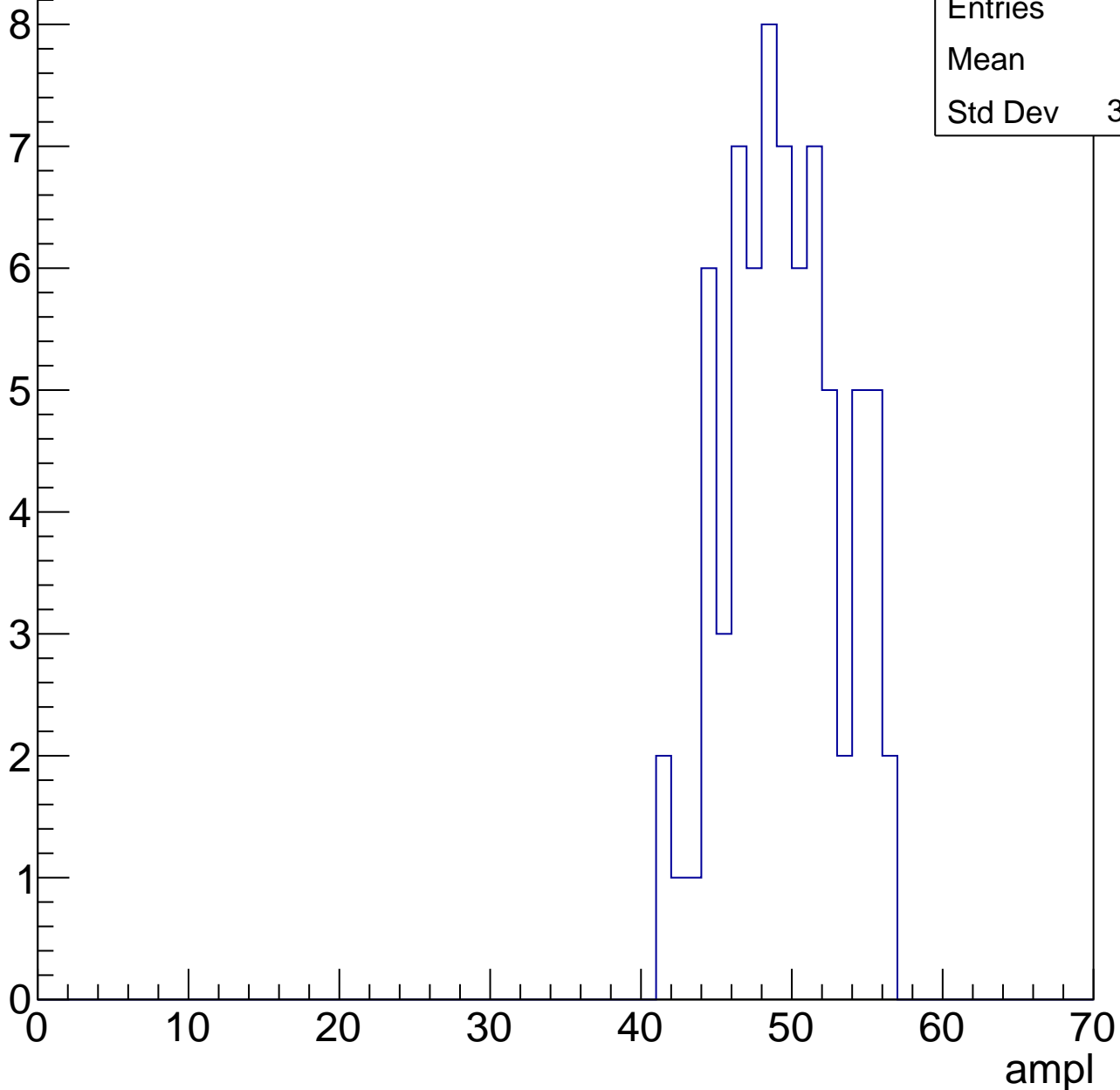


B0L001S, U15-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	49
Std Dev	3.738

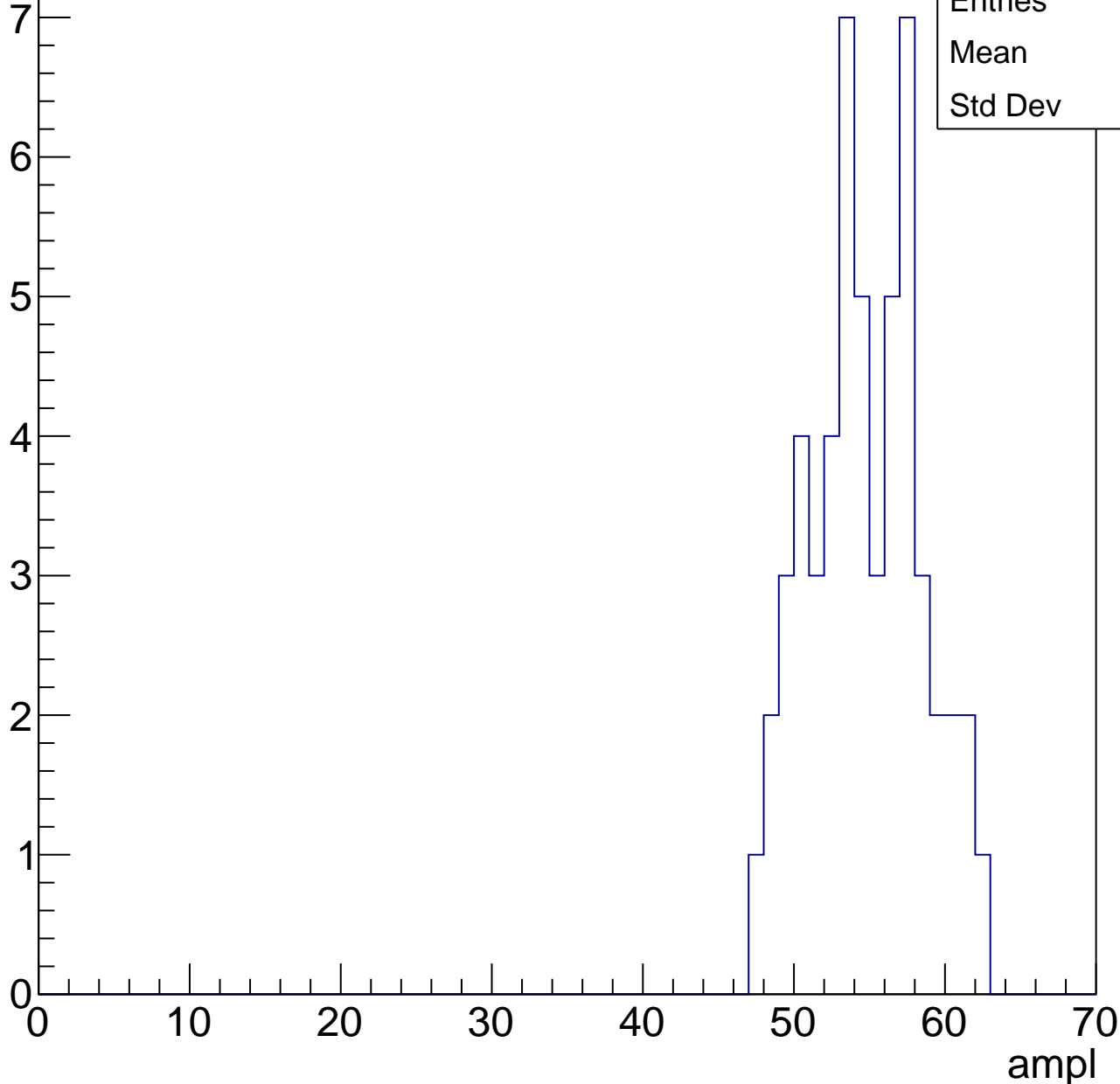


B0L001S, U15-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	54.3
Std Dev	3.66



B0L001S, U15-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

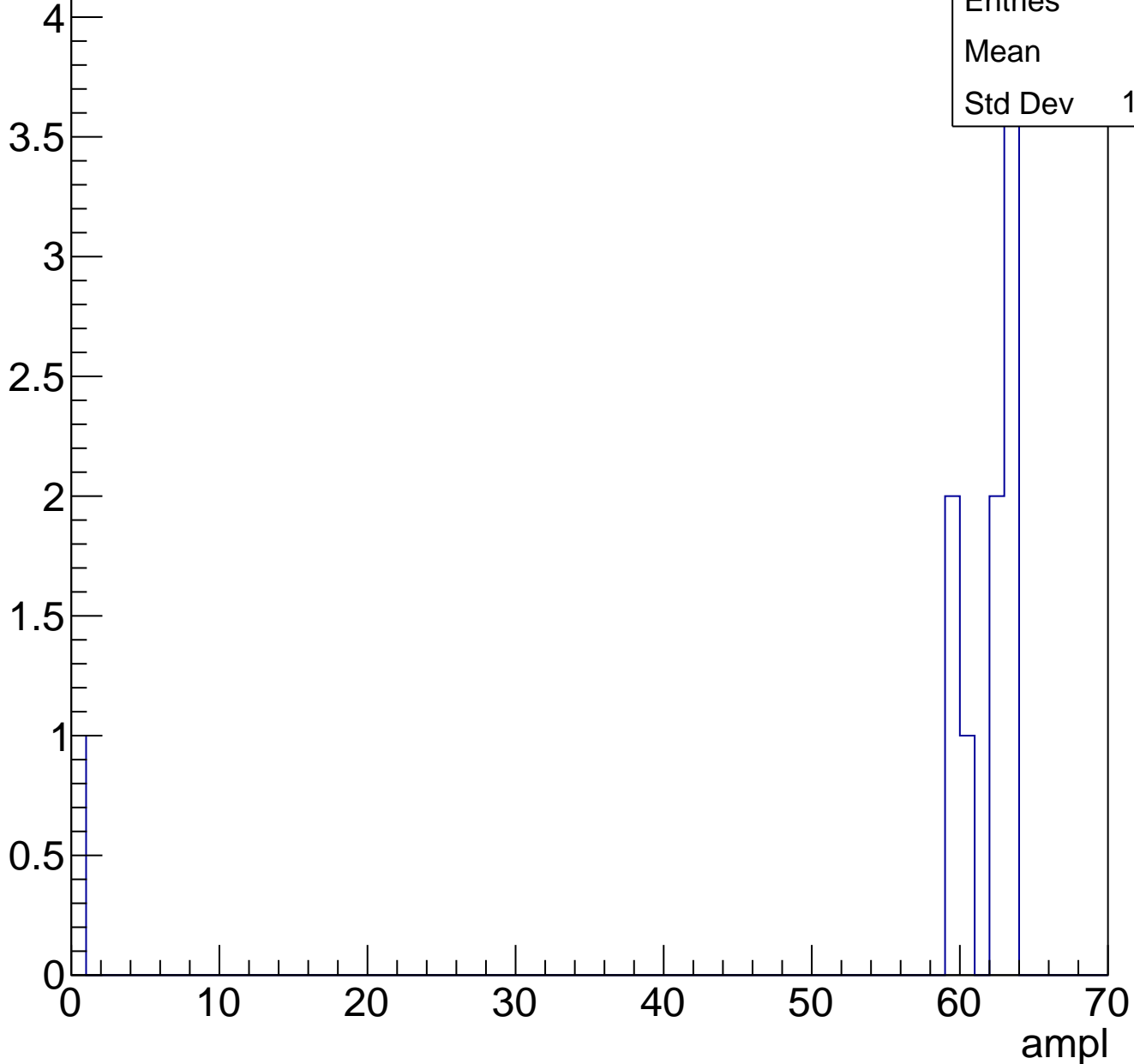
ampl

Entries	49
Mean	59.39
Std Dev	2.609

B0L001S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch86, adc0

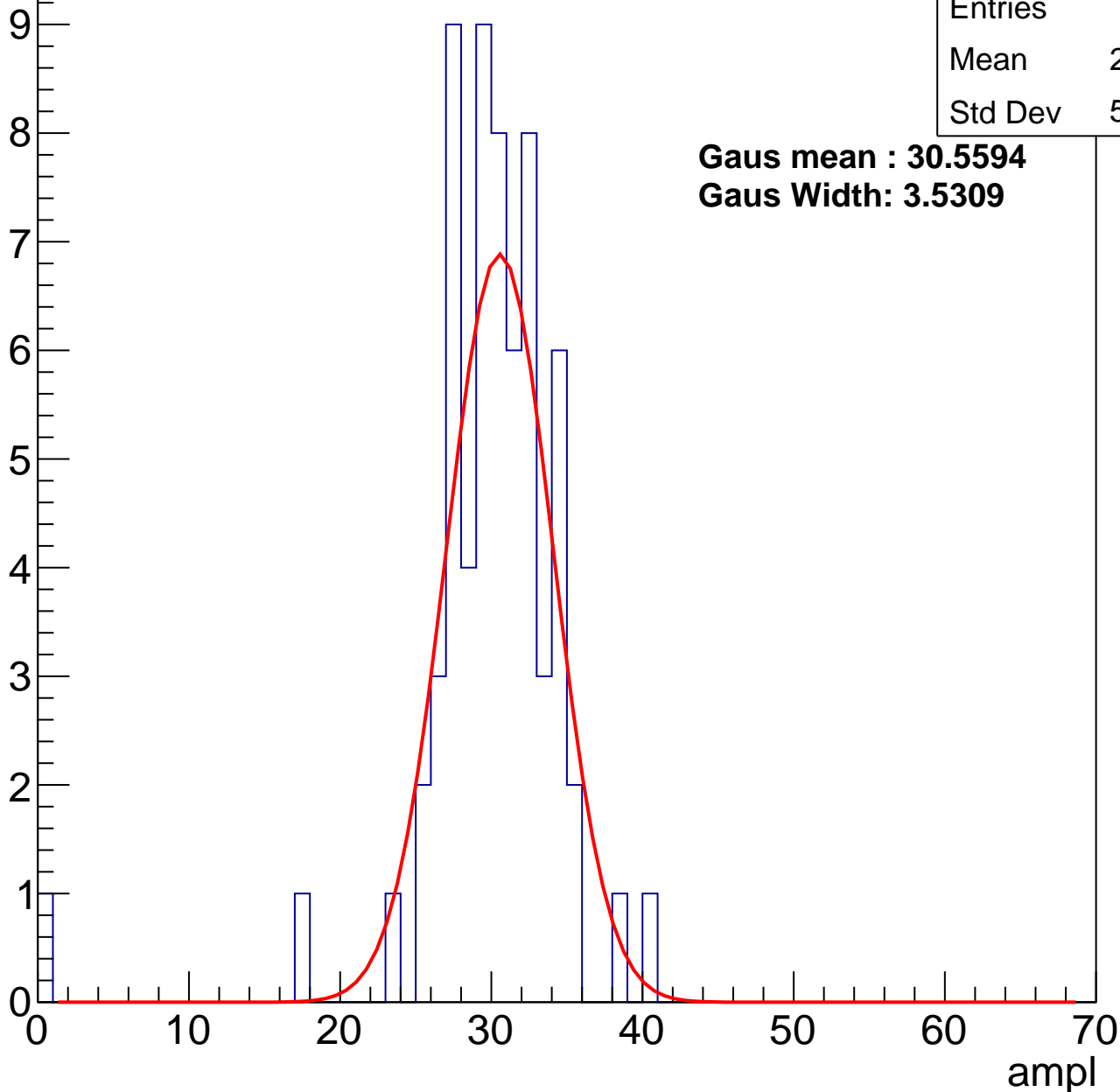
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.49
Std Dev	5.084

Gaus mean : 30.5594

Gaus Width: 3.5309



B0L001S, U15-ch86, adc1

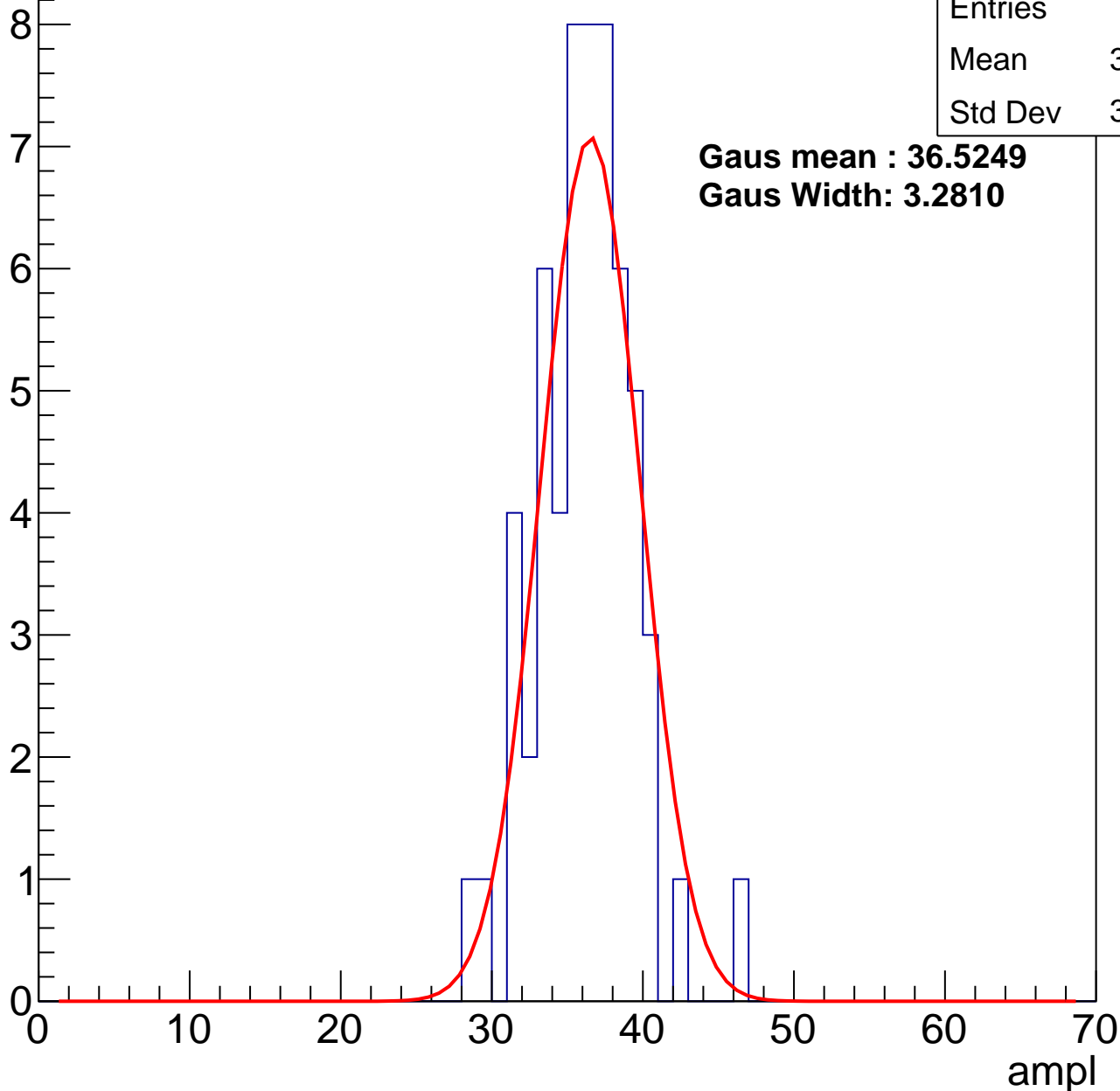
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	35.76
Std Dev	3.164

Gaus mean : 36.5249

Gaus Width: 3.2810



B0L001S, U15-ch86, adc2

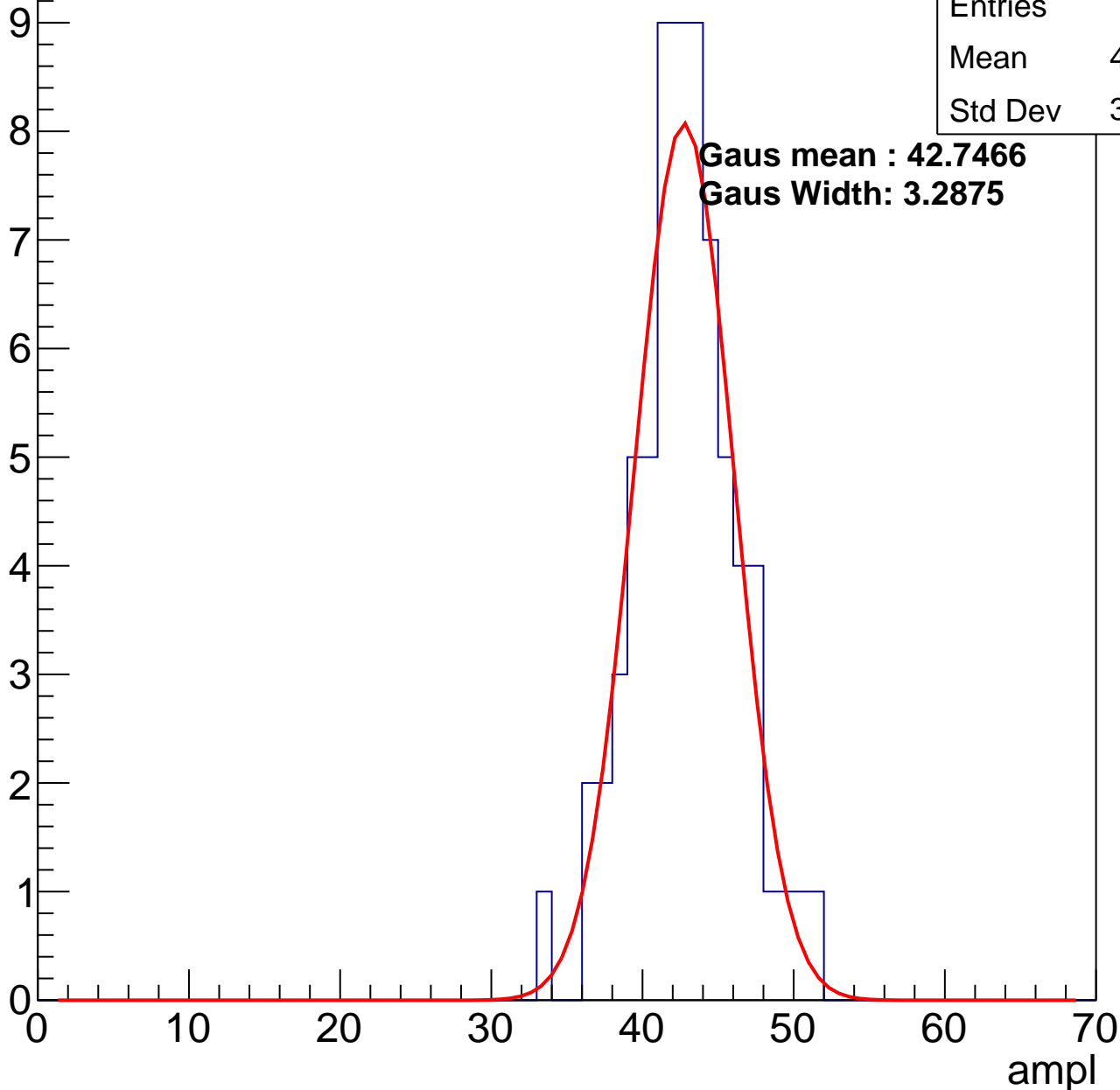
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.39
Std Dev	3.385

Gaus mean : 42.7466

Gaus Width: 3.2875

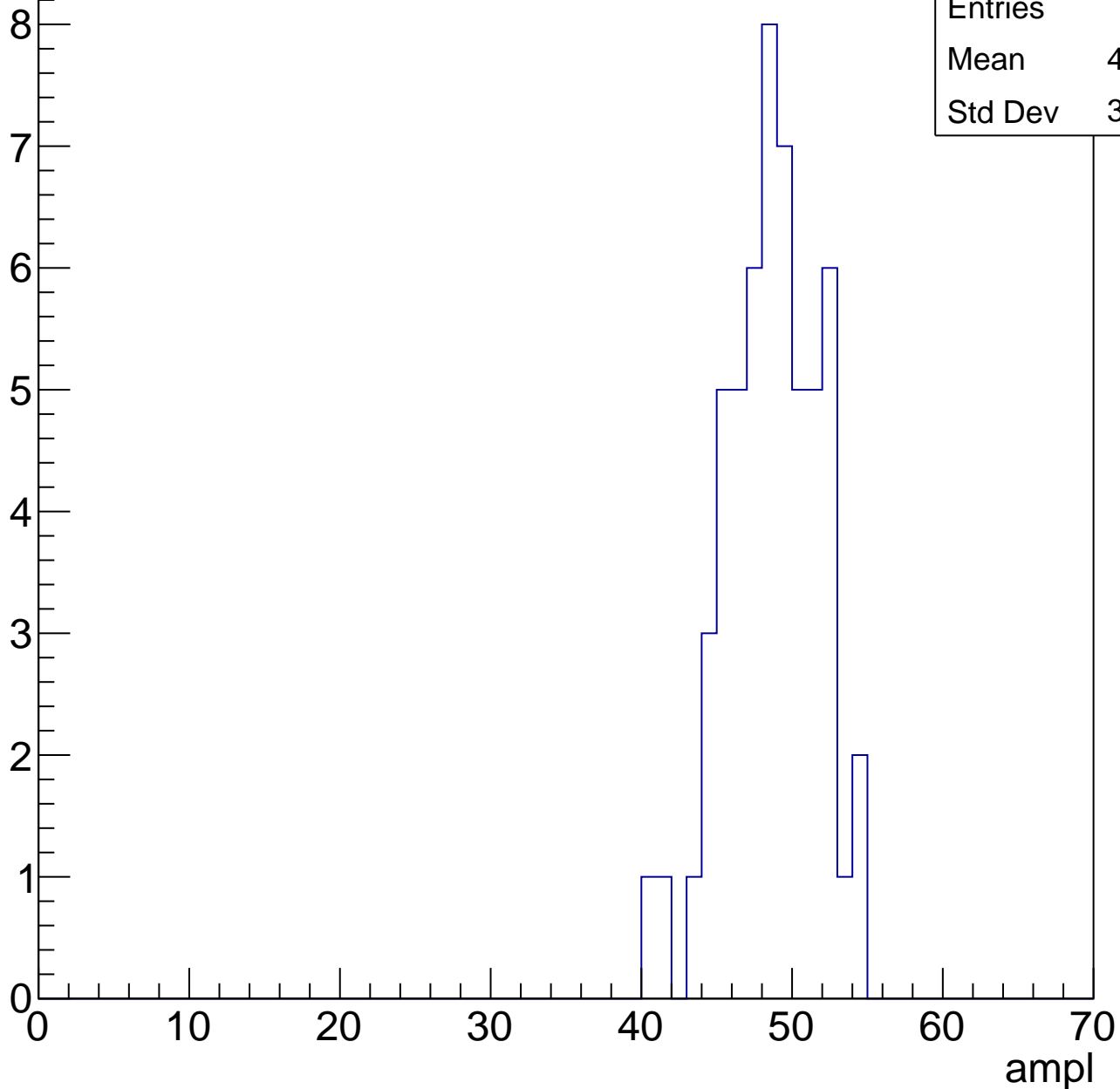


B0L001S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	48.18
Std Dev	3.048



B0L001S, U15-ch86, adc4

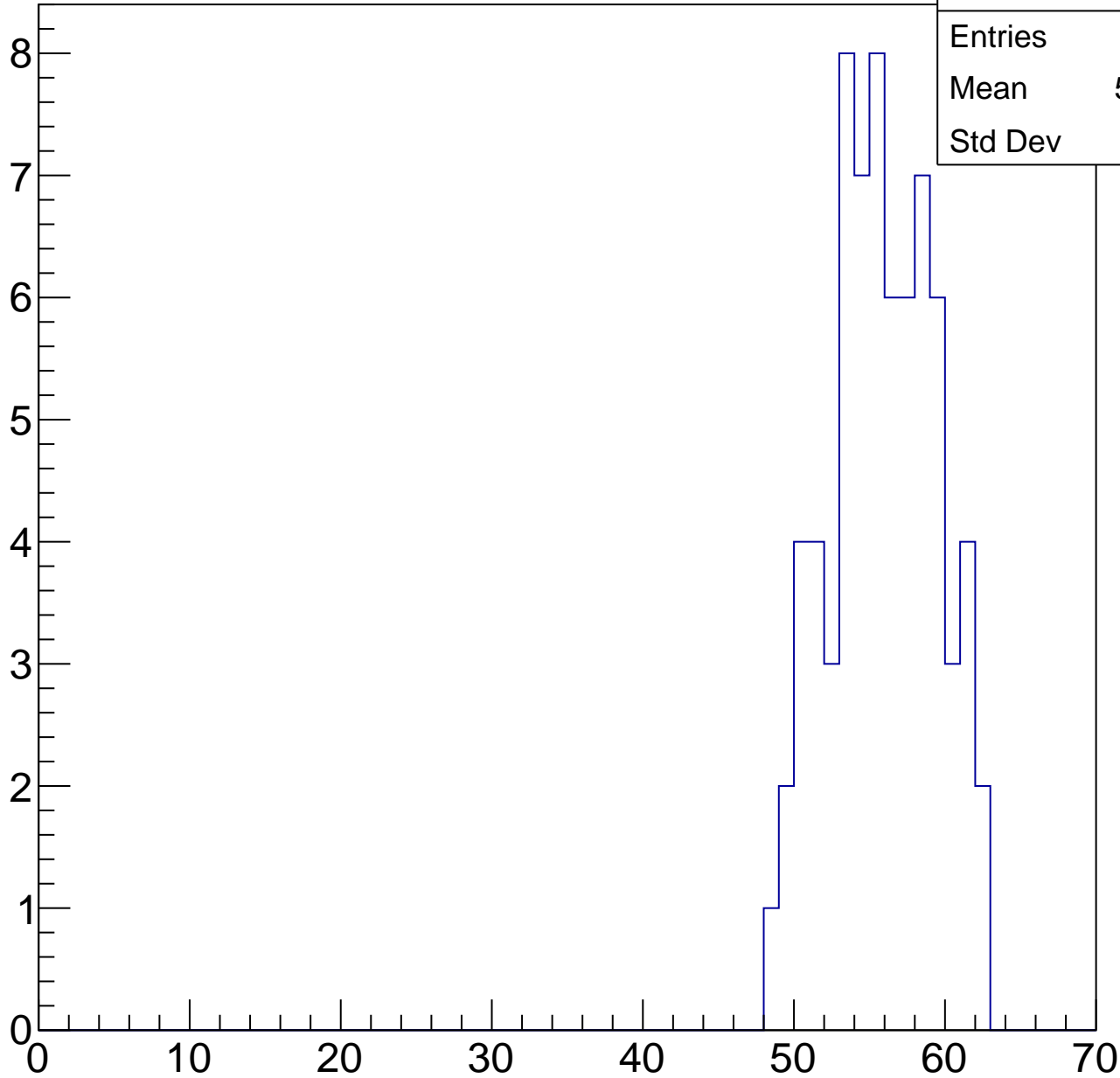
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	55.41
Std Dev	3.45

ampl

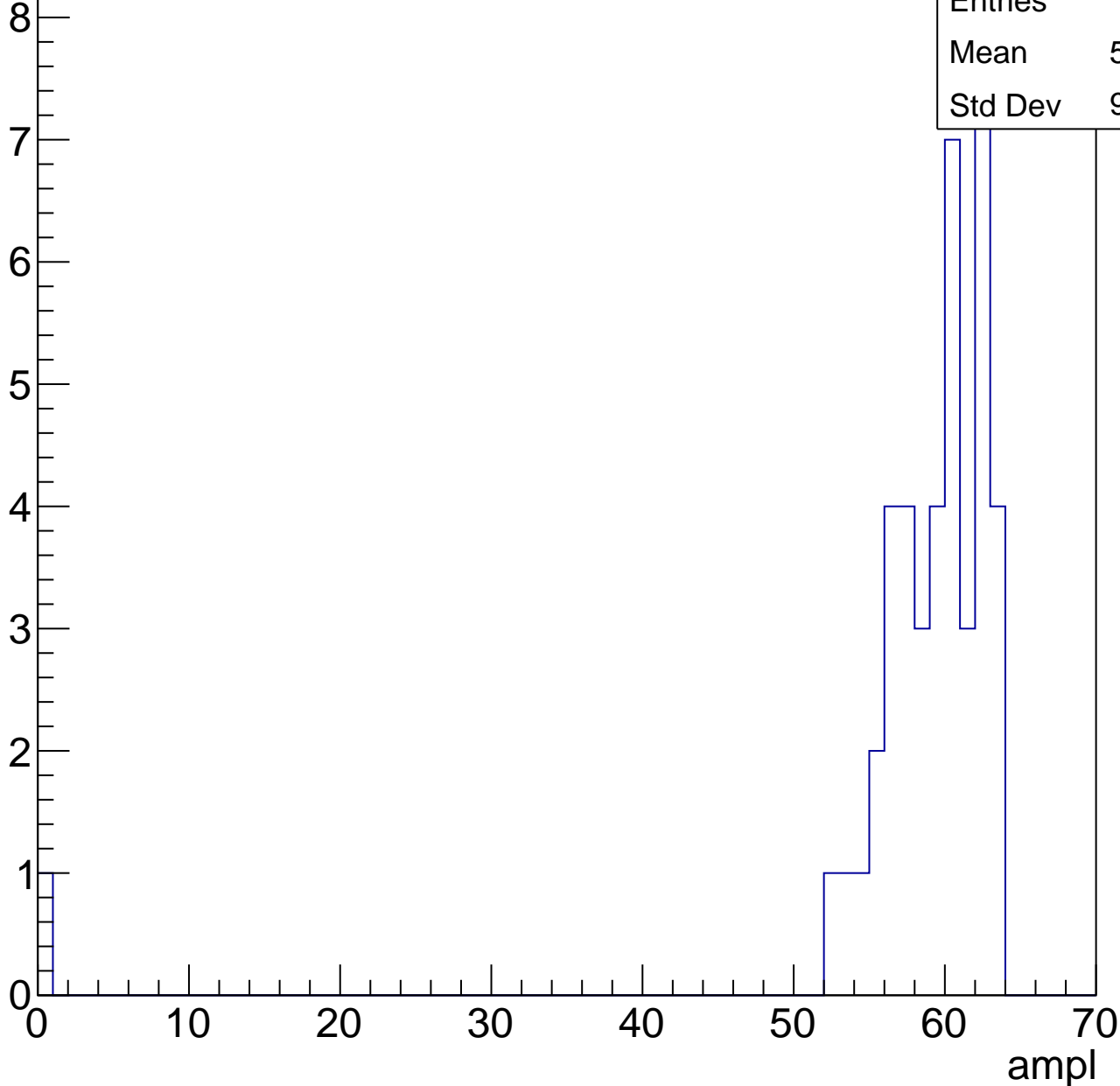


B0L001S, U15-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	57.72
Std Dev	9.354

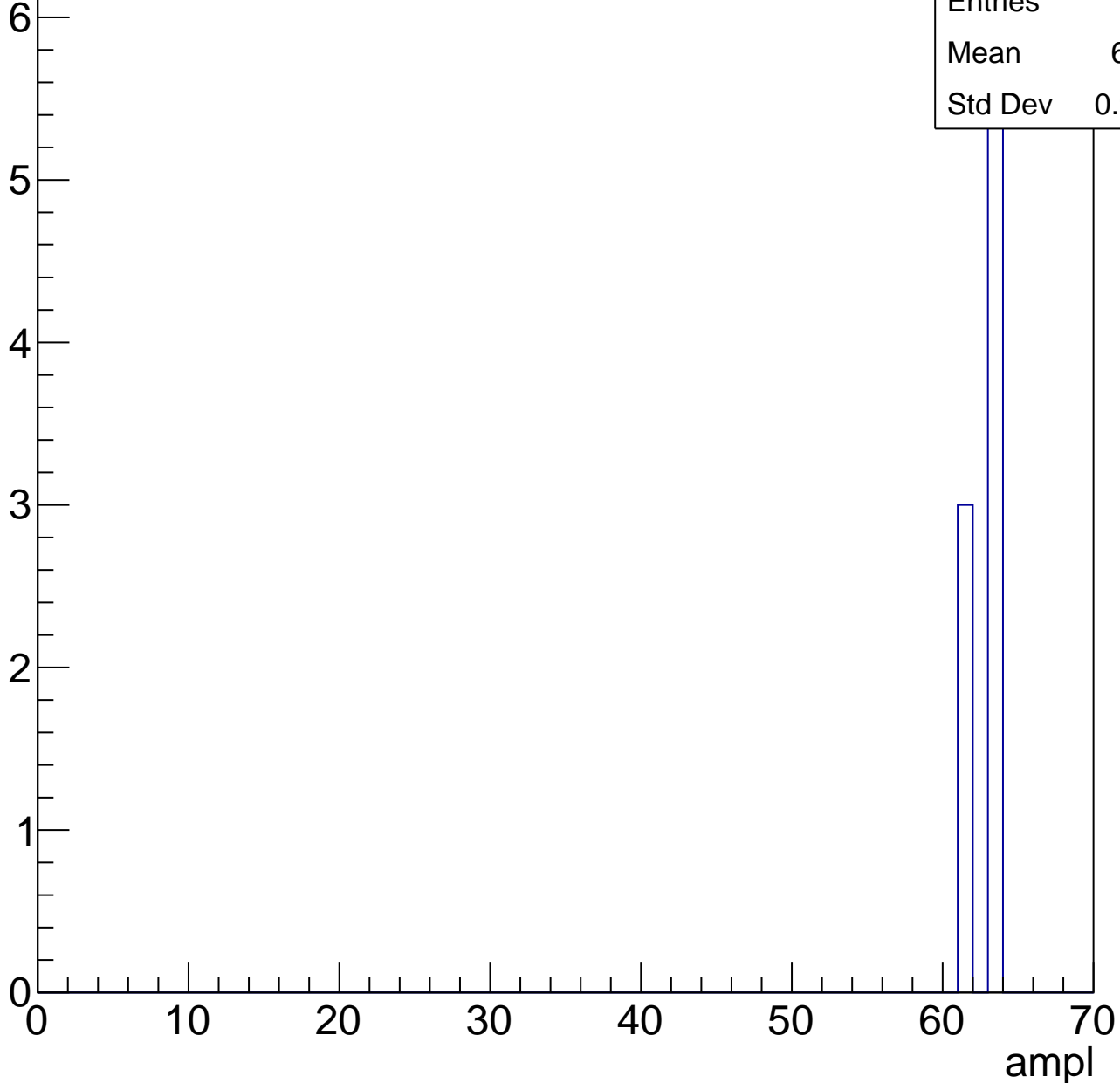


B0L001S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	62.33
Std Dev	0.9428



B0L001S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch87, adc0

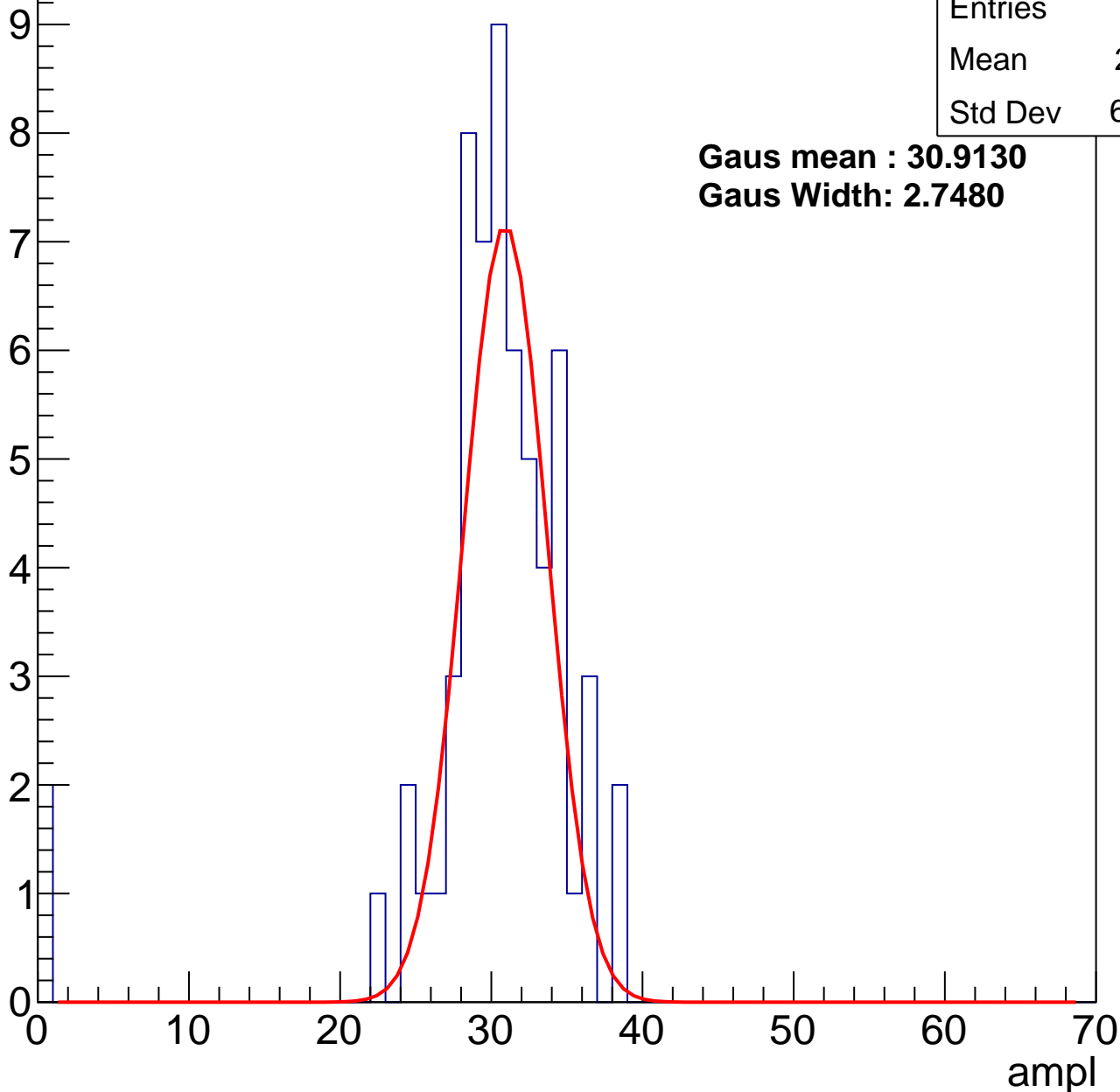
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	29.51
Std Dev	6.334

Gaus mean : 30.9130

Gaus Width: 2.7480



B0L001S, U15-ch87, adc1

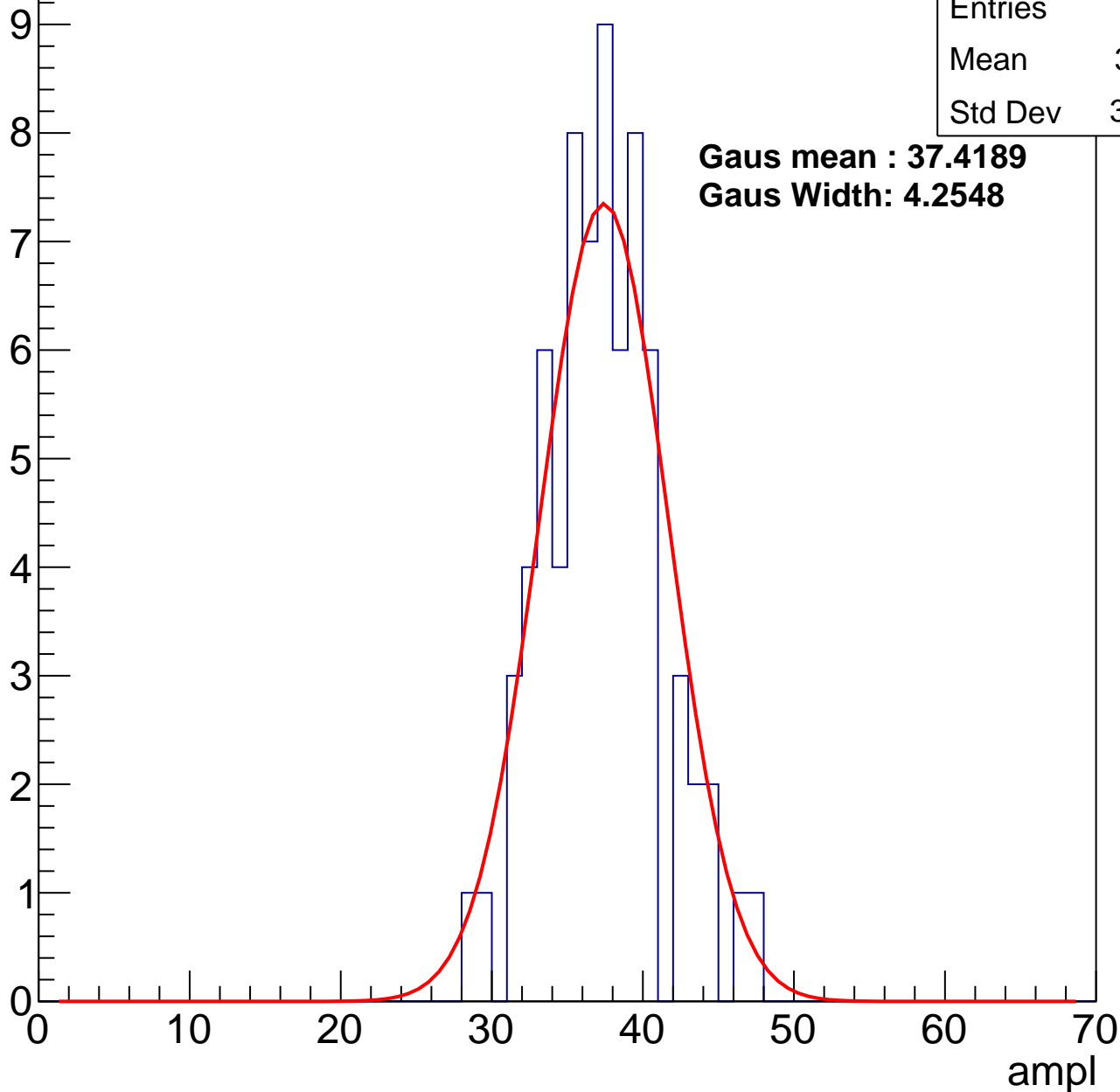
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.81
Std Dev	3.799

Gaus mean : 37.4189

Gaus Width: 4.2548



B0L001S, U15-ch87, adc2

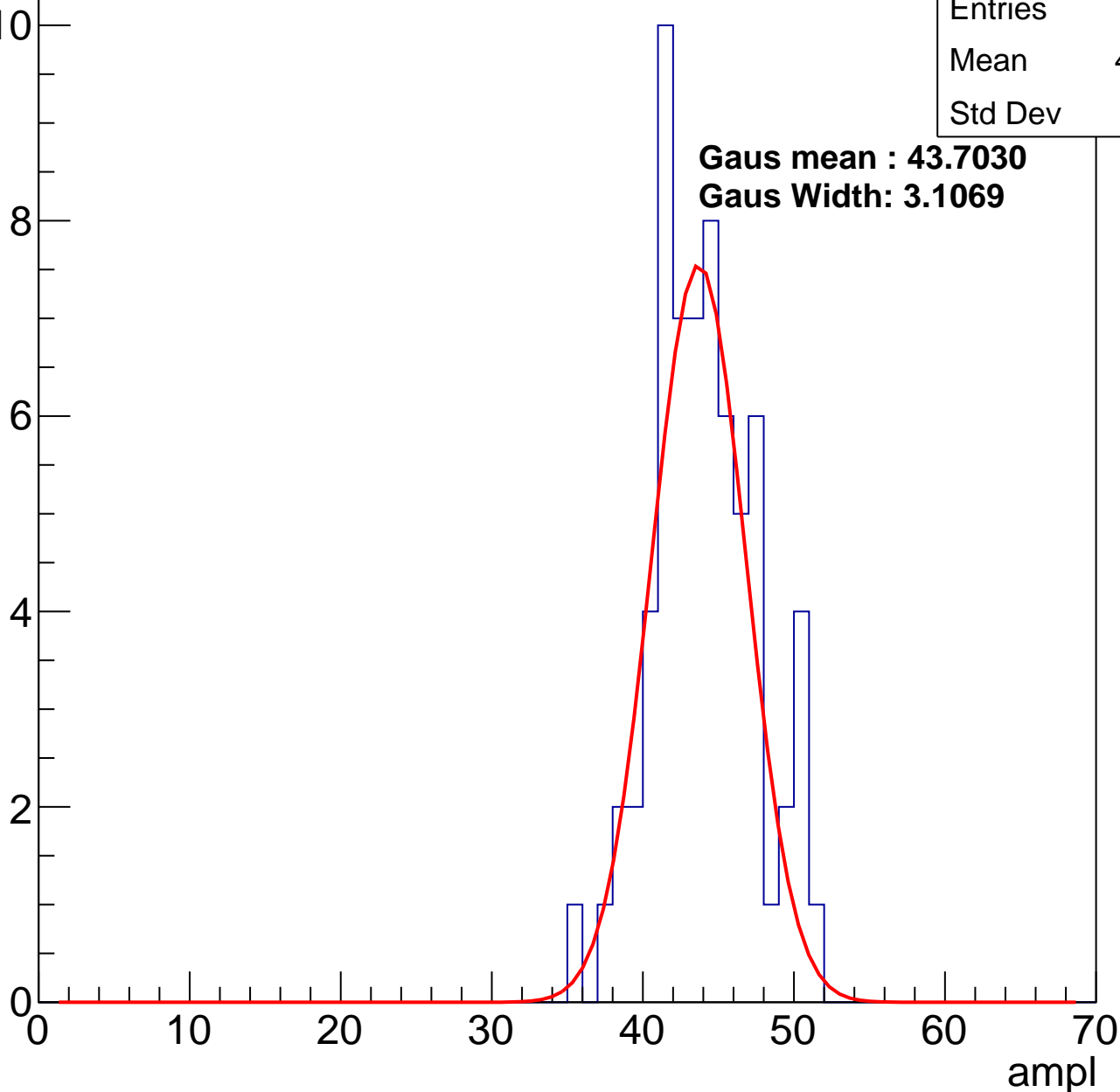
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.61
Std Dev	3.39

Gaus mean : 43.7030

Gaus Width: 3.1069

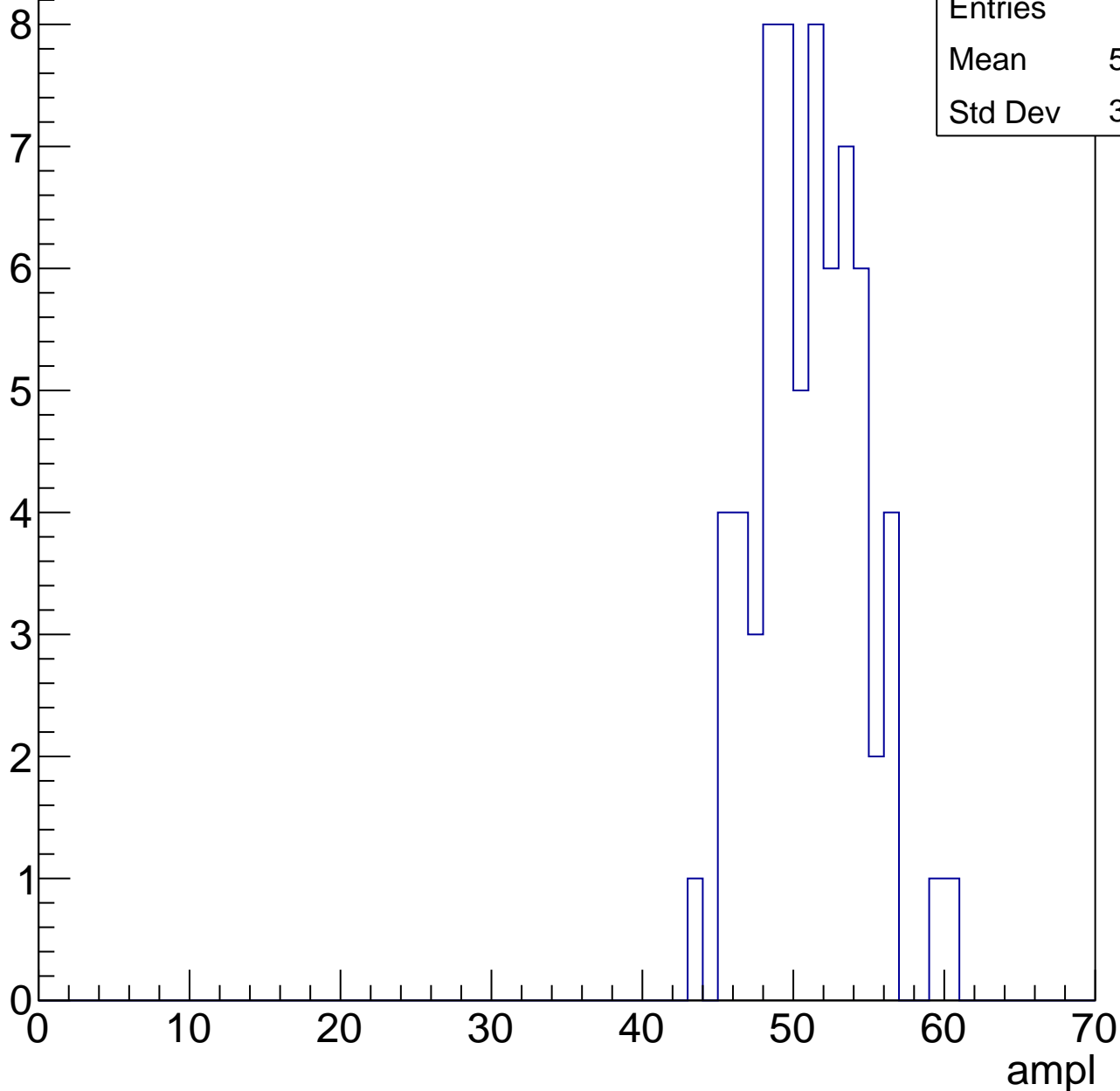


B0L001S, U15-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.62
Std Dev	3.473

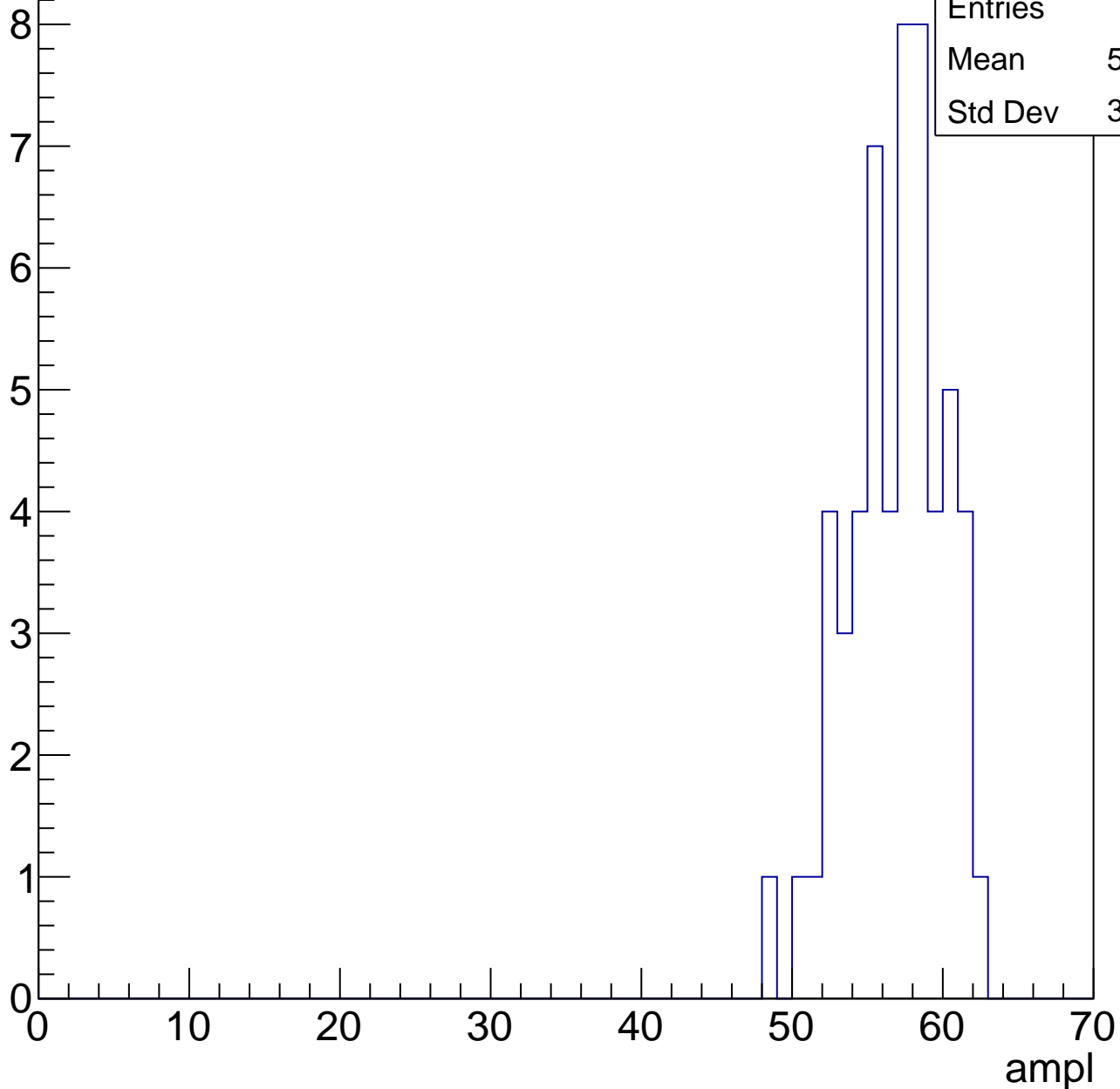


B0L001S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.42
Std Dev	3.079

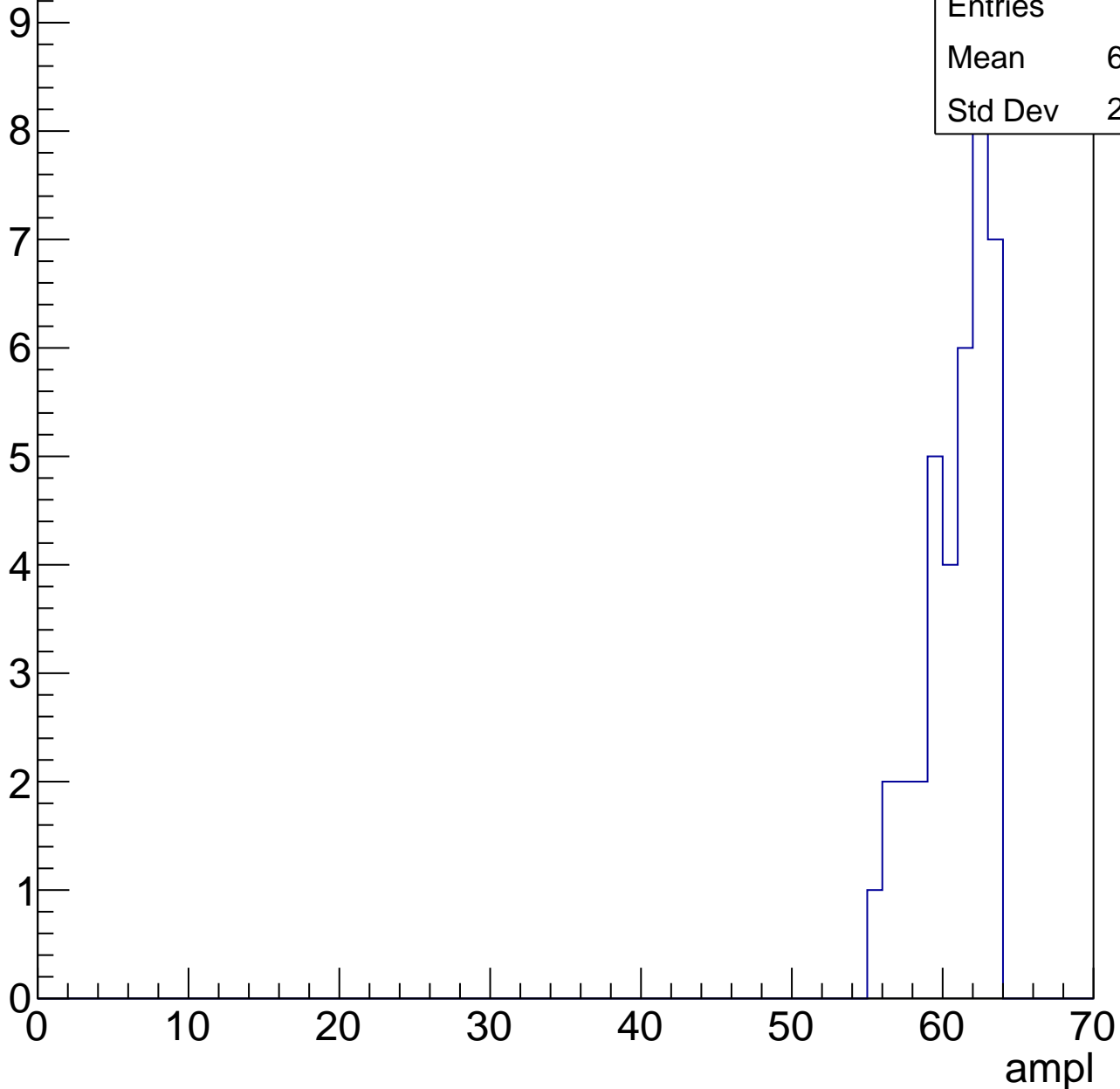


B0L001S, U15-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

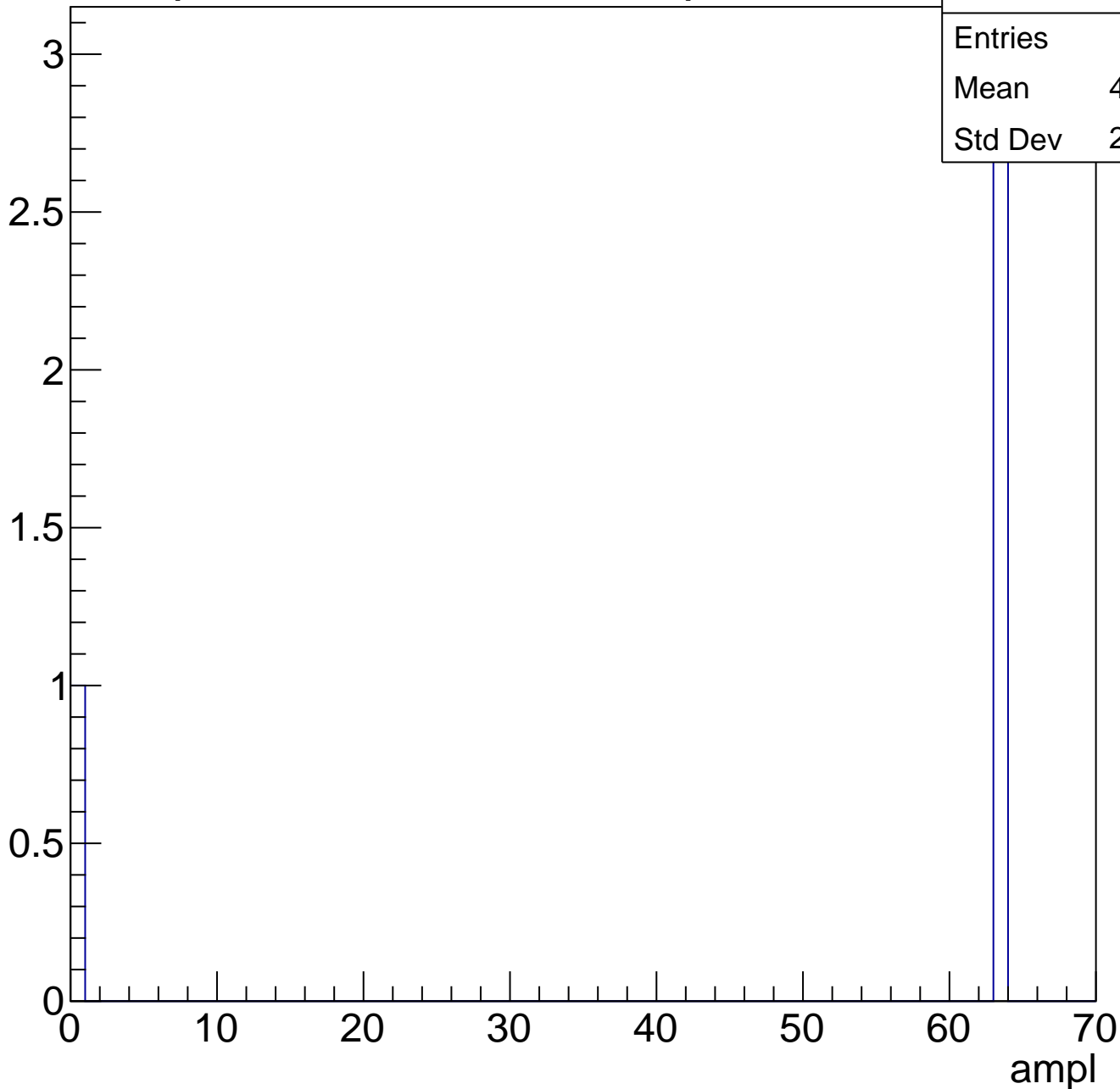
Entries	38
Mean	60.45
Std Dev	2.209



B0L001S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch88, adc0

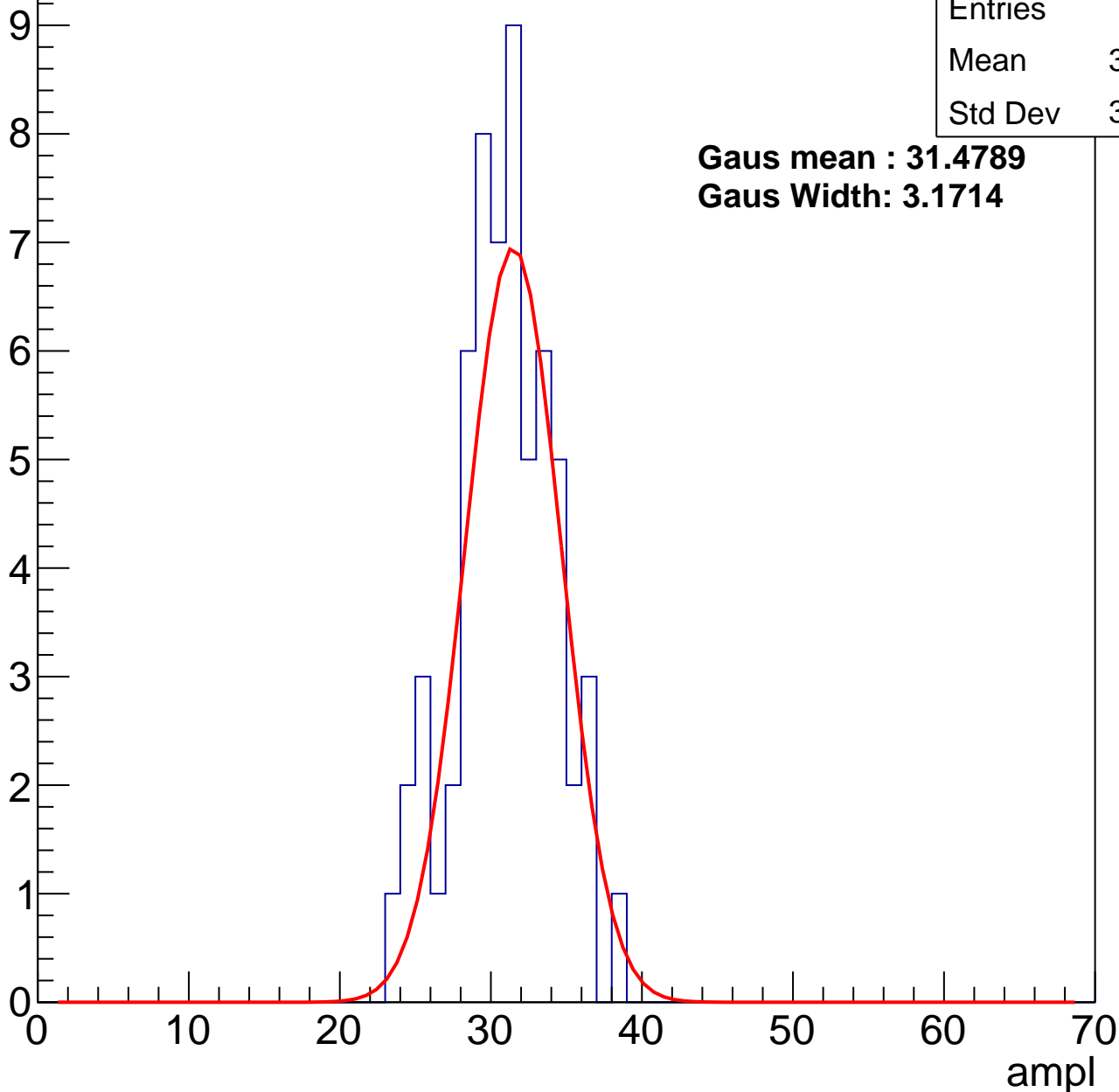
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.48
Std Dev	3.222

Gaus mean : 31.4789

Gaus Width: 3.1714



B0L001S, U15-ch88, adc1

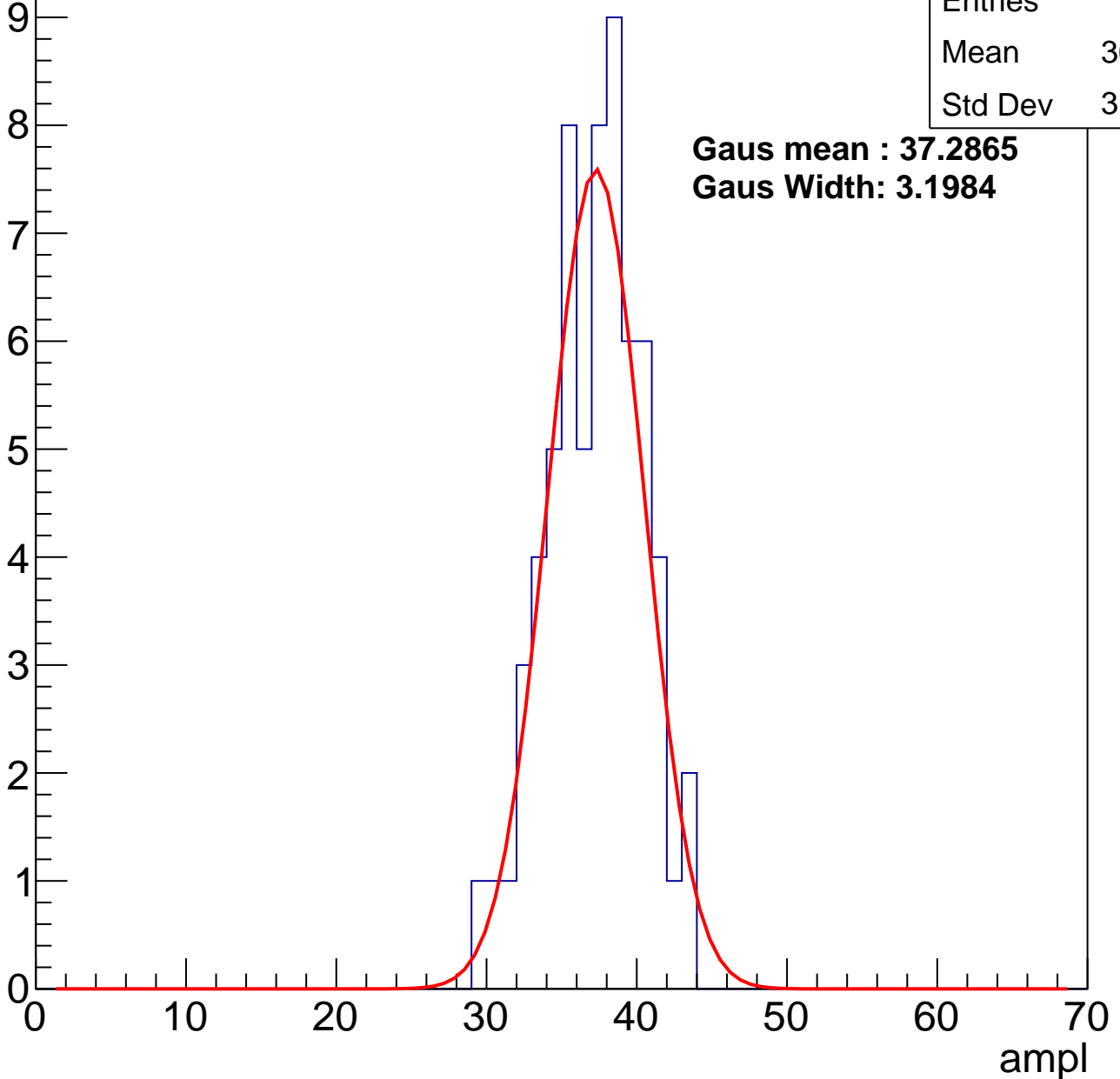
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	36.75
Std Dev	3.097

Gaus mean : 37.2865

Gaus Width: 3.1984



B0L001S, U15-ch88, adc2

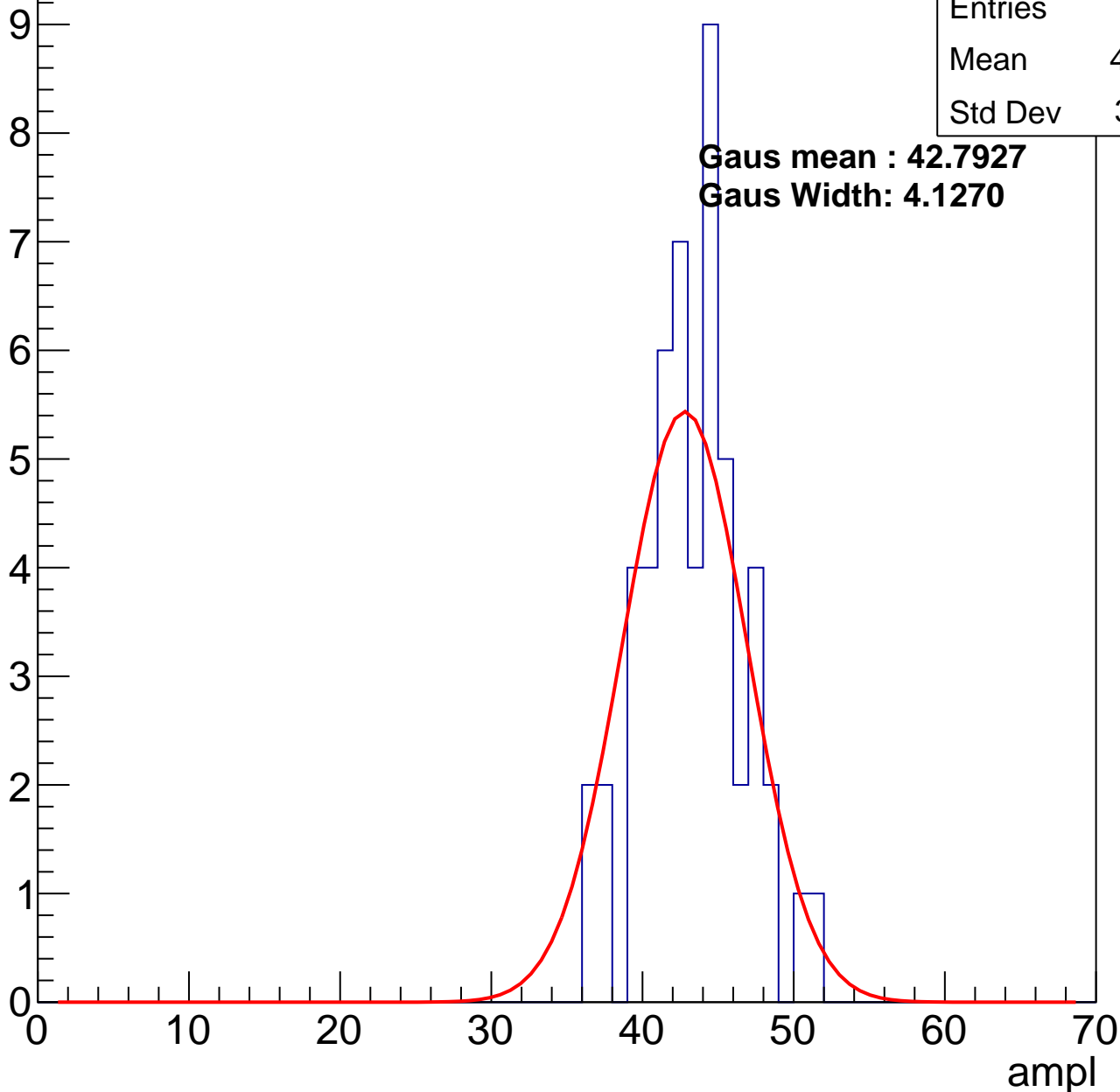
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.87
Std Dev	3.291

Gaus mean : 42.7927

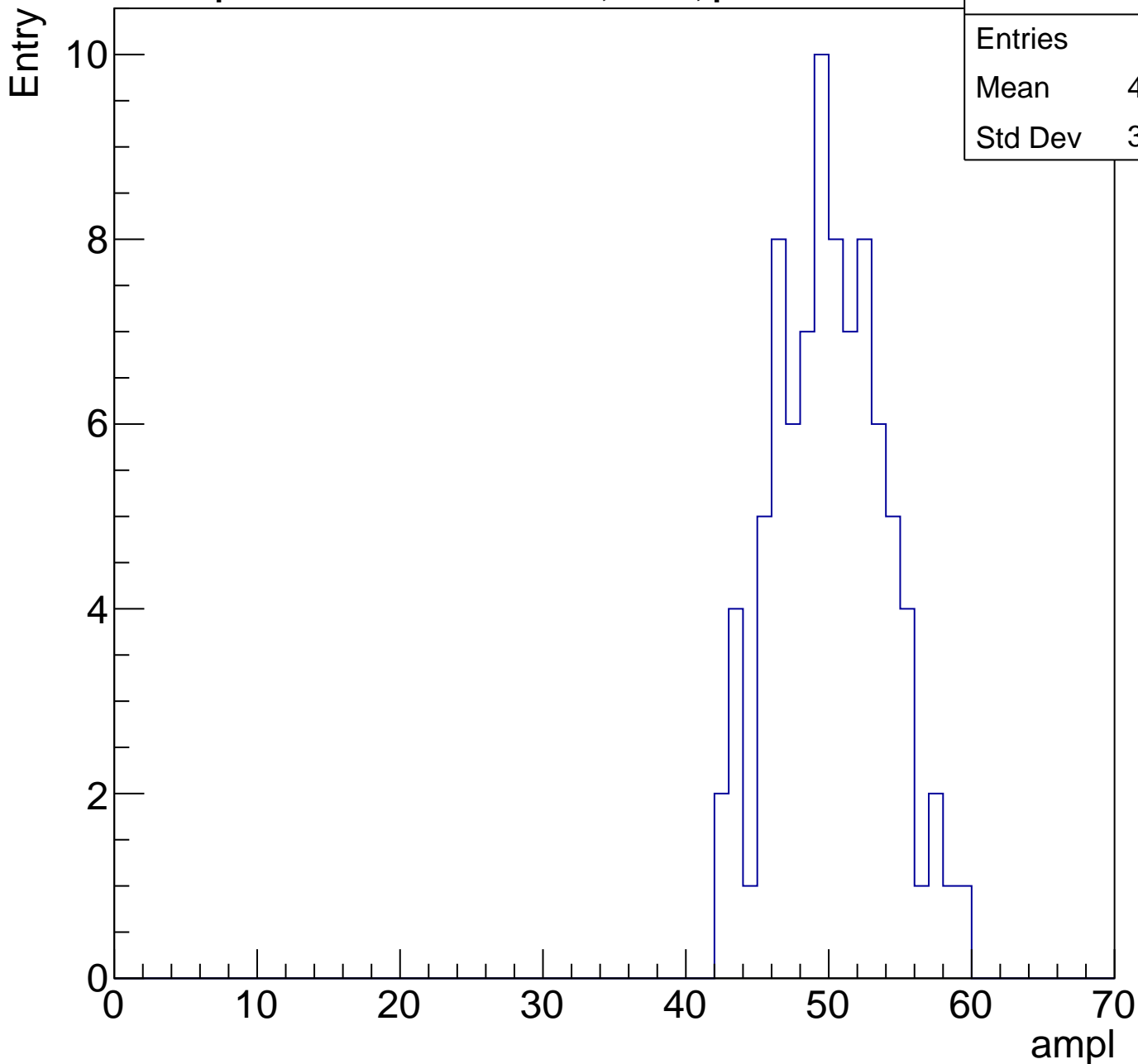
Gaus Width: 4.1270



B0L001S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	49.64
Std Dev	3.806

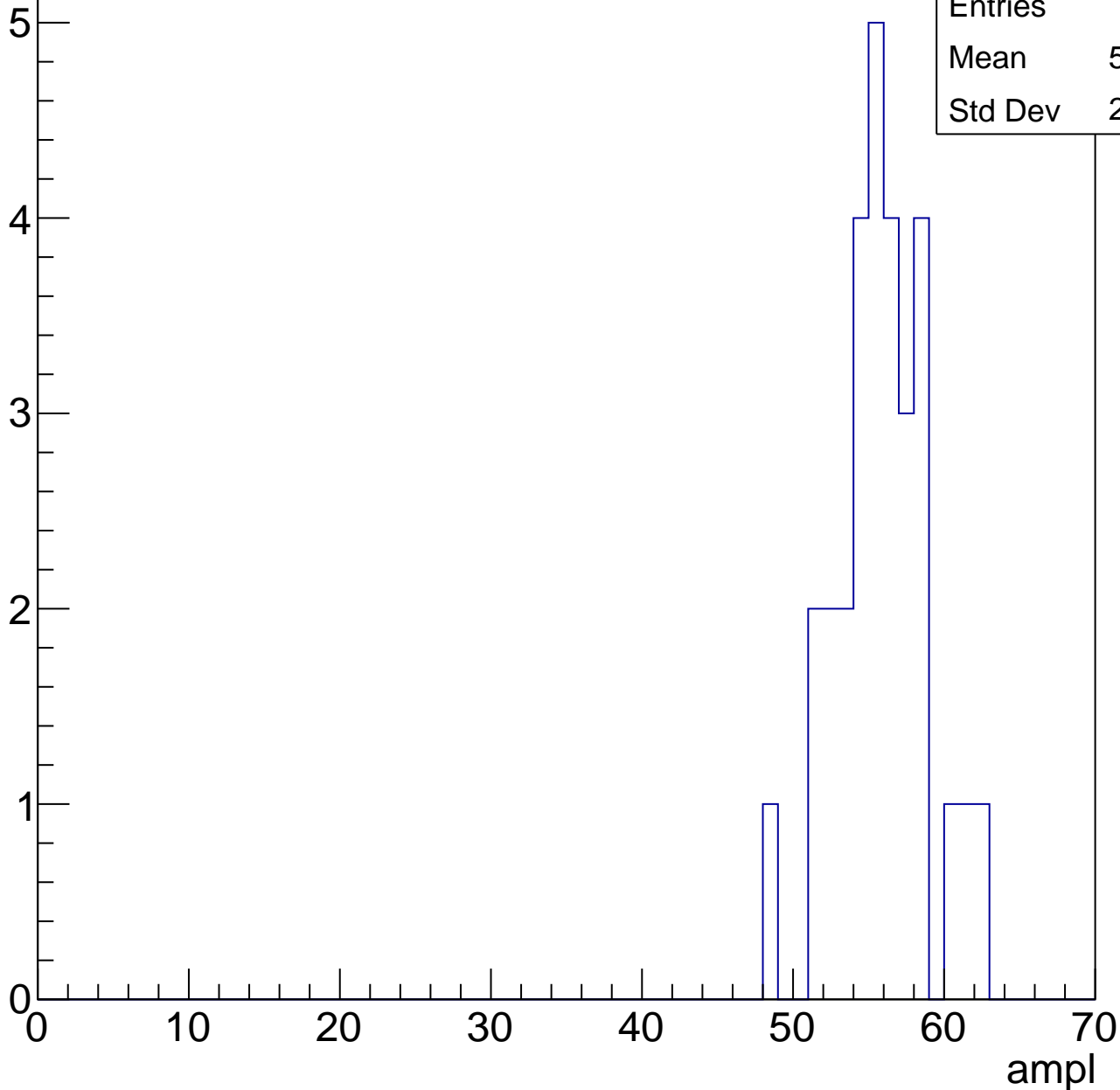


B0L001S, U15-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

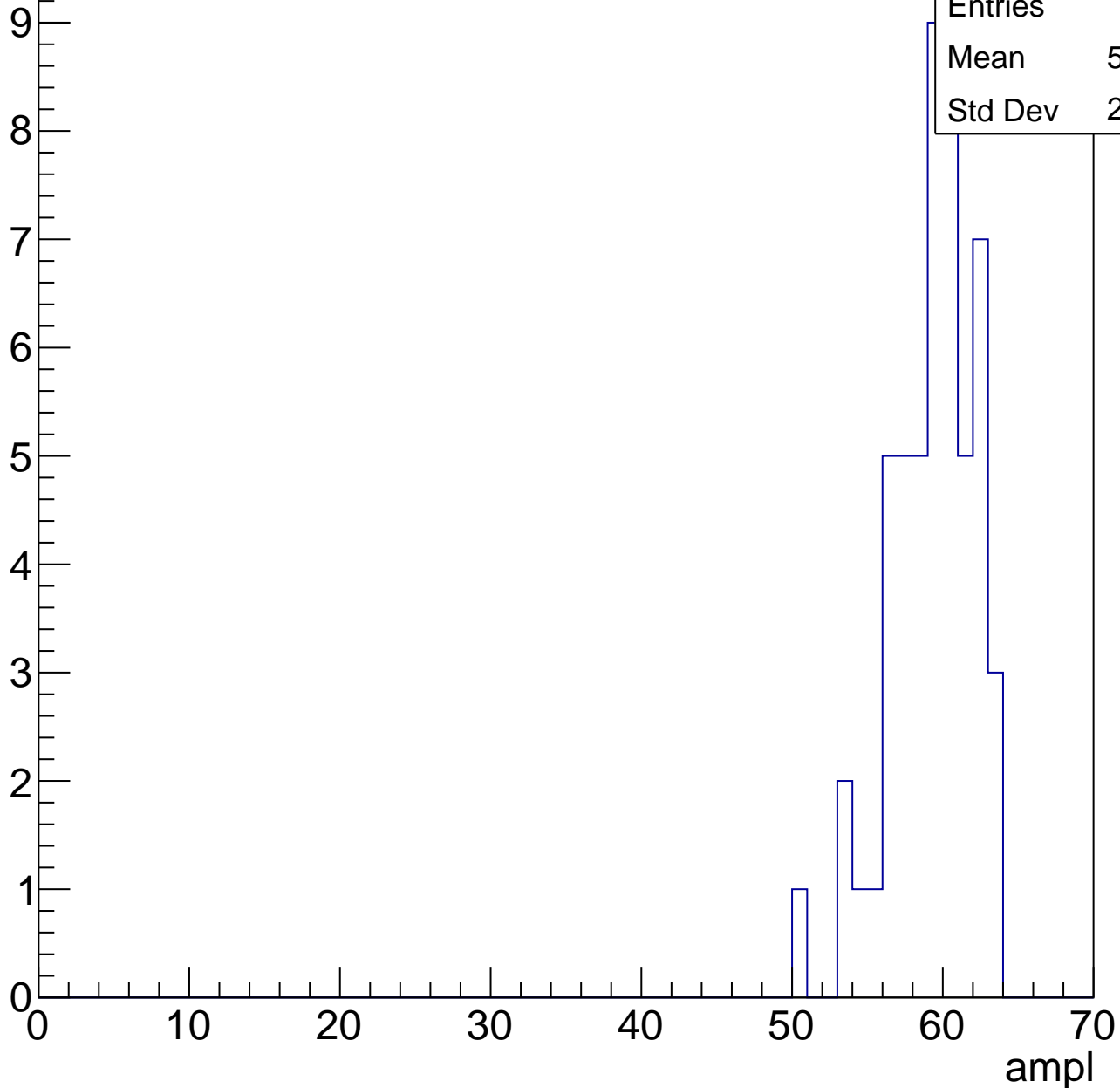
Entries	30
Mean	55.37
Std Dev	2.994



B0L001S, U15-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

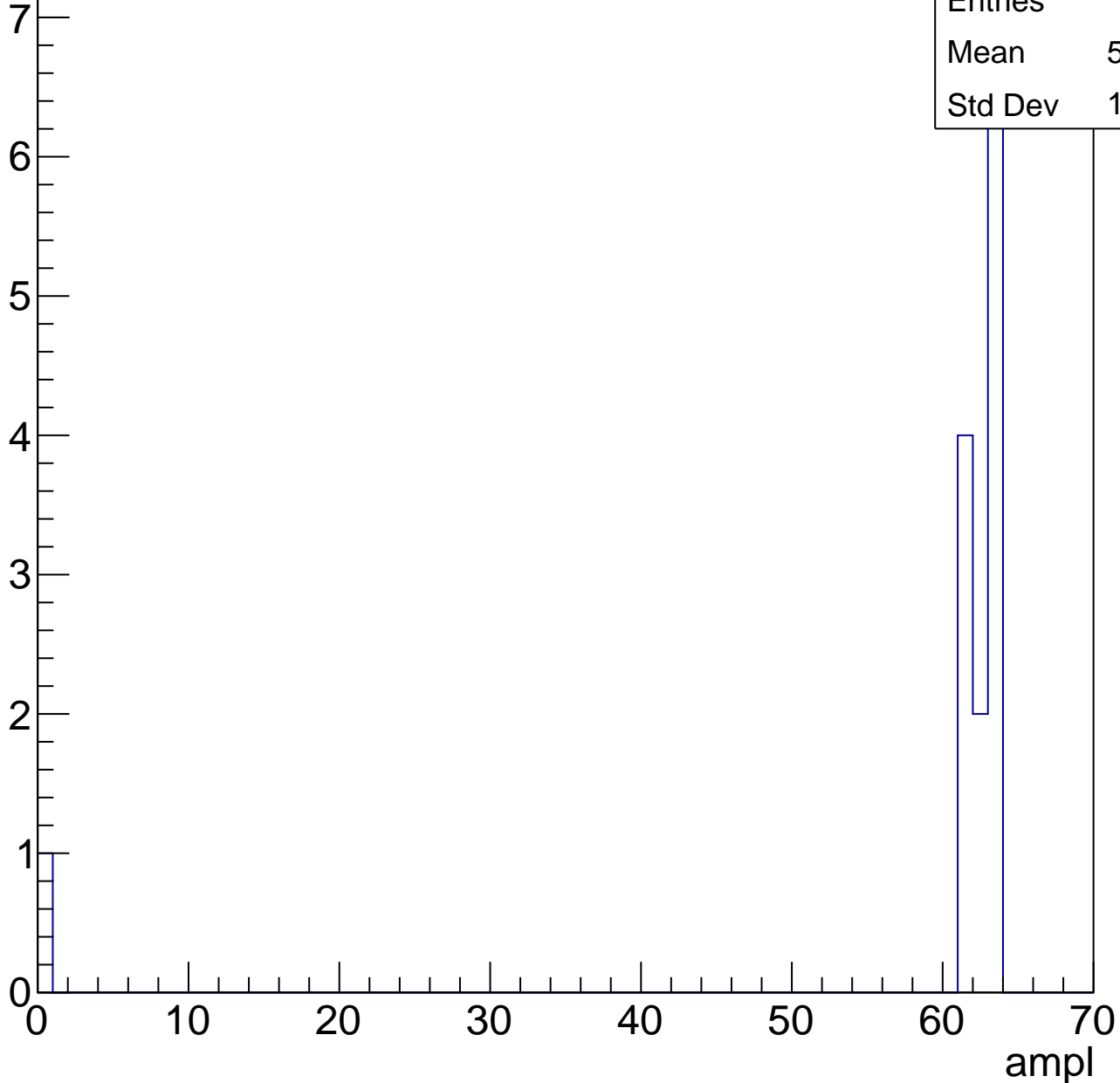


B0L001S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	57.79
Std Dev	16.05



B0L001S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch89, adc0

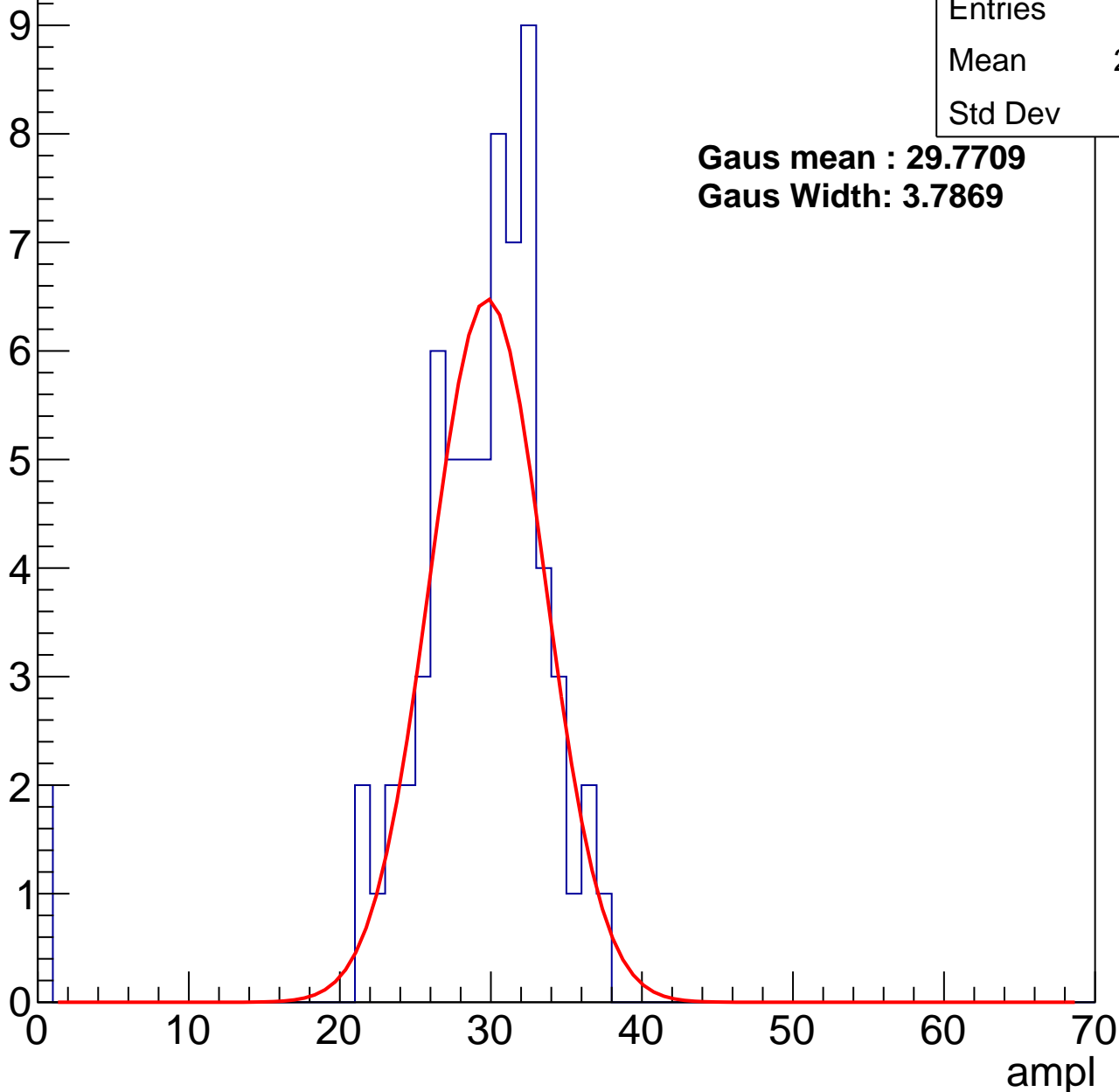
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.41
Std Dev	6.11

Gaus mean : 29.7709

Gaus Width: 3.7869



B0L001S, U15-ch89, adc1

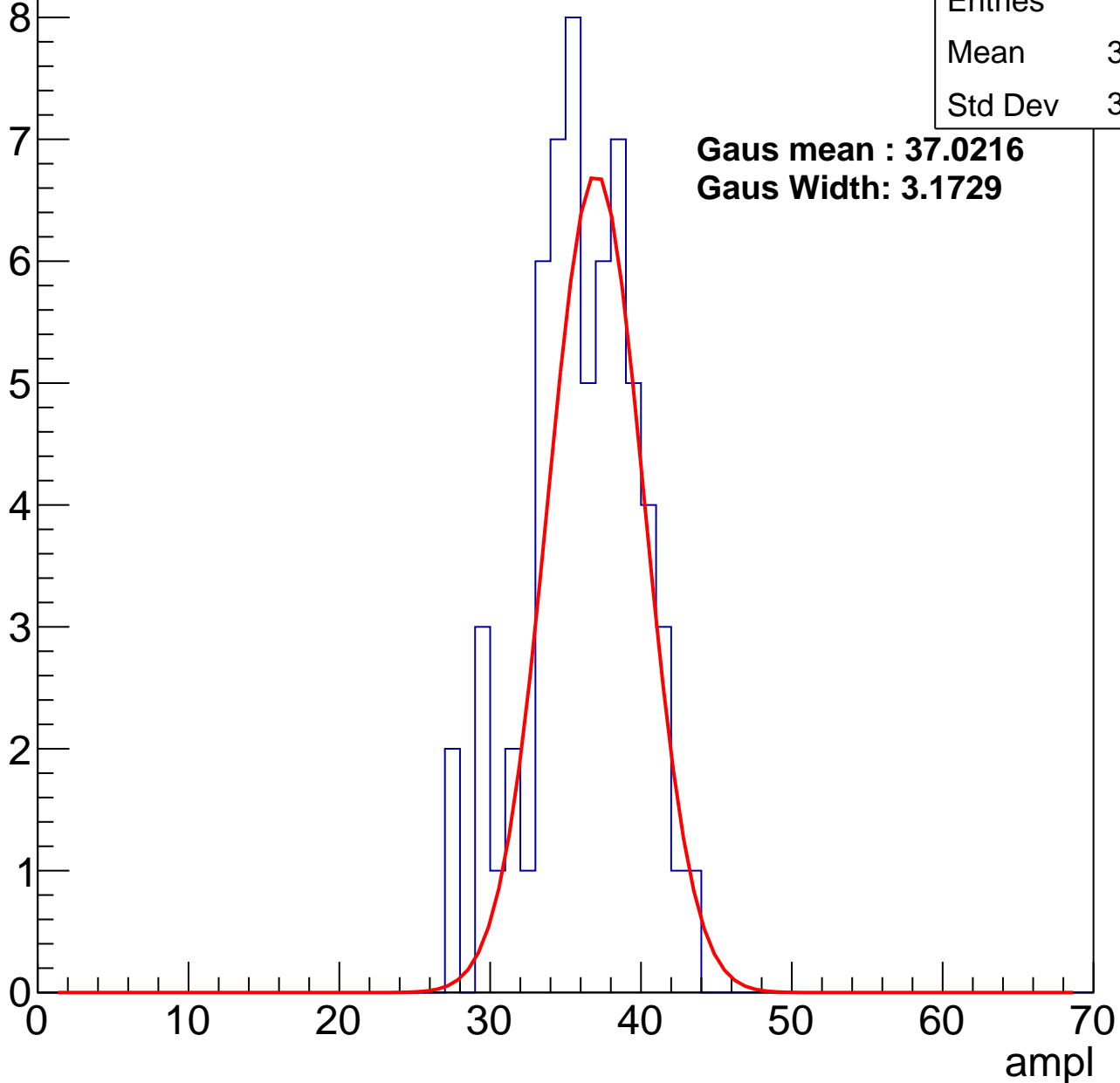
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	35.68
Std Dev	3.587

Gaus mean : 37.0216

Gaus Width: 3.1729



B0L001S, U15-ch89, adc2

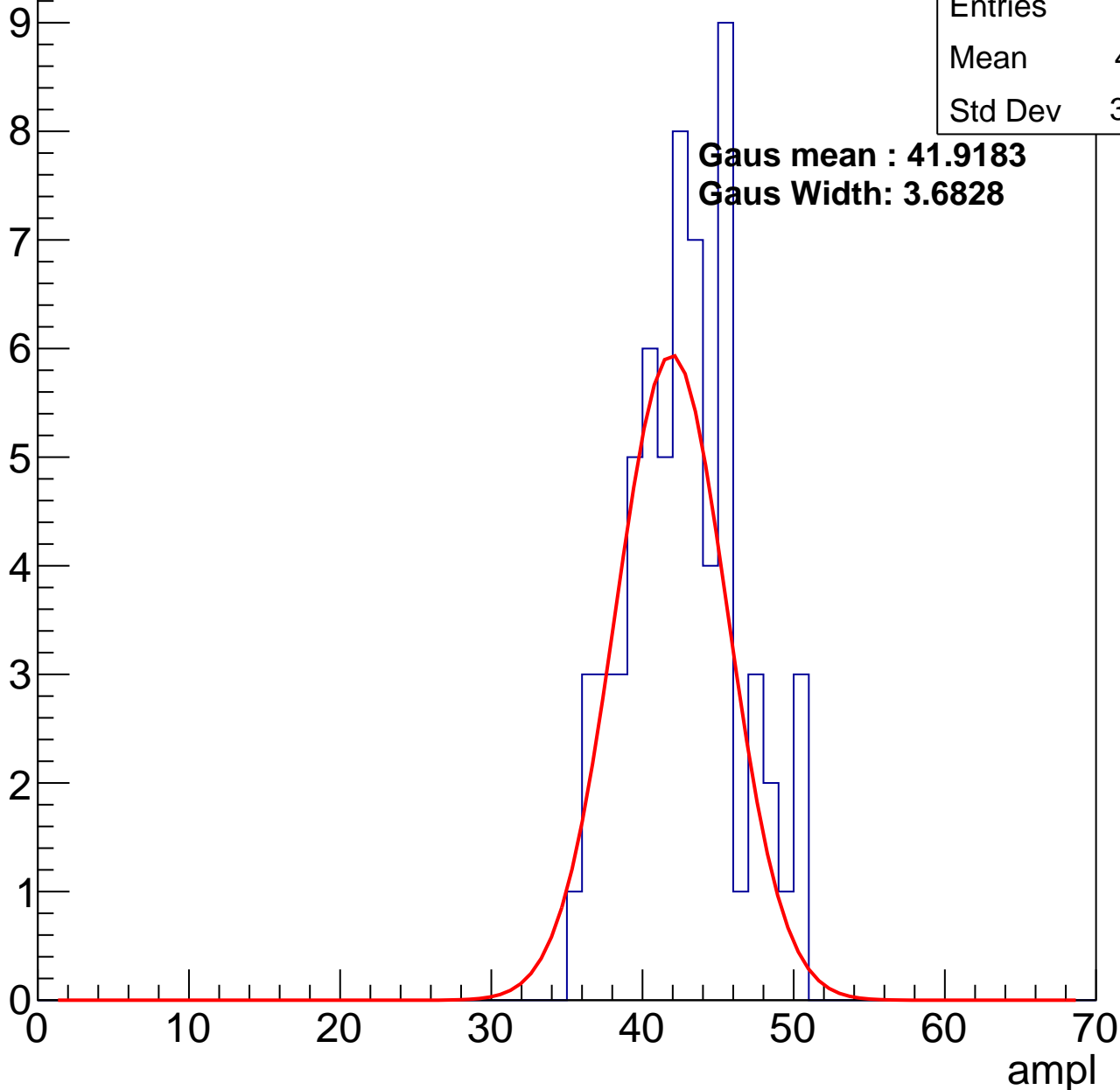
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	42.31
Std Dev	3.657

Gaus mean : 41.9183

Gaus Width: 3.6828

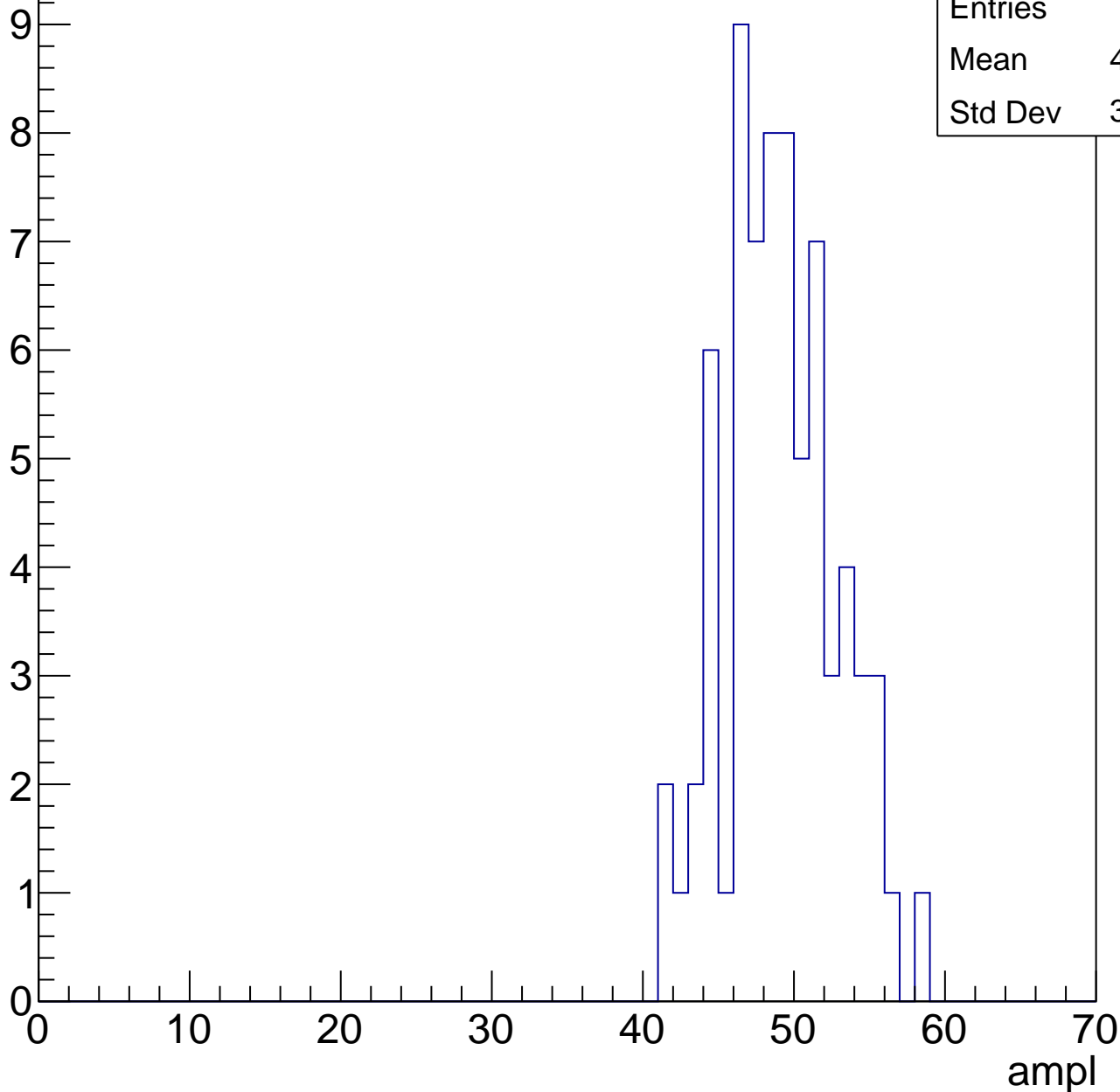


B0L001S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	48.65
Std Dev	3.677

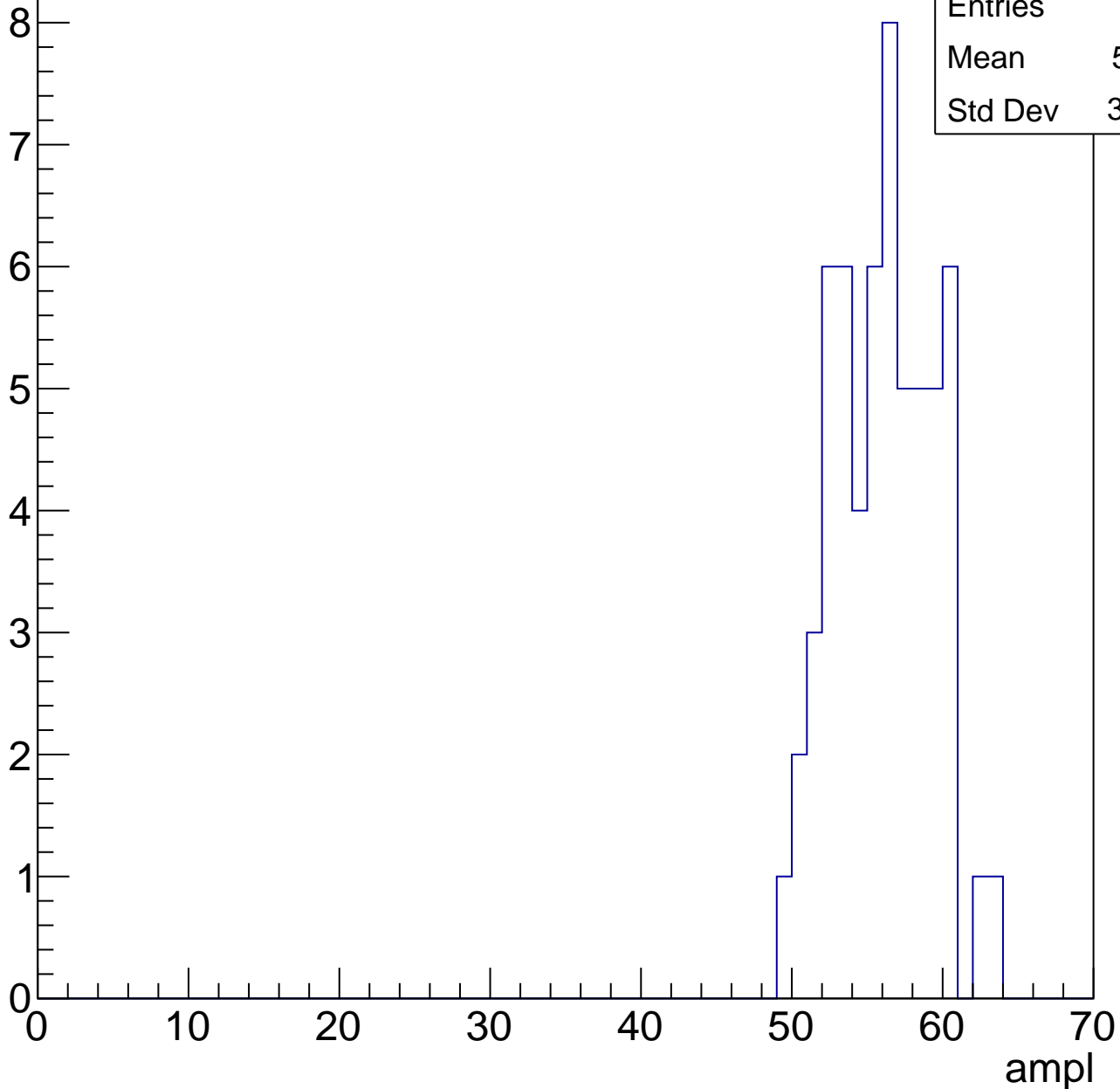


B0L001S, U15-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.61
Std Dev	3.216

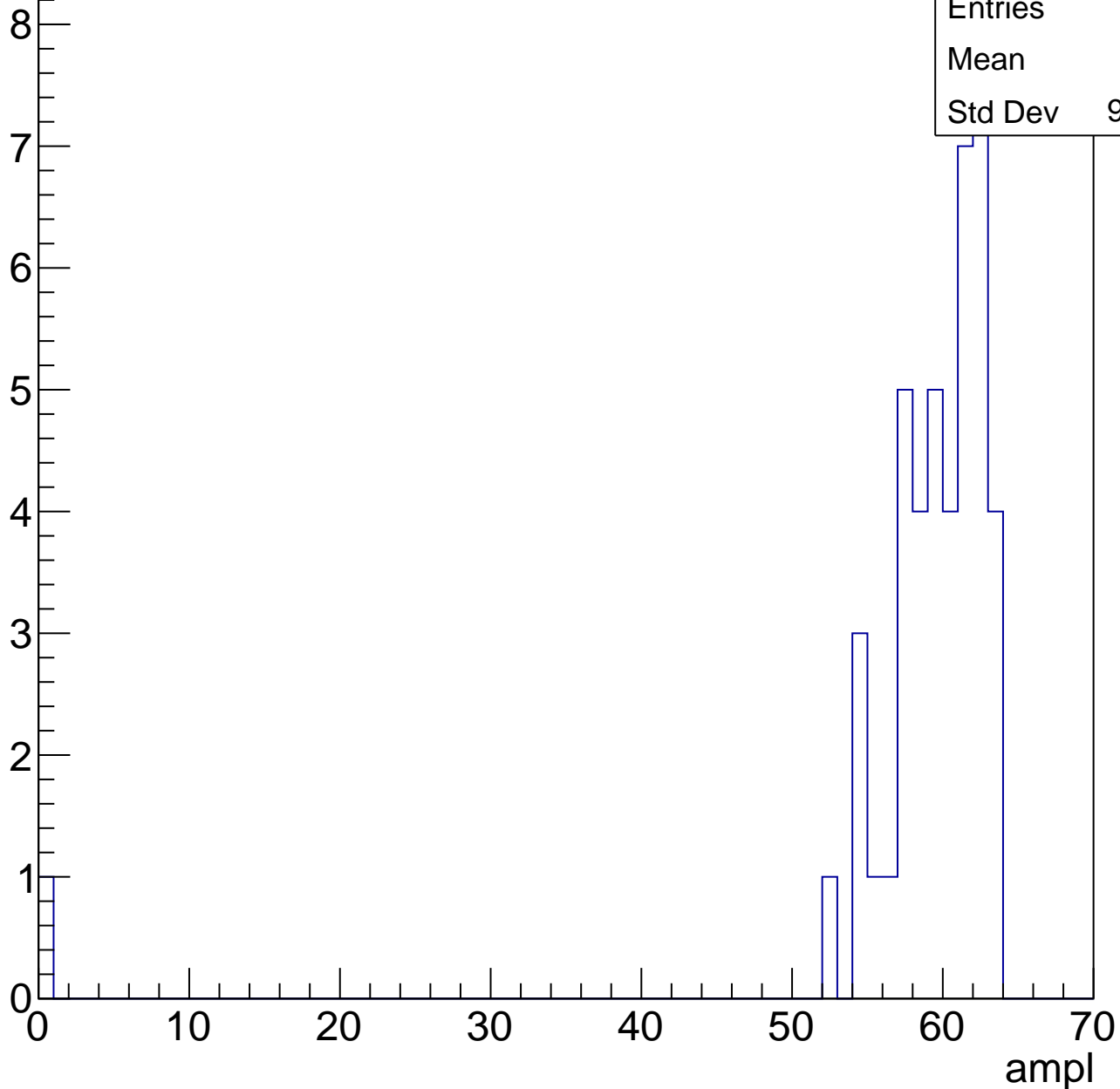


B0L001S, U15-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58
Std Dev	9.266

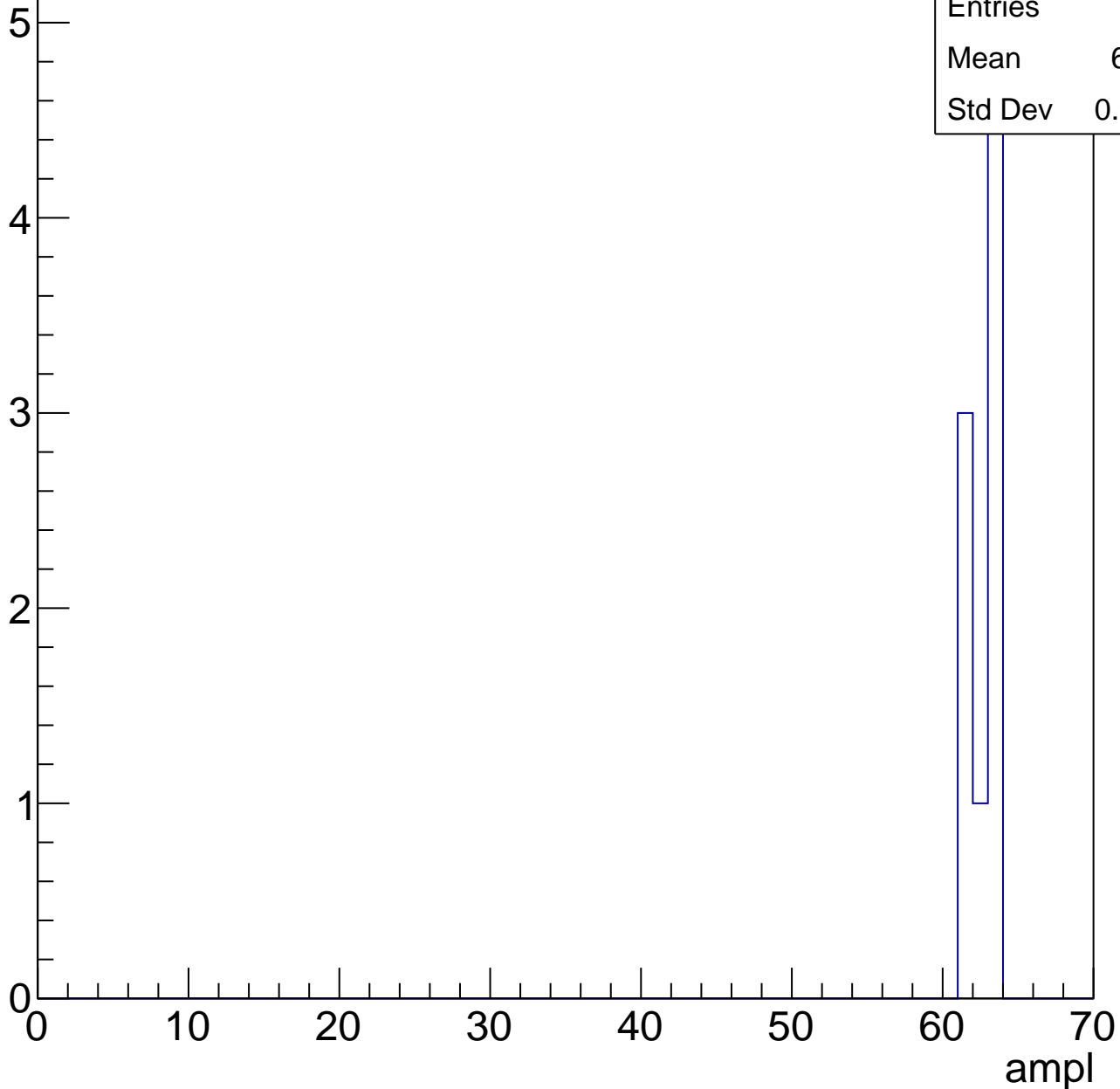


B0L001S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	62.22
Std Dev	0.9162



B0L001S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch90, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	31.25
Std Dev	3.91

Gaus mean : 31.8577

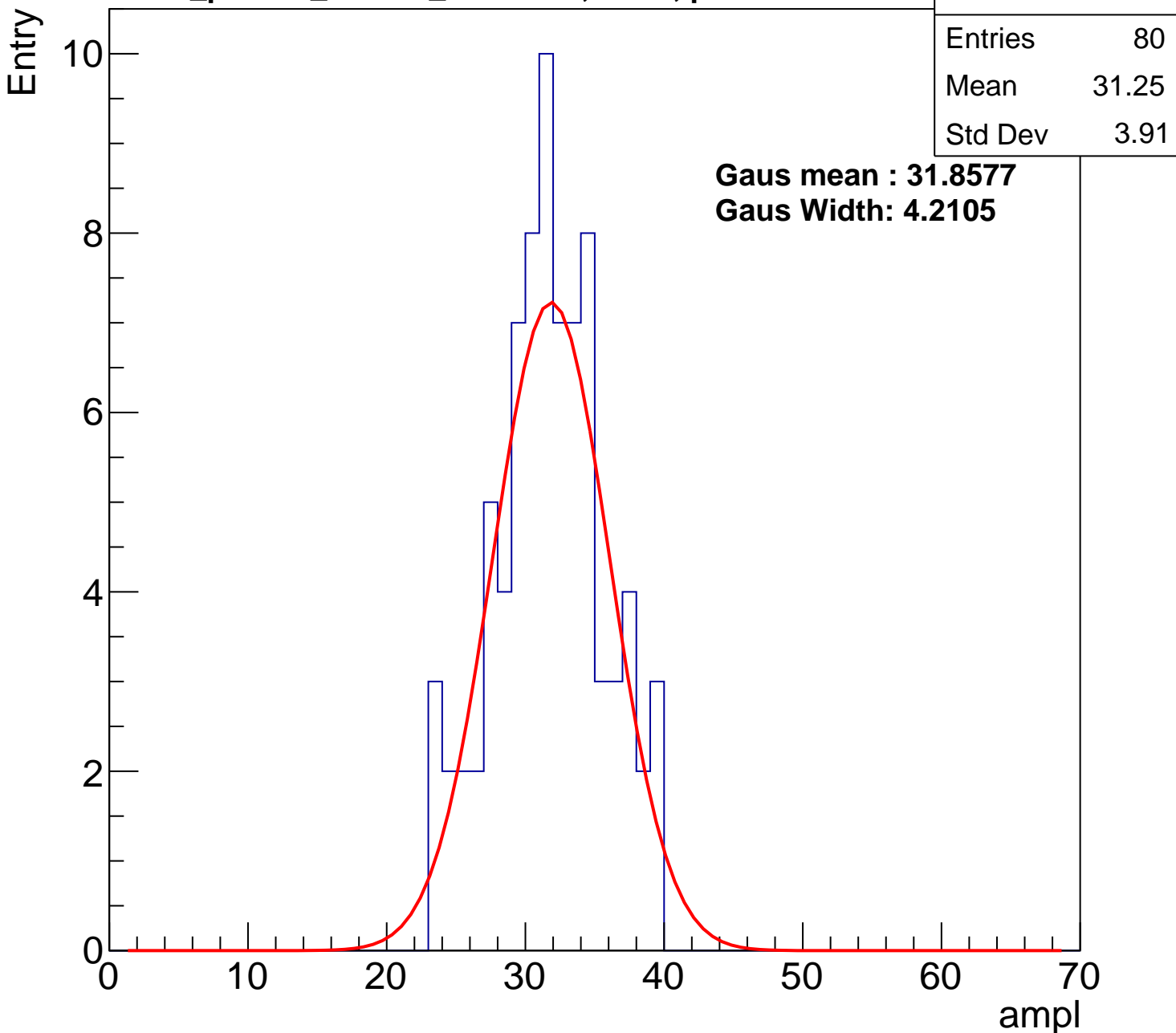
Gaus Width: 4.2105

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch90, adc1

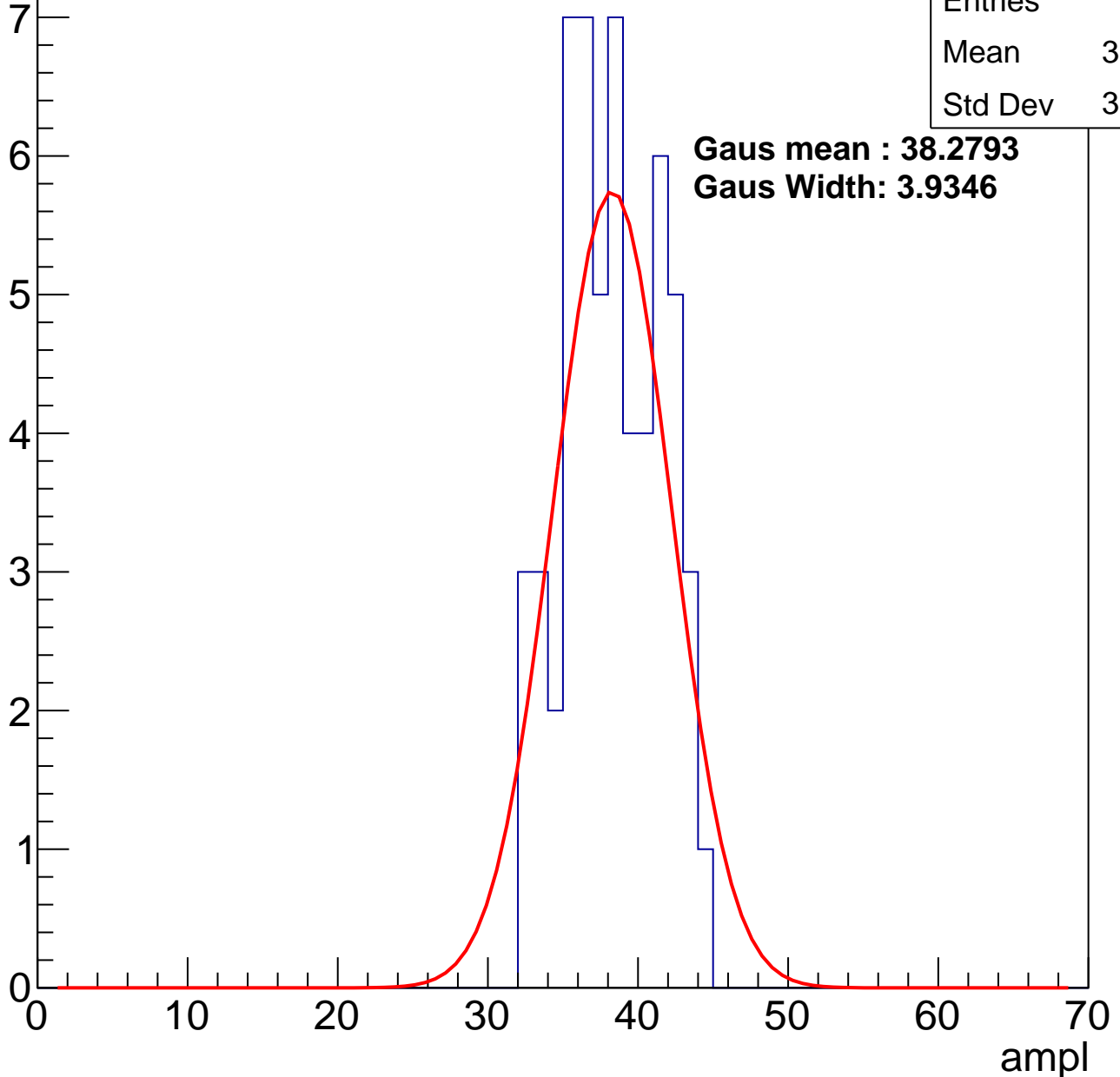
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.82
Std Dev	3.174

Gaus mean : 38.2793

Gaus Width: 3.9346



B0L001S, U15-ch90, adc2

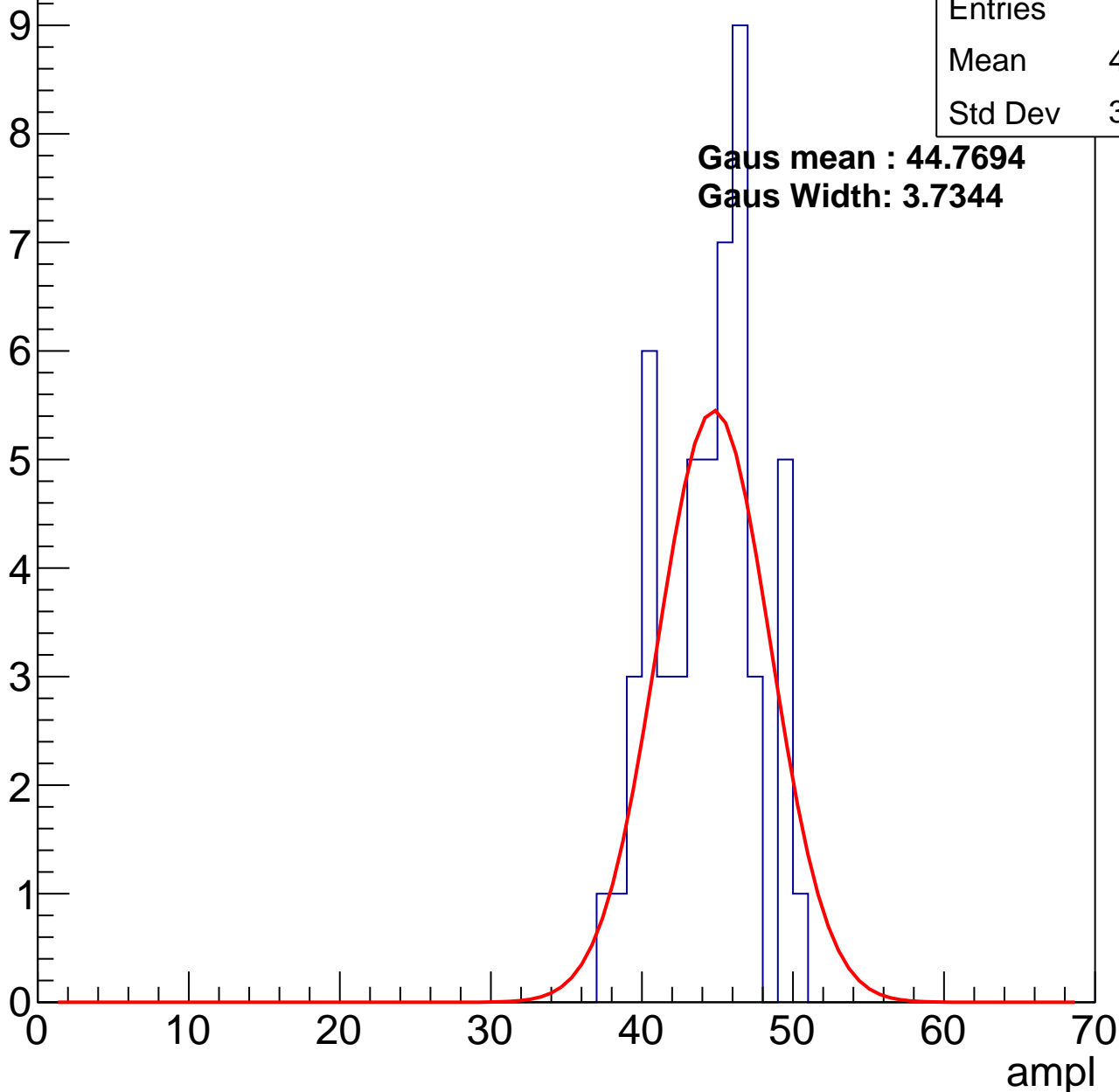
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.87
Std Dev	3.193

Gaus mean : 44.7694

Gaus Width: 3.7344



B0L001S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

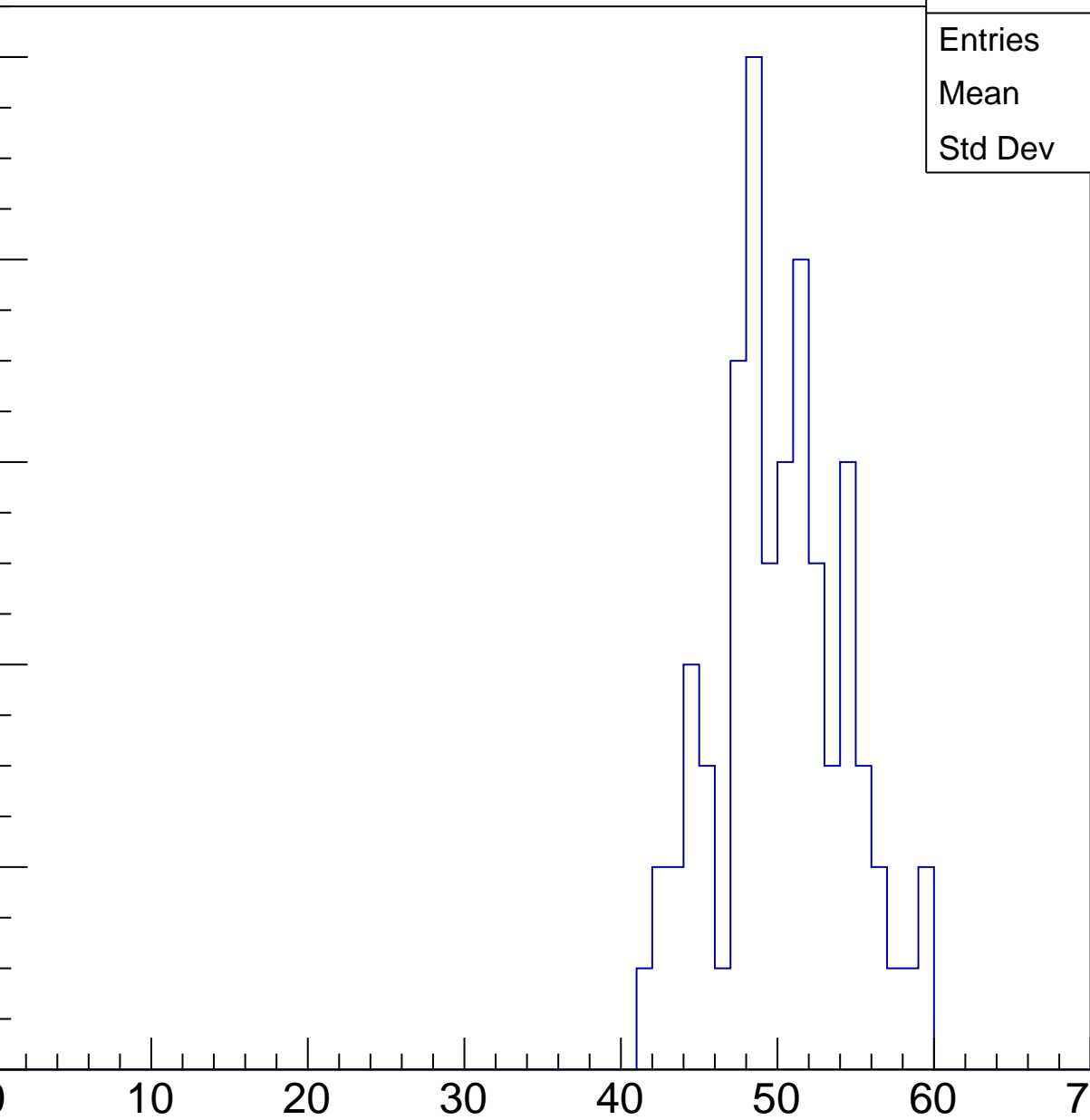
Entries	72
Mean	49.76
Std Dev	4.115

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

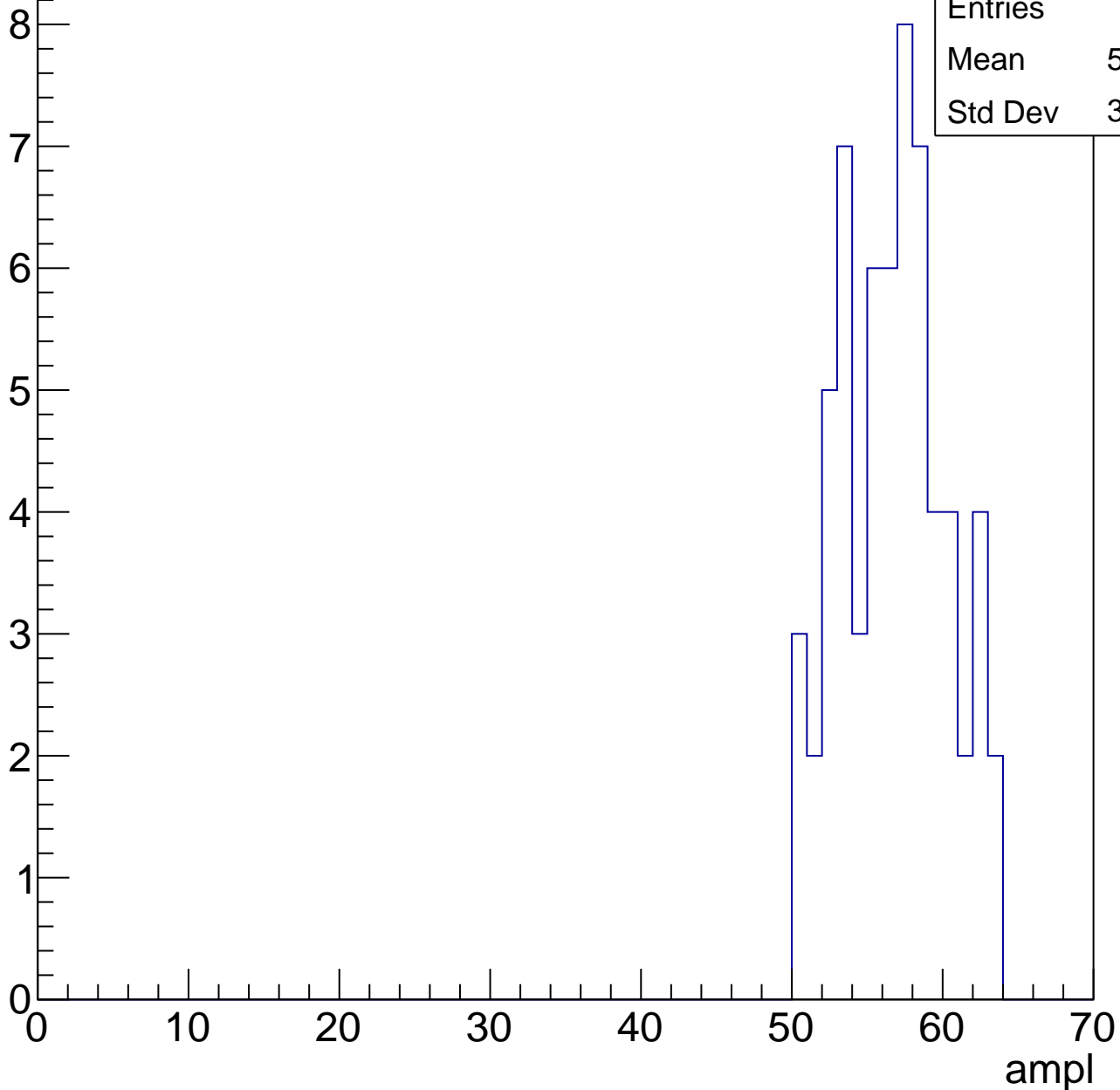


B0L001S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

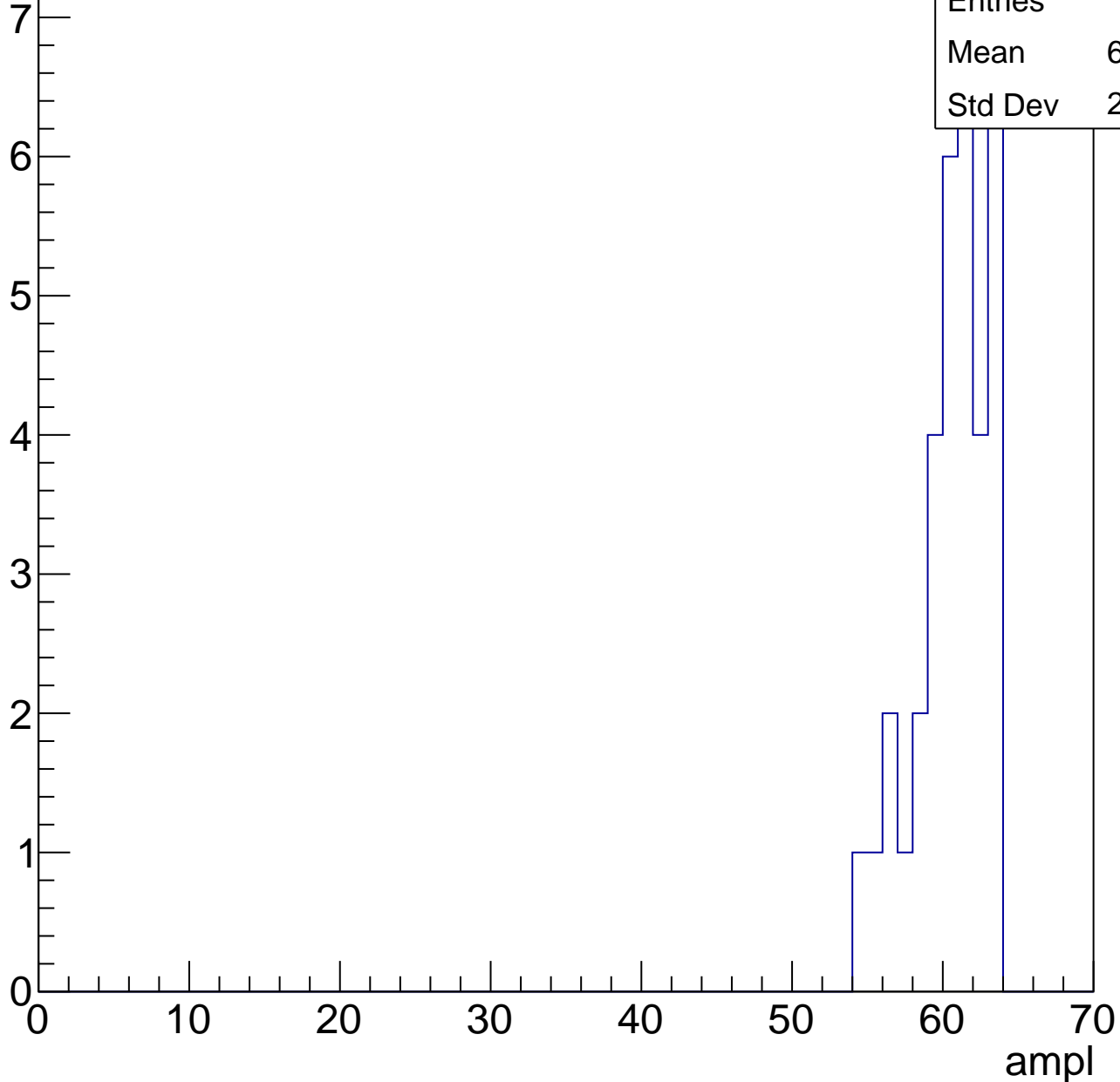
Entries	63
Mean	56.27
Std Dev	3.433



B0L001S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch91, adc0

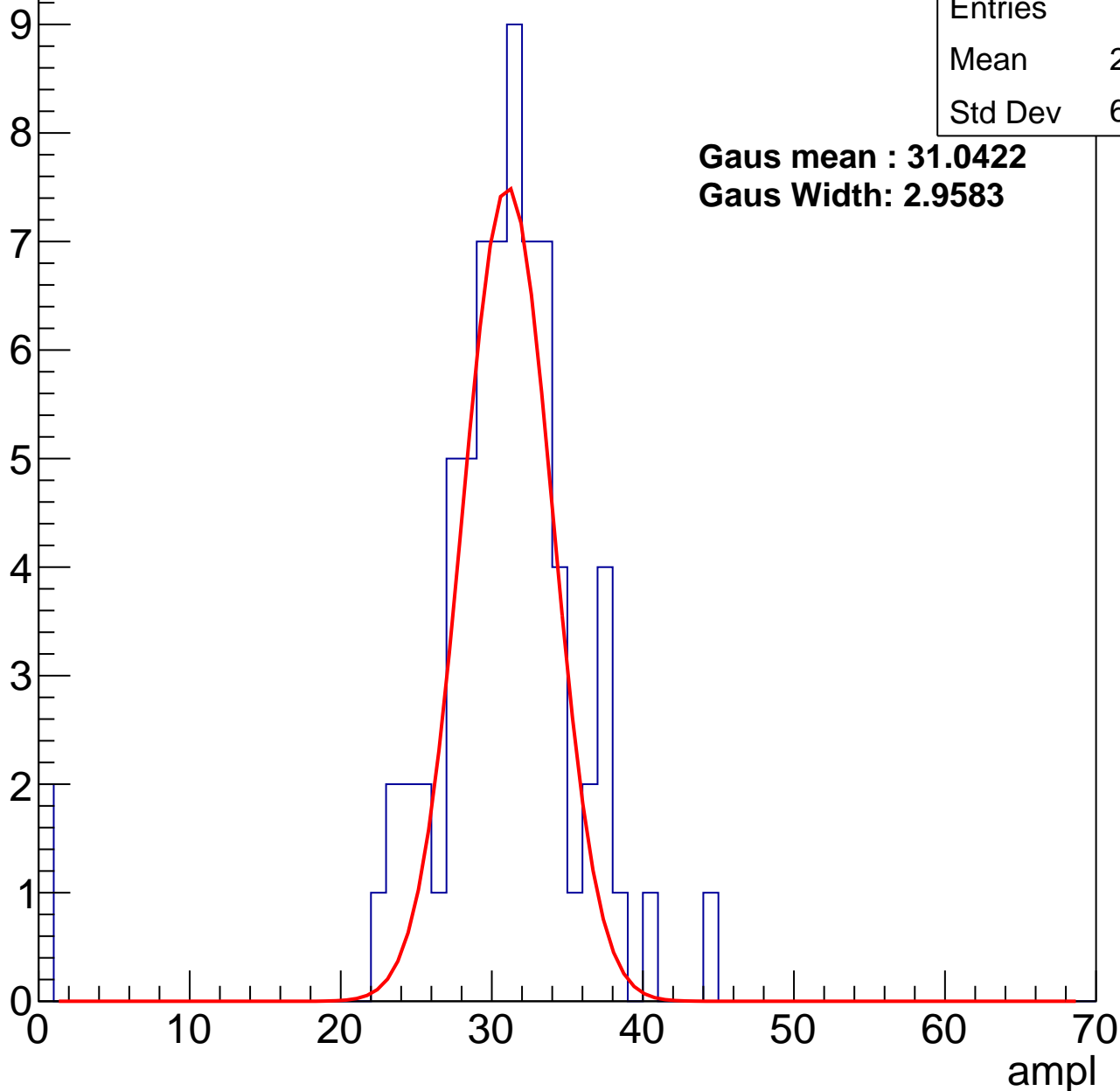
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.96
Std Dev	6.488

Gaus mean : 31.0422

Gaus Width: 2.9583



B0L001S, U15-ch91, adc1

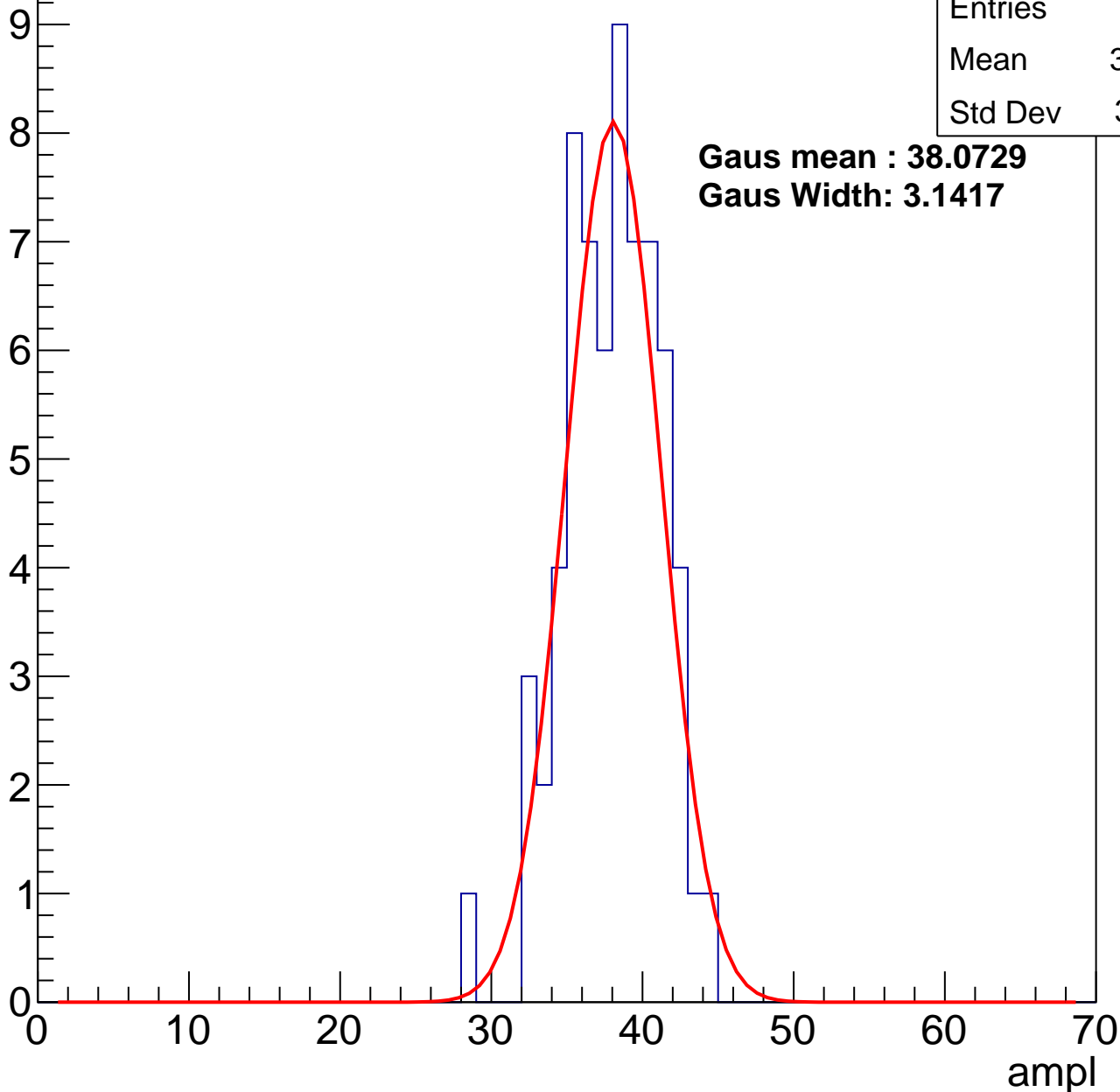
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.52
Std Dev	3.081

Gaus mean : 38.0729

Gaus Width: 3.1417



B0L001S, U15-ch91, adc2

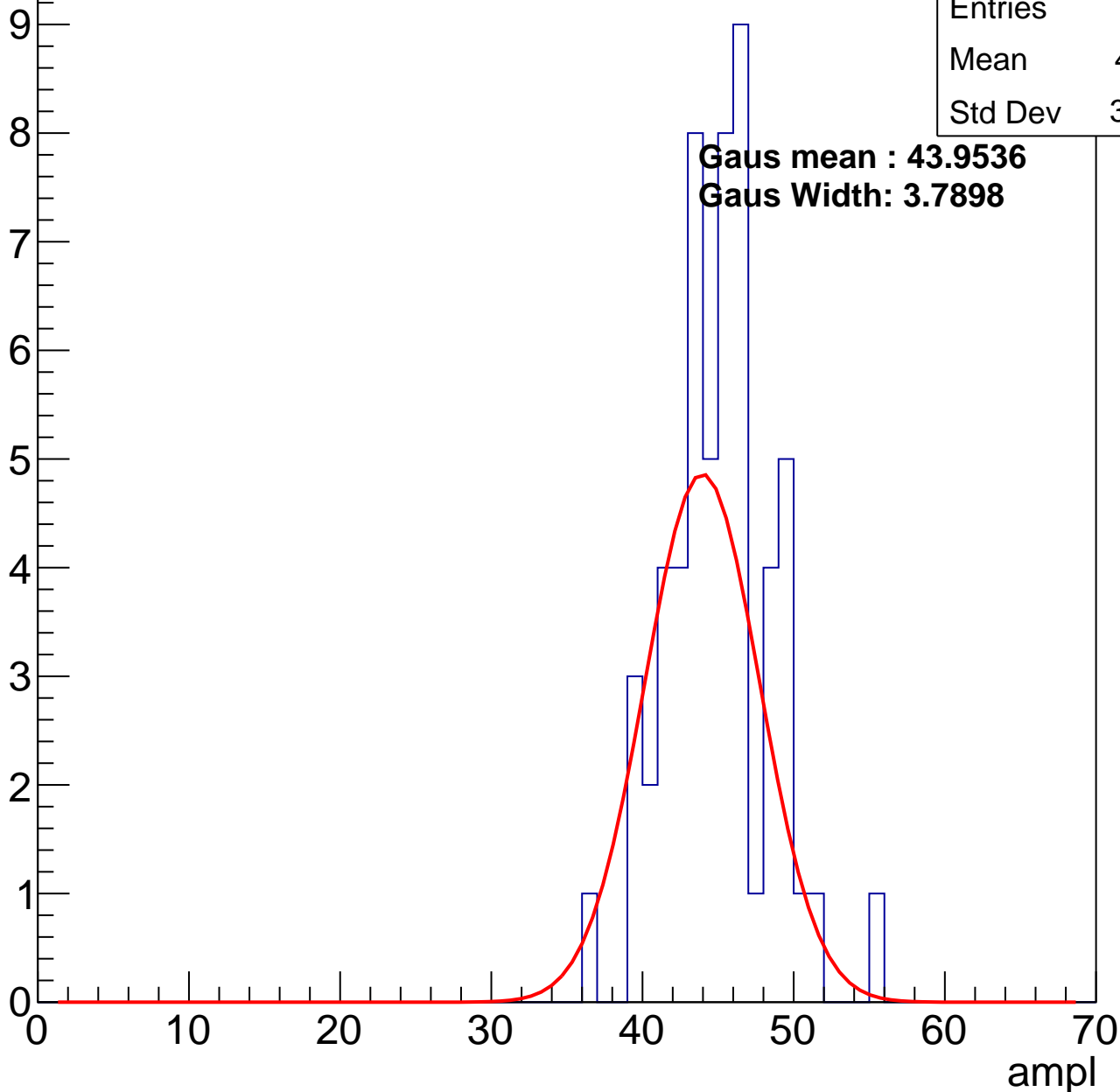
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.61
Std Dev	3.396

Gaus mean : 43.9536

Gaus Width: 3.7898

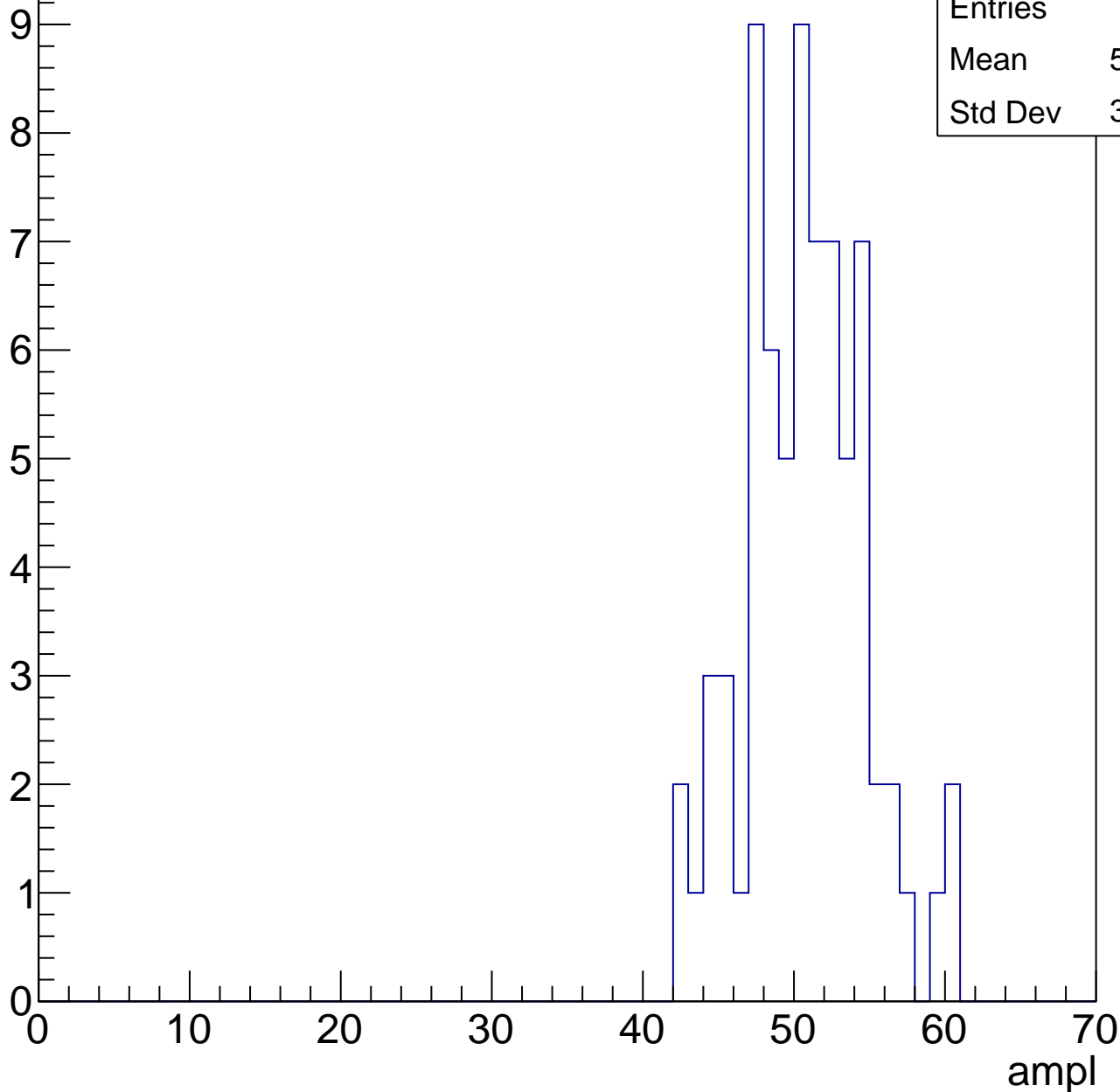


B0L001S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.25
Std Dev	3.927

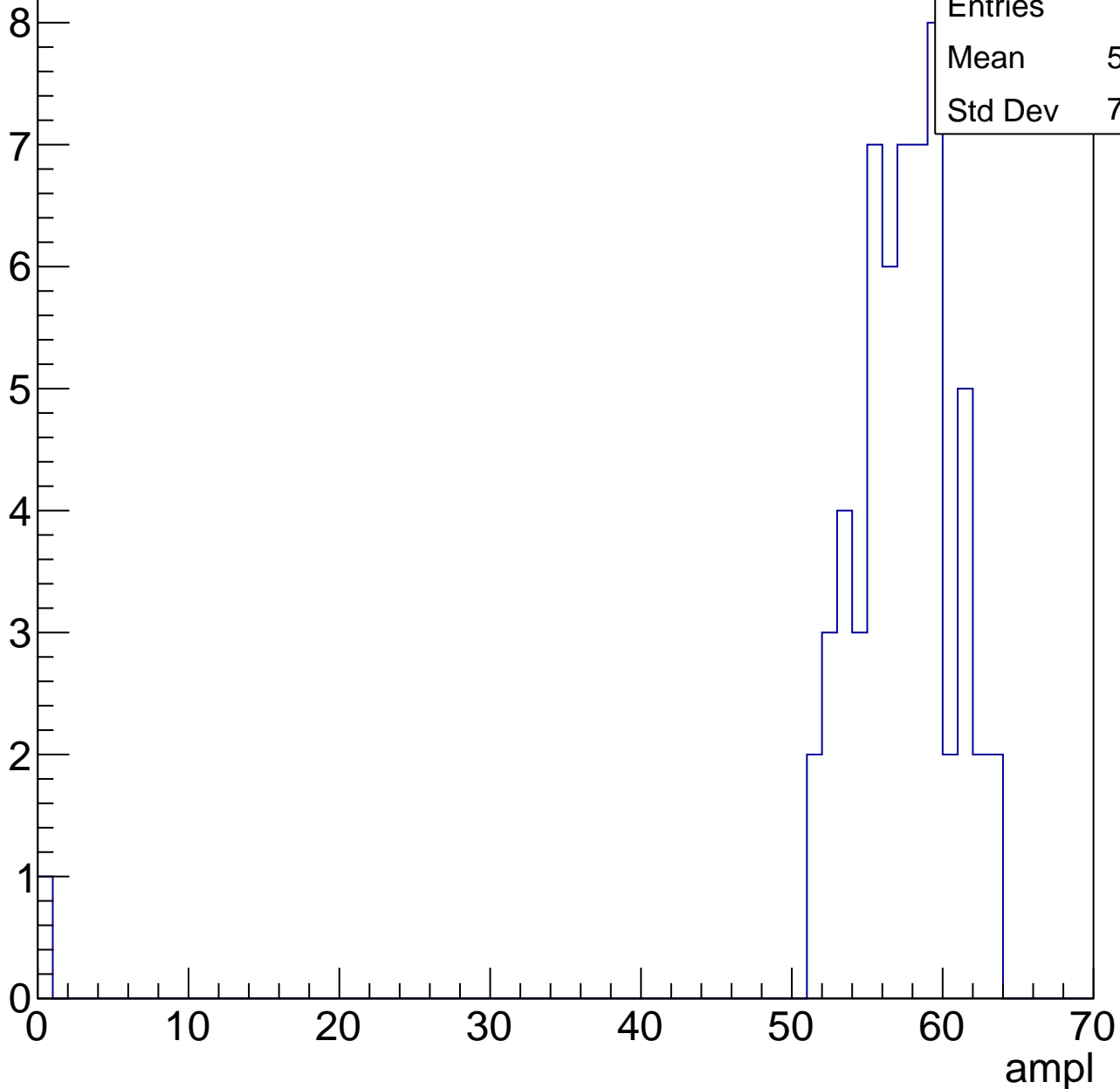


B0L001S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.02
Std Dev	7.944

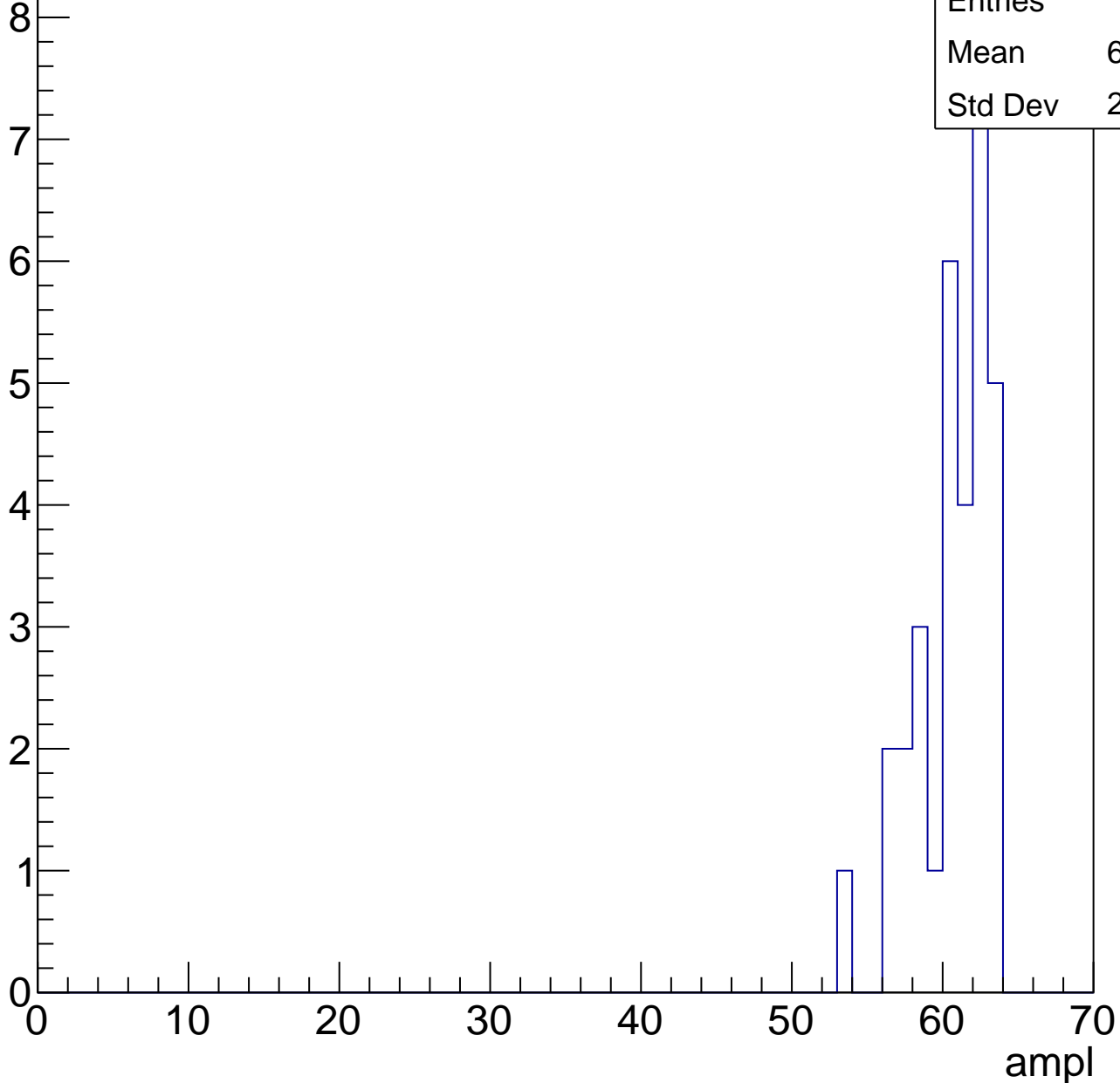


B0L001S, U15-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.22
Std Dev	2.446



B0L001S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch92, adc0

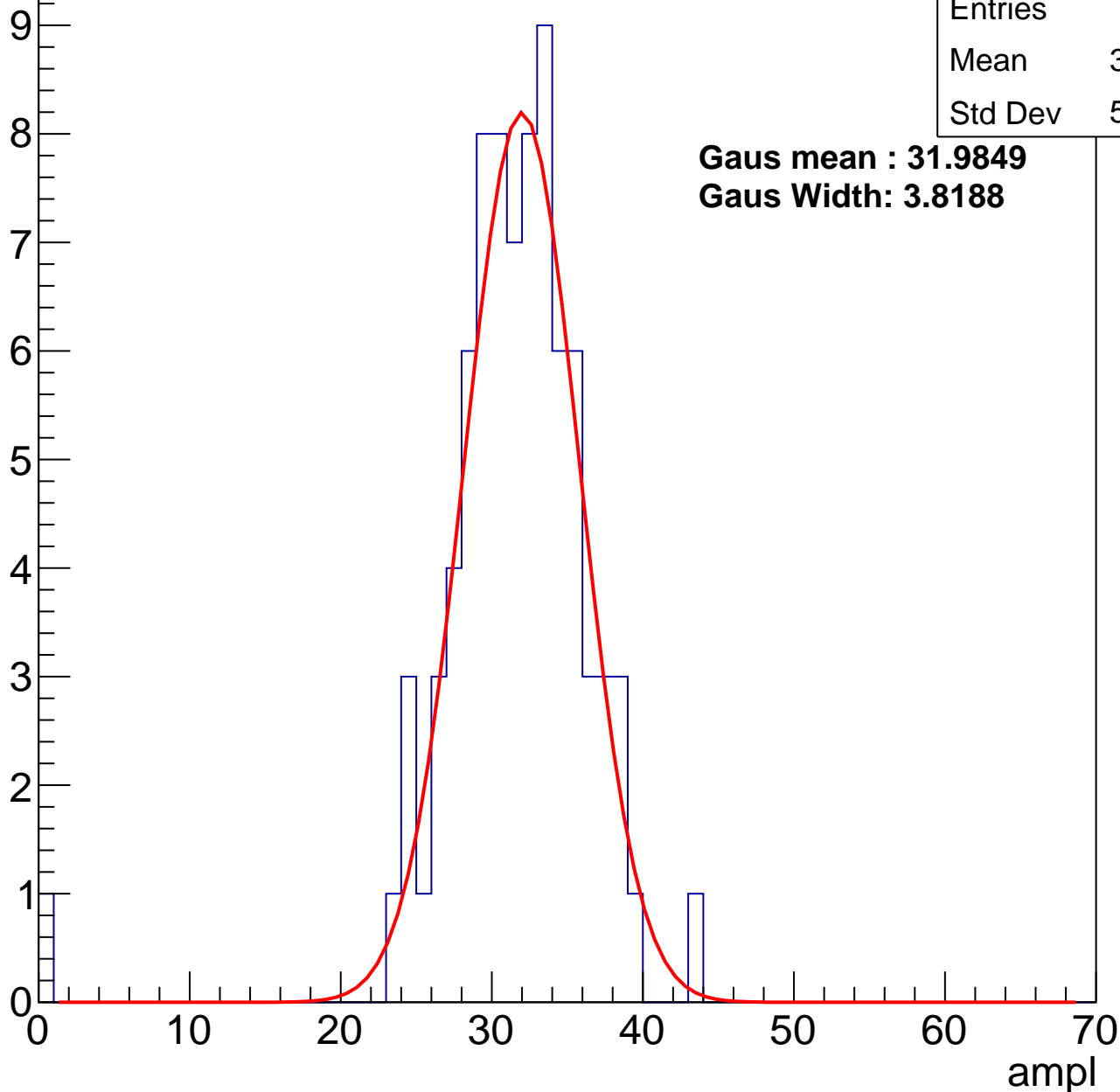
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	31.04
Std Dev	5.148

Gaus mean : 31.9849

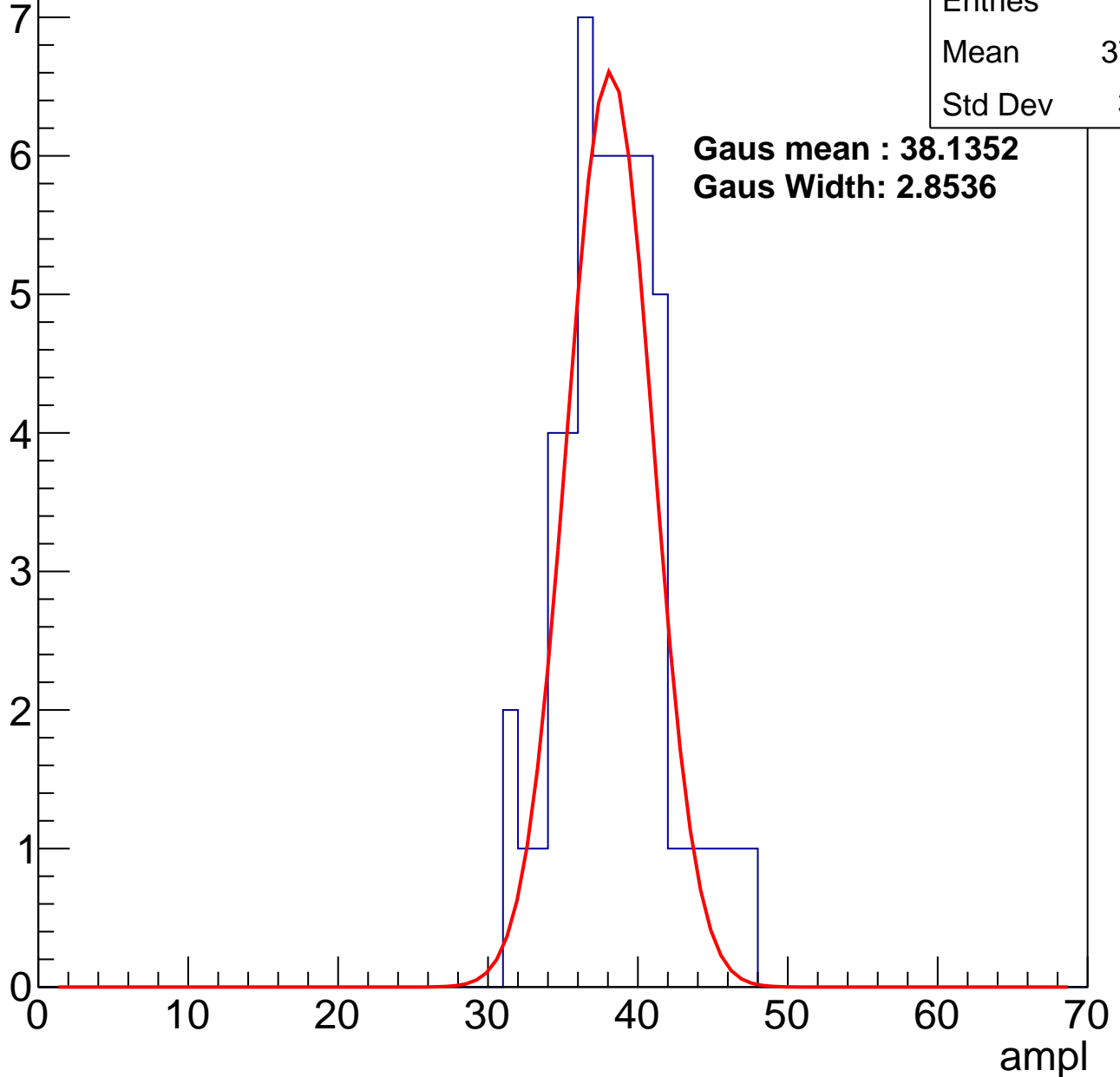
Gaus Width: 3.8188



B0L001S, U15-ch92, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch92, adc2

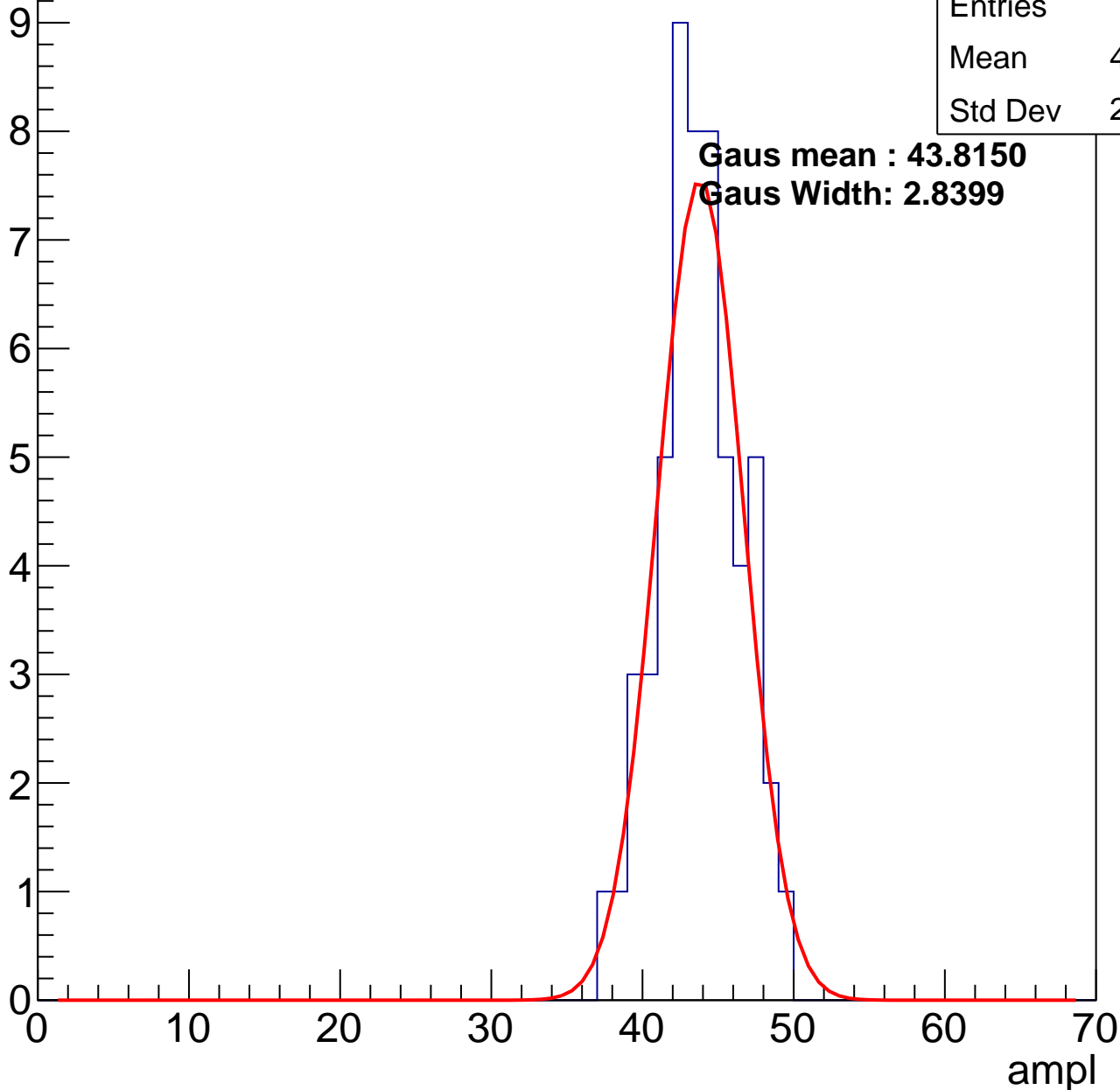
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.27
Std Dev	2.666

Gaus mean : 43.8150

Gaus Width: 2.8399

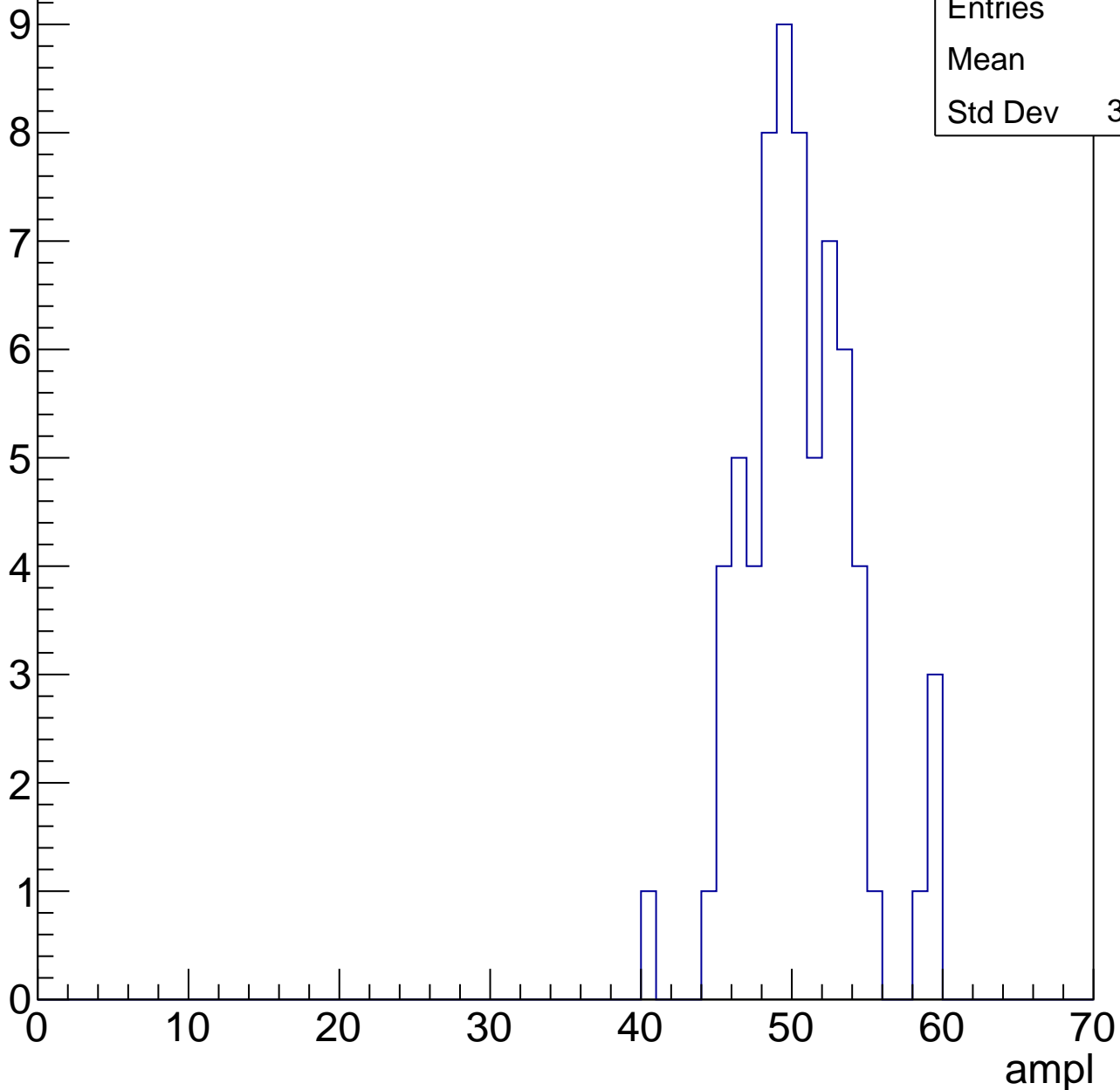


B0L001S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50
Std Dev	3.616

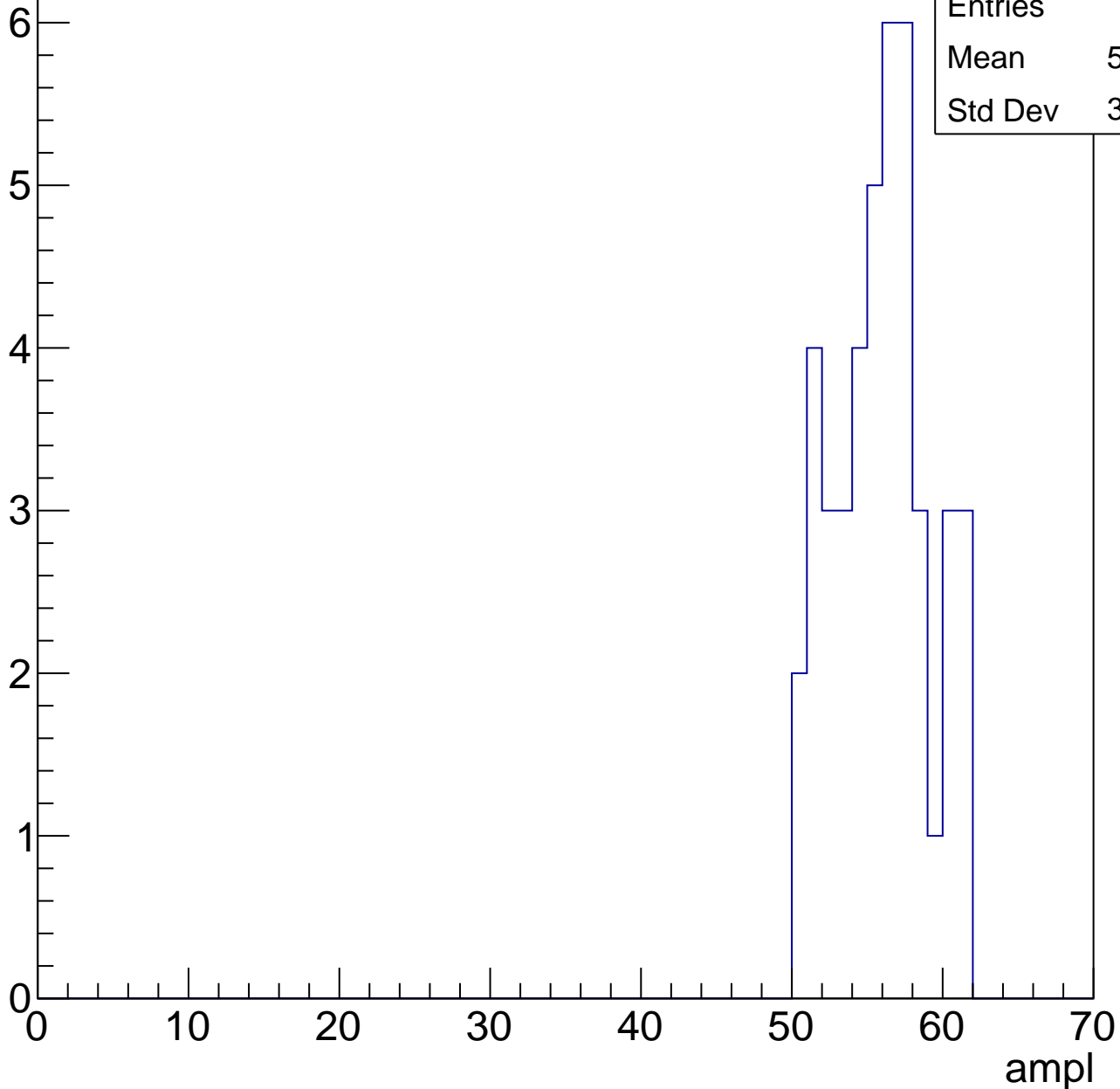


B0L001S, U15-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	55.44
Std Dev	3.067

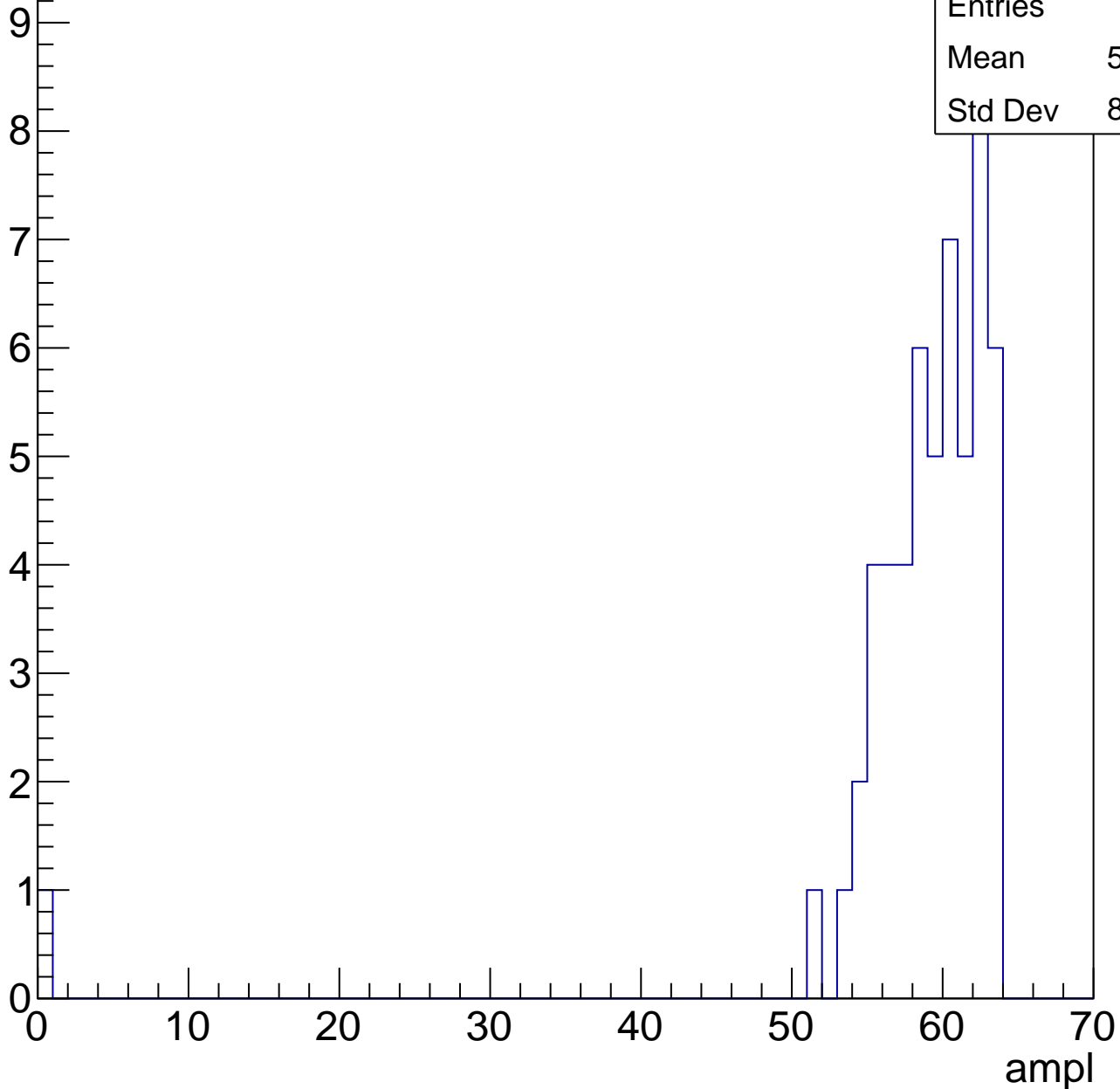


B0L001S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

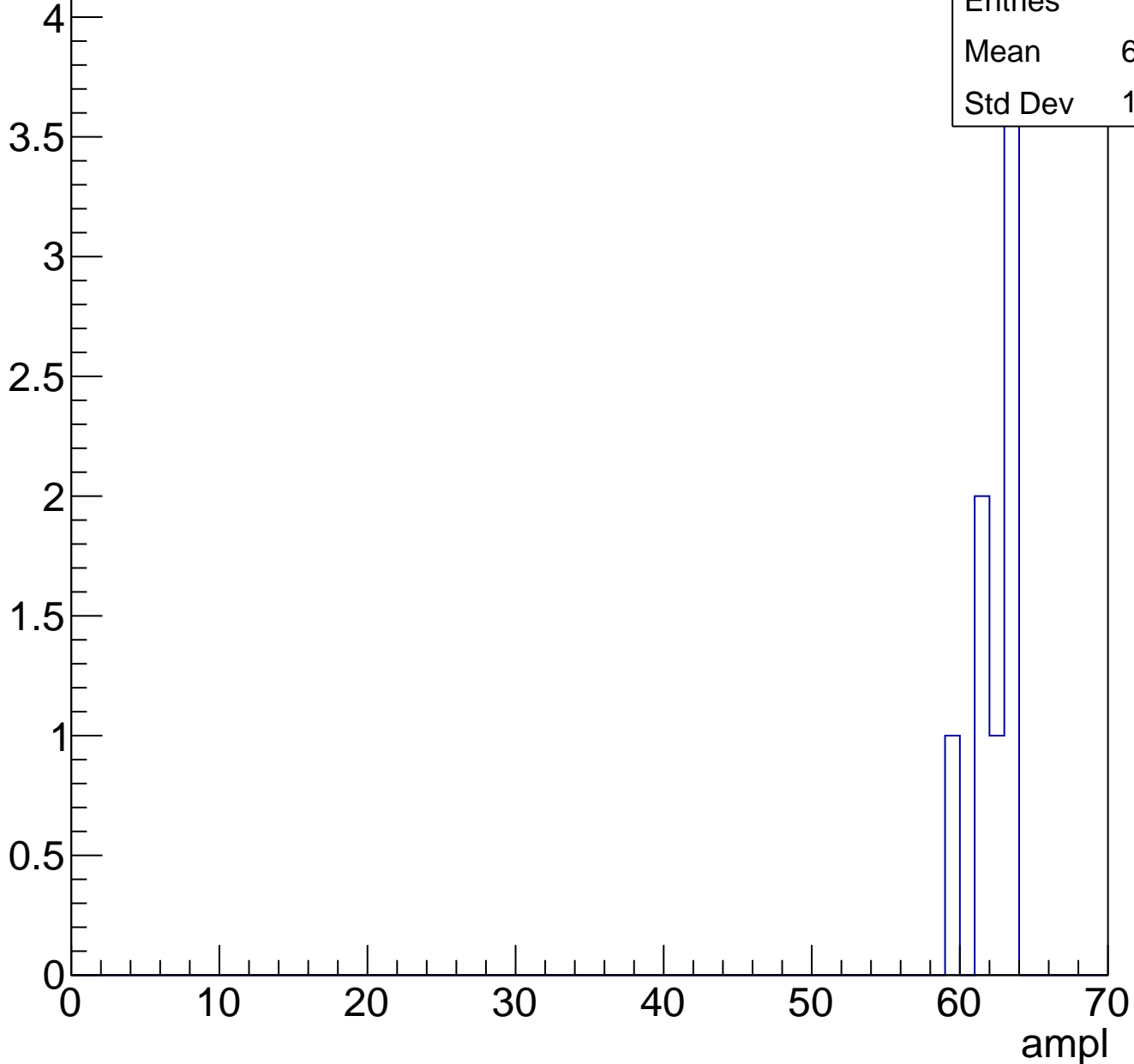
Entries	55
Mean	57.96
Std Dev	8.419



B0L001S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch93, adc0

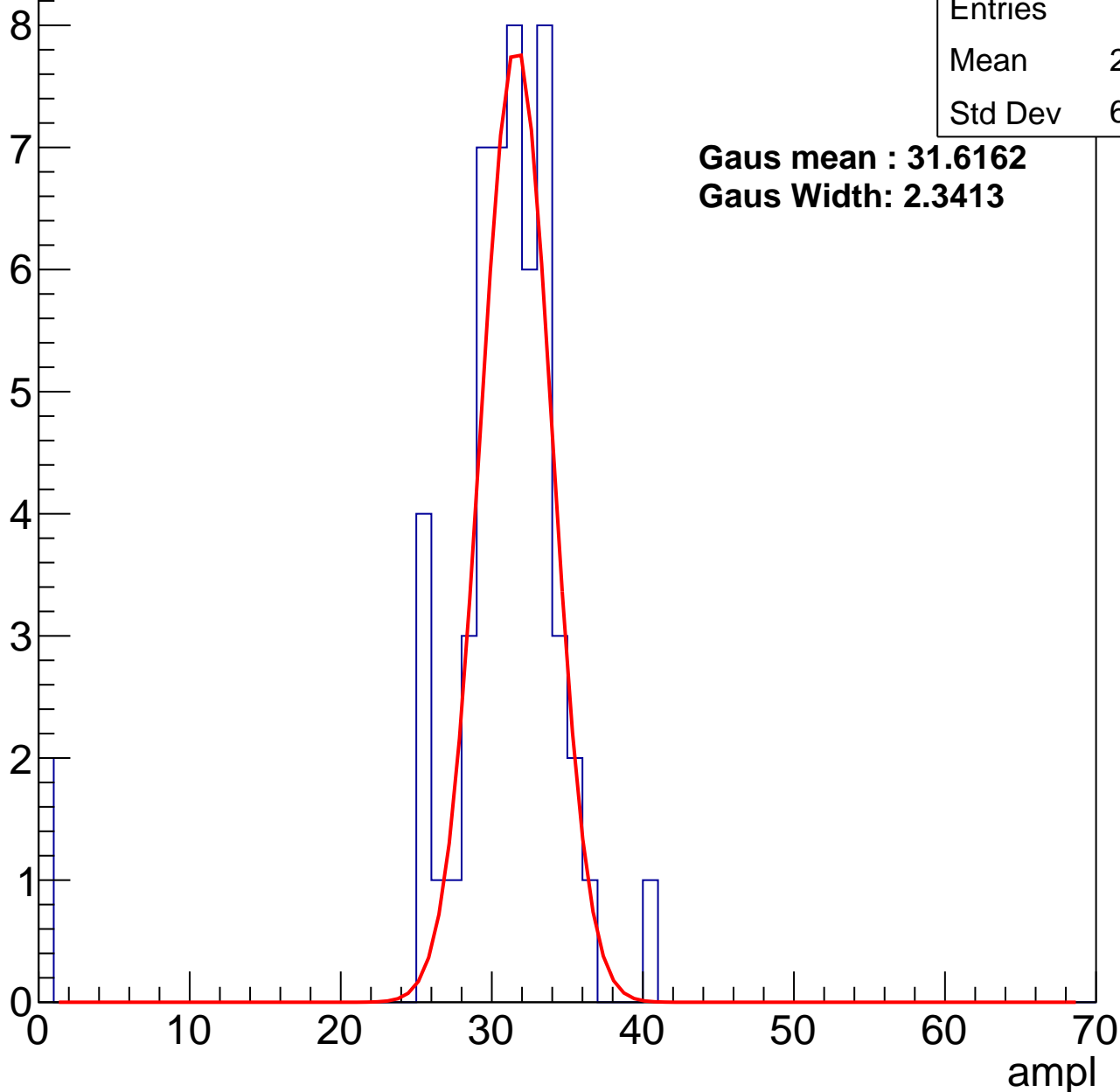
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.67
Std Dev	6.492

Gaus mean : 31.6162

Gaus Width: 2.3413



B0L001S, U15-ch93, adc1

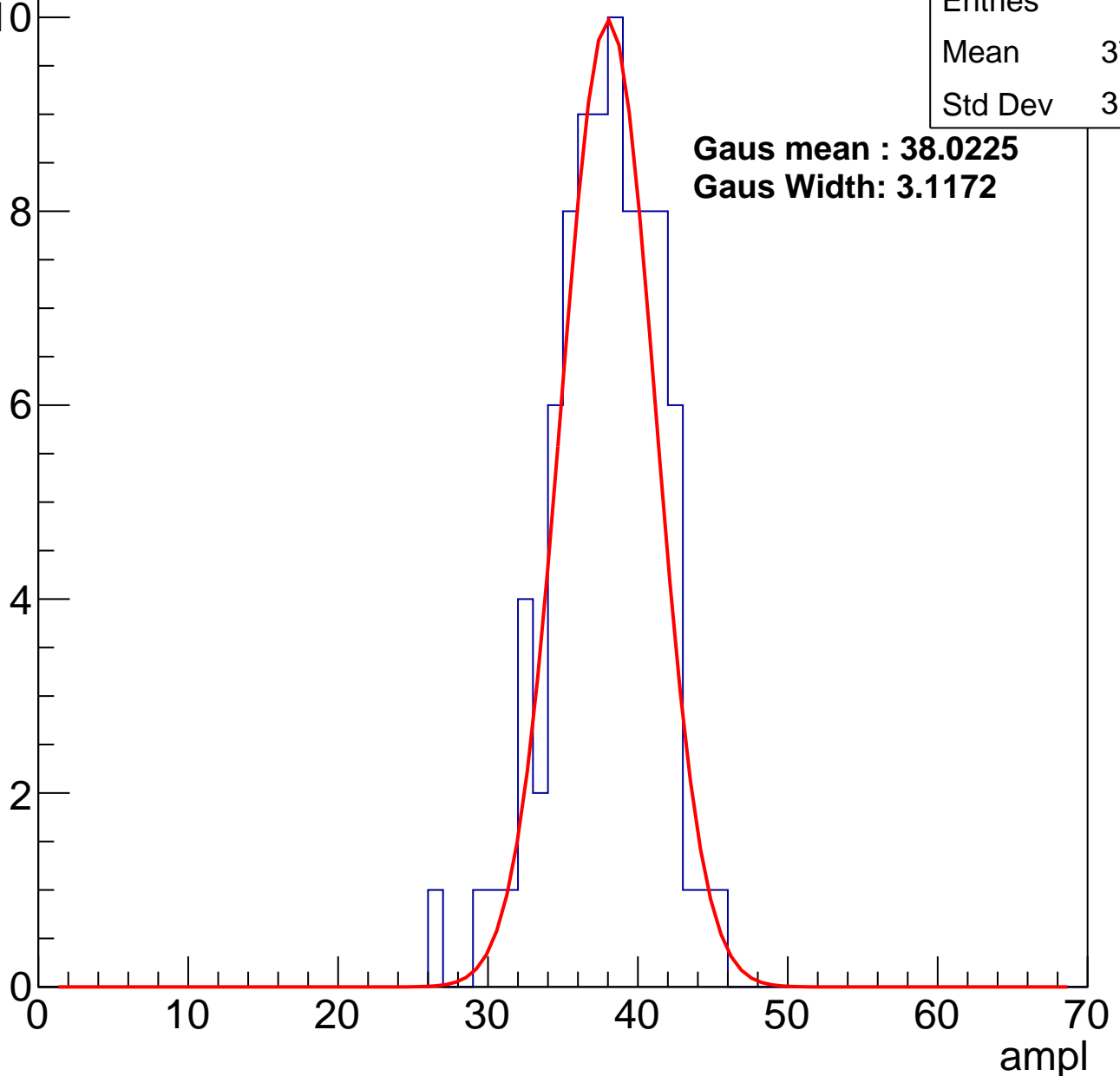
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	37.35
Std Dev	3.477

Gaus mean : 38.0225

Gaus Width: 3.1172



B0L001S, U15-ch93, adc2

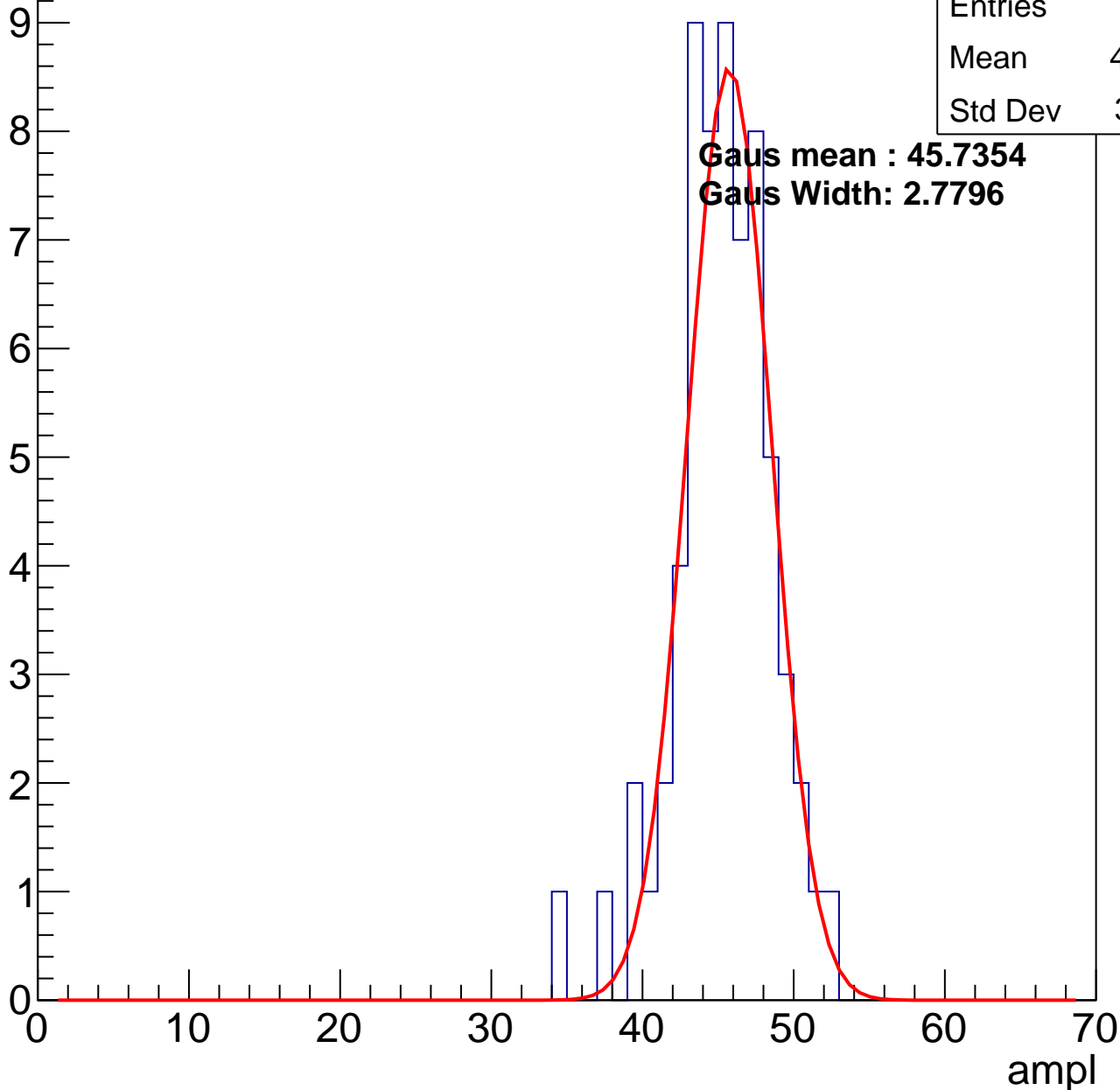
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.86
Std Dev	3.211

Gaus mean : 45.7354

Gaus Width: 2.7796

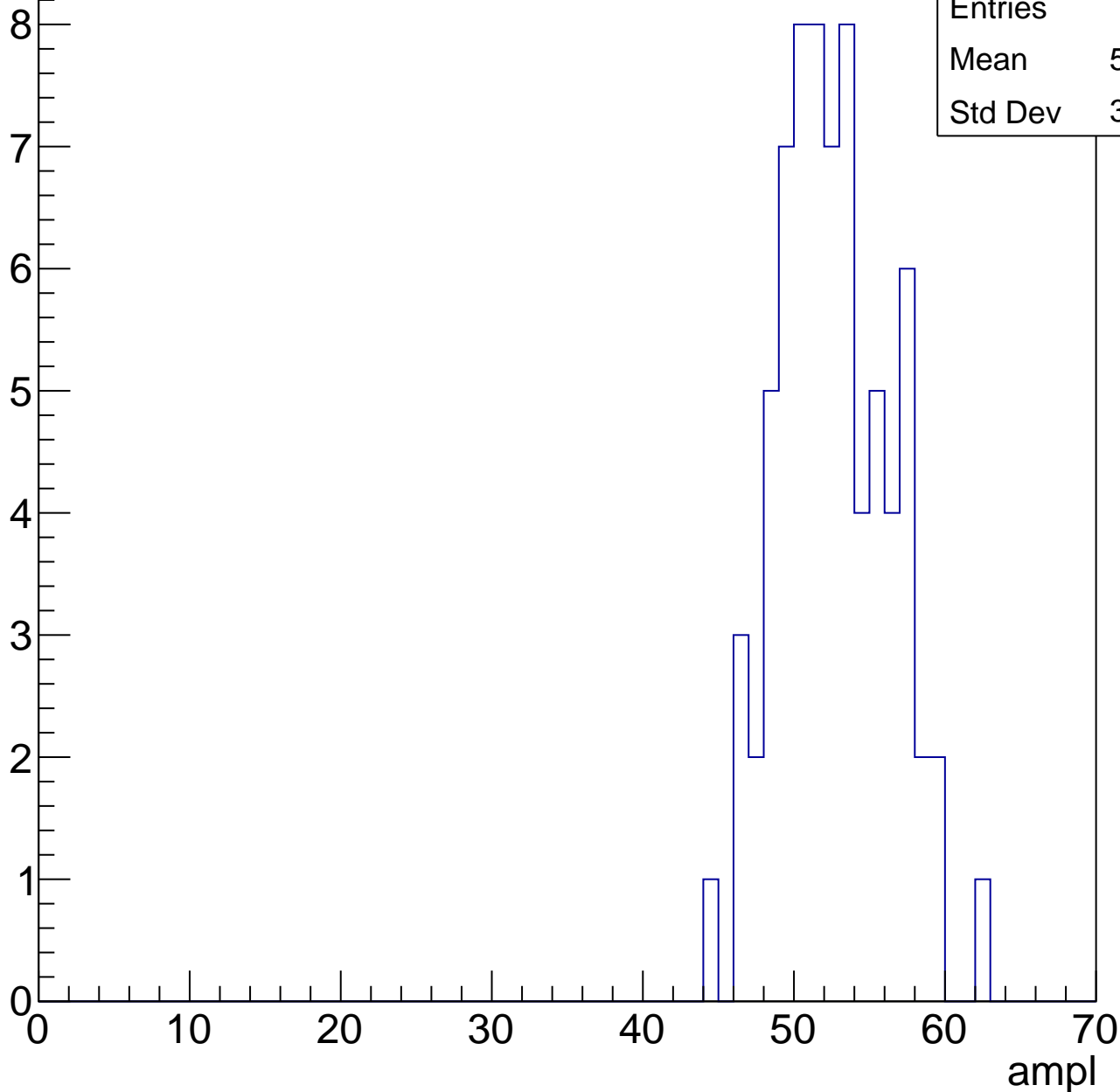


B0L001S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	52.16
Std Dev	3.619

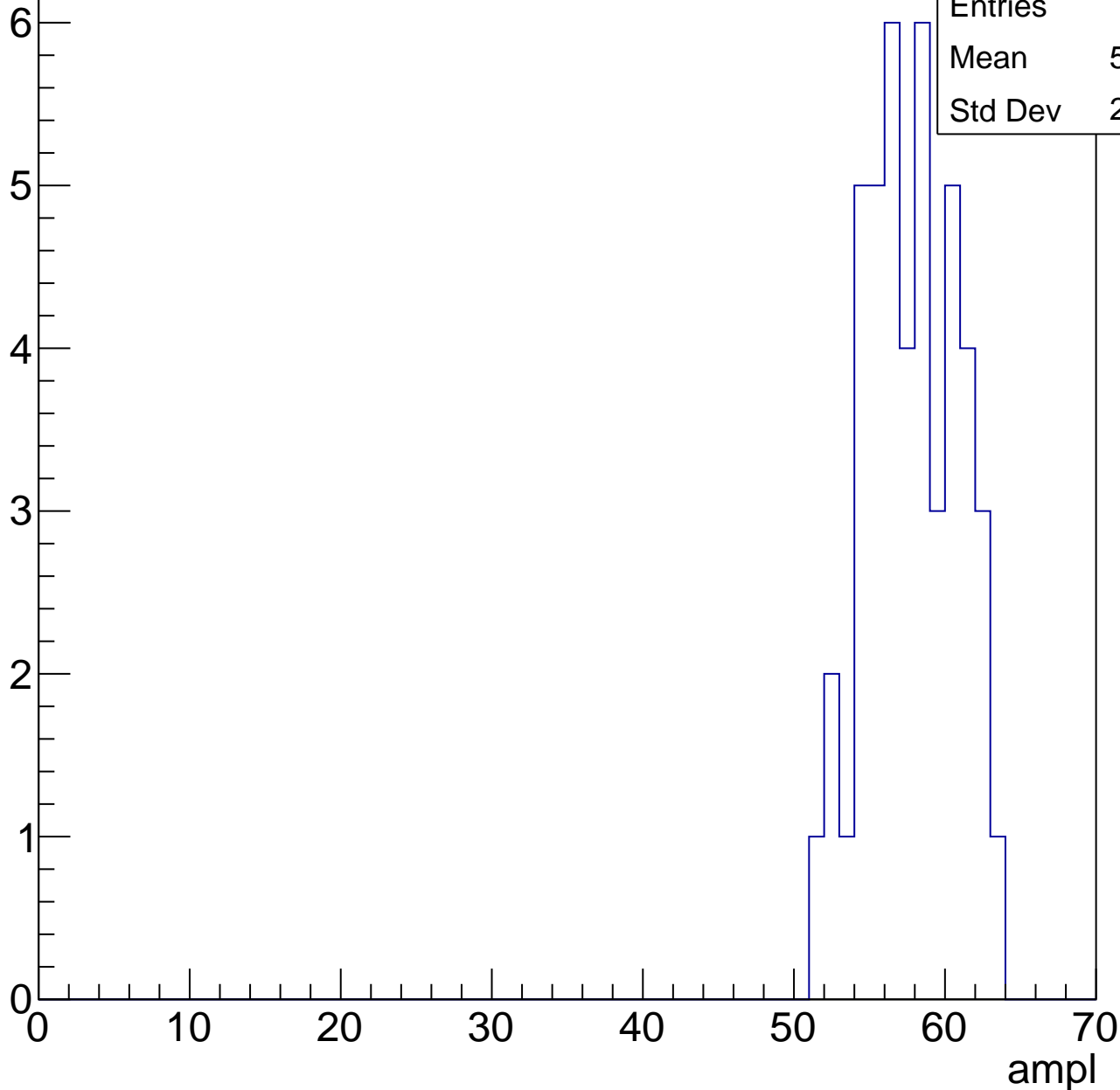


B0L001S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.28
Std Dev	2.976

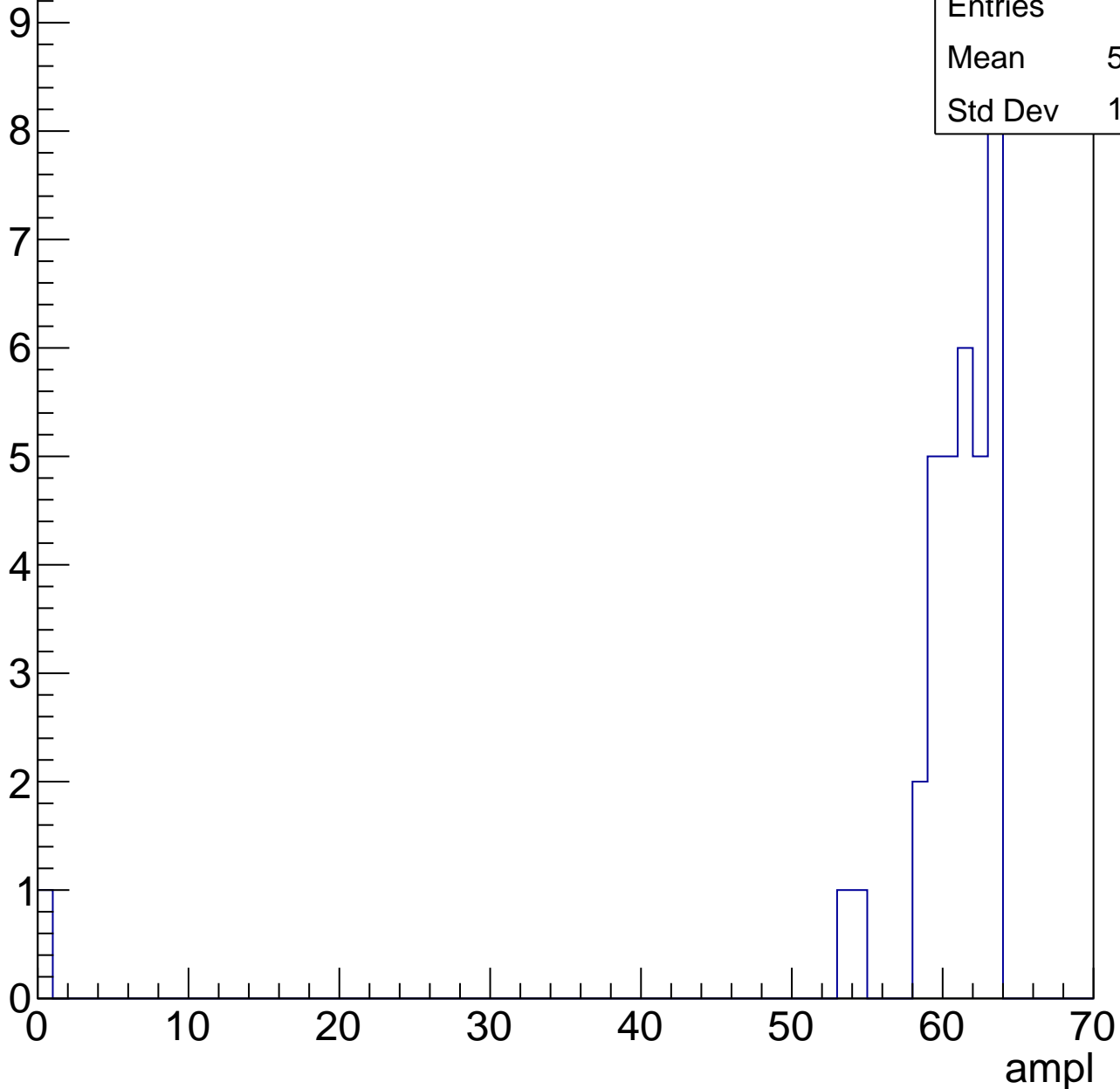


B0L001S, U15-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.89
Std Dev	10.37



B0L001S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	10
Std Dev	10

B0L001S, U15-ch94, adc0

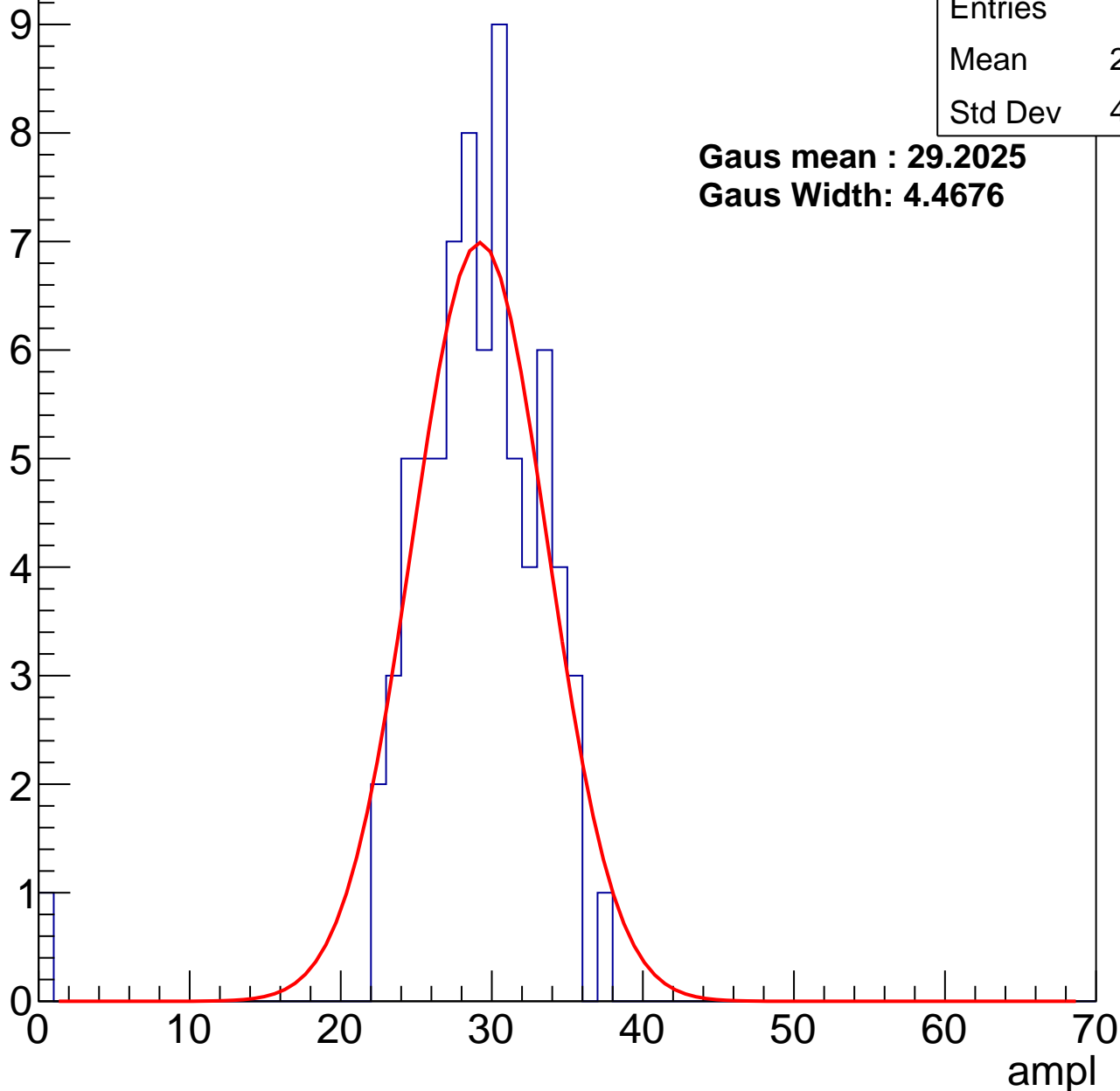
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	28.43
Std Dev	4.852

Gaus mean : 29.2025

Gaus Width: 4.4676



B0L001S, U15-ch94, adc1

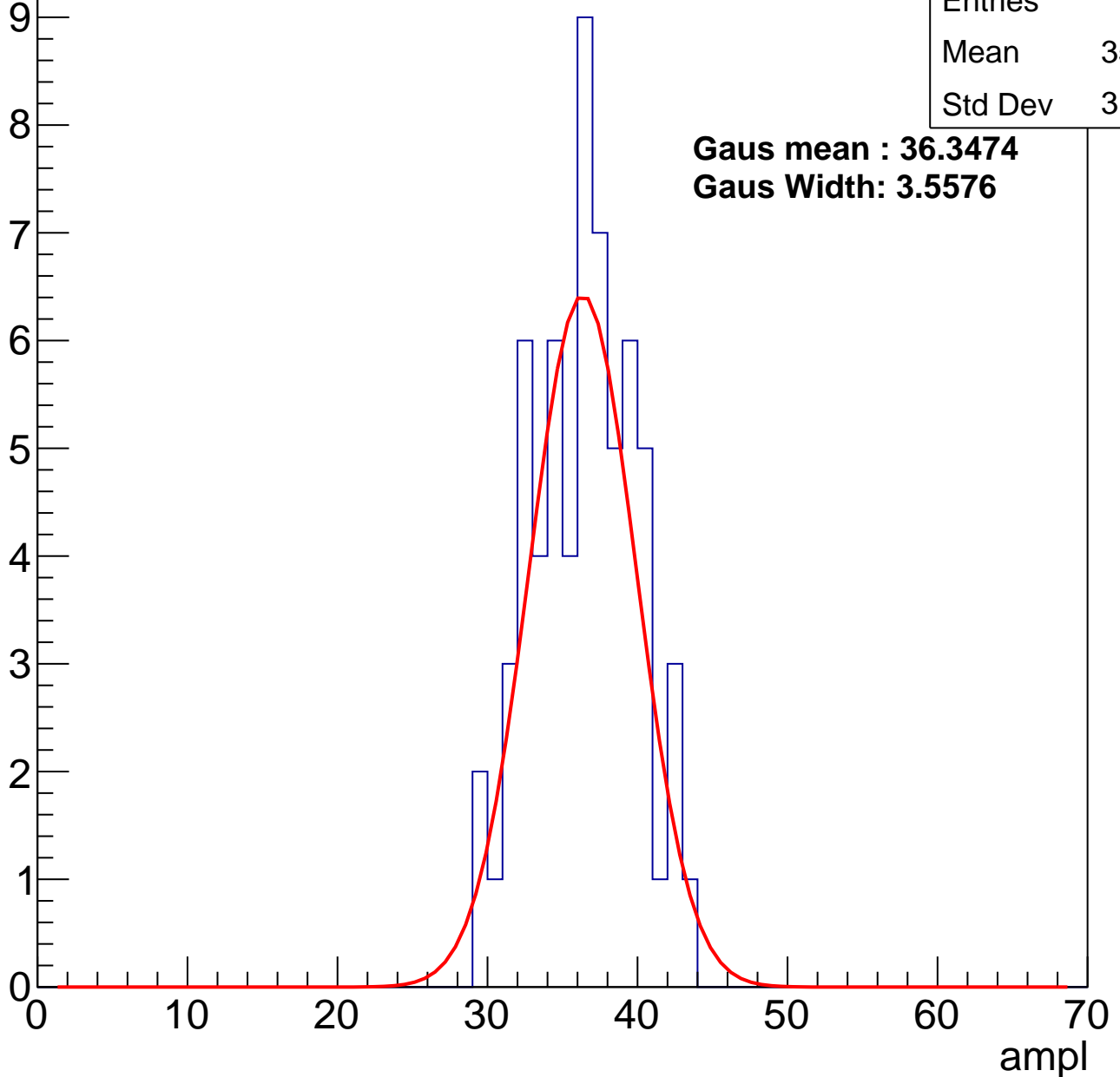
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	35.97
Std Dev	3.362

Gaus mean : 36.3474

Gaus Width: 3.5576



B0L001S, U15-ch94, adc2

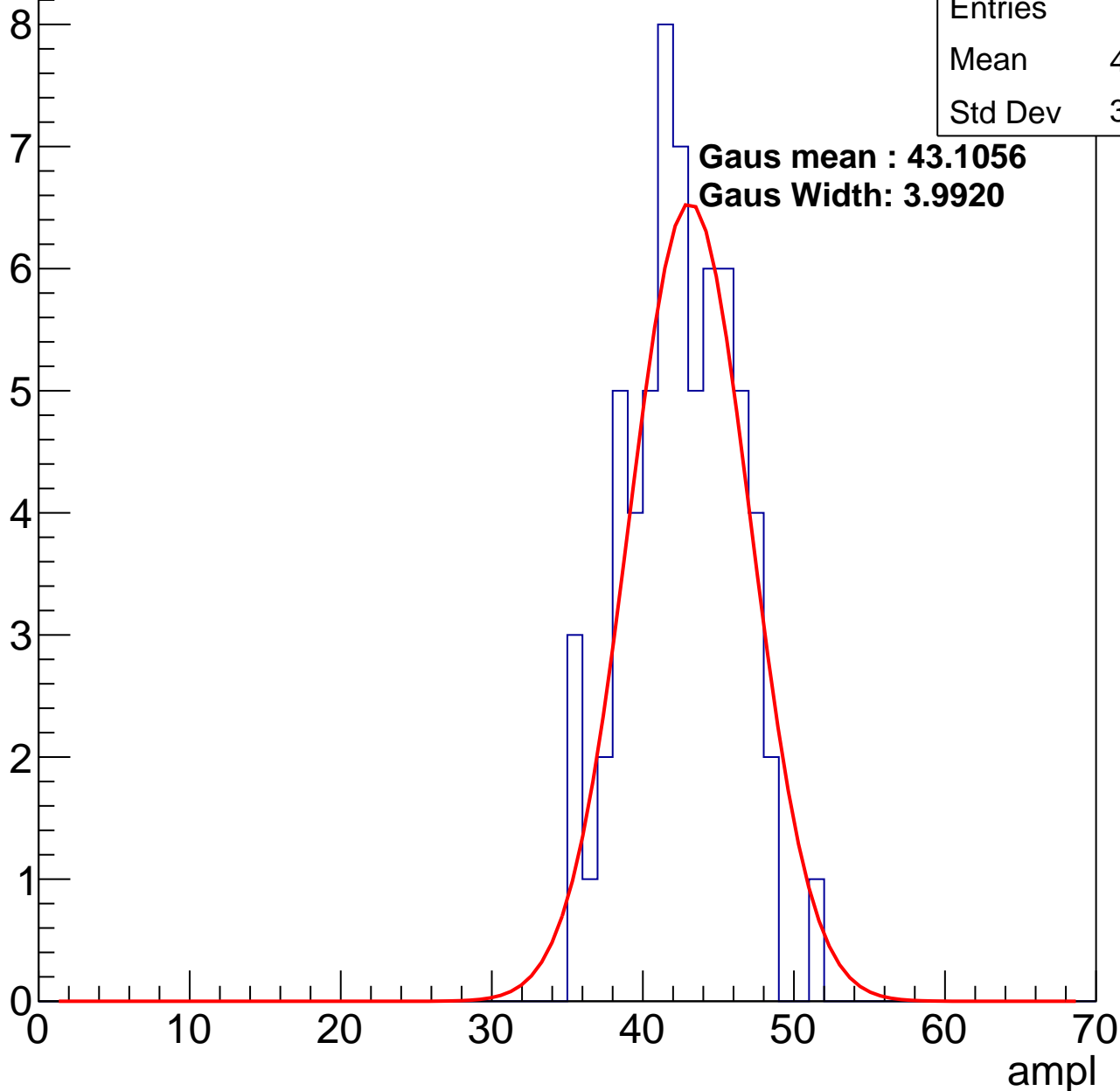
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	42.14
Std Dev	3.517

Gaus mean : 43.1056

Gaus Width: 3.9920

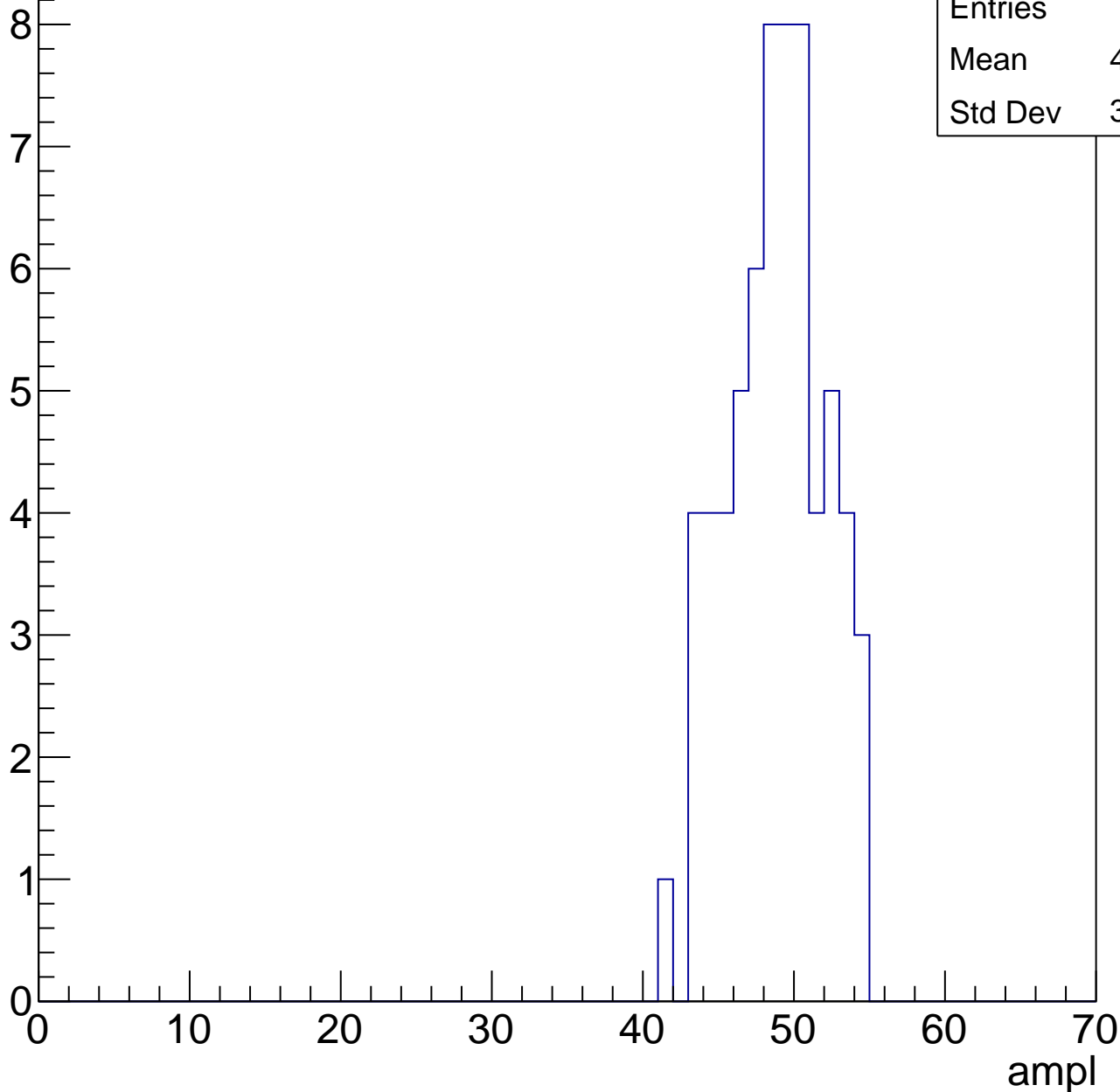


B0L001S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

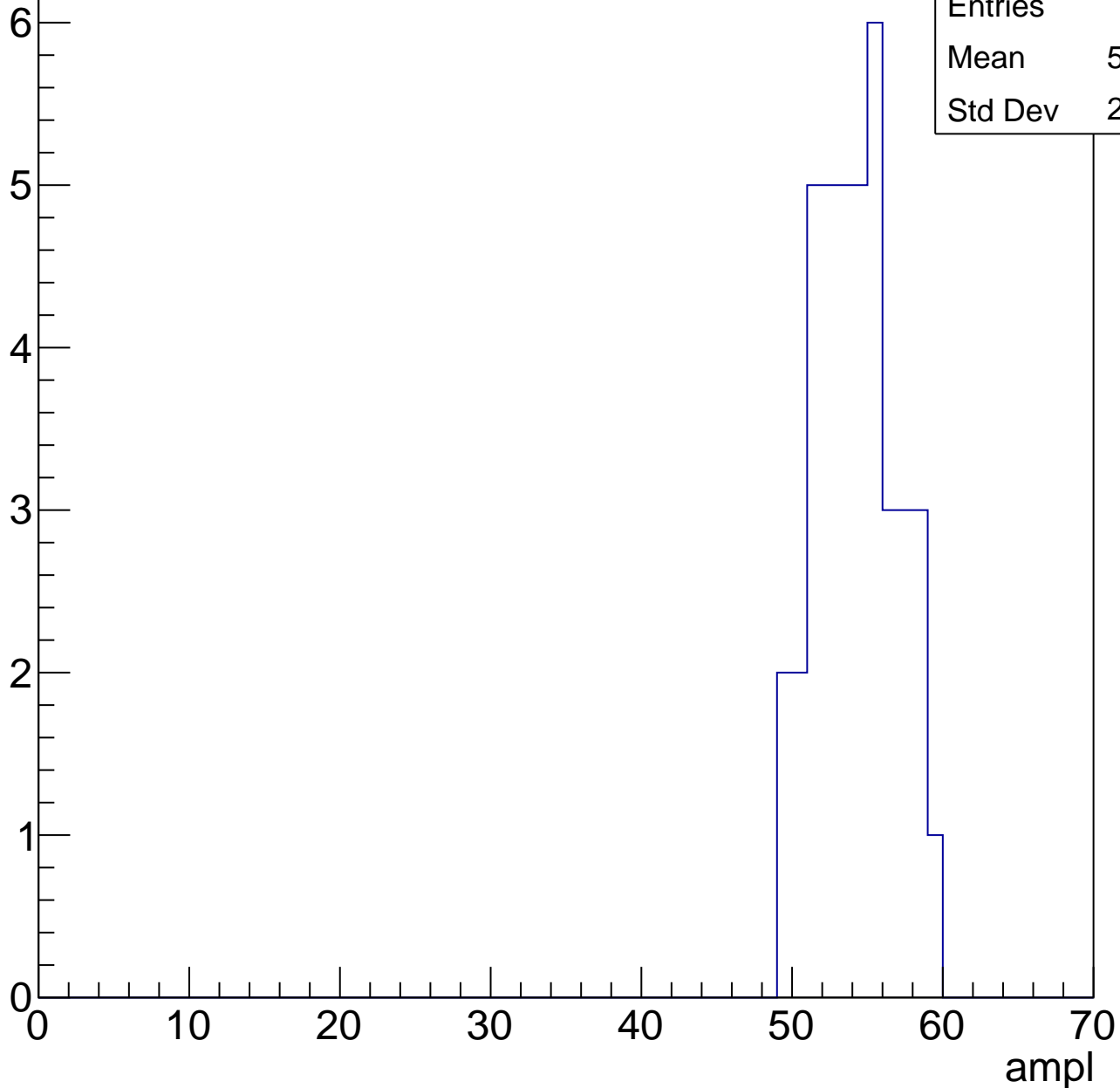
Entries	64
Mean	48.36
Std Dev	3.139



B0L001S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

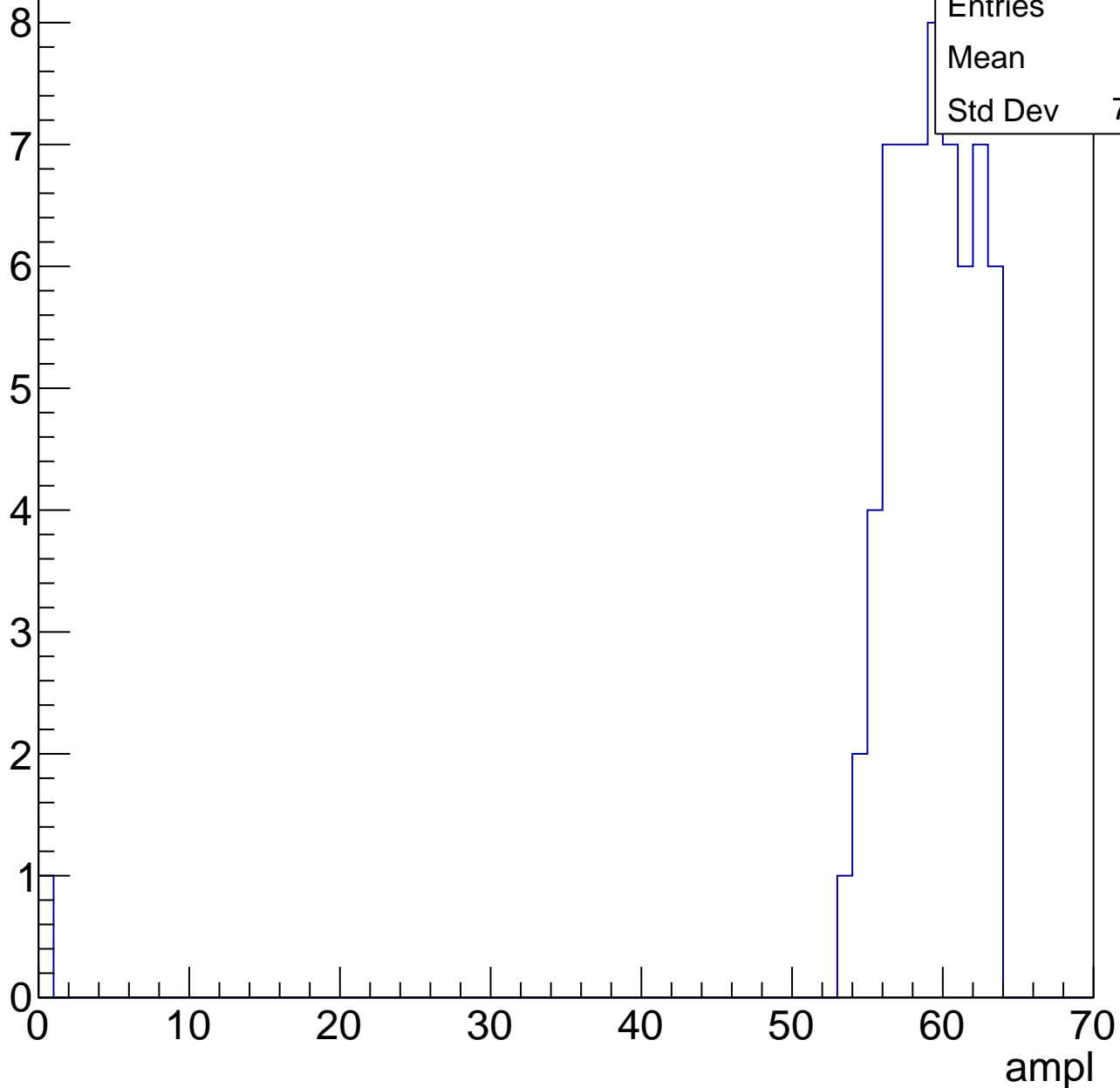
Entry



B0L001S, U15-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

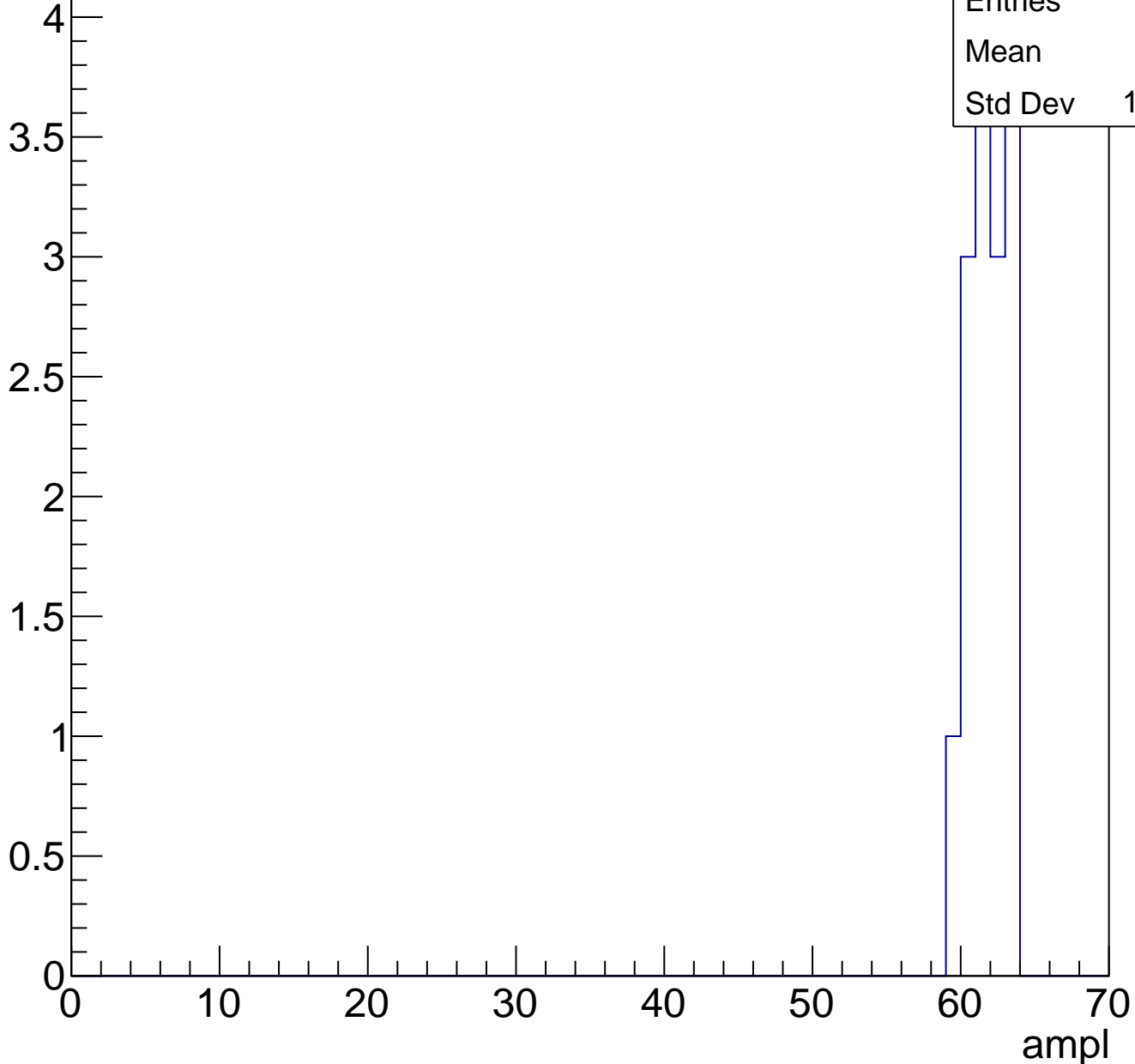
Entry



B0L001S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	15
Mean	61.4
Std Dev	1.254

B0L001S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch95, adc0

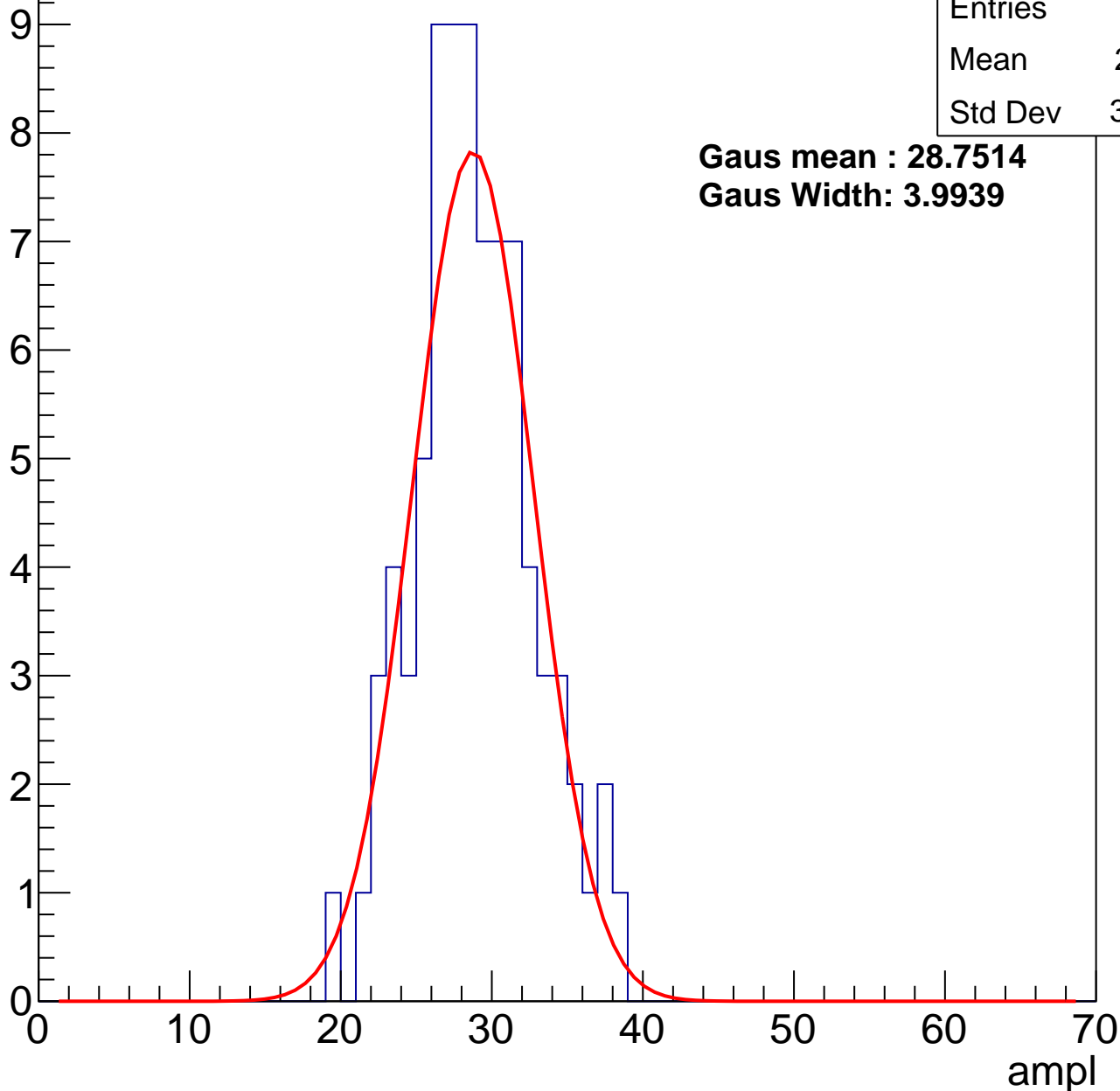
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	28.41
Std Dev	3.899

Gaus mean : 28.7514

Gaus Width: 3.9939



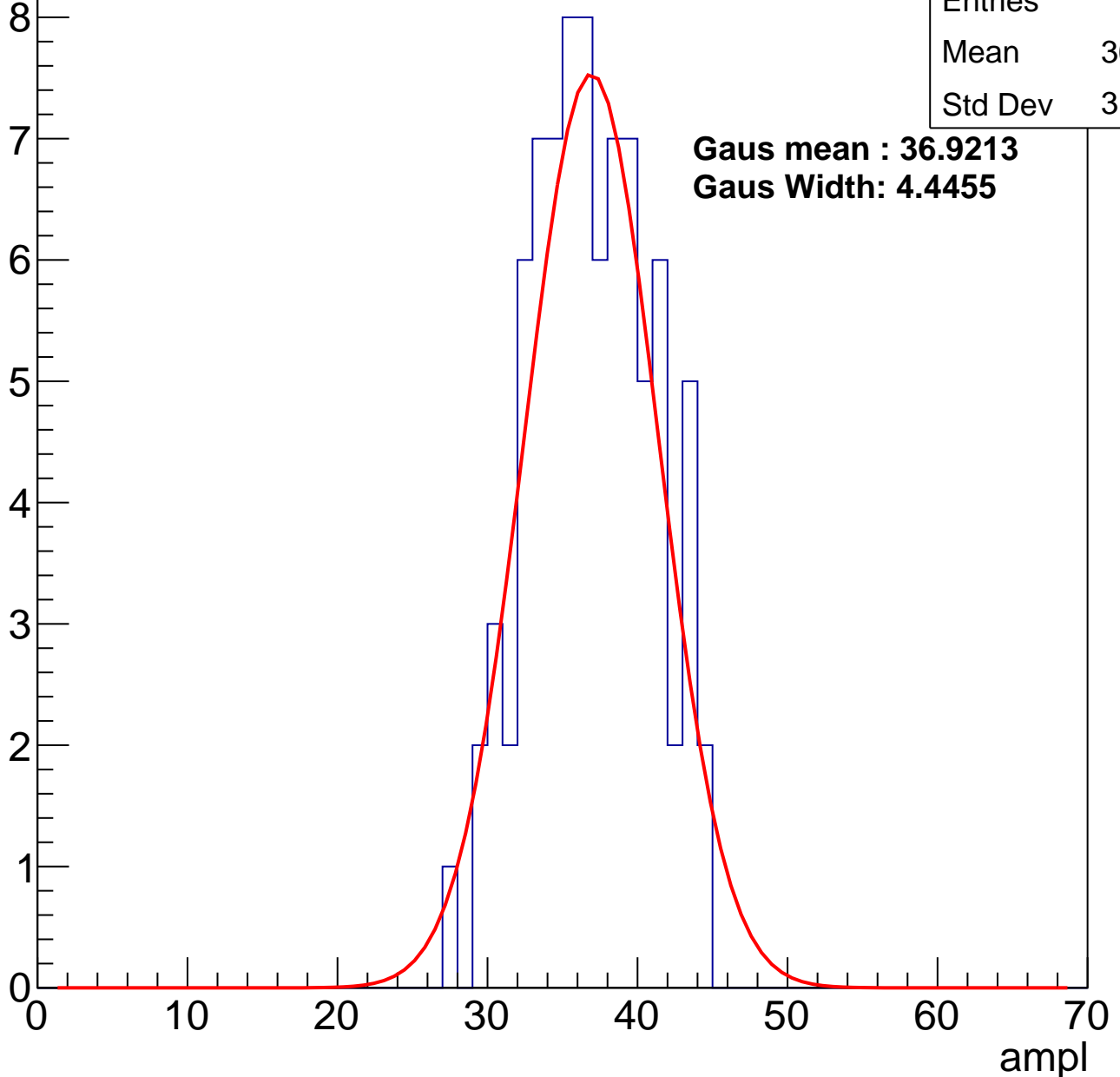
B0L001S, U15-ch95, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	36.43
Std Dev	3.917

Gaus mean : 36.9213
Gaus Width: 4.4455



B0L001S, U15-ch95, adc2

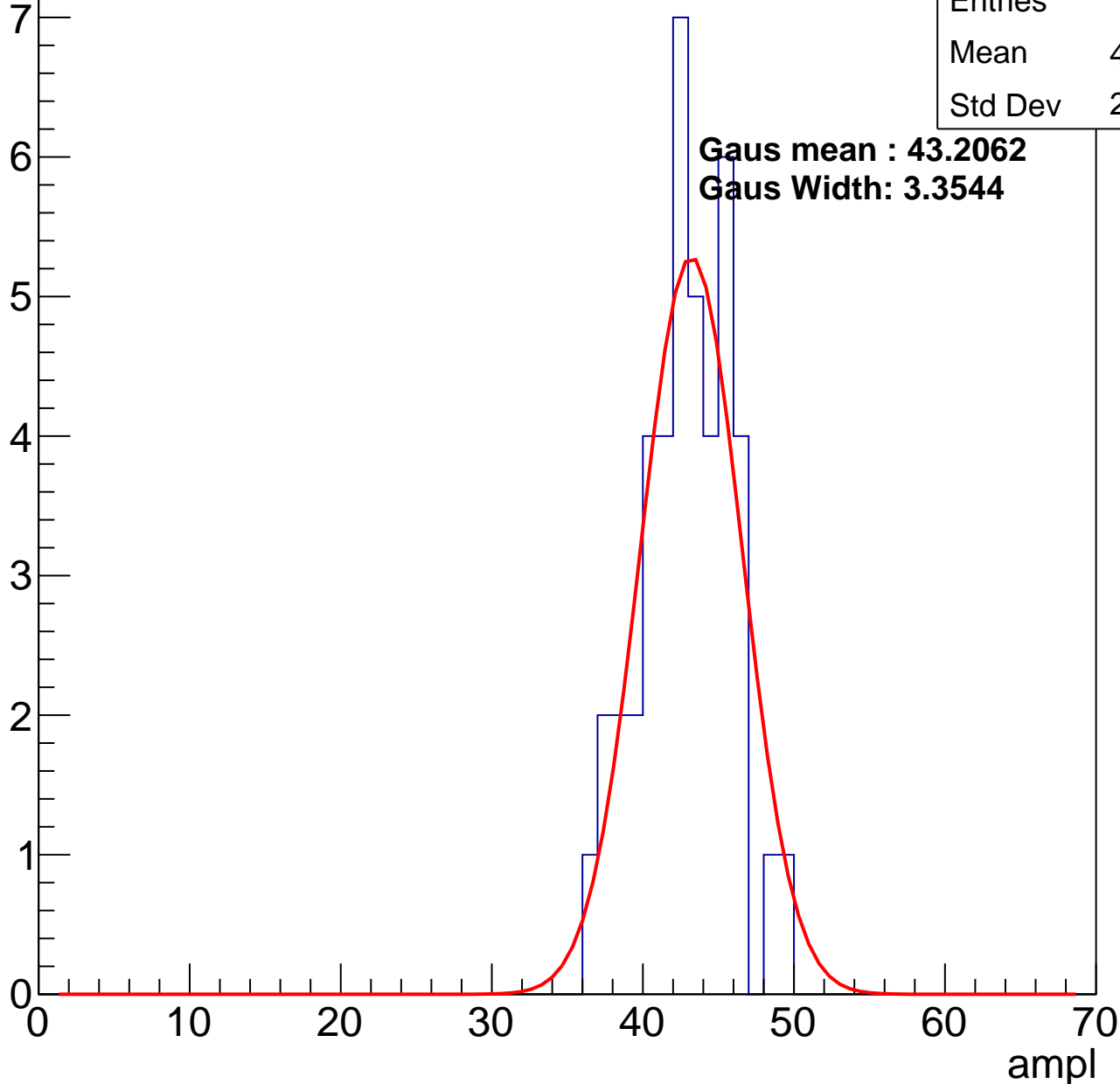
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	42.42
Std Dev	2.943

Gaus mean : 43.2062

Gaus Width: 3.3544



B0L001S, U15-ch95, adc3

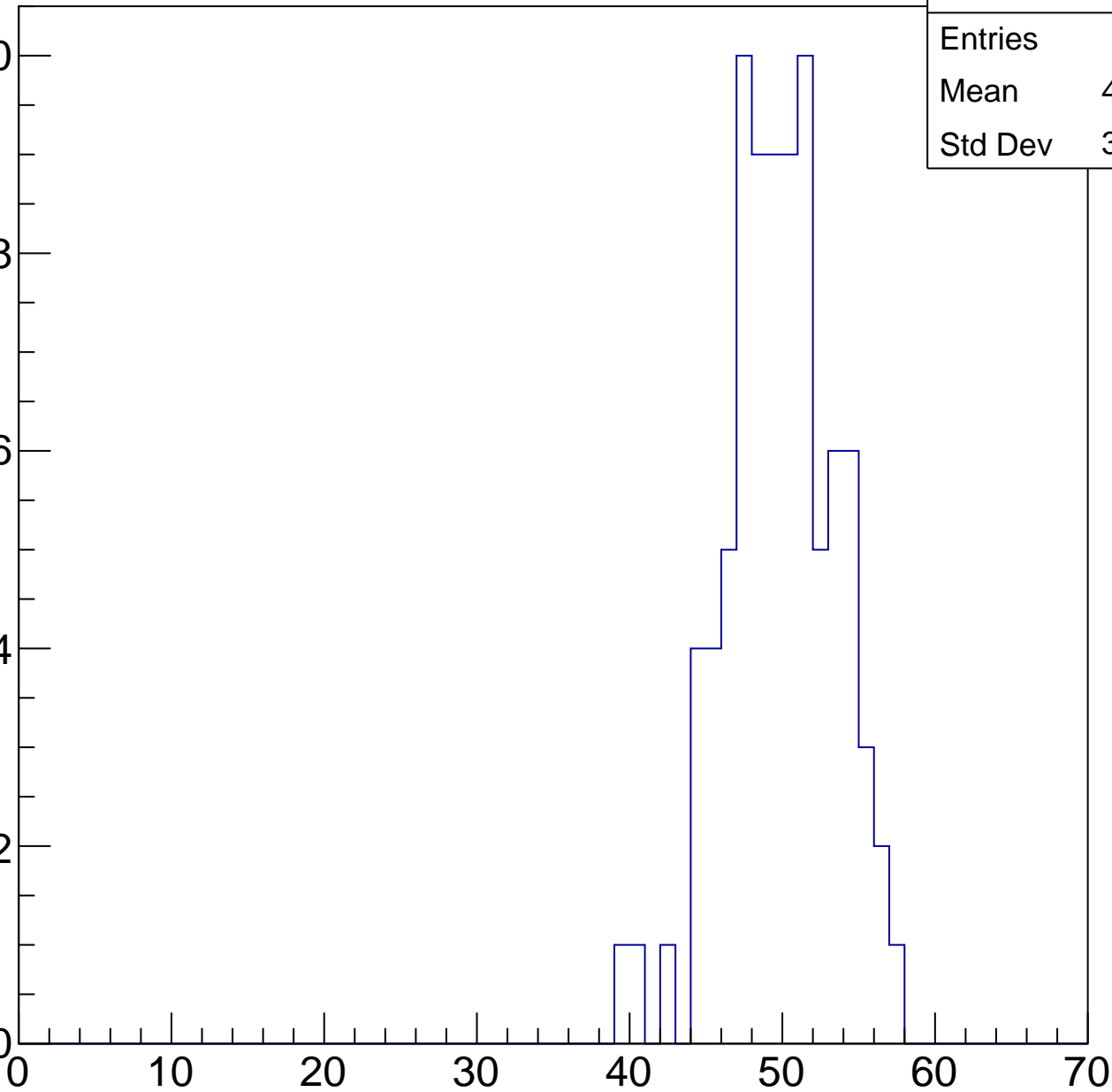
calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	49.37
Std Dev	3.554

Entry

10
8
6
4
2
0

ampl

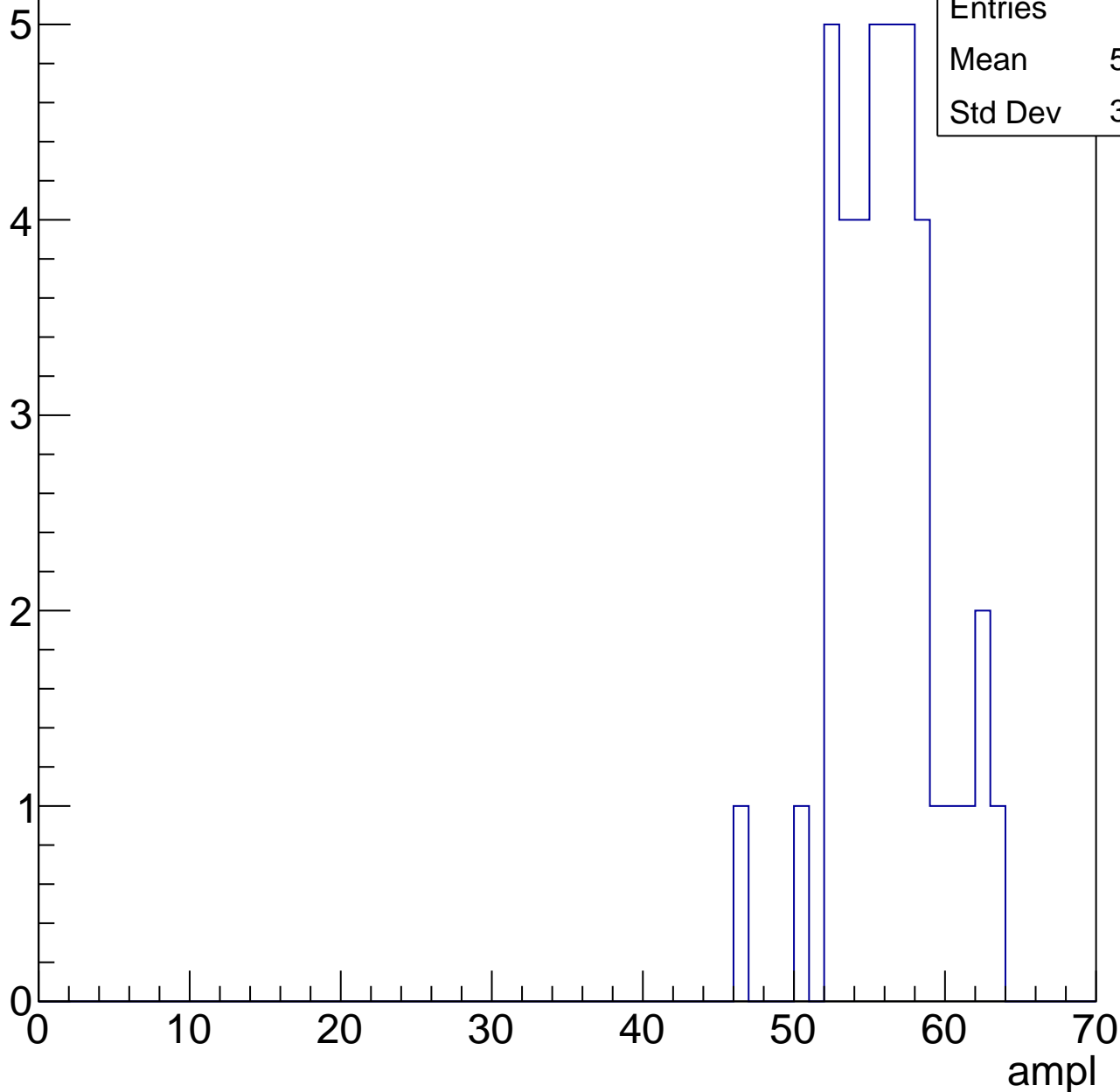


B0L001S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

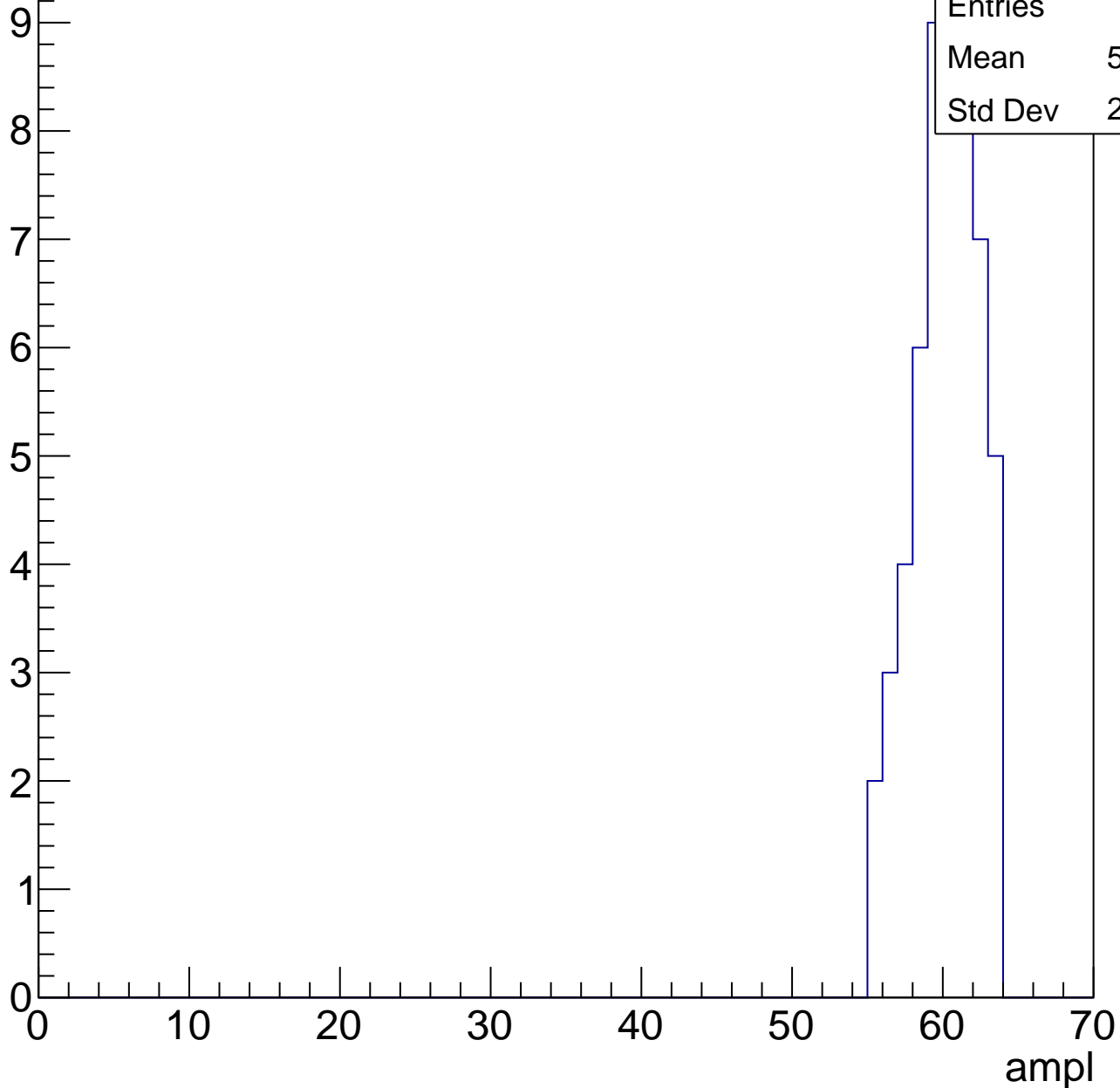
Entries	40
Mean	55.58
Std Dev	3.383



B0L001S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

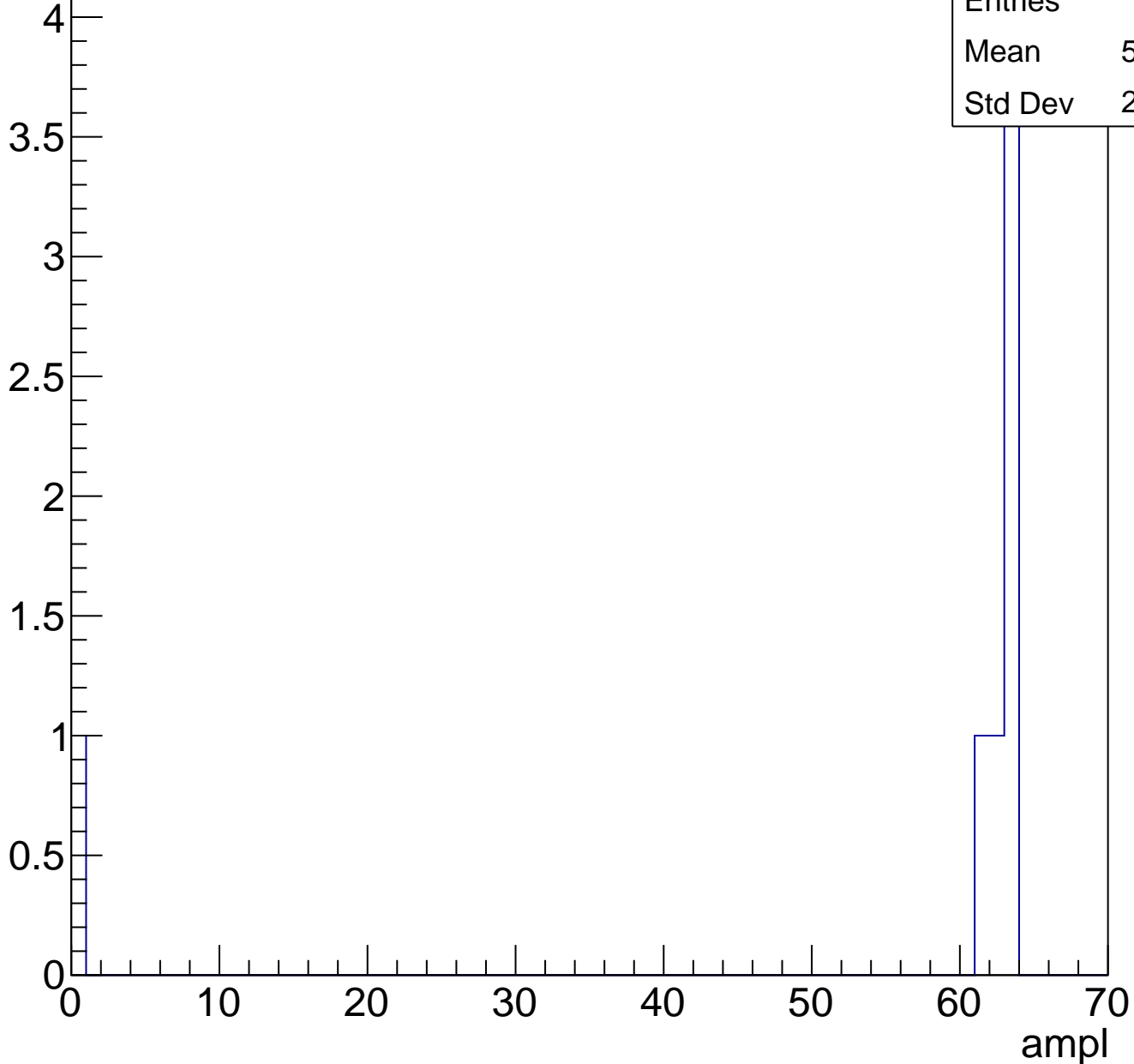
Entry



B0L001S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch96, adc0

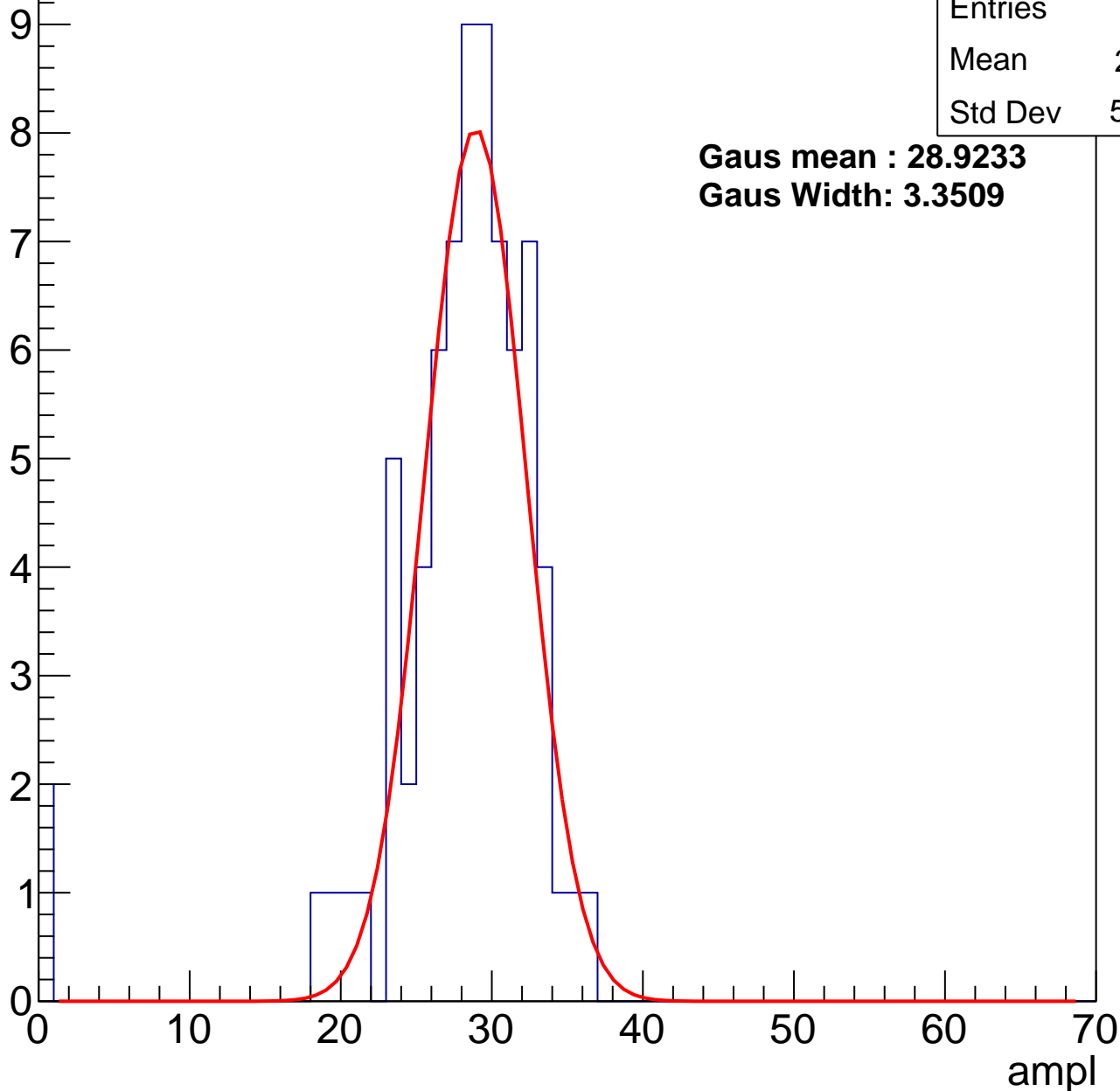
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	27.41
Std Dev	5.788

Gaus mean : 28.9233

Gaus Width: 3.3509



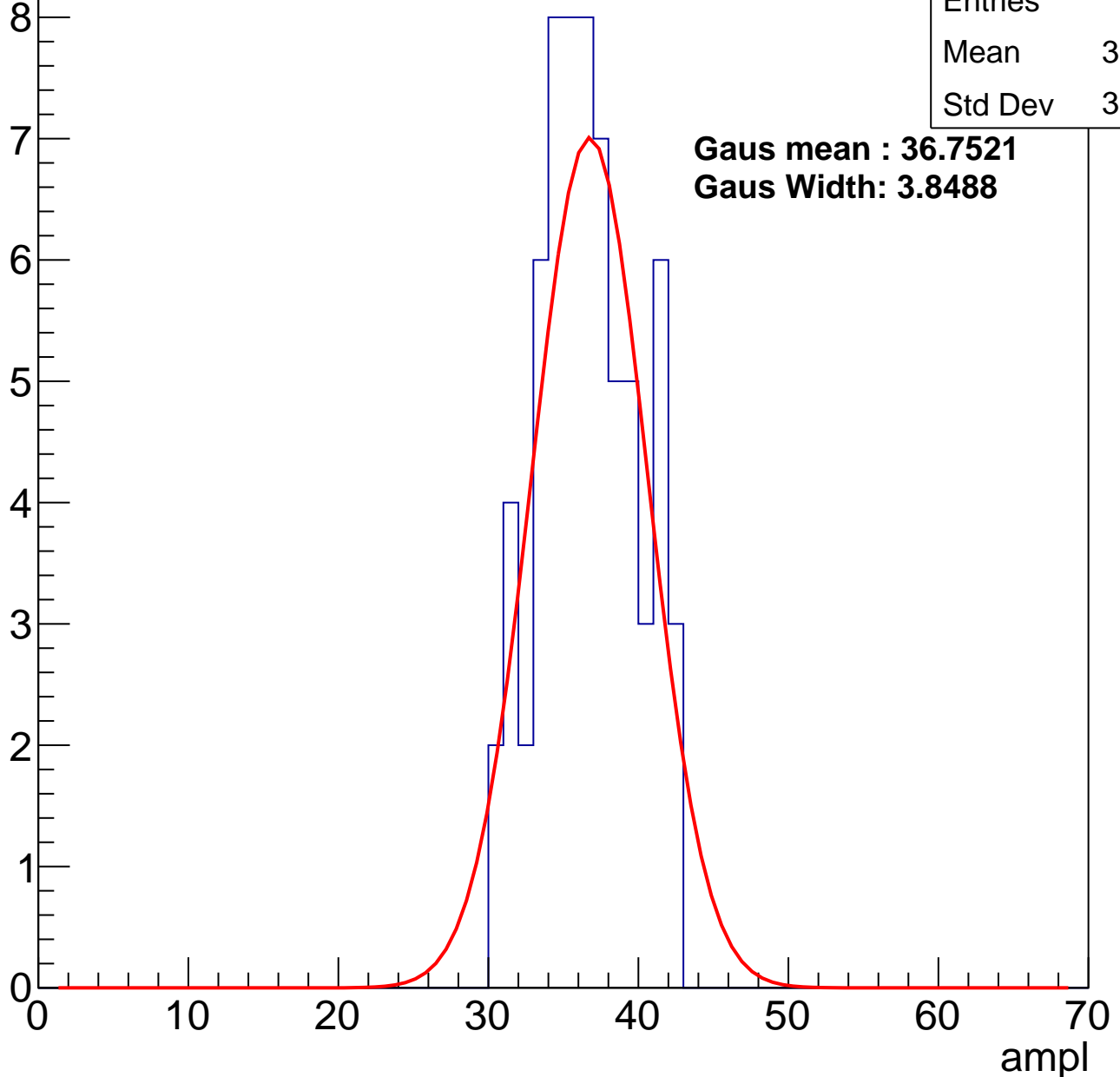
B0L001S, U15-ch96, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.15
Std Dev	3.173

Gaus mean : 36.7521
Gaus Width: 3.8488



B0L001S, U15-ch96, adc2

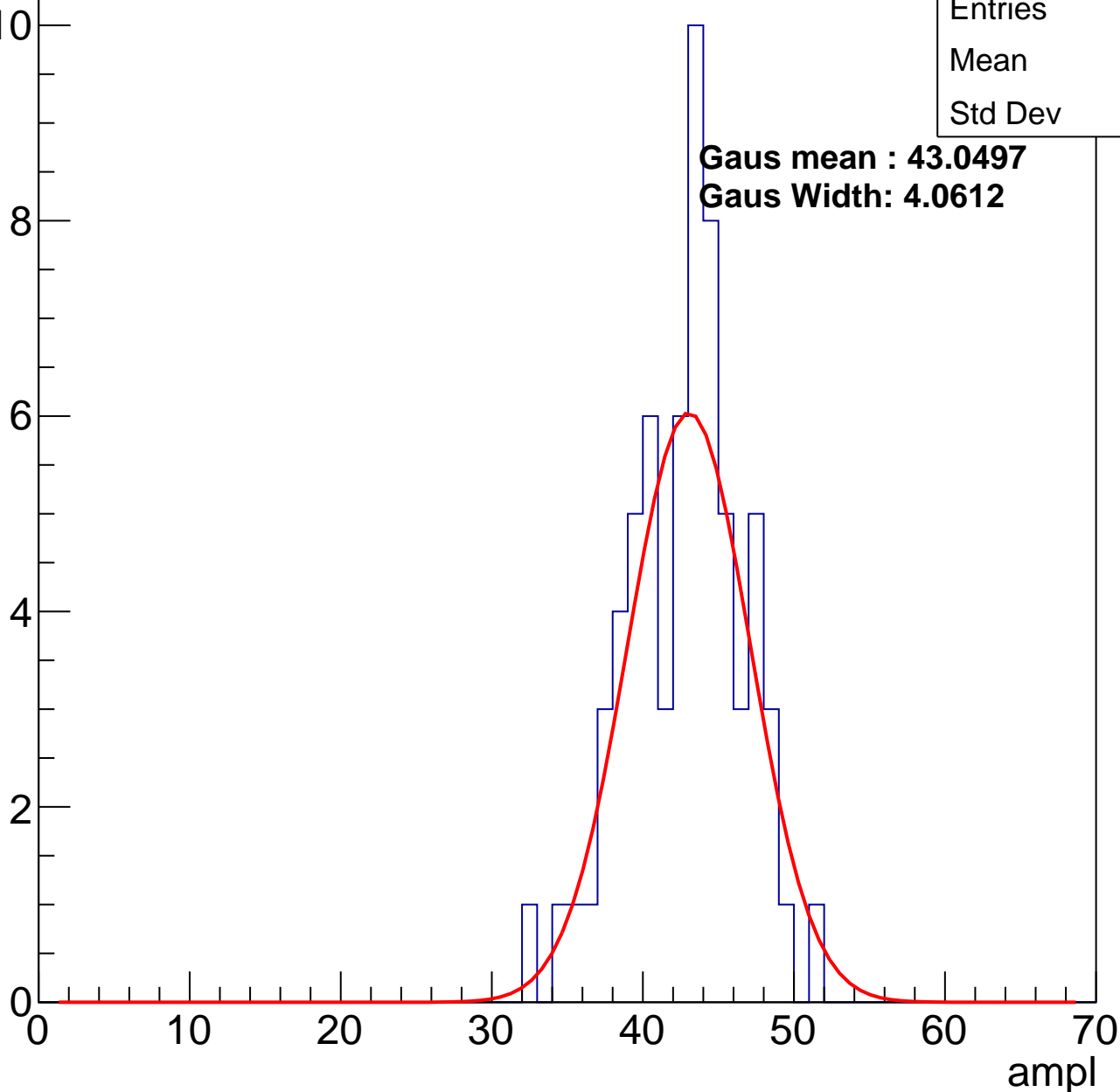
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.3
Std Dev	3.77

Gaus mean : 43.0497

Gaus Width: 4.0612

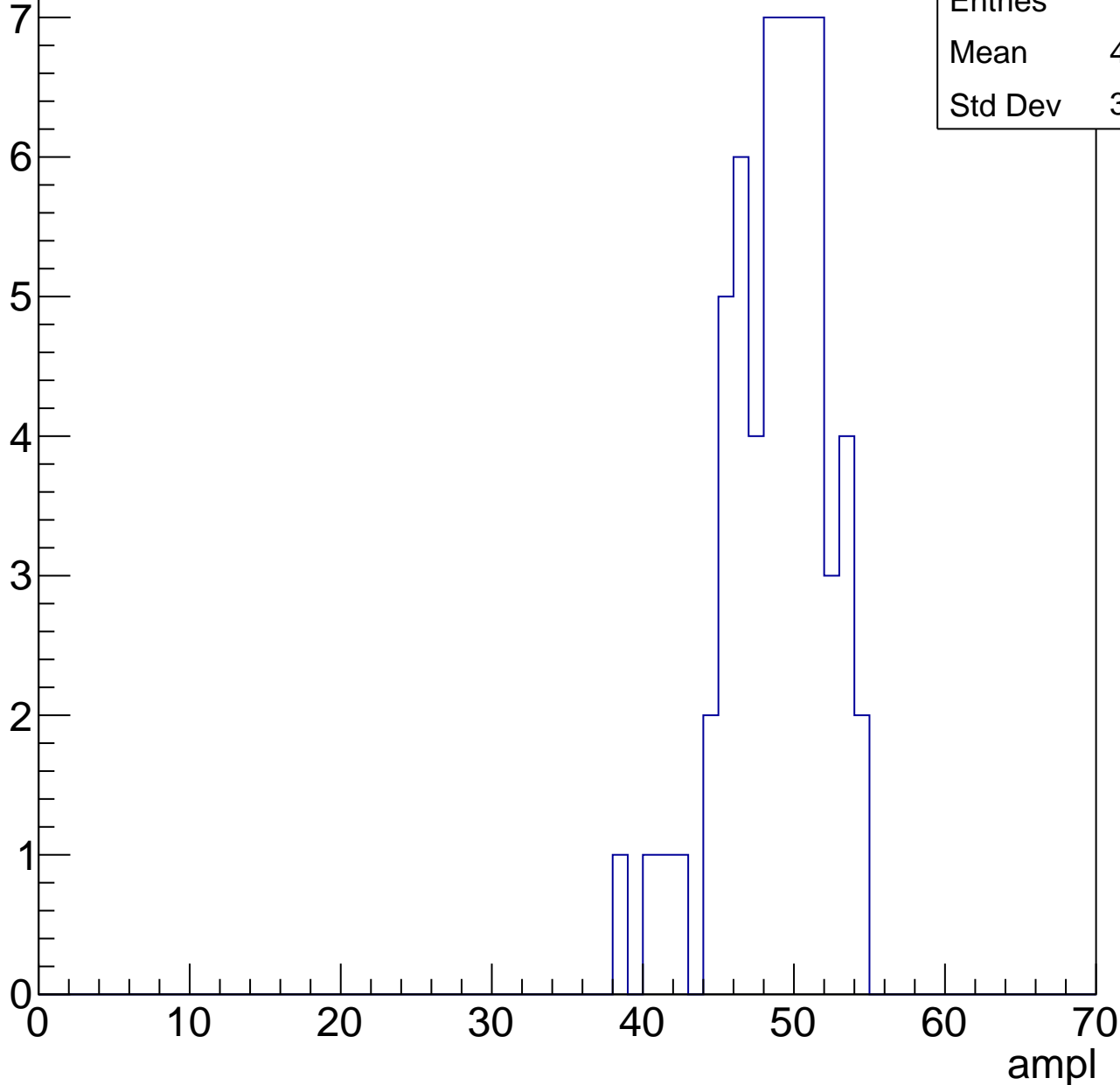


B0L001S, U15-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	48.28
Std Dev	3.393



B0L001S, U15-ch96, adc4

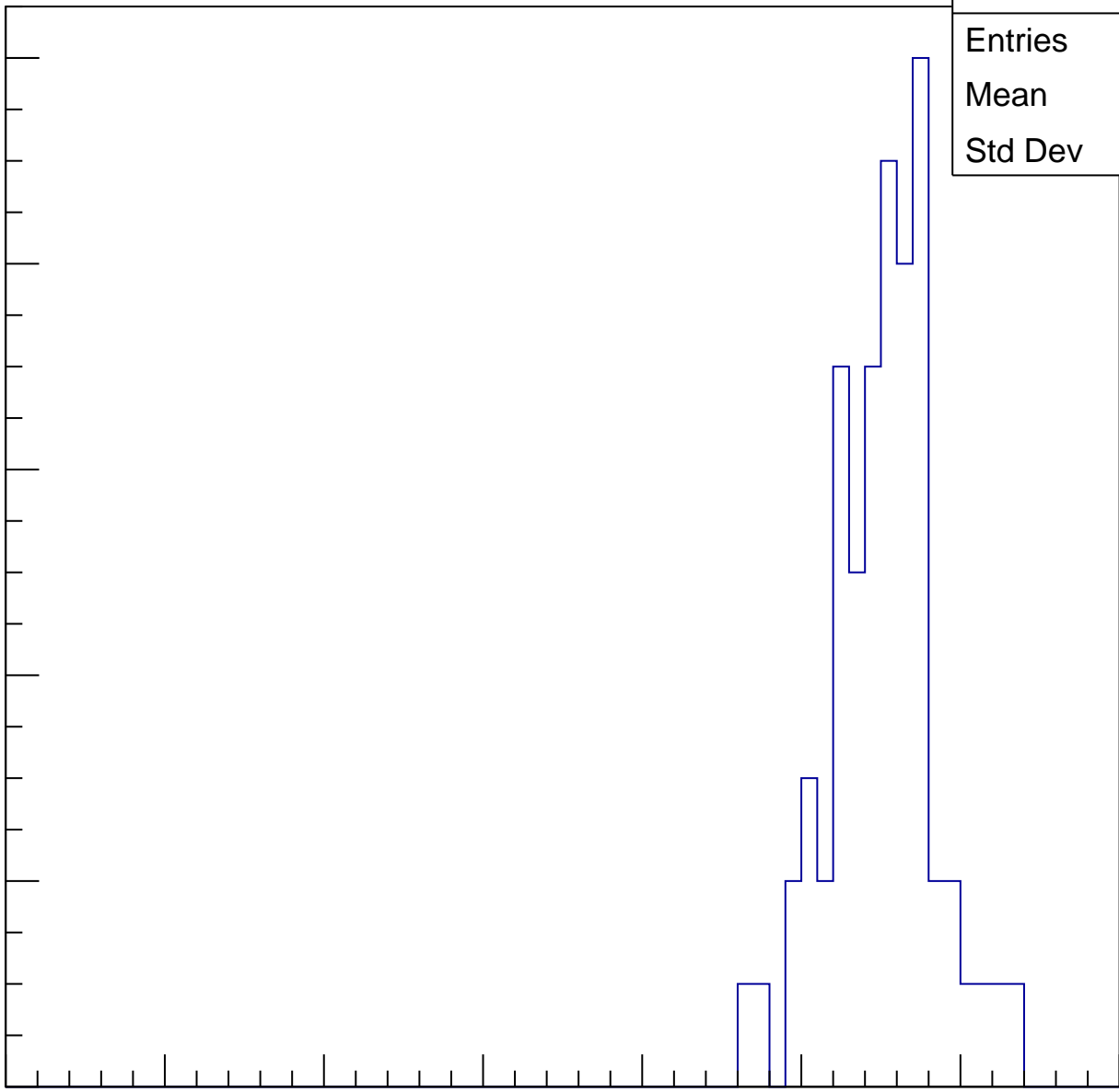
calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	54.65
Std Dev	3.281

Entry

10
8
6
4
2
0

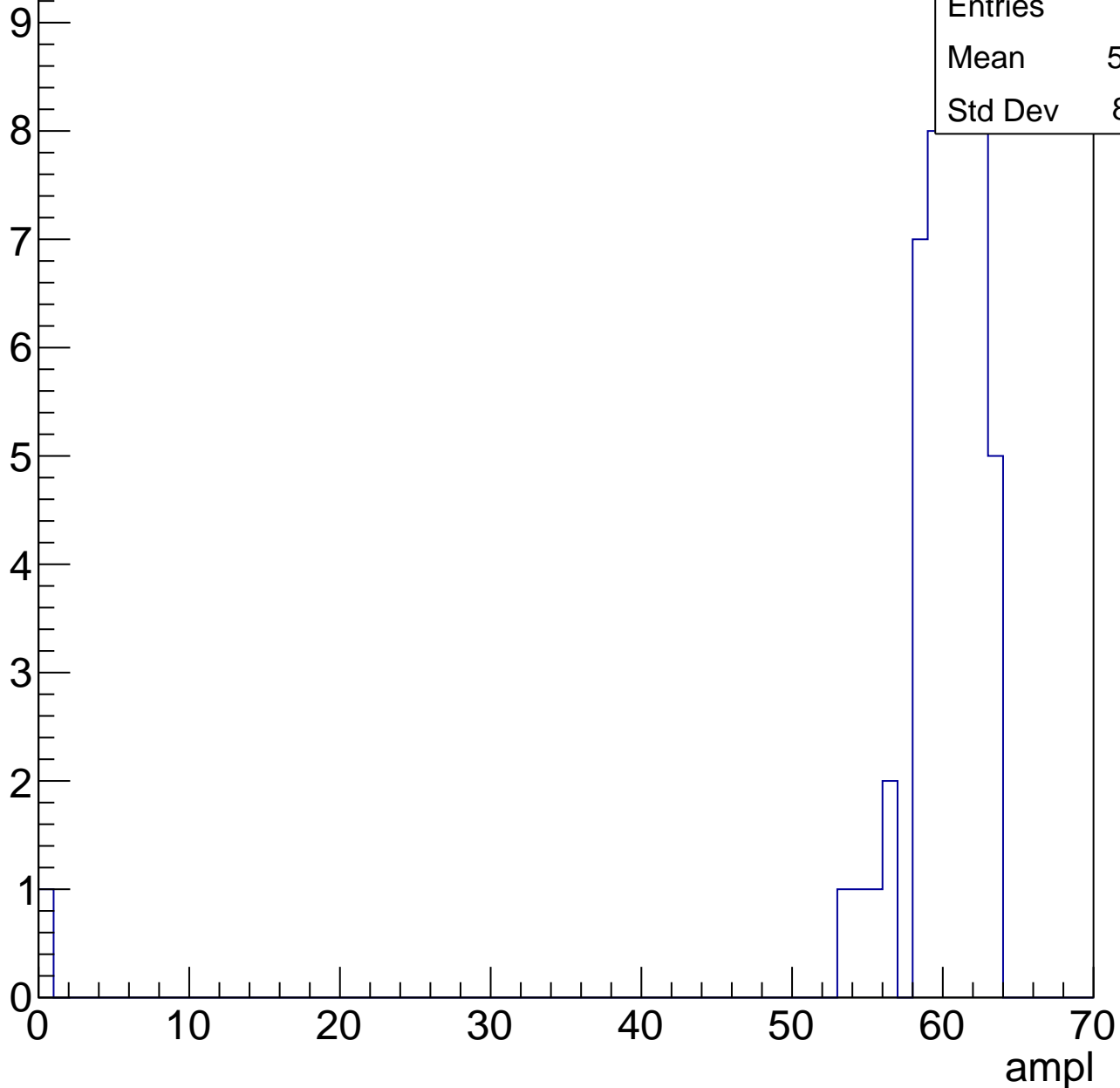
0 10 20 30 40 50 60 70
ampl



B0L001S, U15-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

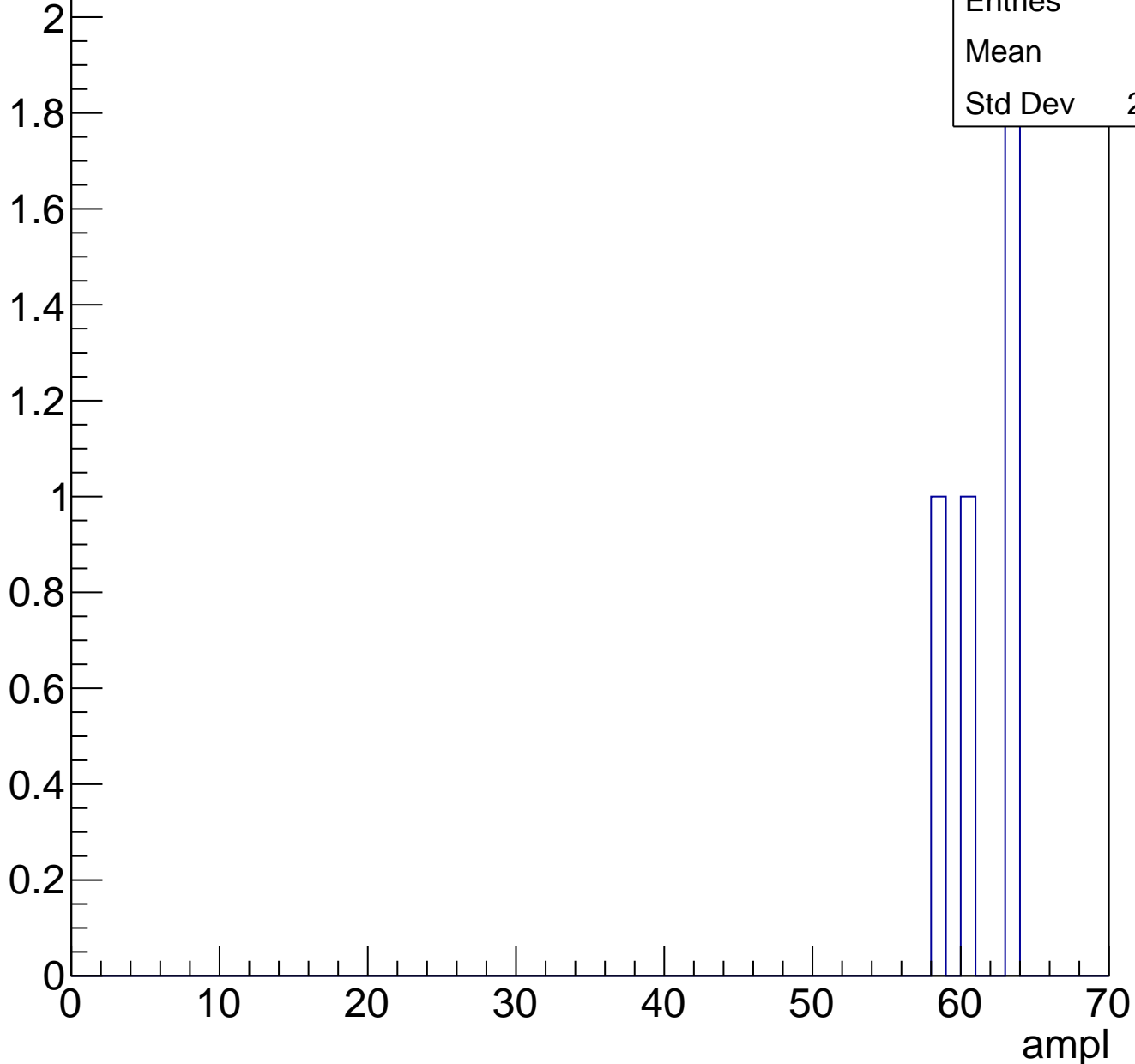
Entry



B0L001S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch97, adc0

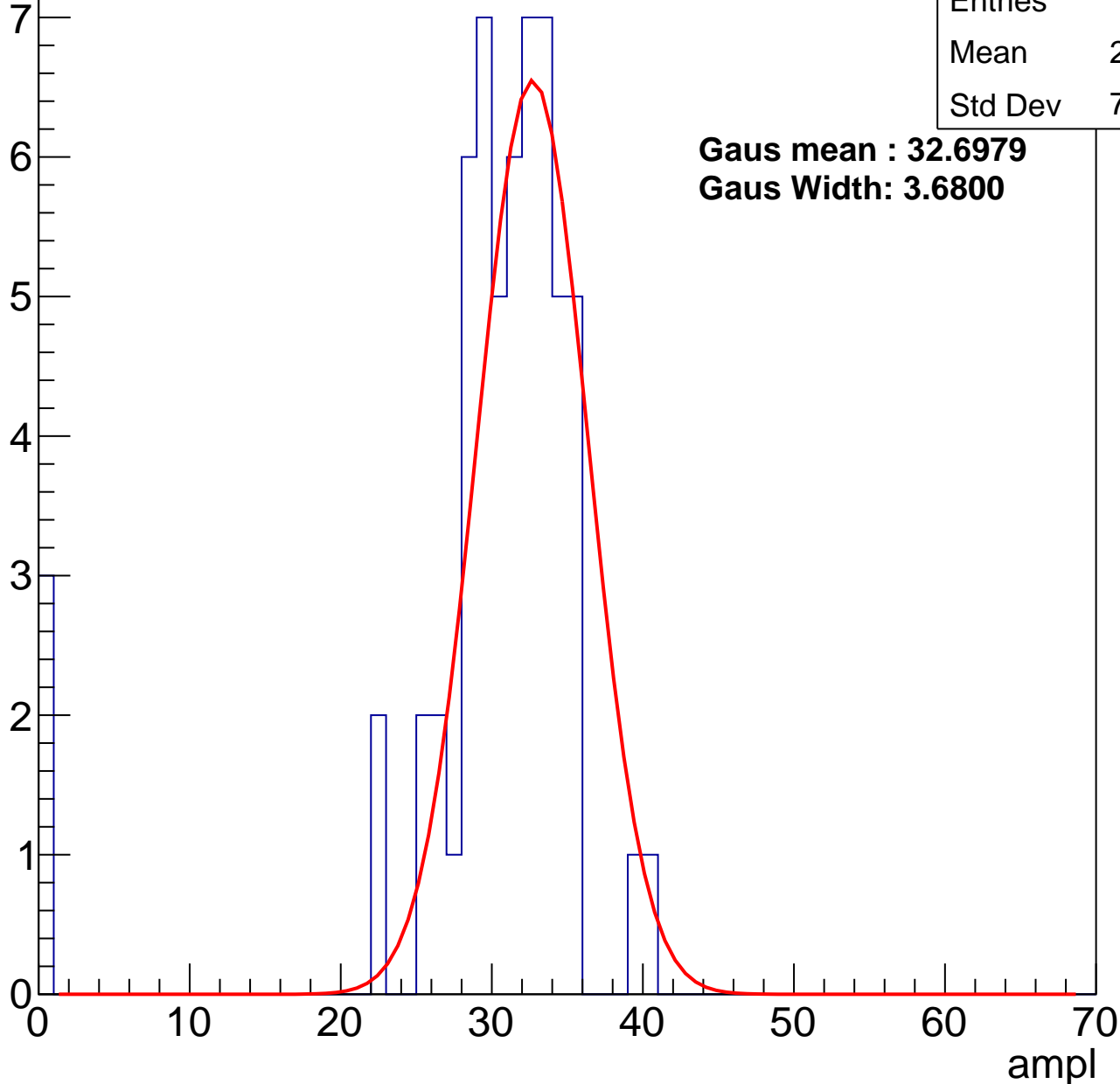
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.32
Std Dev	7.533

Gaus mean : 32.6979

Gaus Width: 3.6800



B0L001S, U15-ch97, adc1

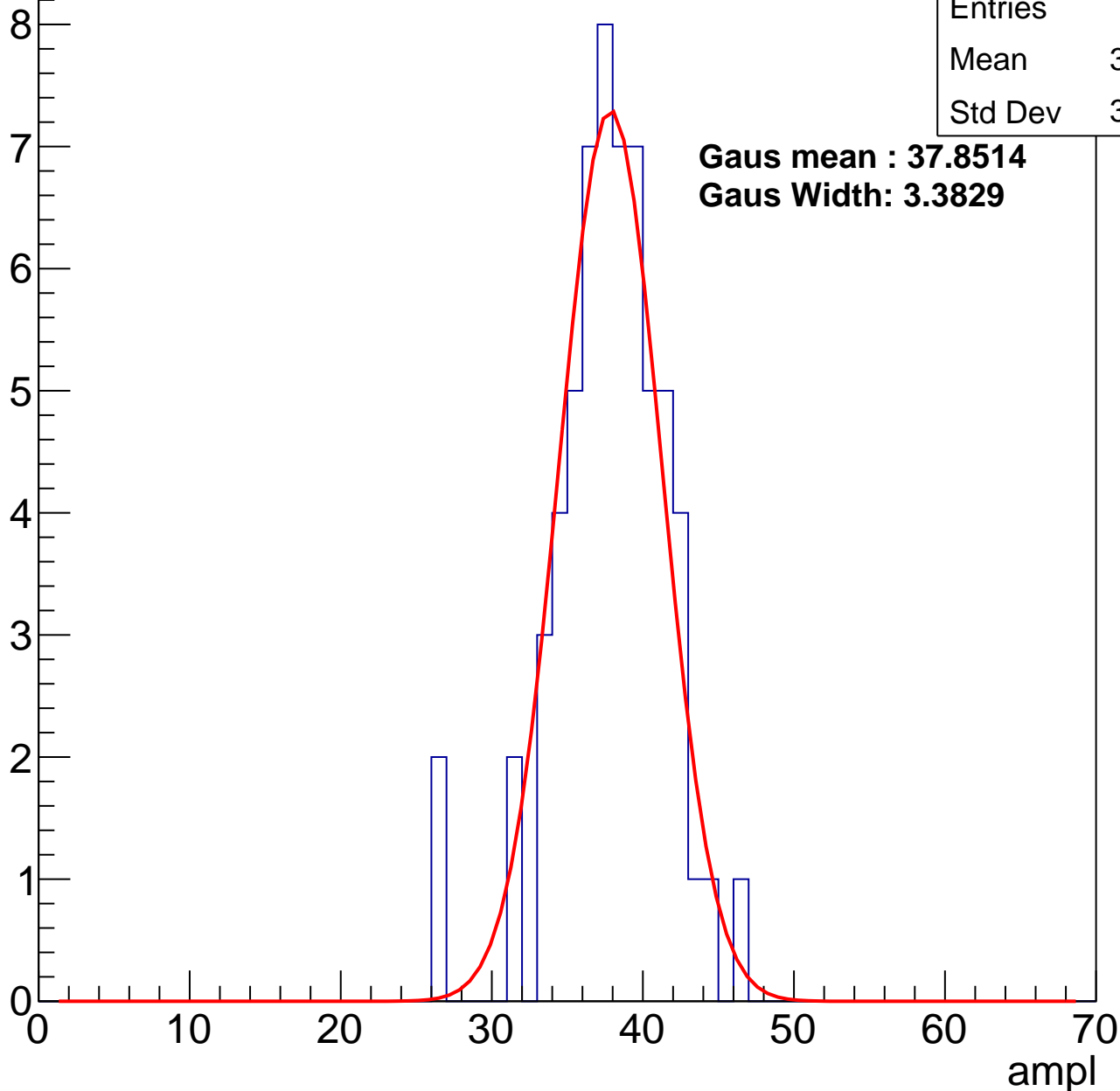
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.37
Std Dev	3.686

Gaus mean : 37.8514

Gaus Width: 3.3829



B0L001S, U15-ch97, adc2

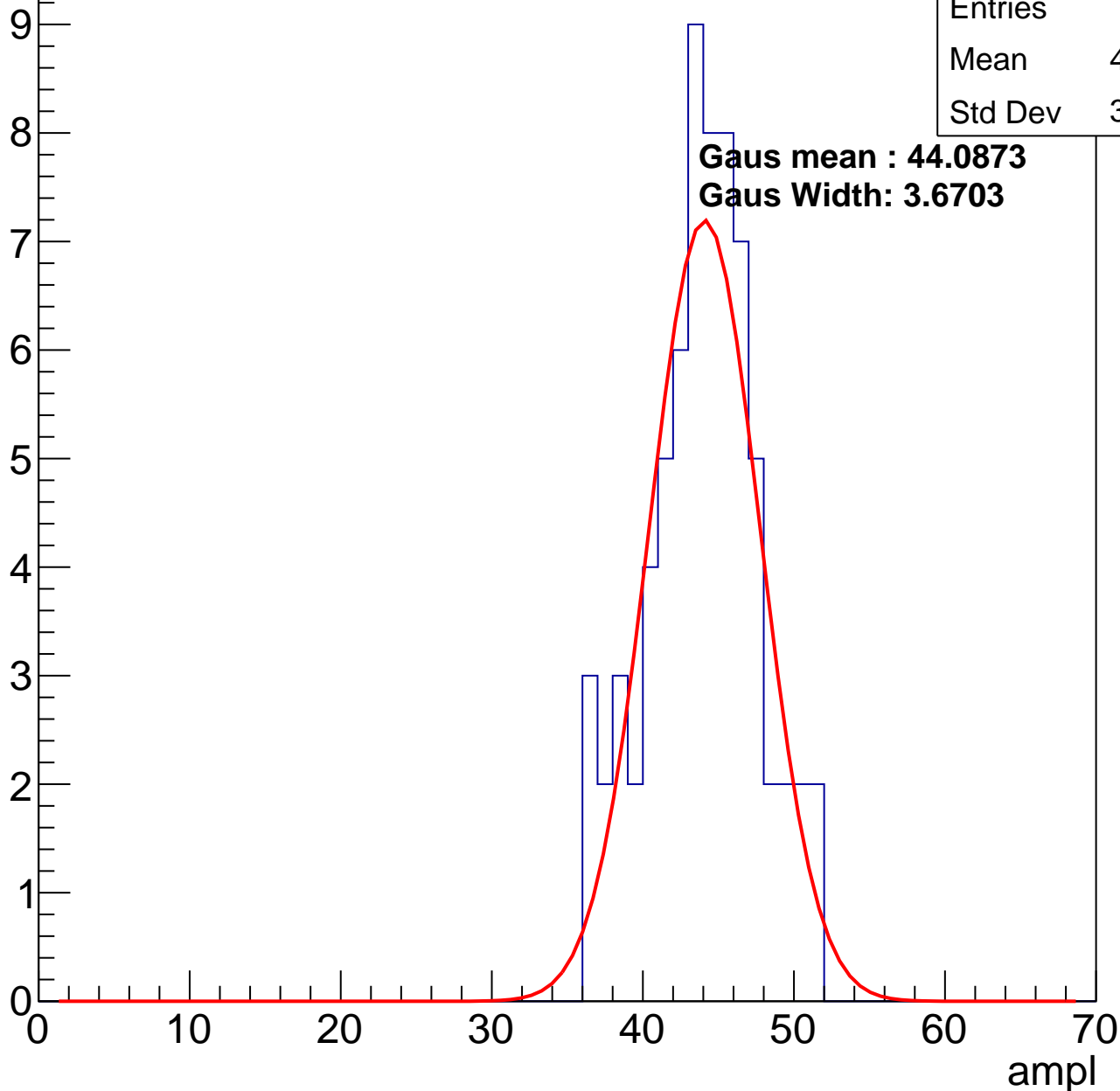
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.47
Std Dev	3.593

Gaus mean : 44.0873

Gaus Width: 3.6703

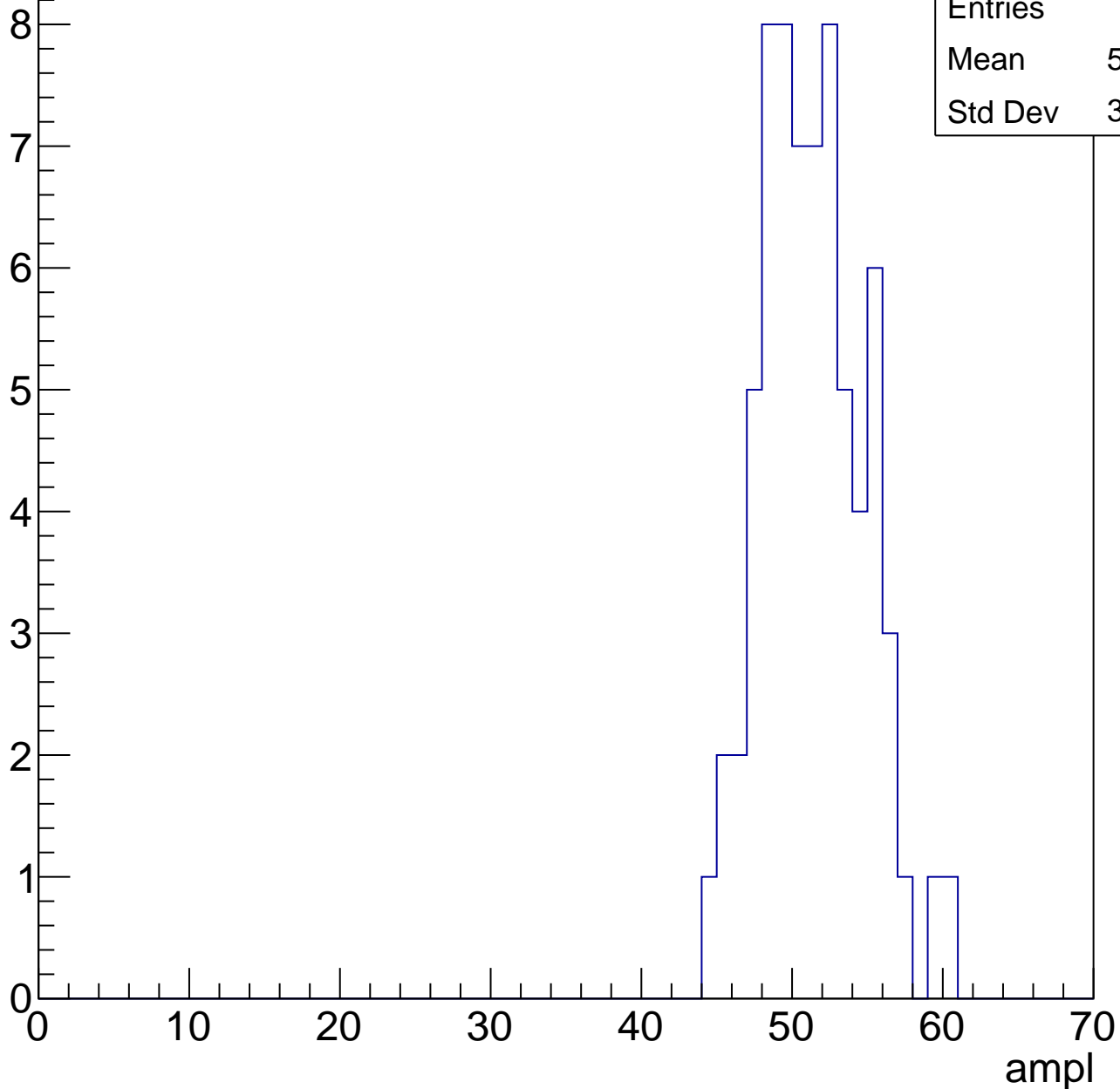


B0L001S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.94
Std Dev	3.357

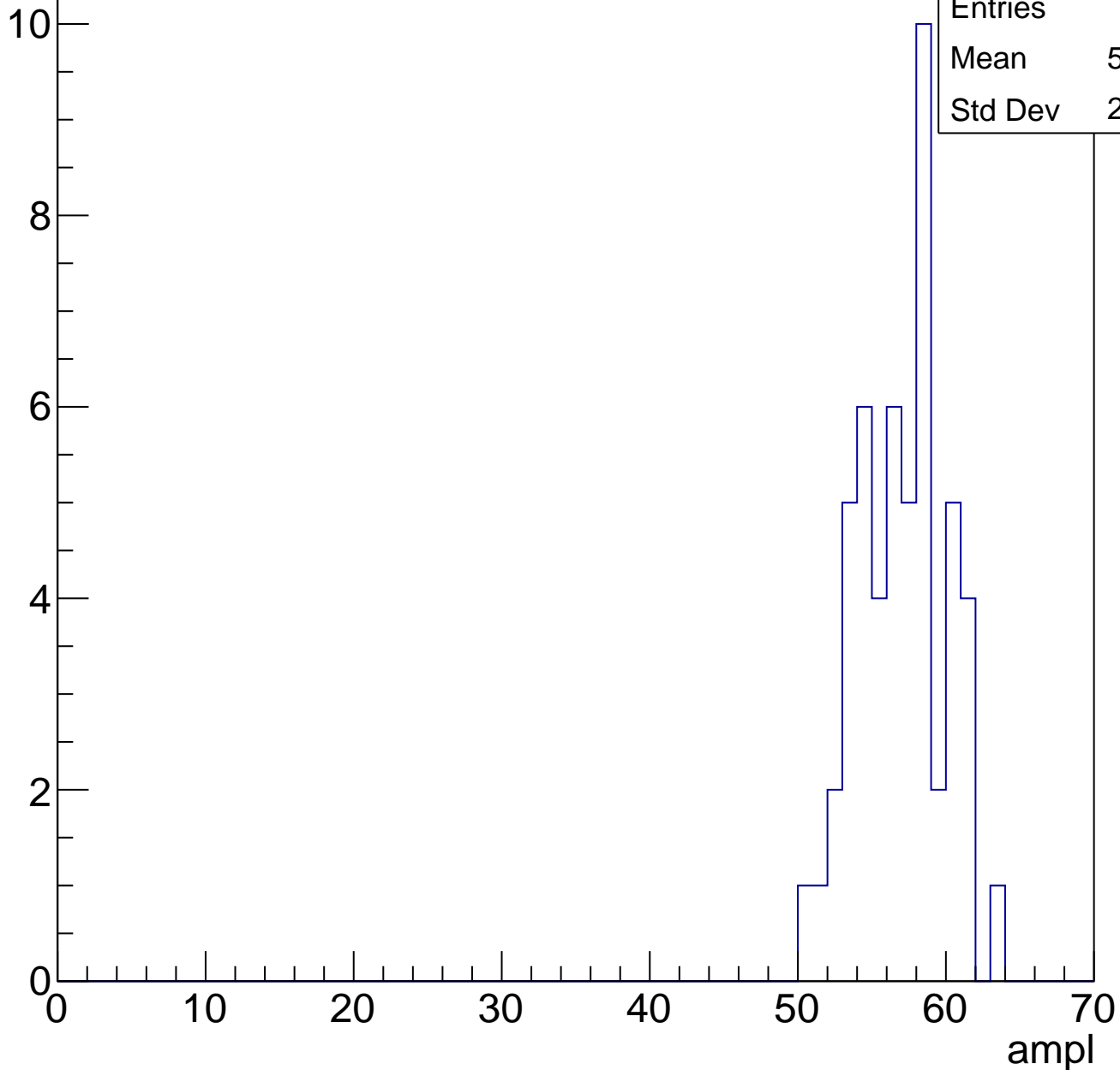


B0L001S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	52
Mean	56.54
Std Dev	2.919

Entry



B0L001S, U15-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	40
Mean	60.67
Std Dev	1.992

Entry

10

8

6

4

2

0

0

10

20

30

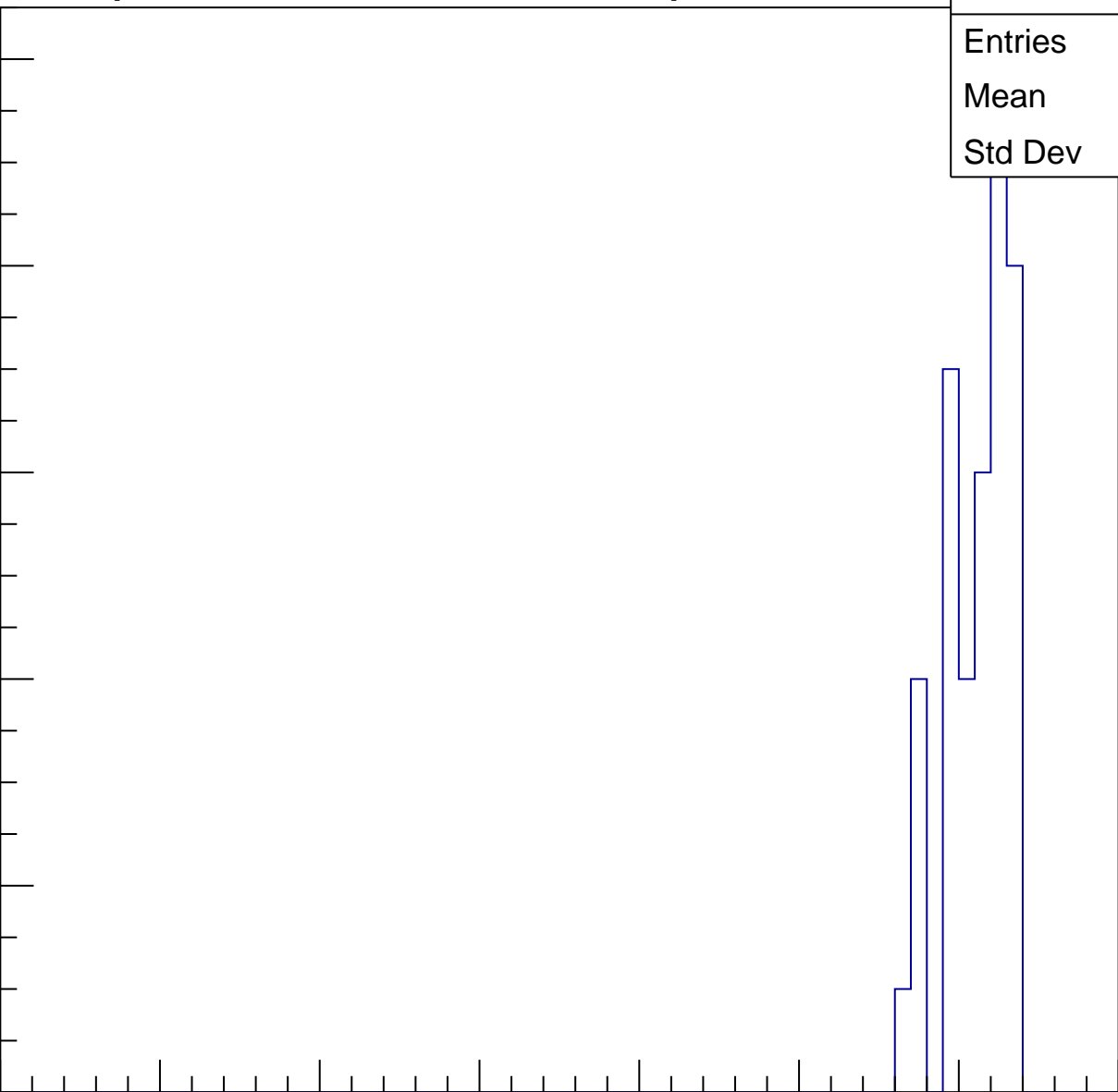
40

50

60

ampl

70



B0L001S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch98, adc0

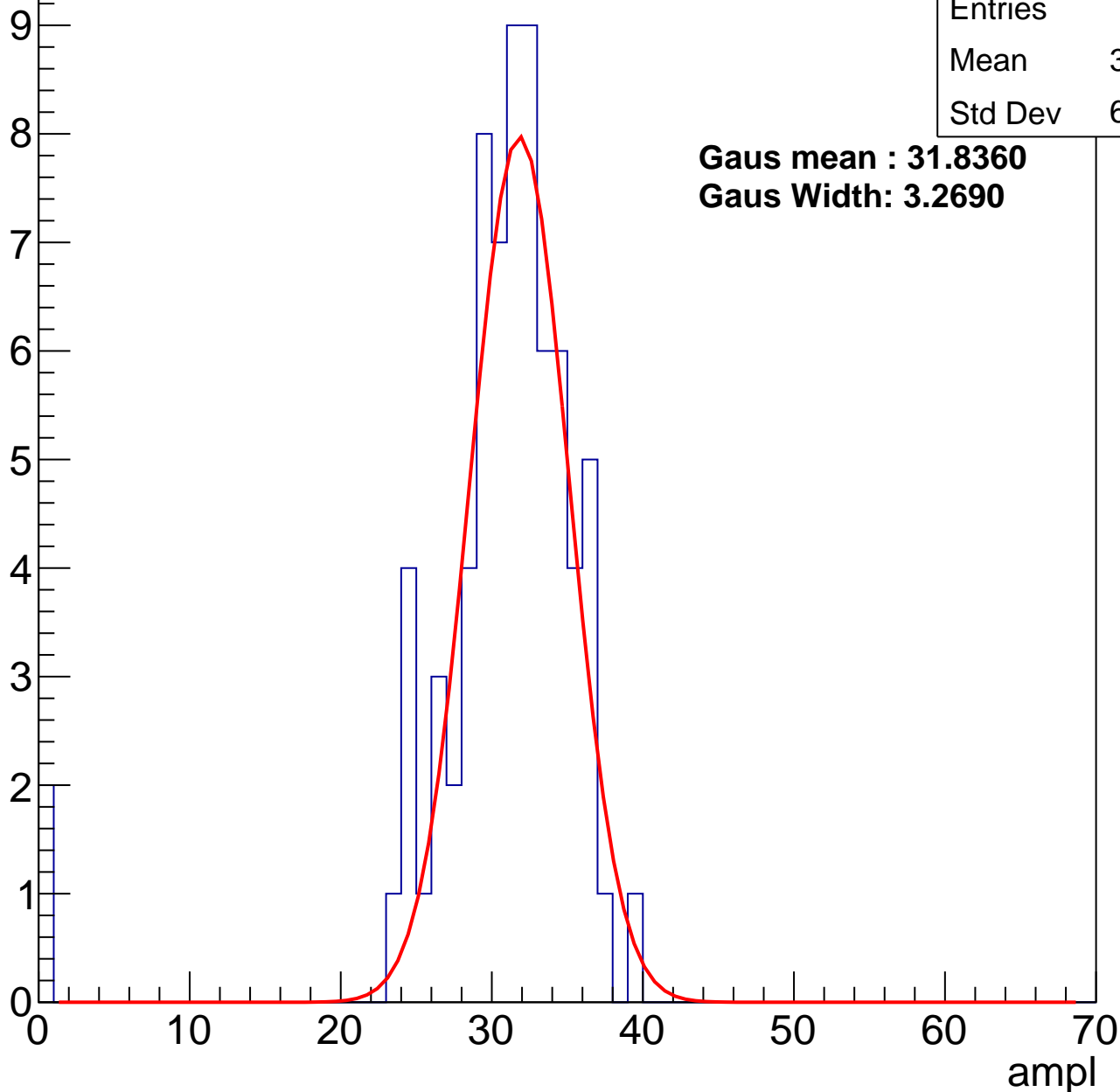
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.07
Std Dev	6.096

Gaus mean : 31.8360

Gaus Width: 3.2690



B0L001S, U15-ch98, adc1

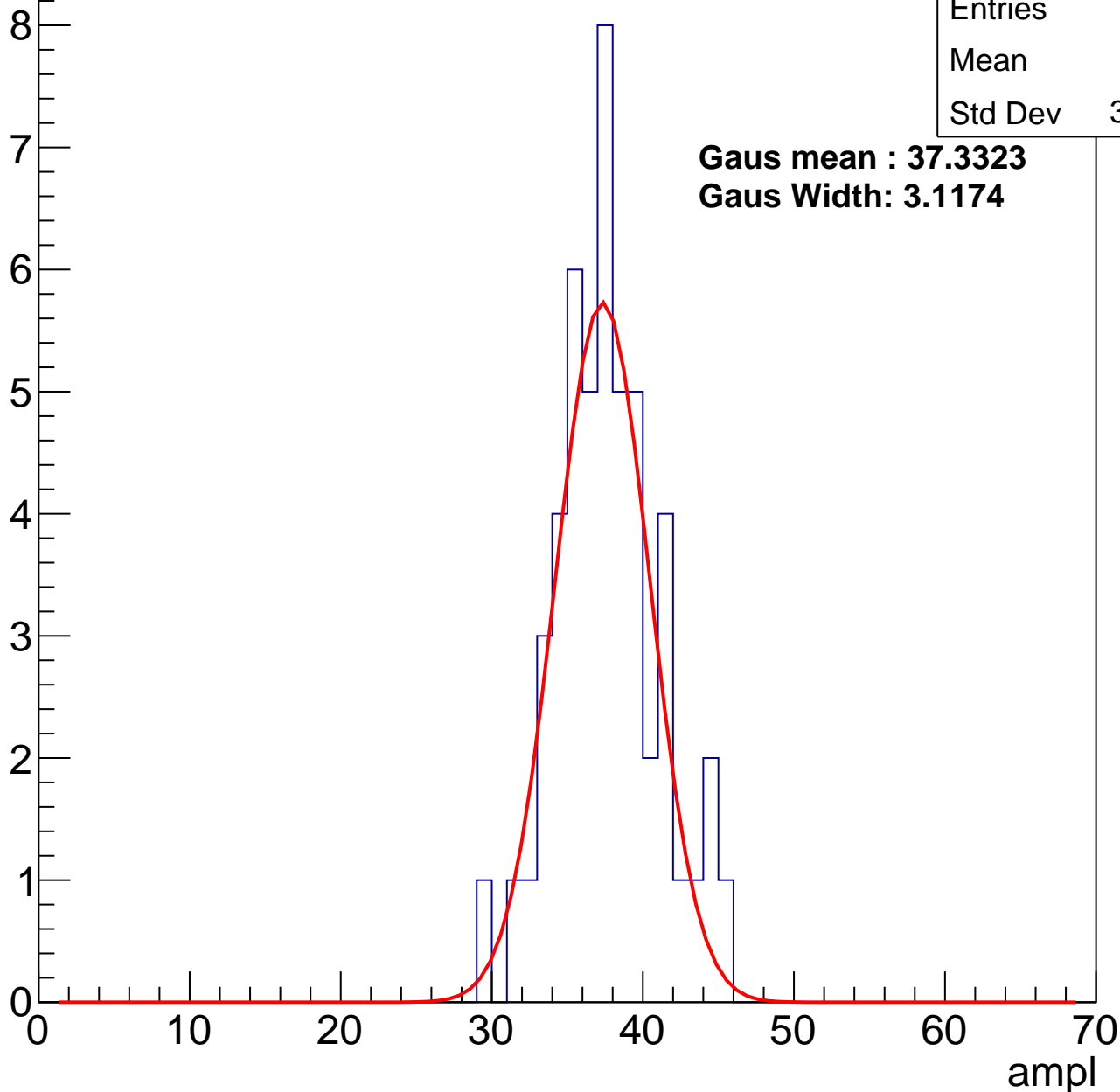
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	37.2
Std Dev	3.365

Gaus mean : 37.3323

Gaus Width: 3.1174



B0L001S, U15-ch98, adc2

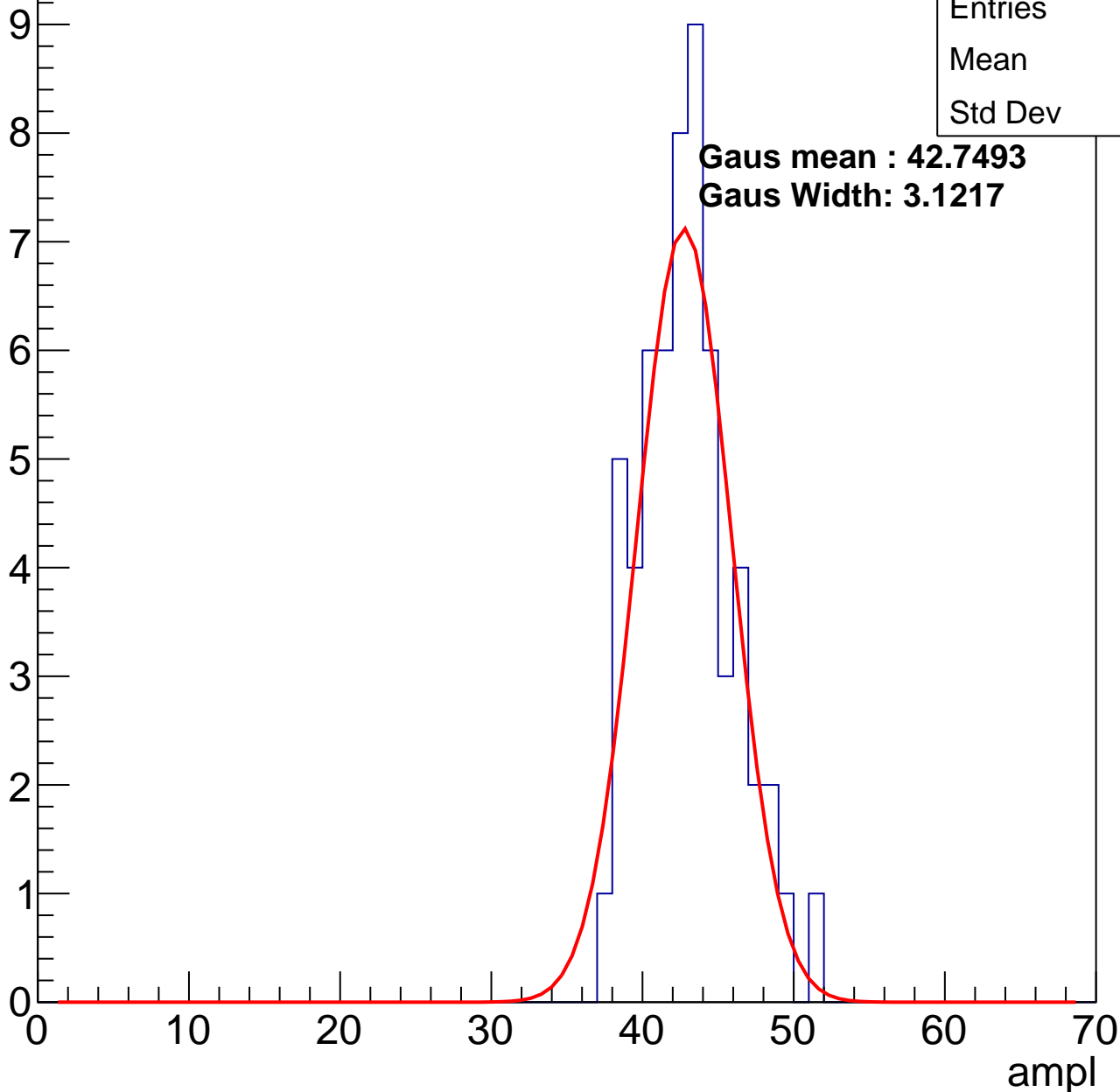
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	42.5
Std Dev	3.03

Gaus mean : 42.7493

Gaus Width: 3.1217

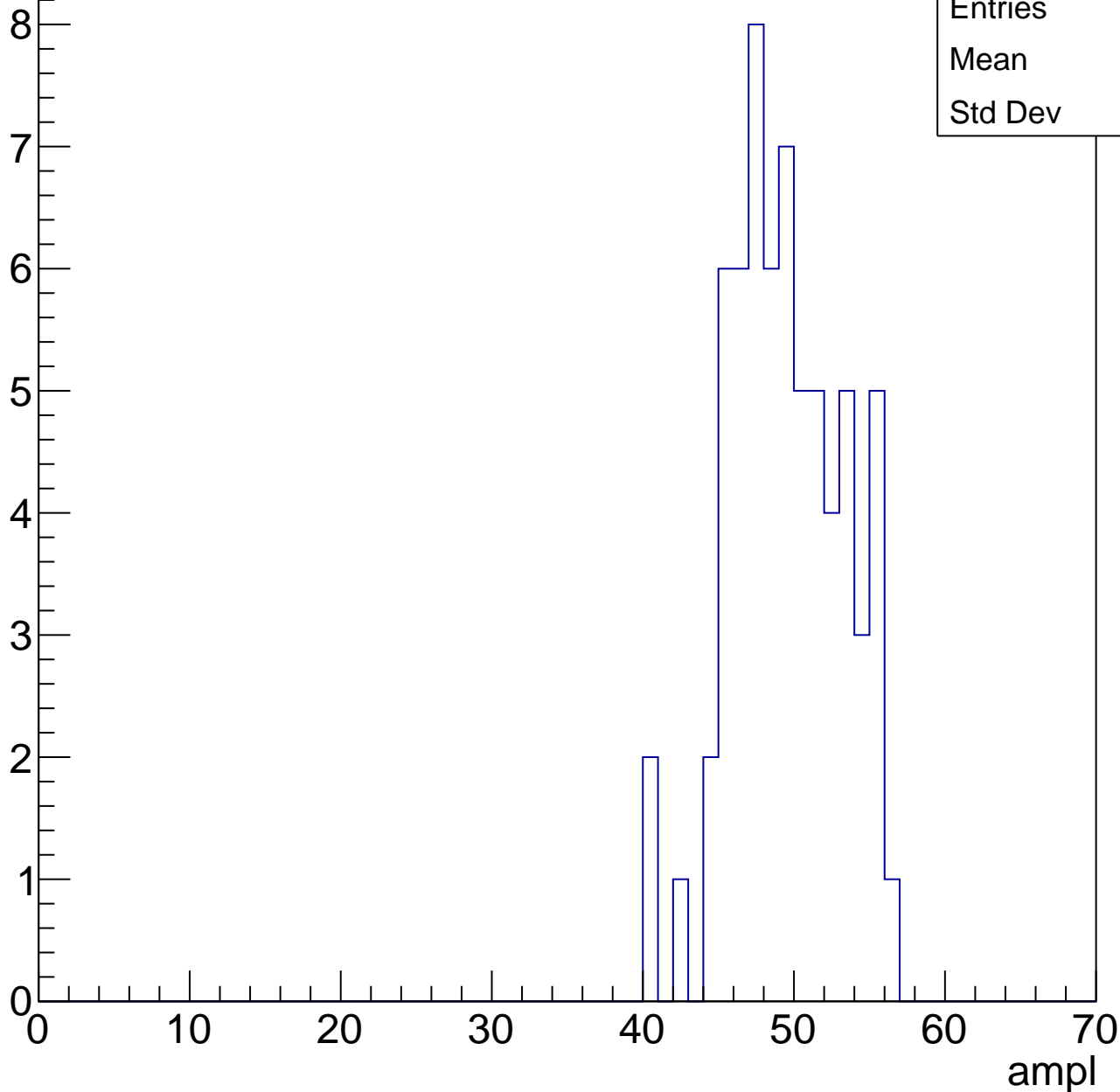


B0L001S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49
Std Dev	3.68

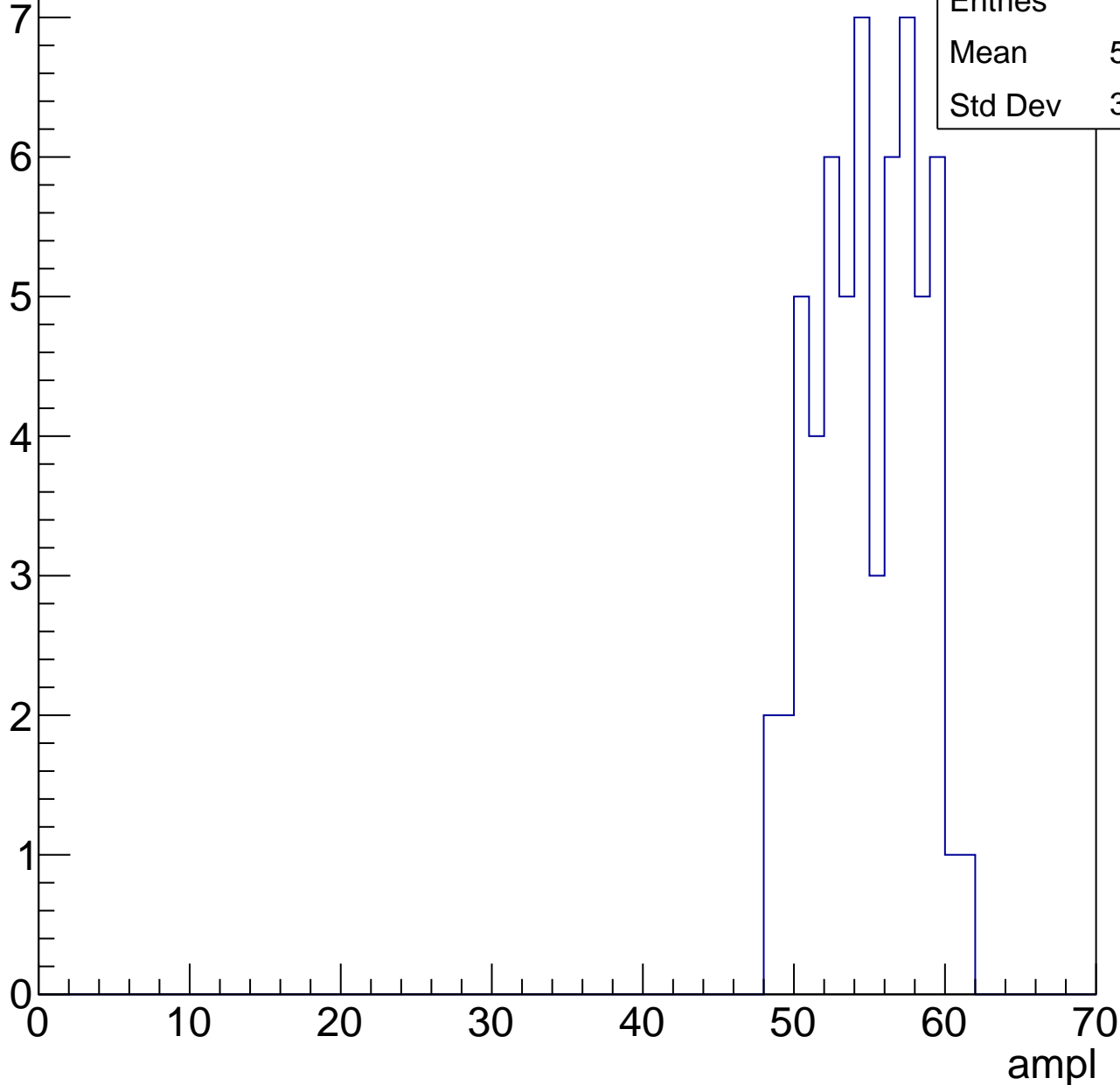


B0L001S, U15-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	54.47
Std Dev	3.314



B0L001S, U15-ch98, adc5

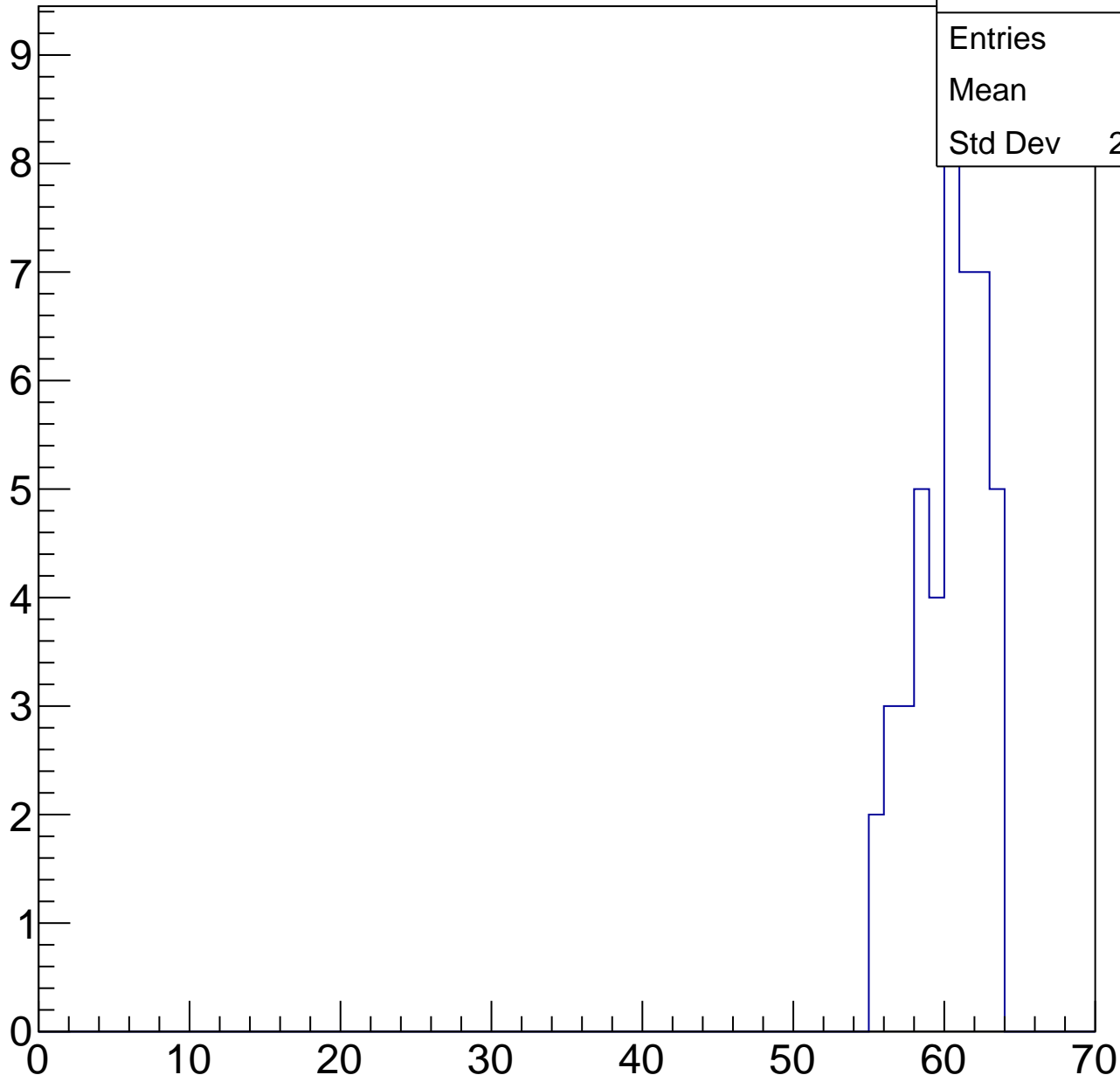
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	45
Mean	59.8
Std Dev	2.247

ampl

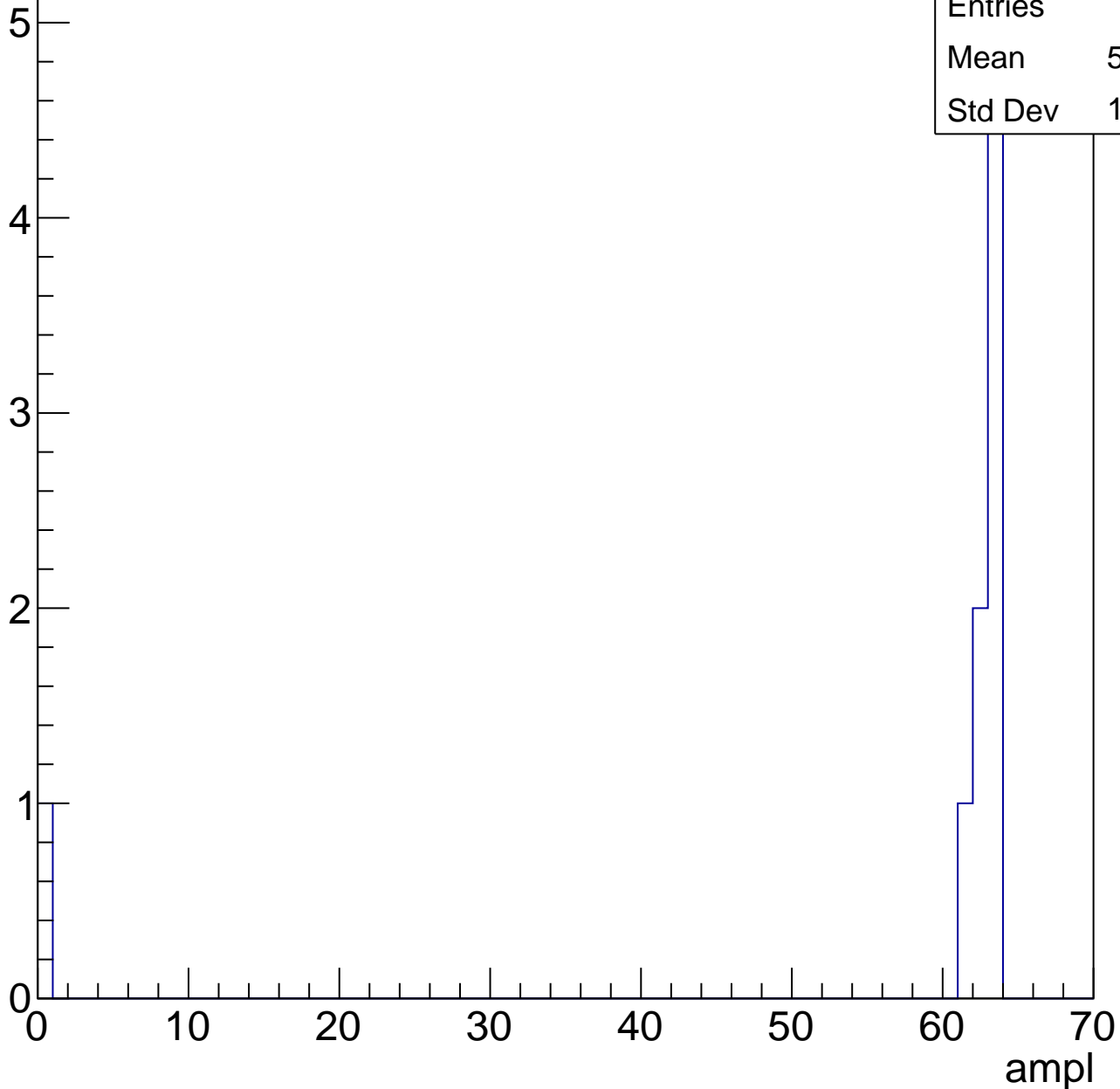


B0L001S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

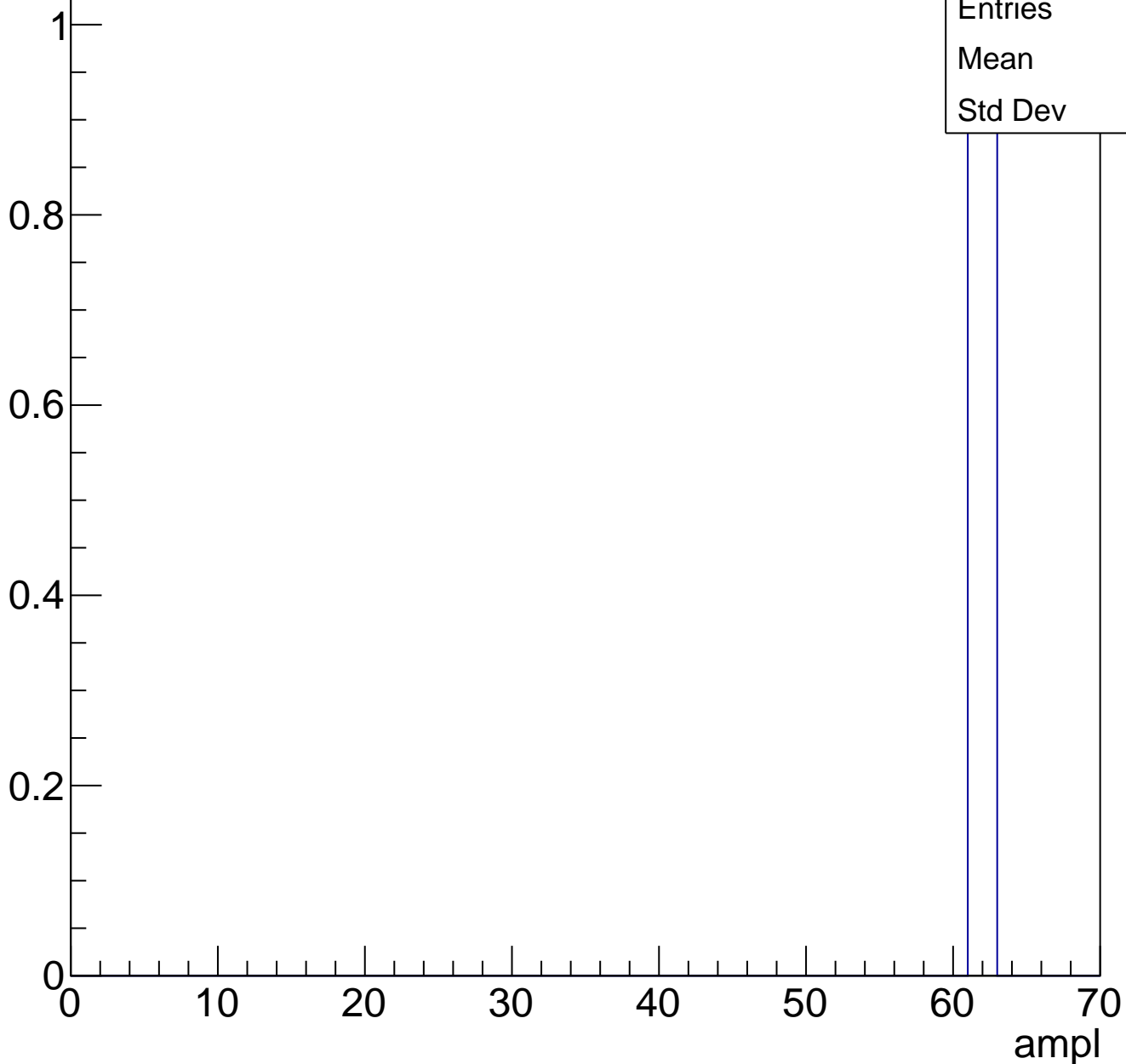
Entries	9
Mean	55.56
Std Dev	19.65



B0L001S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch99, adc0

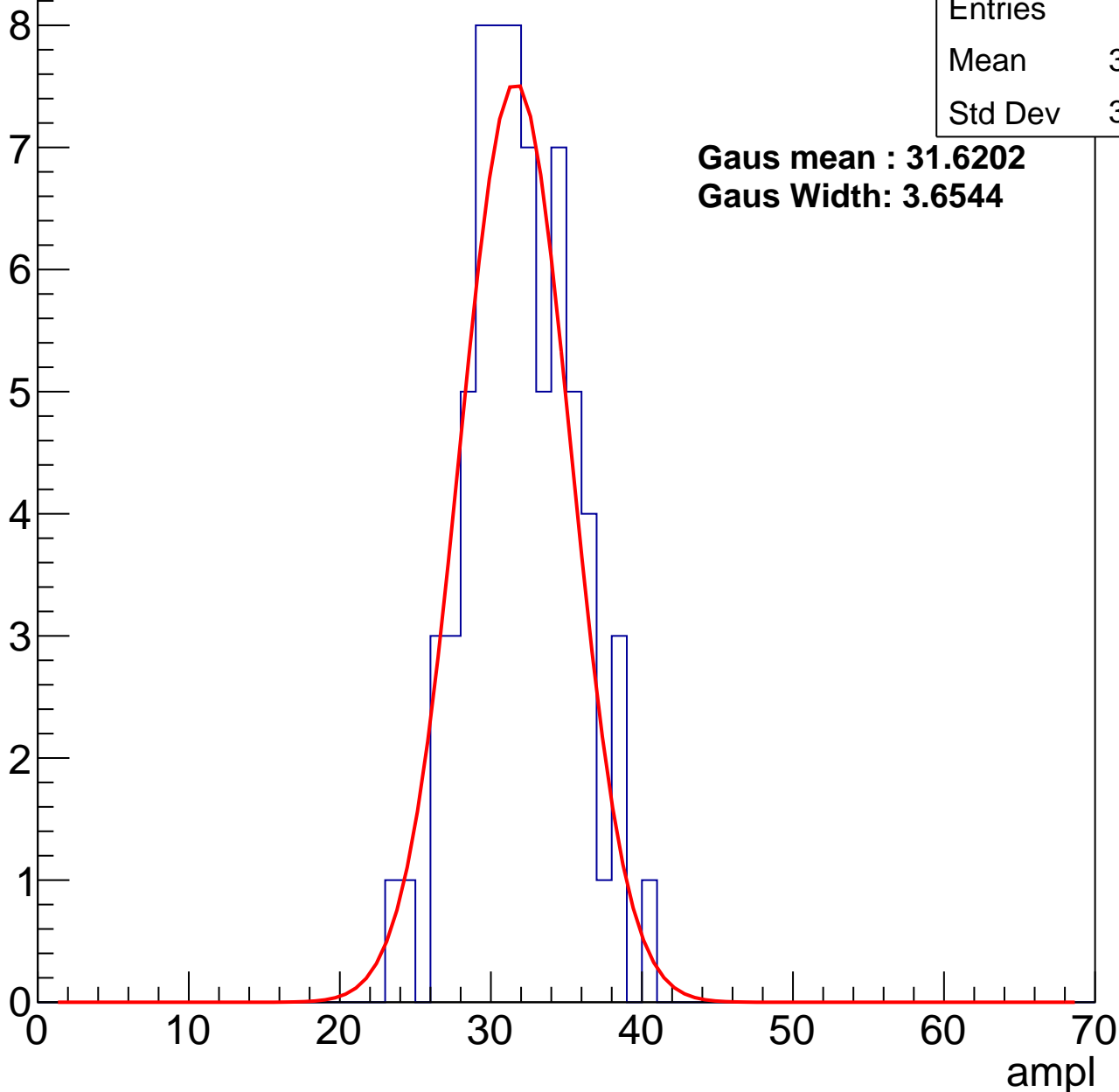
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	31.47
Std Dev	3.463

Gaus mean : 31.6202

Gaus Width: 3.6544



B0L001S, U15-ch99, adc1

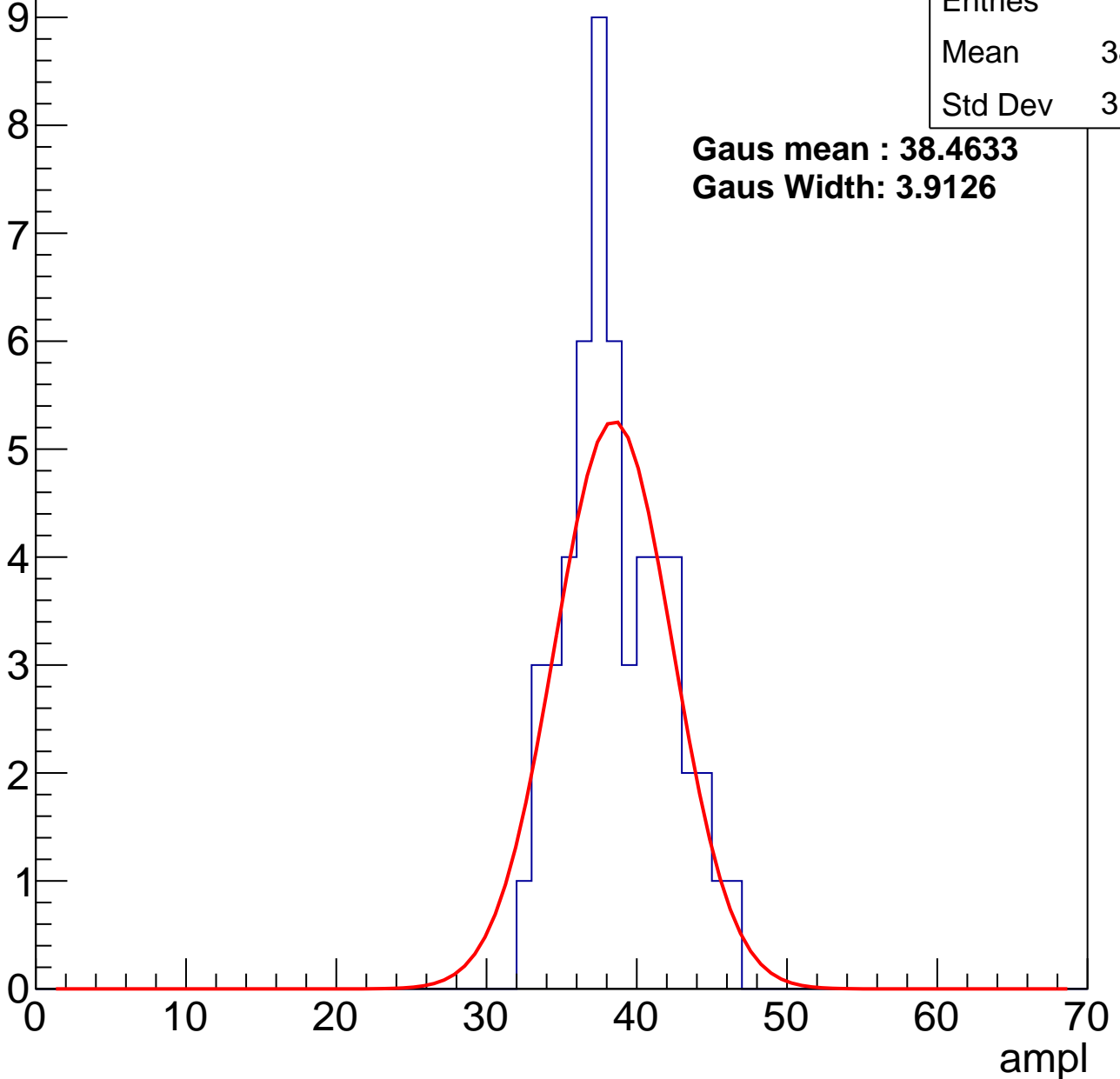
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	38.19
Std Dev	3.308

Gaus mean : 38.4633

Gaus Width: 3.9126



B0L001S, U15-ch99, adc2

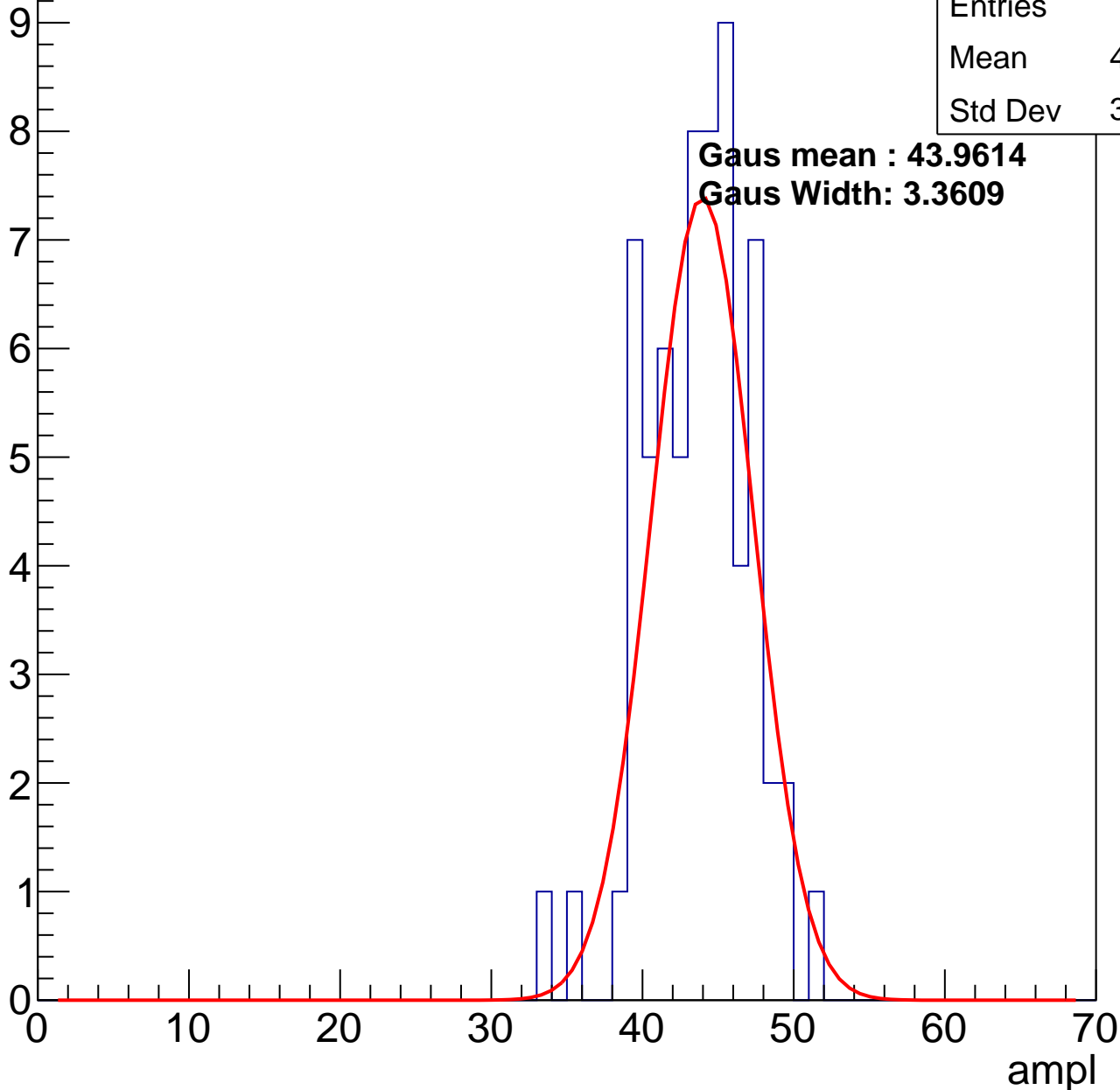
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.19
Std Dev	3.347

Gaus mean : 43.9614

Gaus Width: 3.3609

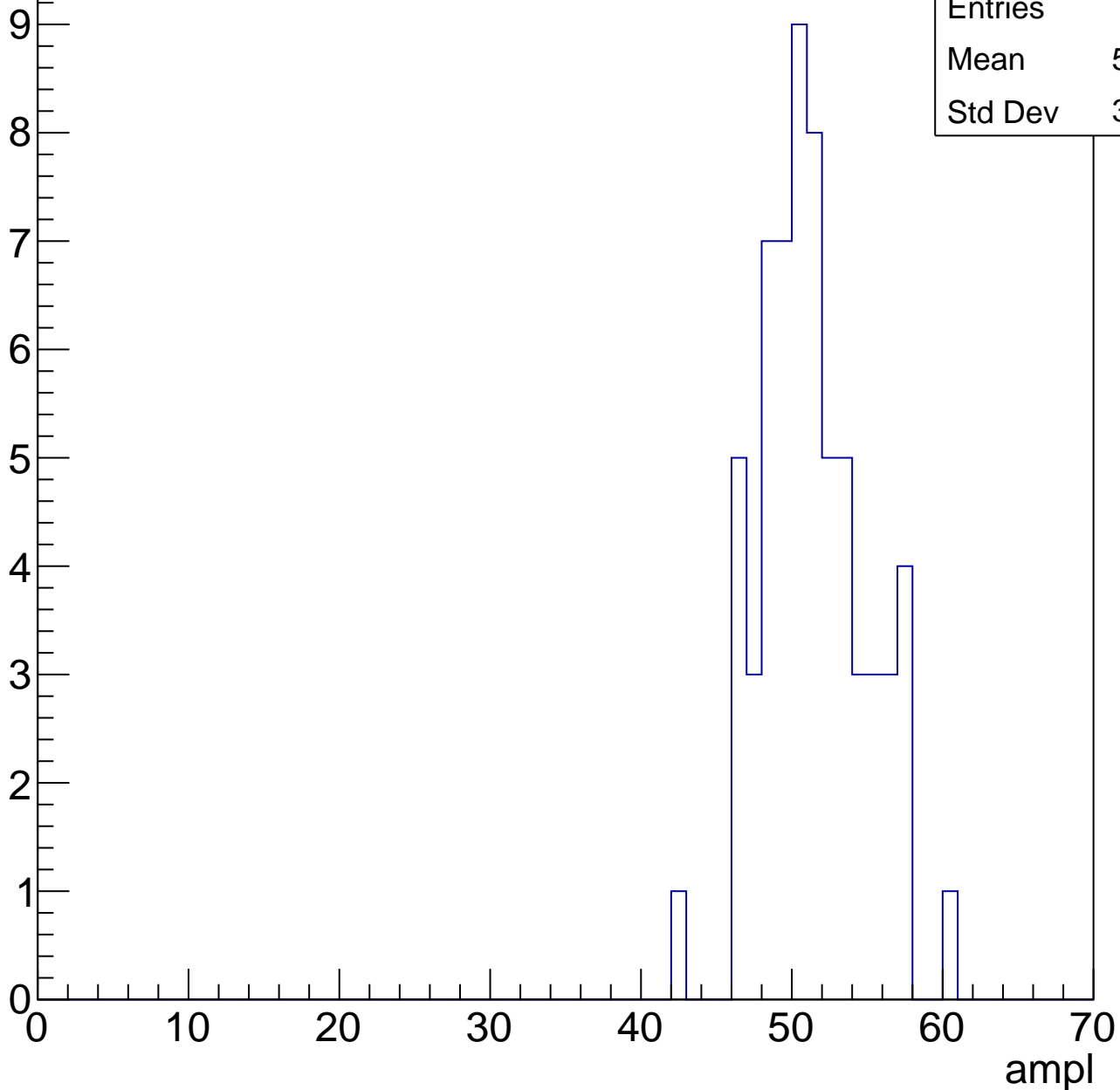


B0L001S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.91
Std Dev	3.431

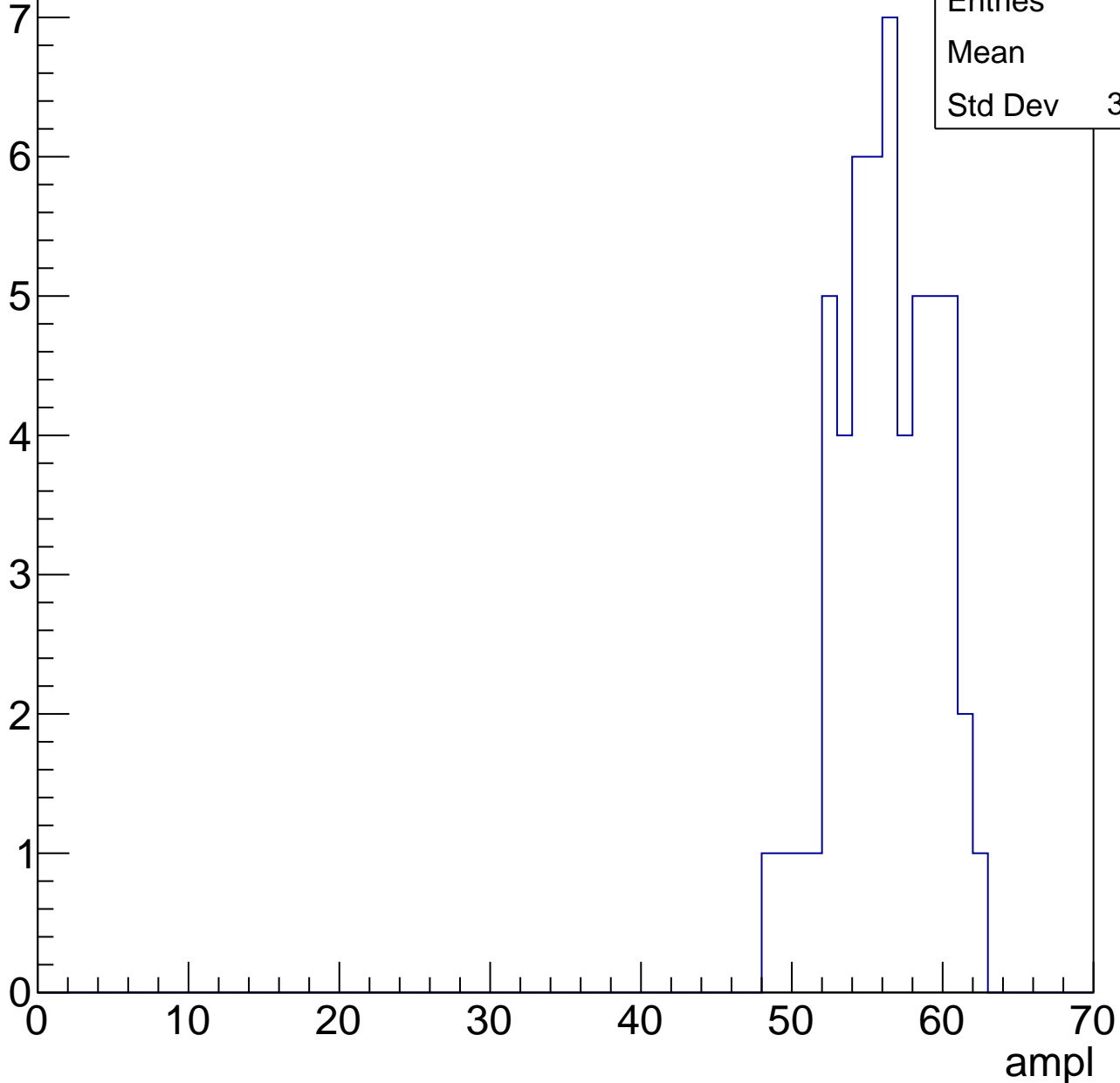


B0L001S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

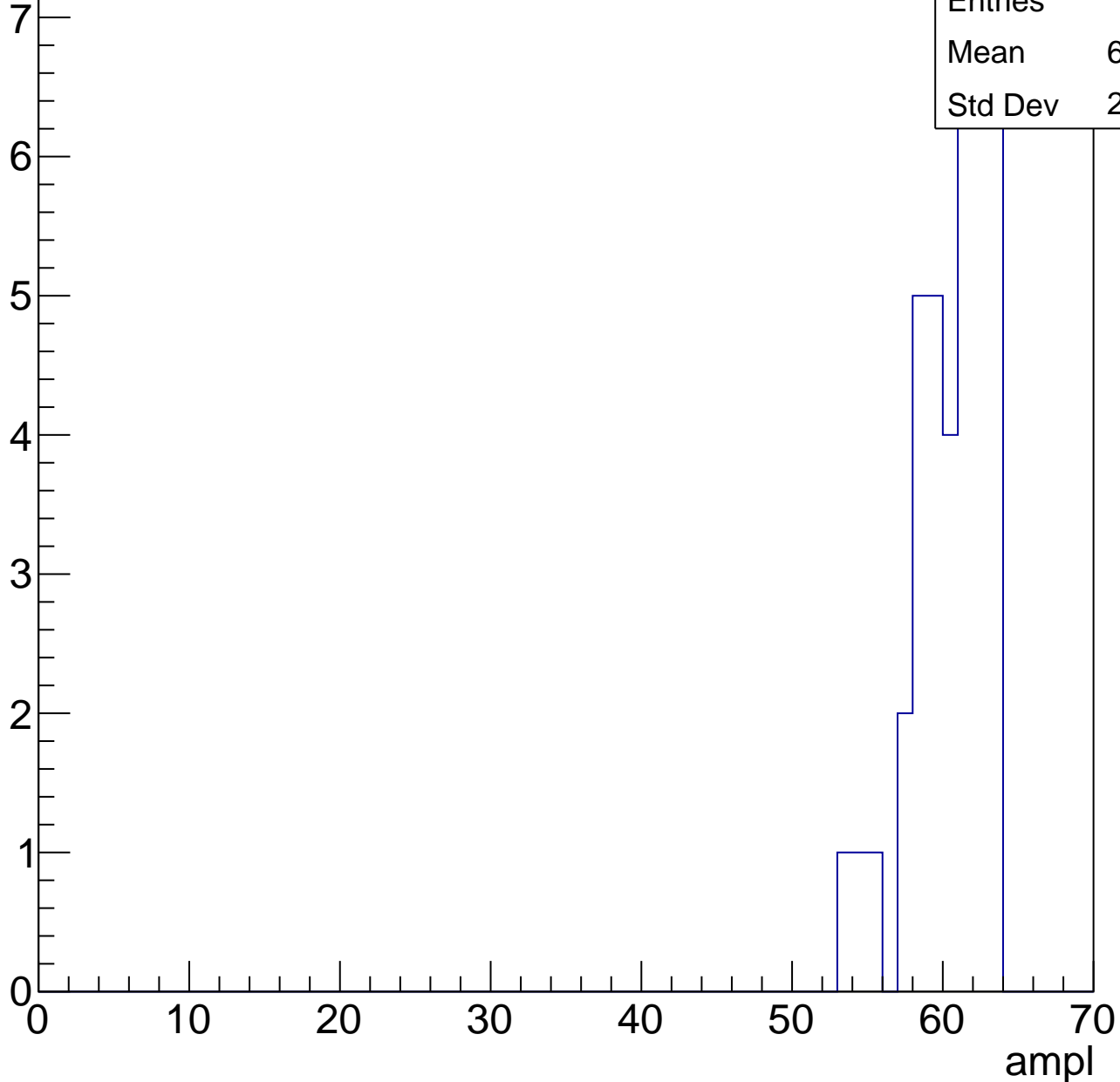
Entries	54
Mean	55.8
Std Dev	3.199



B0L001S, U15-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

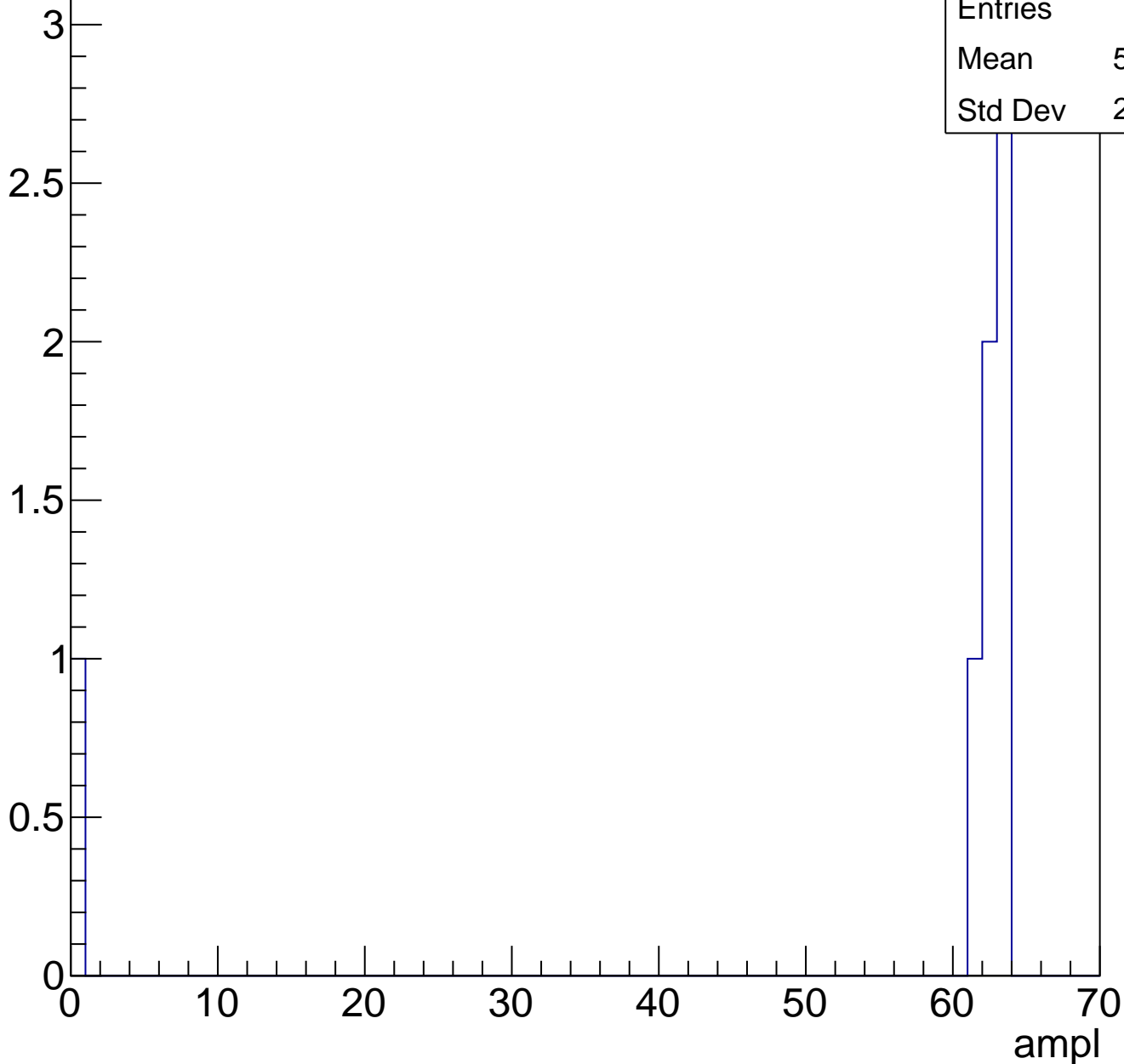
Entry



B0L001S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch100, adc0

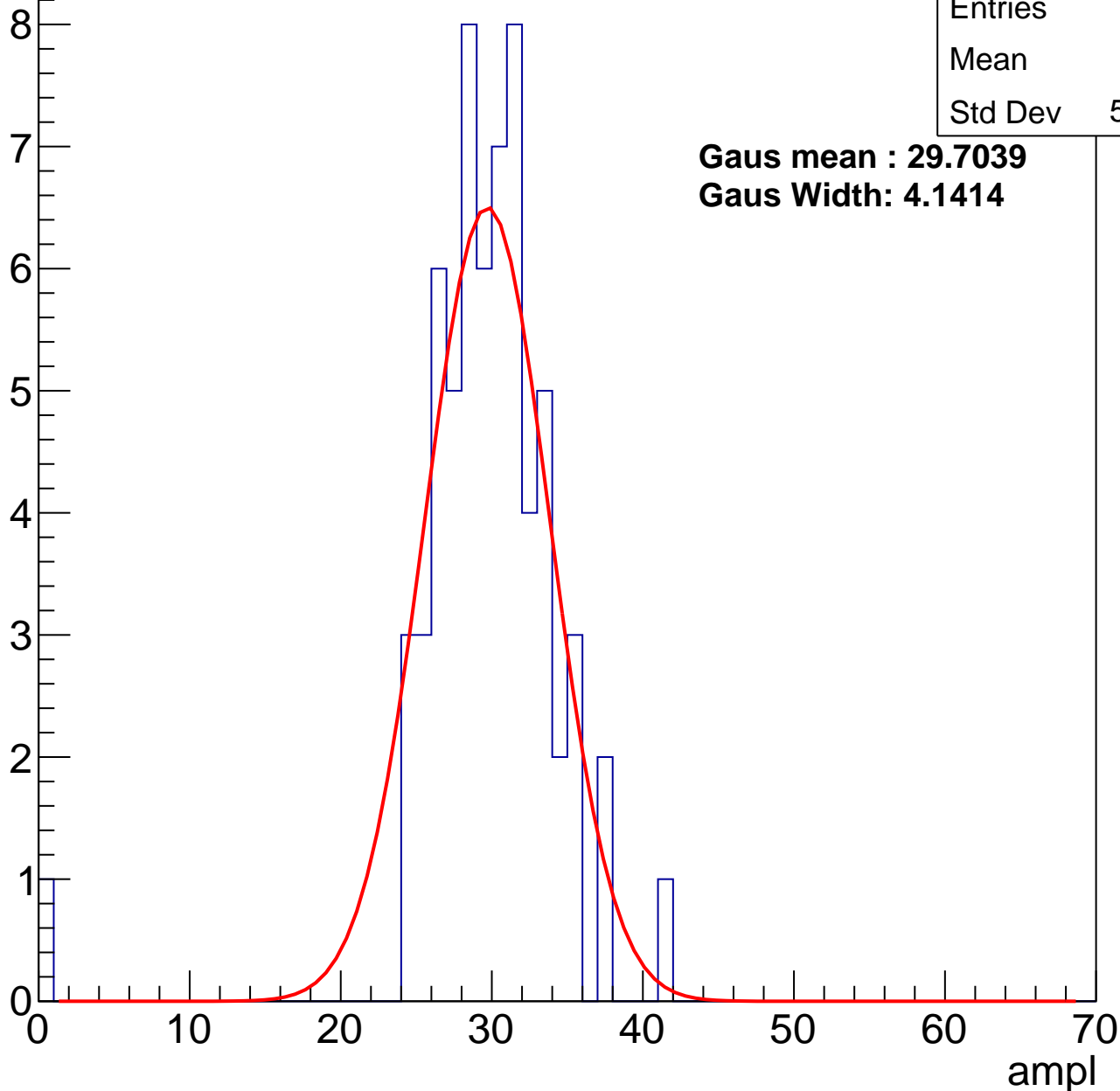
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	29.3
Std Dev	5.039

Gaus mean : 29.7039

Gaus Width: 4.1414



B0L001S, U15-ch100, adc1

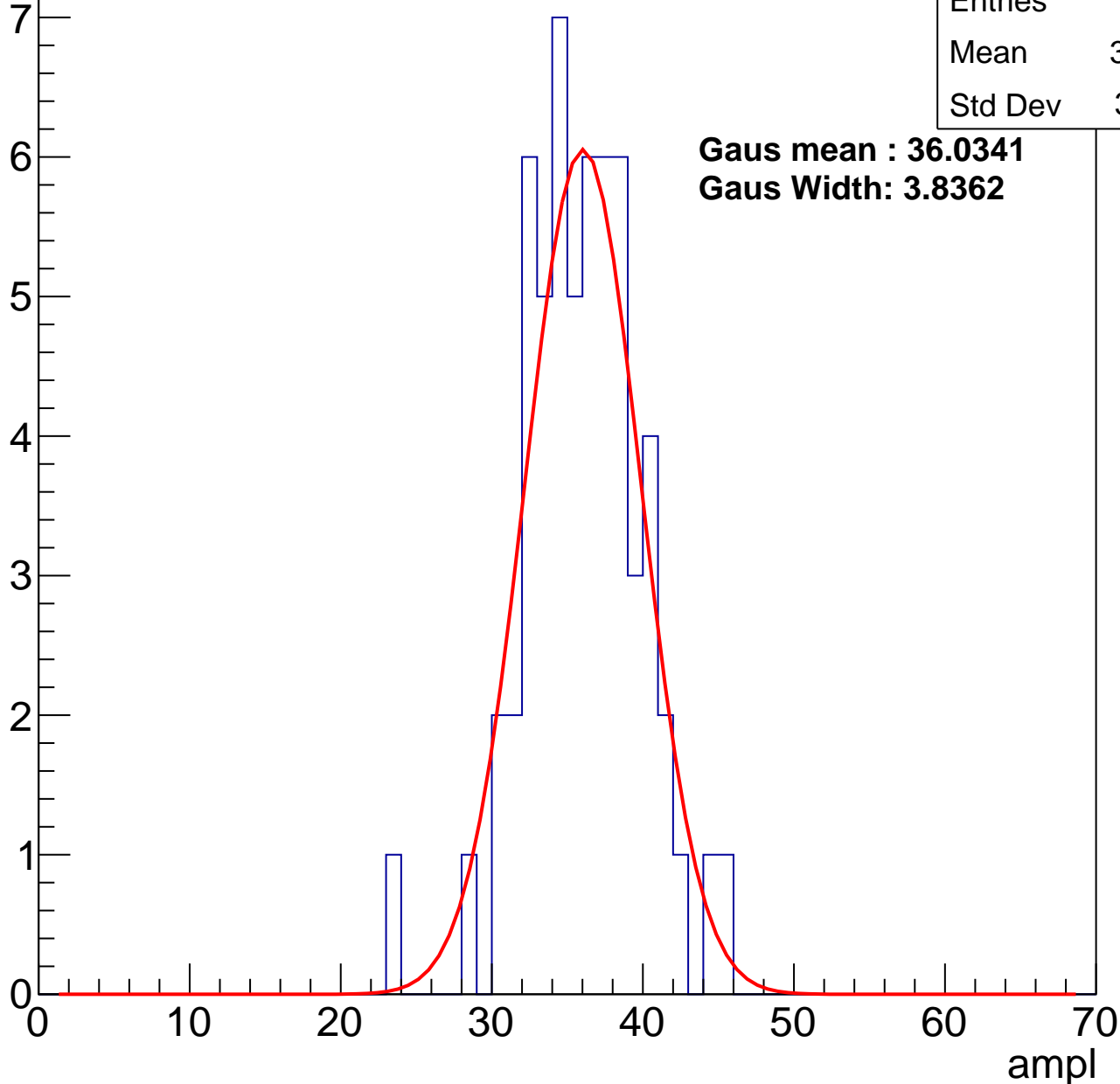
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	35.58
Std Dev	3.841

Gaus mean : 36.0341

Gaus Width: 3.8362

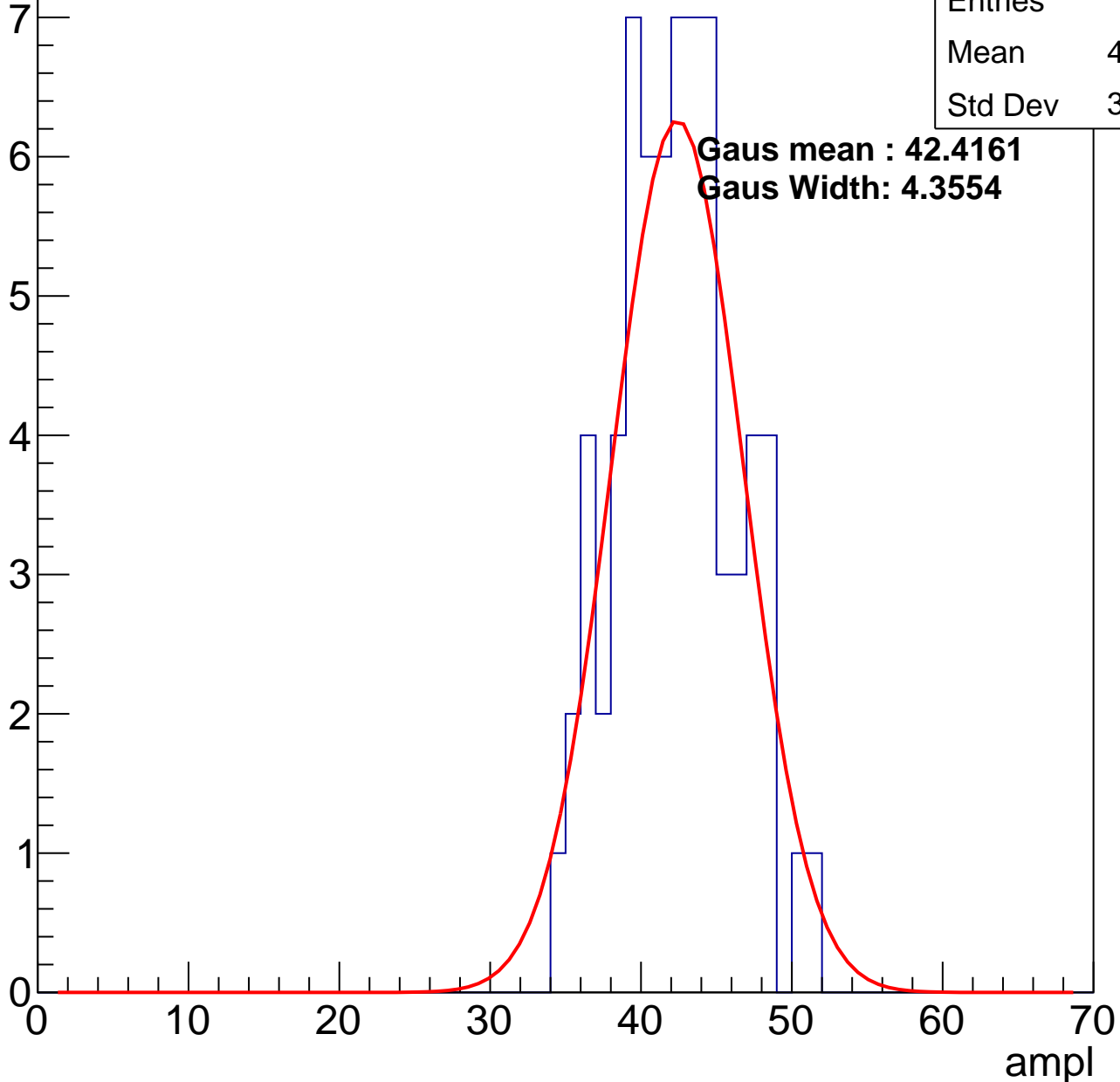


B0L001S, U15-ch100, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	41.88
Std Dev	3.828

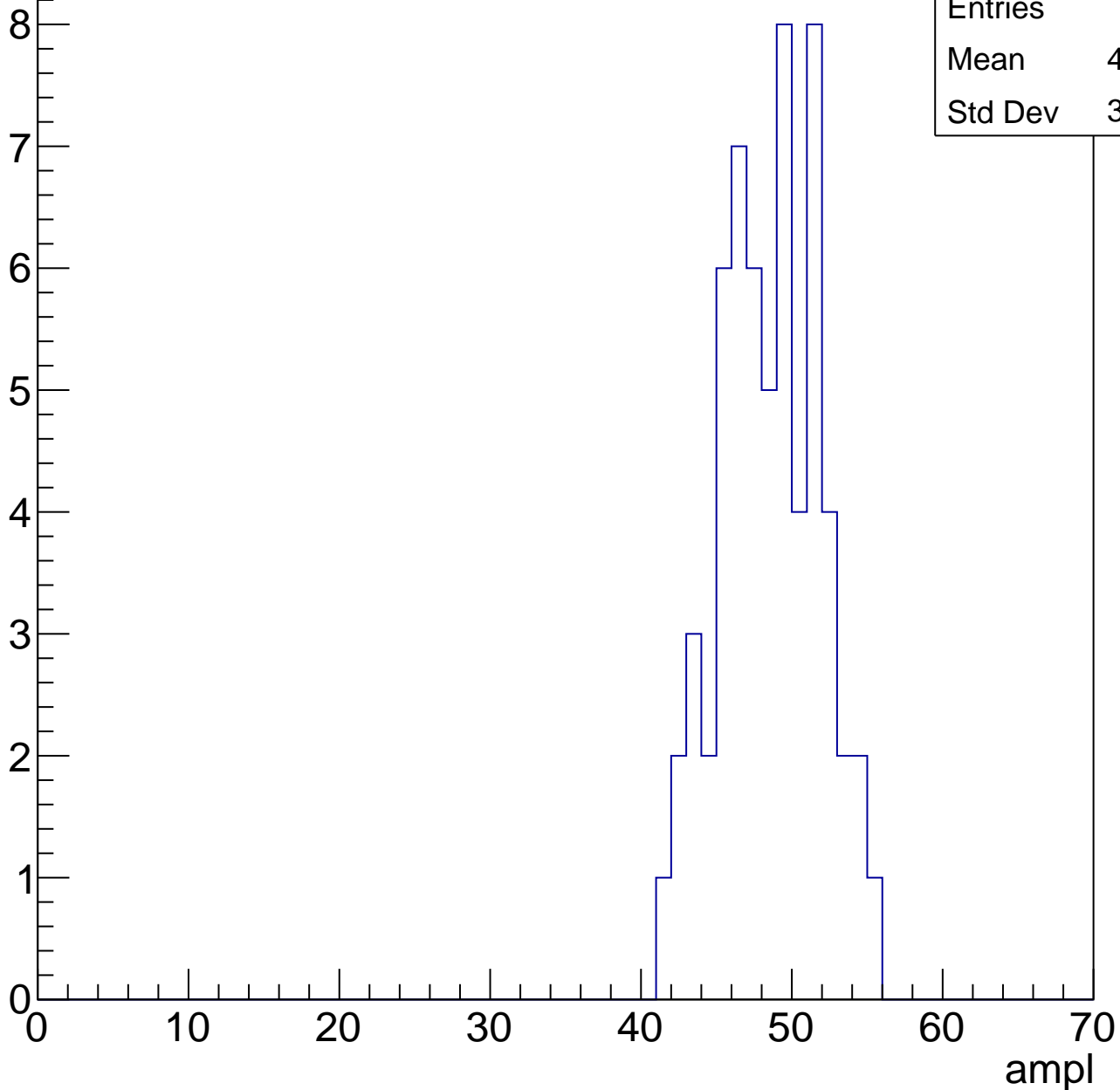


B0L001S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	48.08
Std Dev	3.256

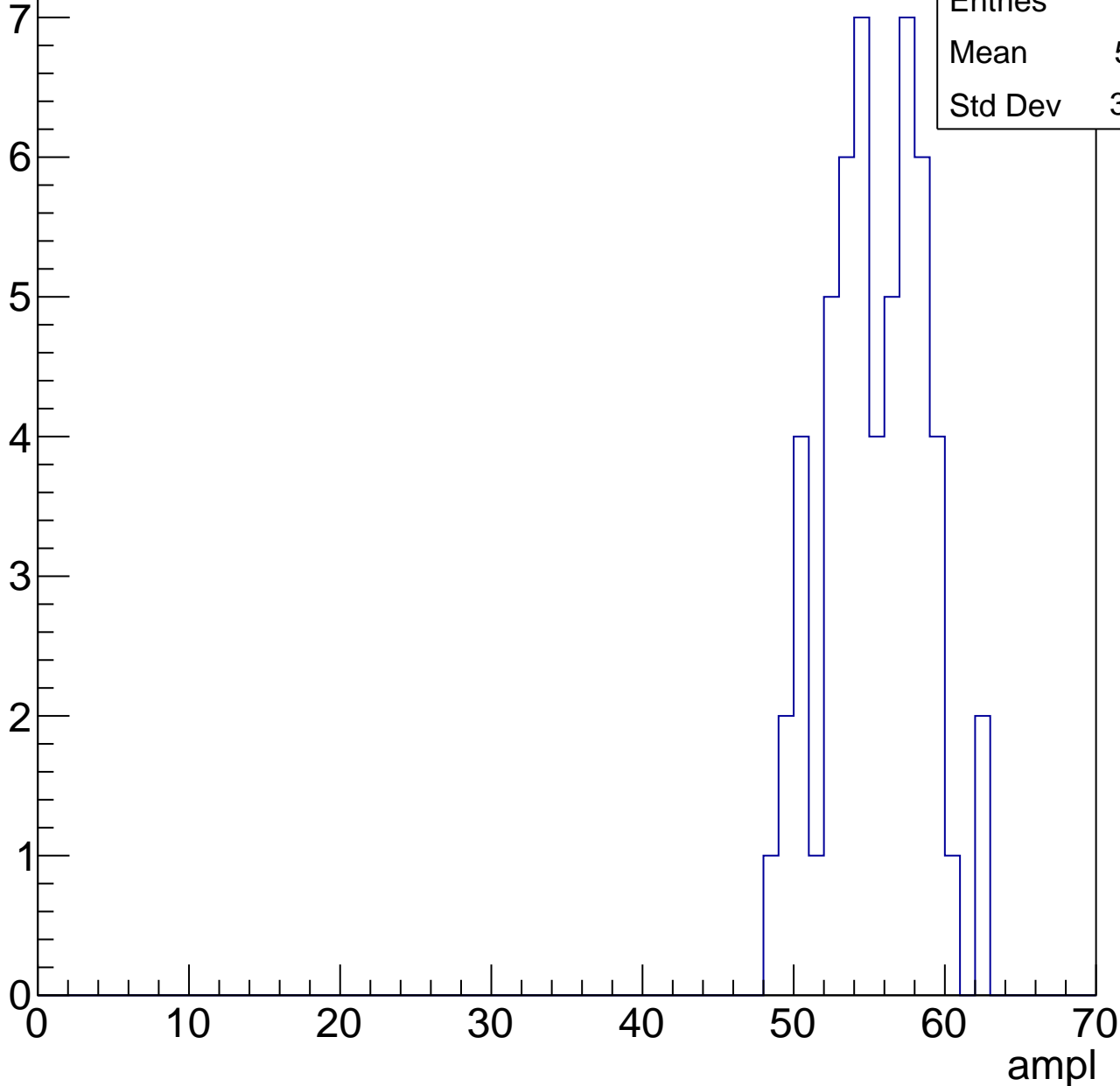


B0L001S, U15-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	54.91
Std Dev	3.266

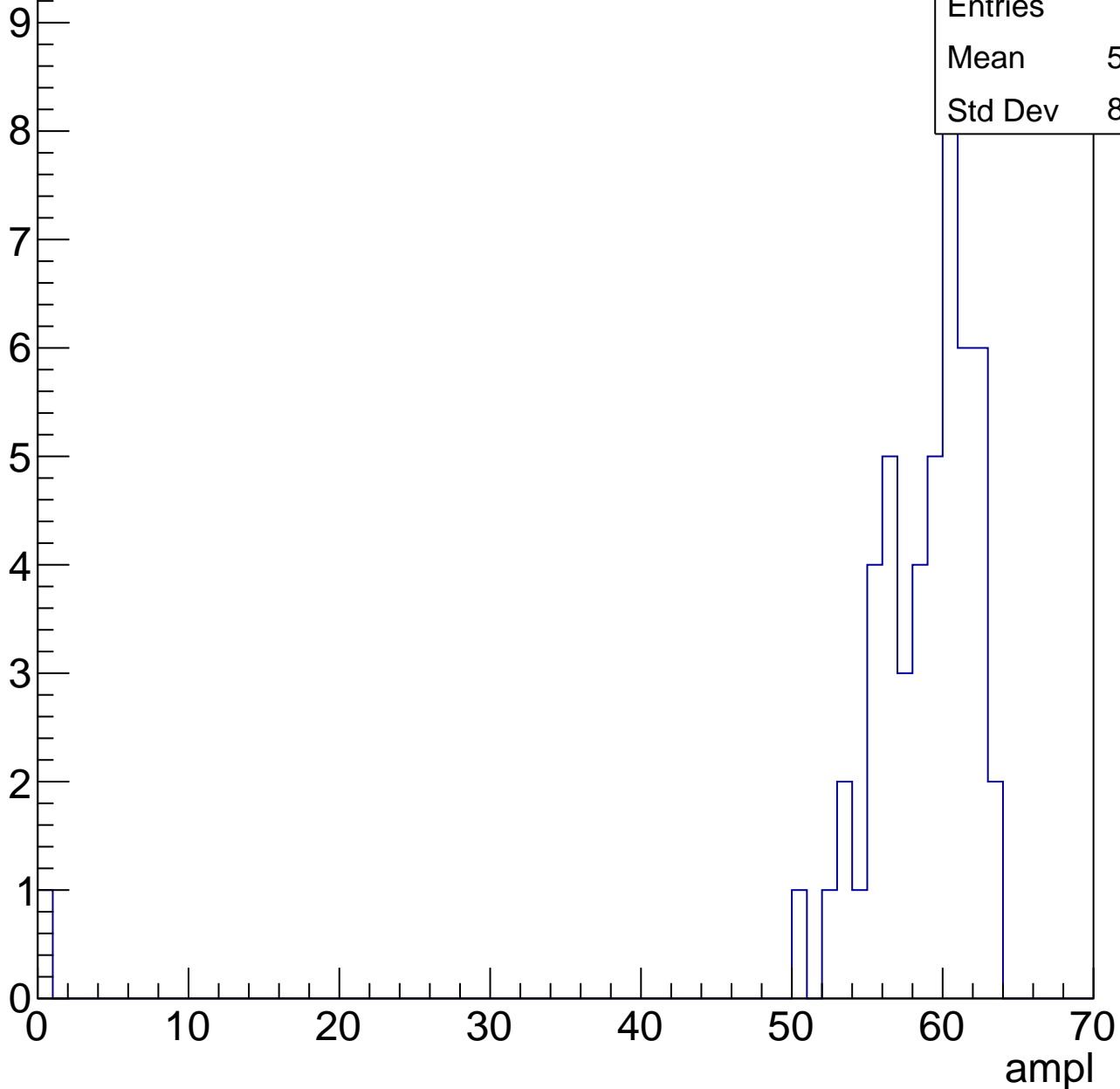


B0L001S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.28
Std Dev	8.722

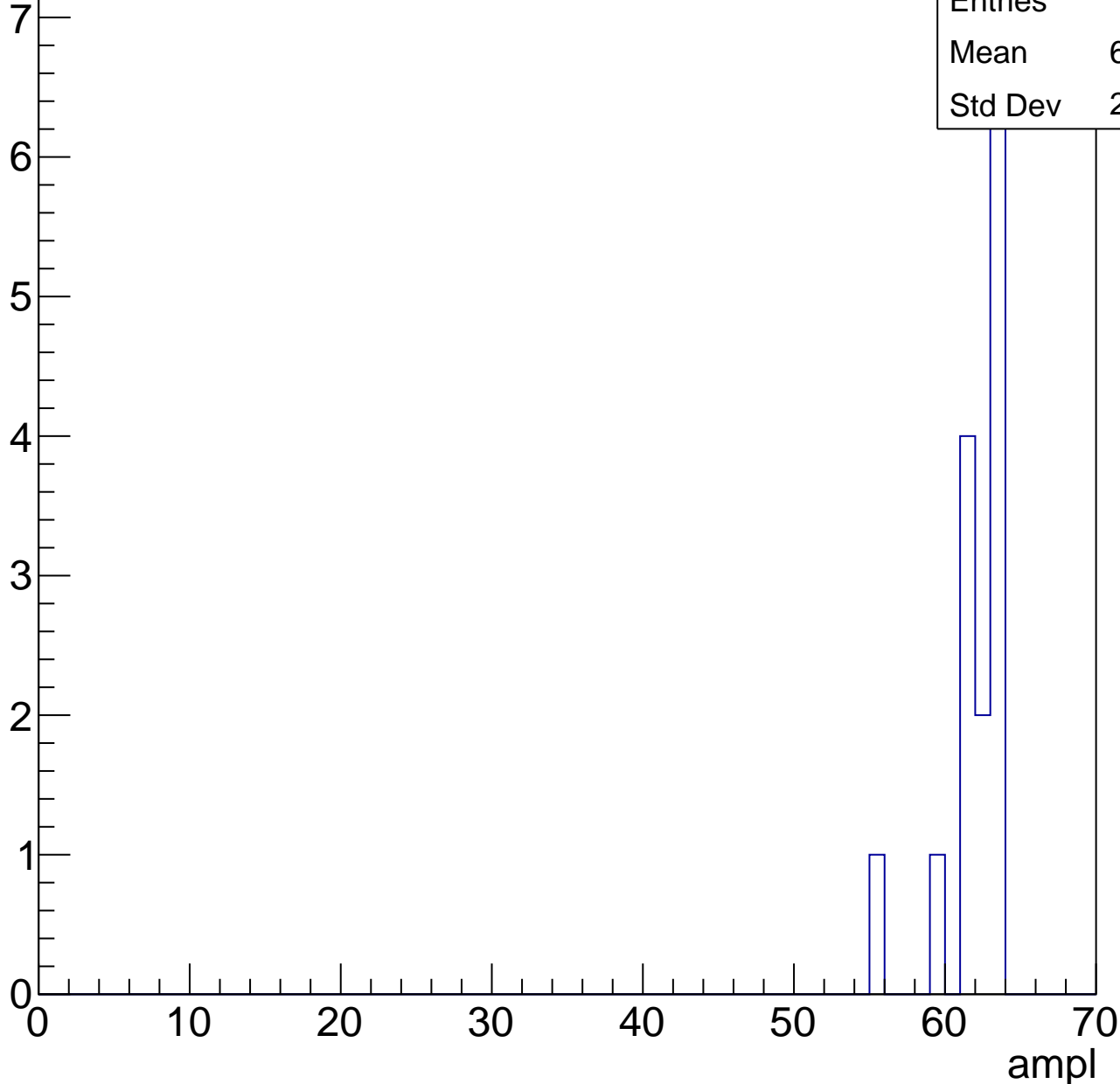


B0L001S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.53
Std Dev	2.093



B0L001S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



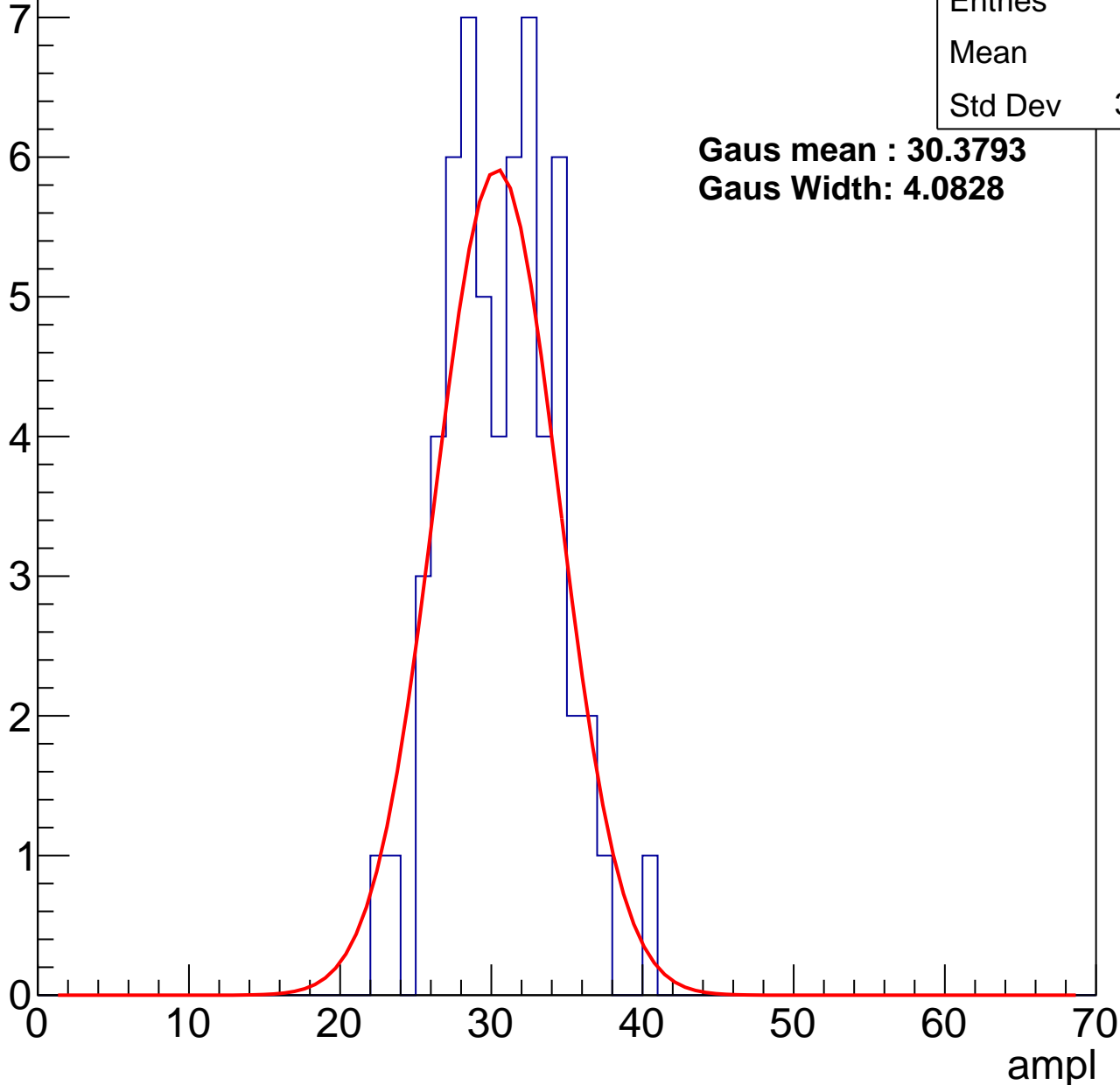
B0L001S, U15-ch101, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.2
Std Dev	3.591

Gaus mean : 30.3793
Gaus Width: 4.0828



B0L001S, U15-ch101, adc1

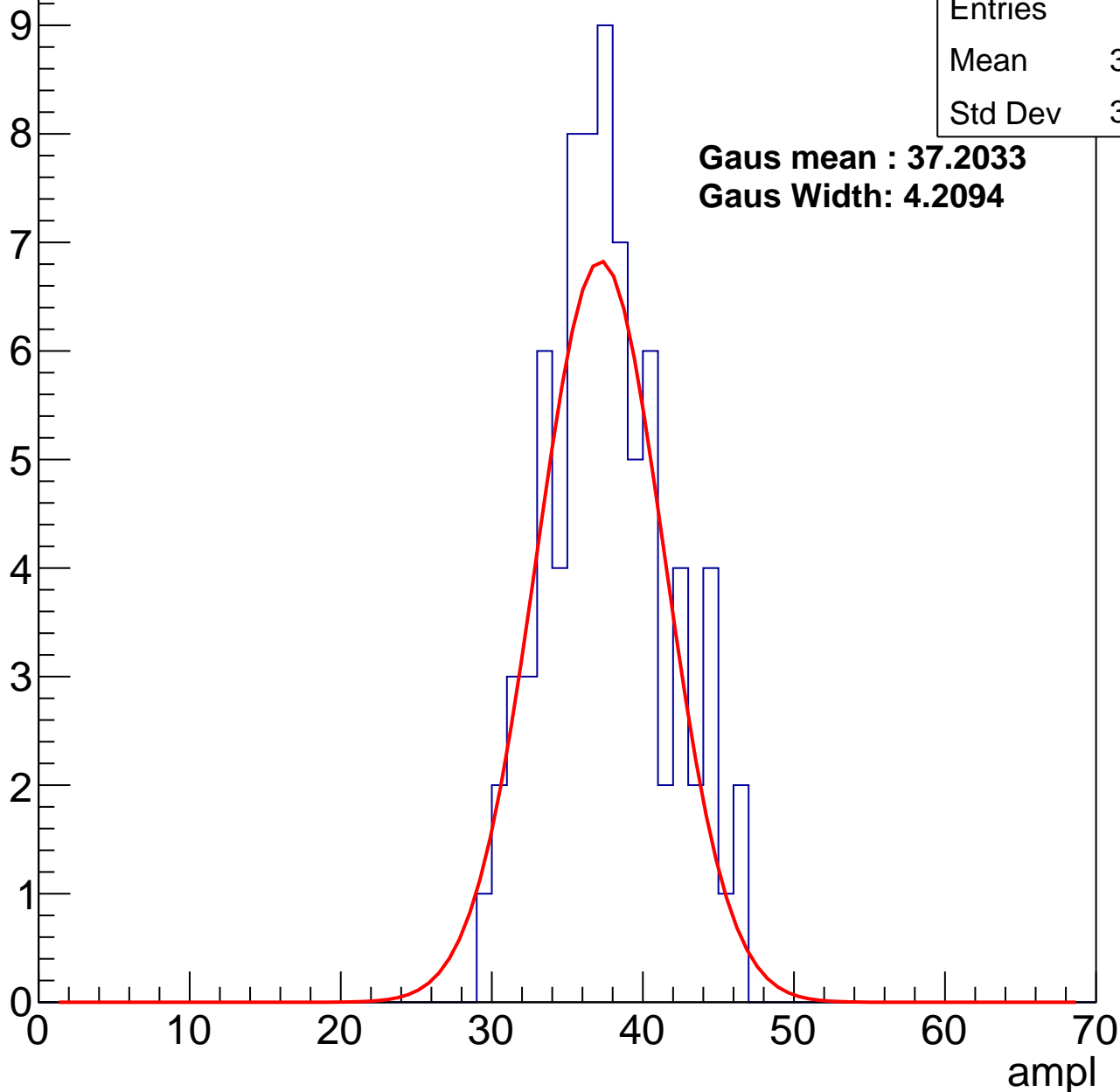
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.18
Std Dev	3.983

Gaus mean : 37.2033

Gaus Width: 4.2094



B0L001S, U15-ch101, adc2

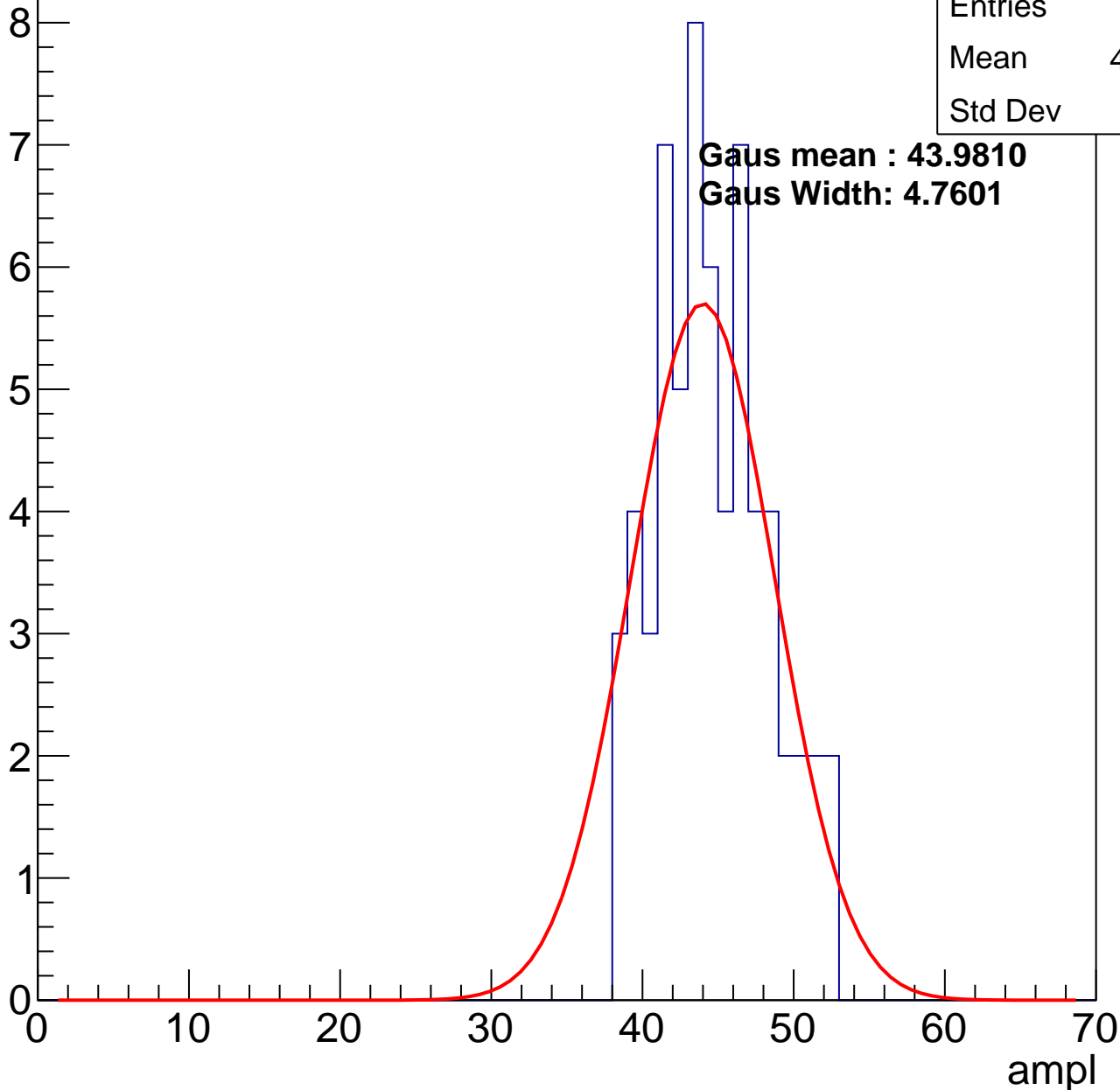
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.14
Std Dev	3.62

Gaus mean : 43.9810

Gaus Width: 4.7601

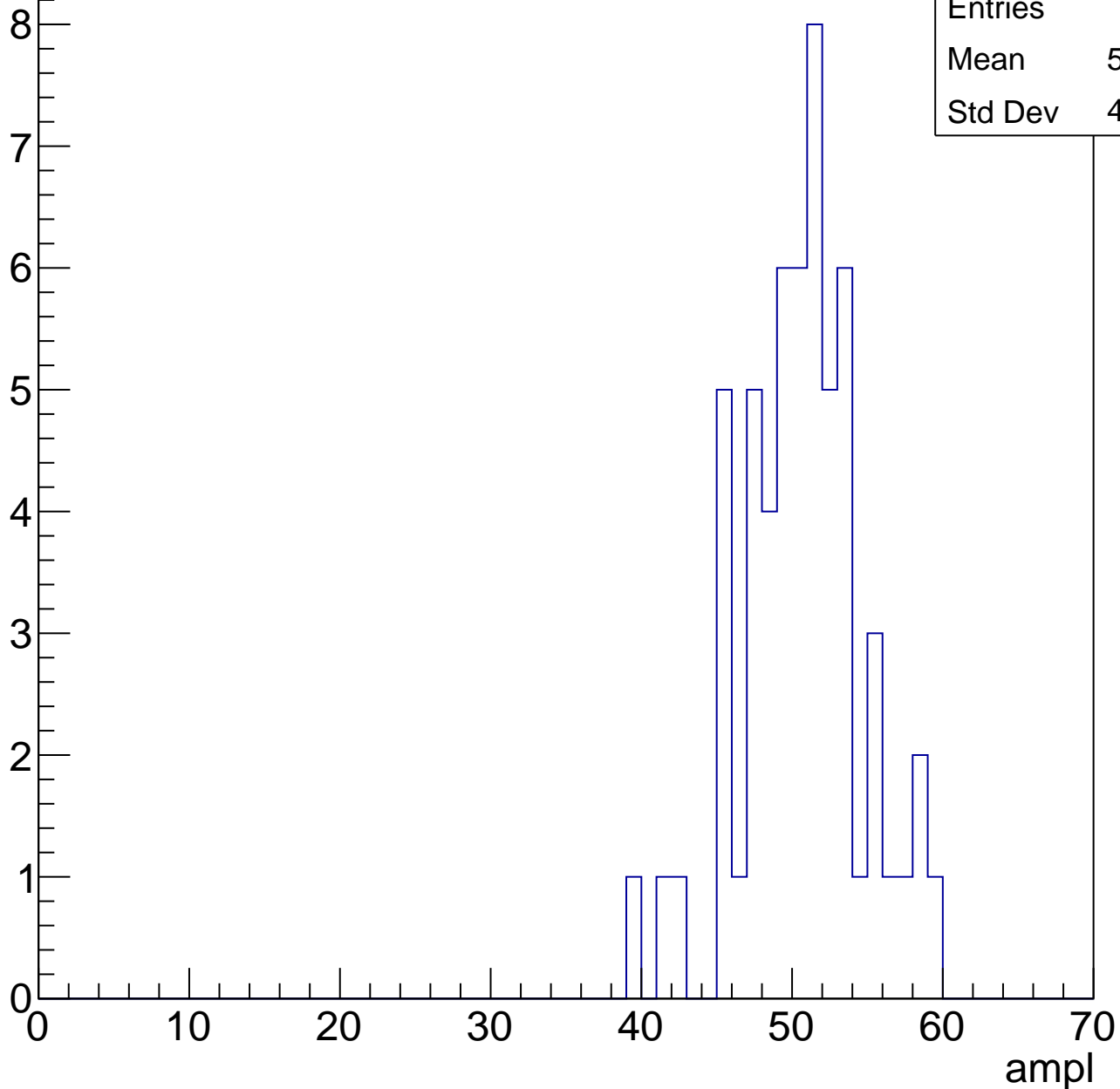


B0L001S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.12
Std Dev	4.026

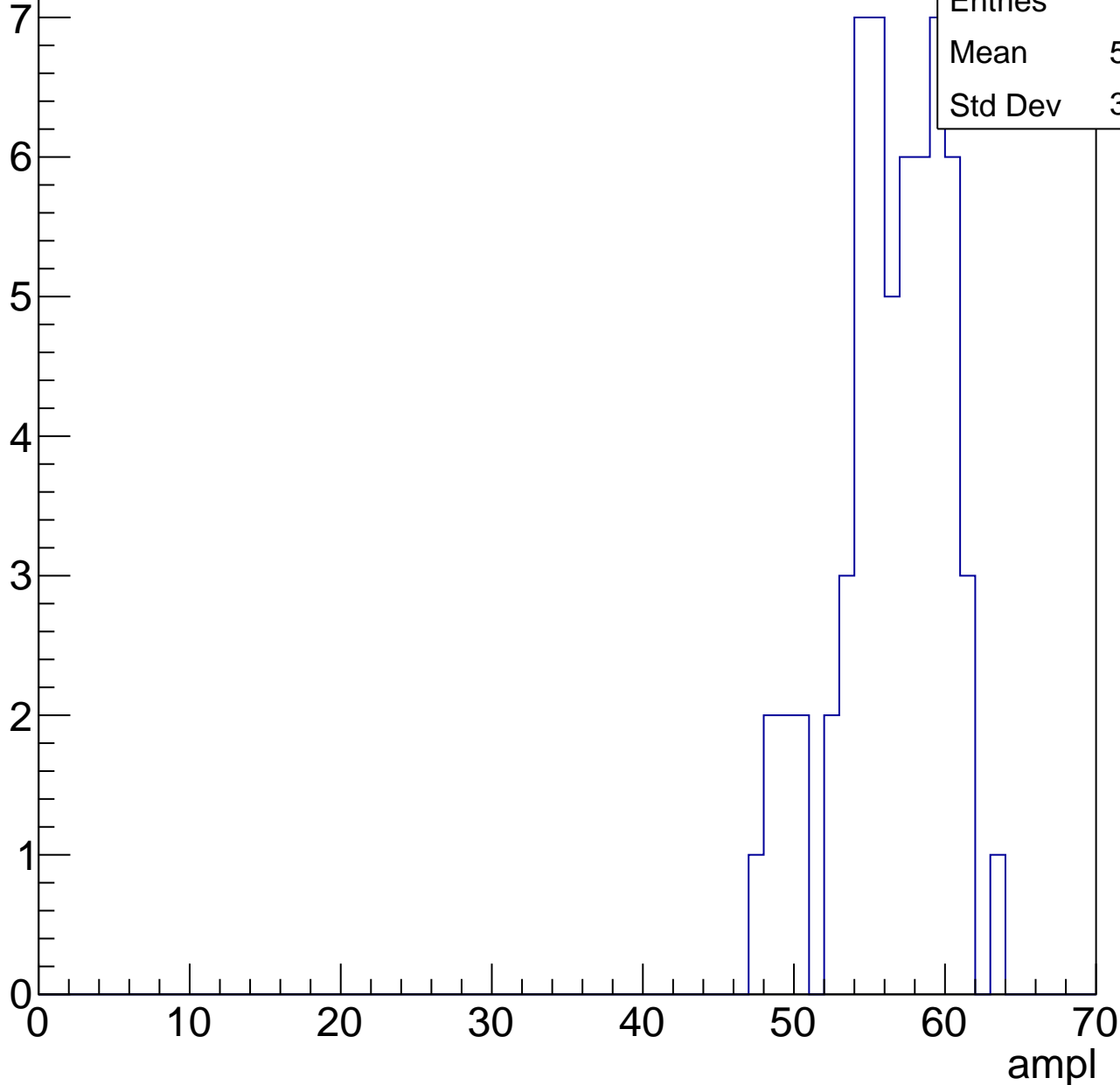


B0L001S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.93
Std Dev	3.619

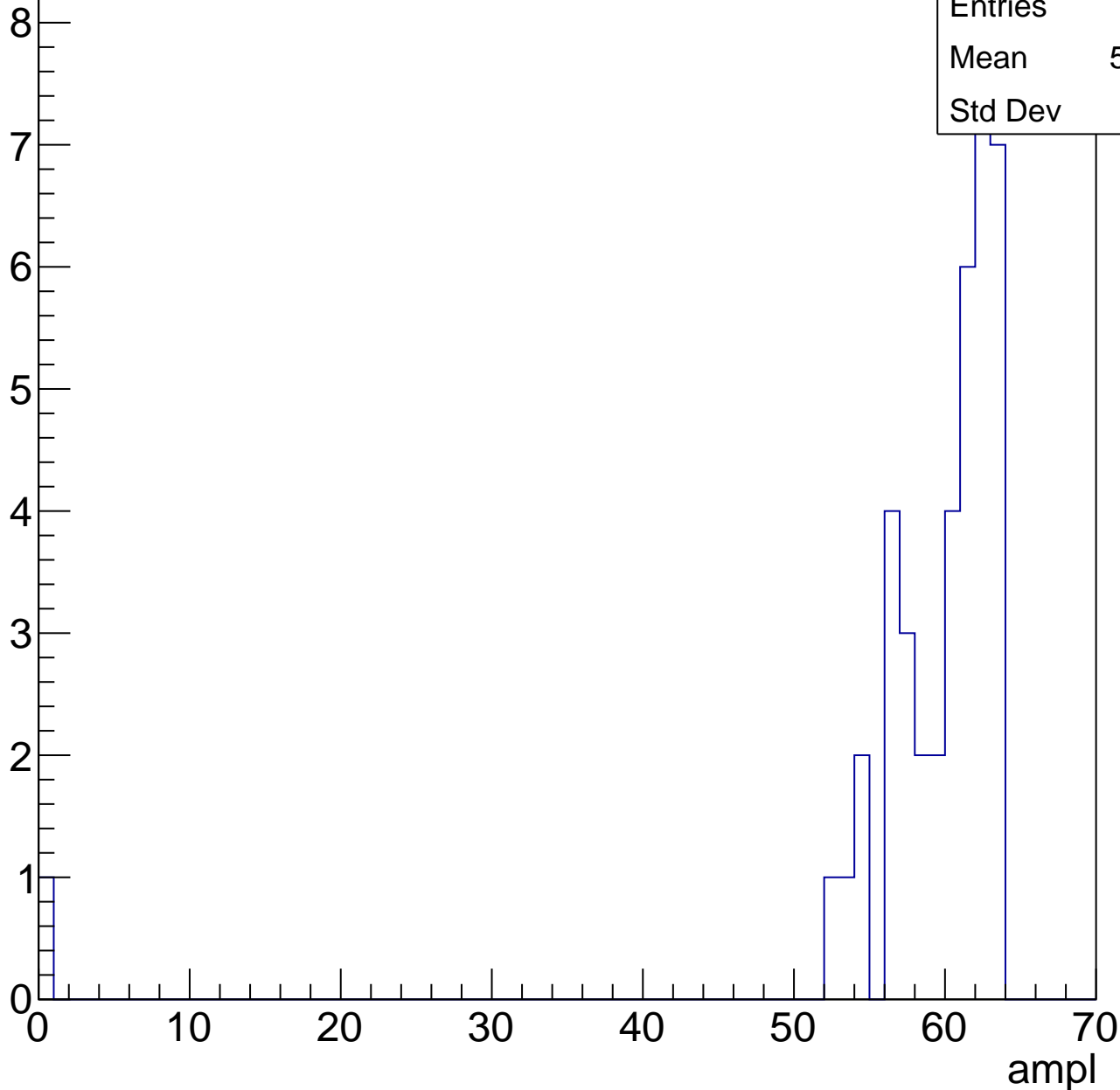


B0L001S, U15-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.17
Std Dev	9.69



B0L001S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch102, adc0

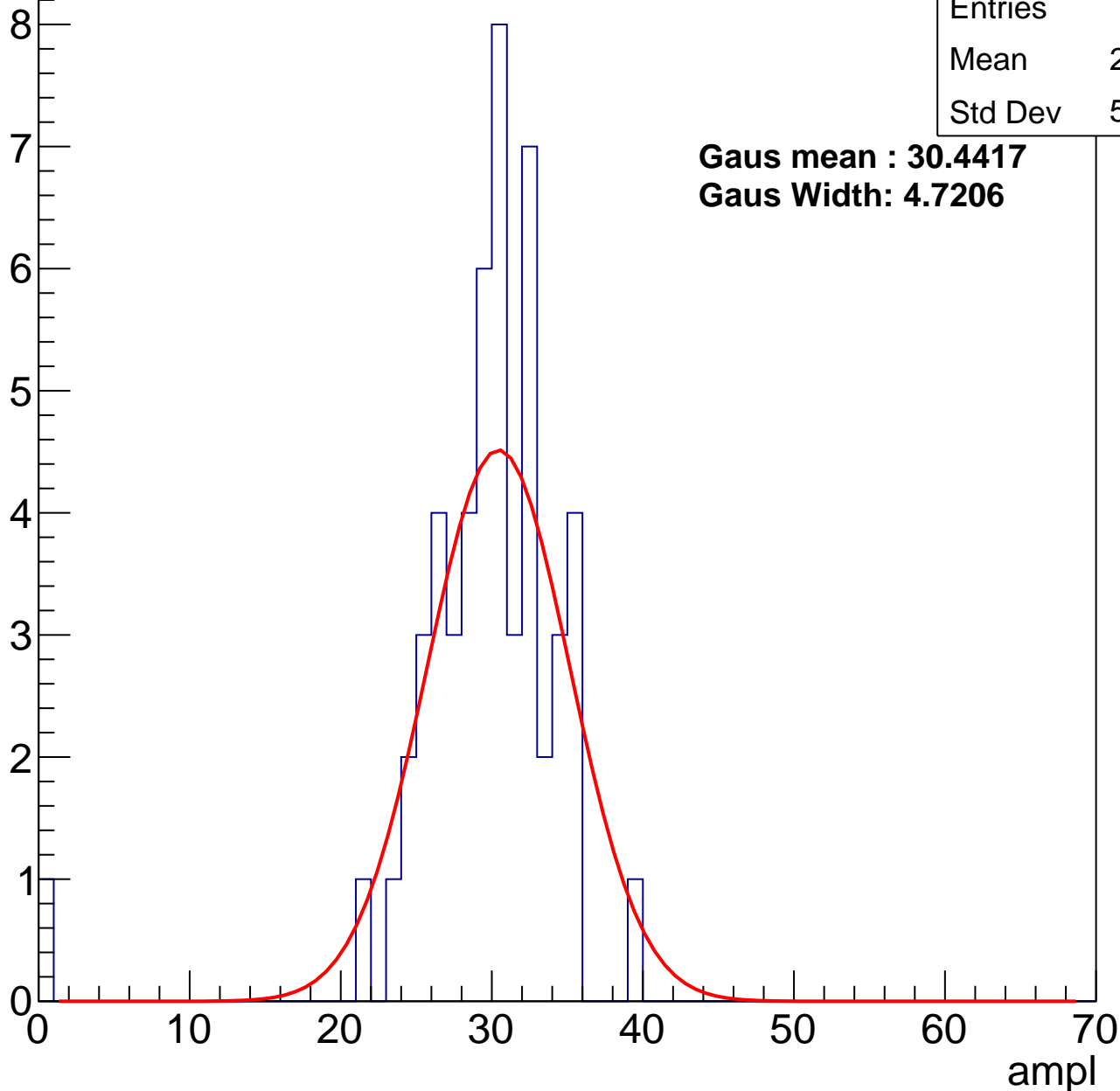
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	29.09
Std Dev	5.363

Gaus mean : 30.4417

Gaus Width: 4.7206



B0L001S, U15-ch102, adc1

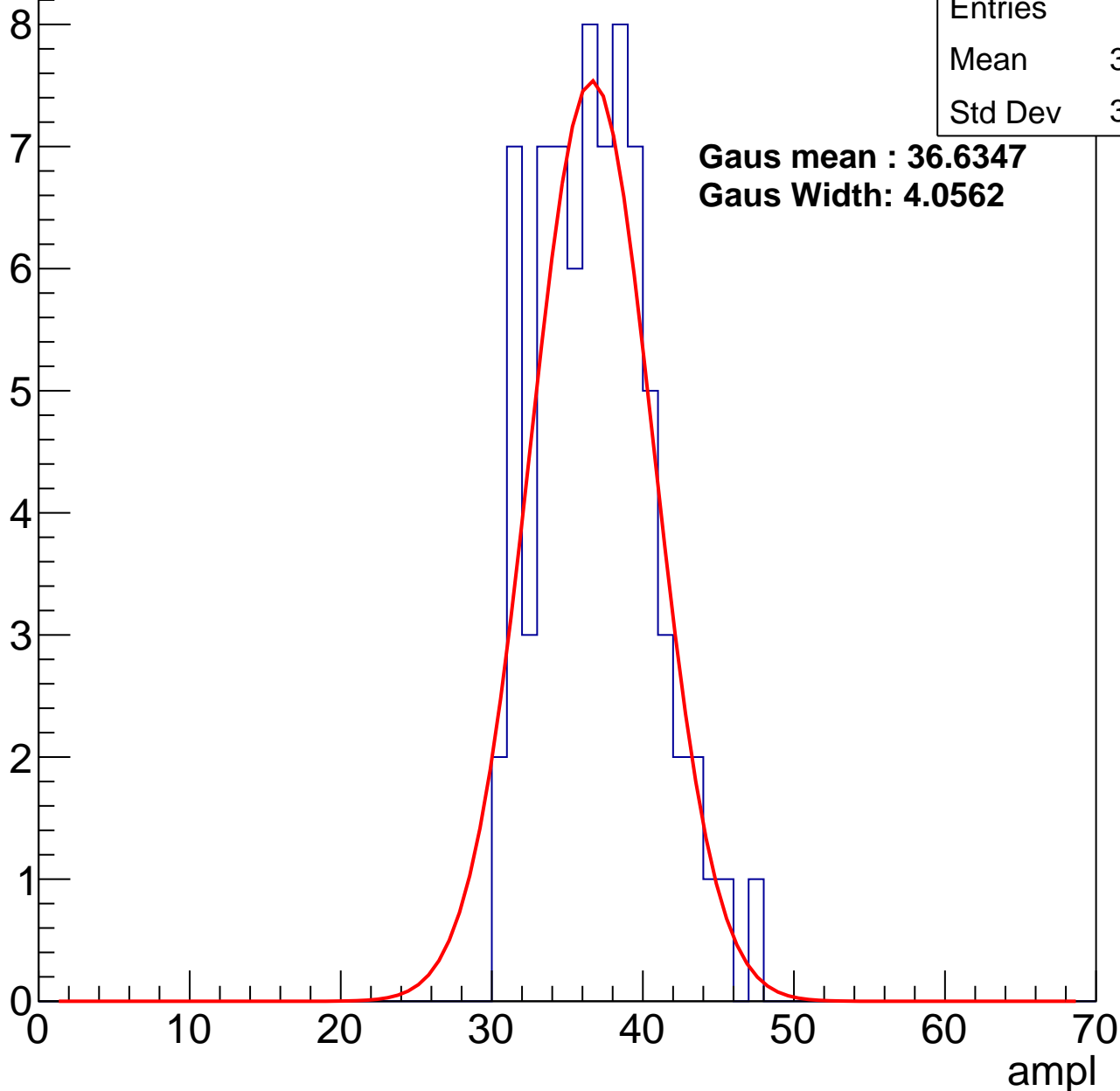
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.43
Std Dev	3.729

Gaus mean : 36.6347

Gaus Width: 4.0562



B0L001S, U15-ch102, adc2

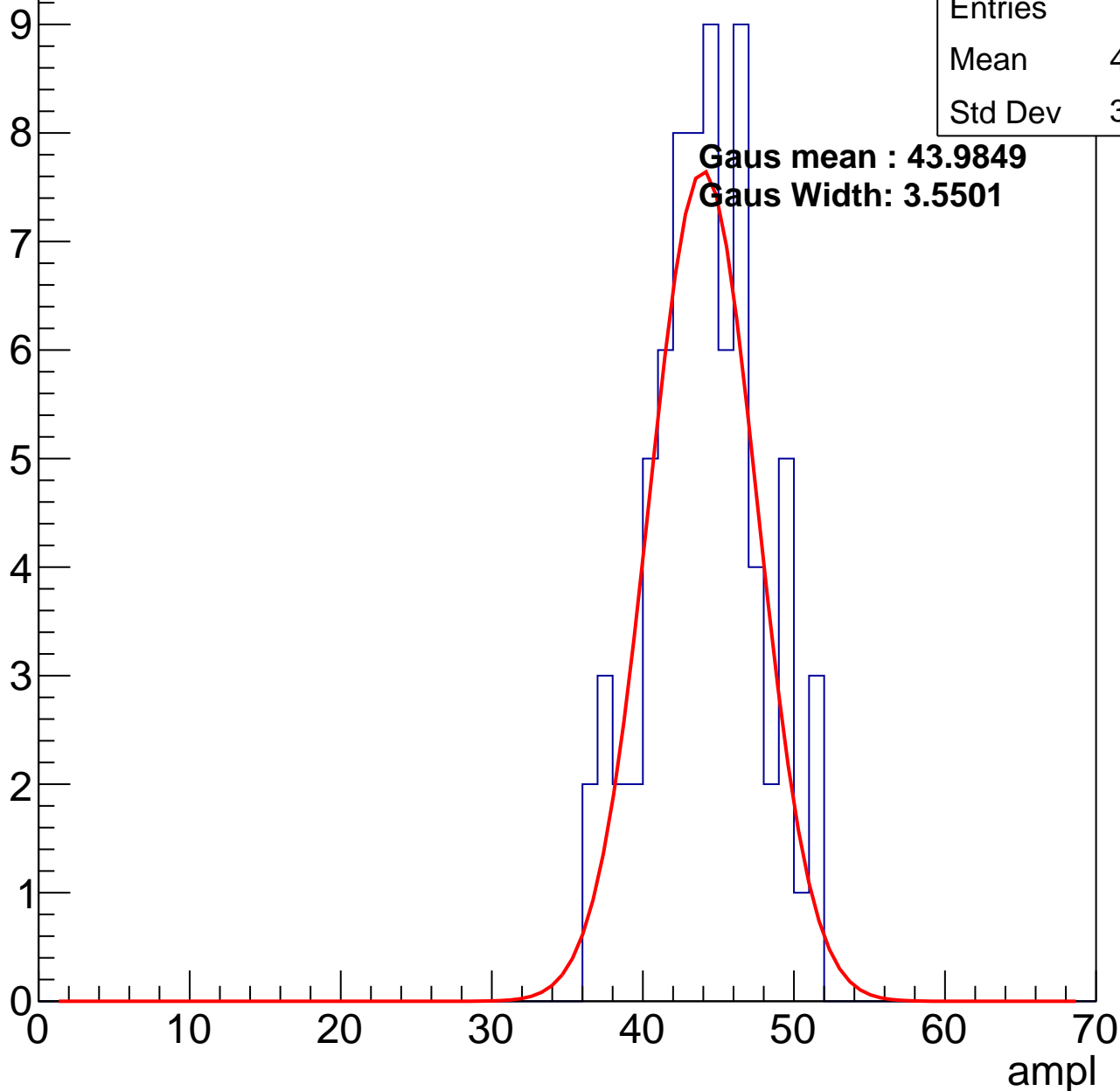
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.67
Std Dev	3.616

Gaus mean : 43.9849

Gaus Width: 3.5501

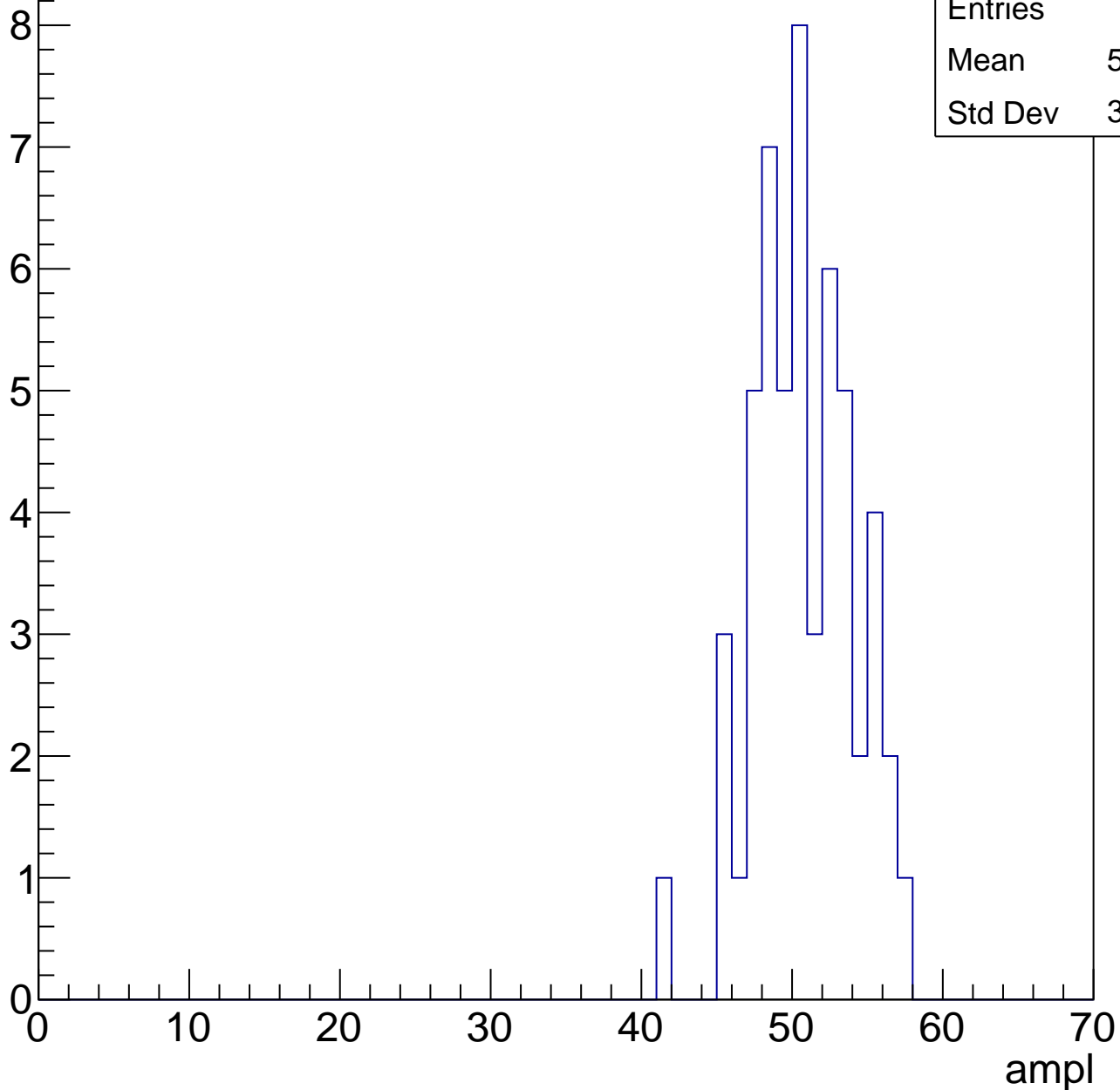


B0L001S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.28
Std Dev	3.282

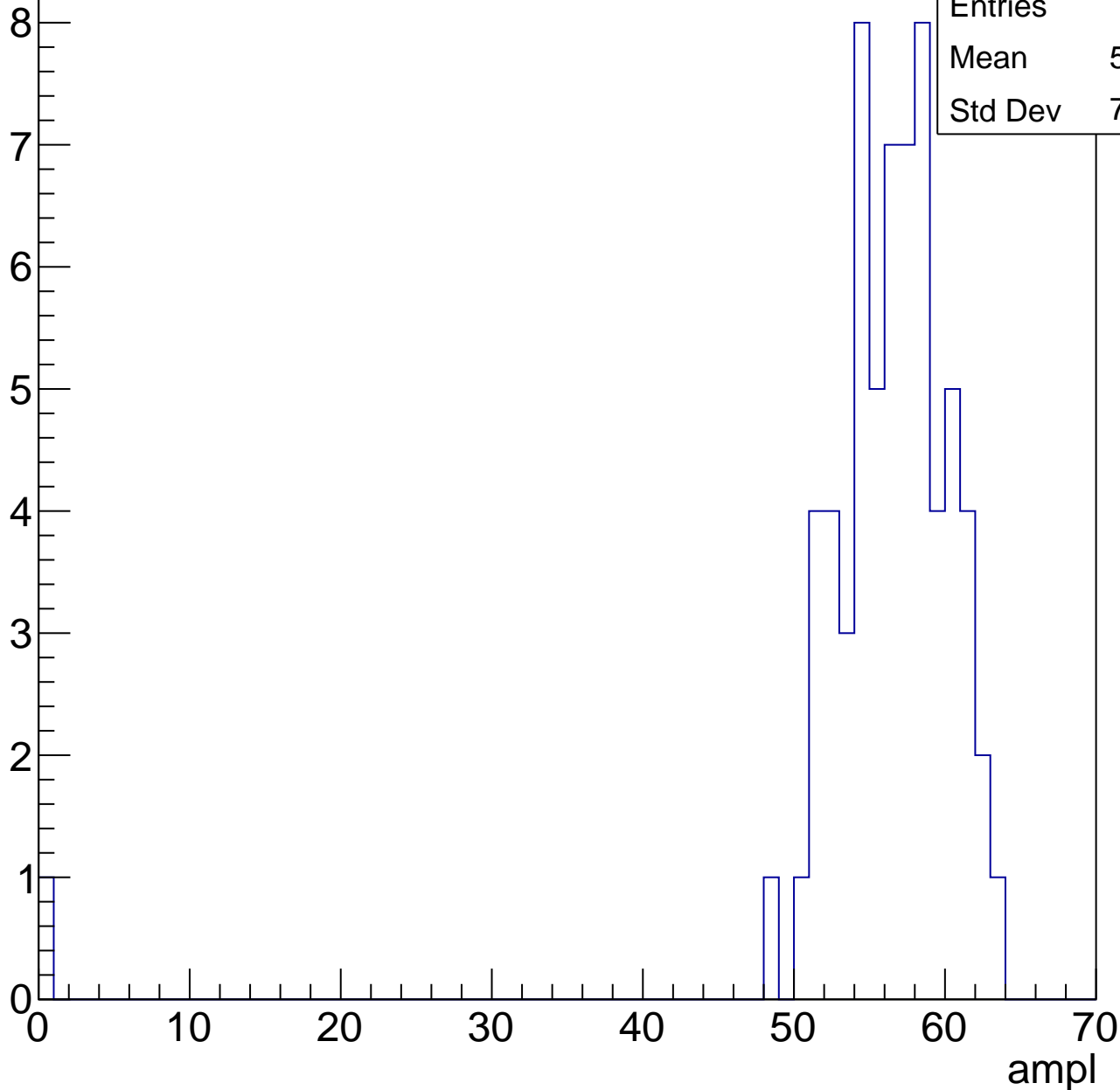


B0L001S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

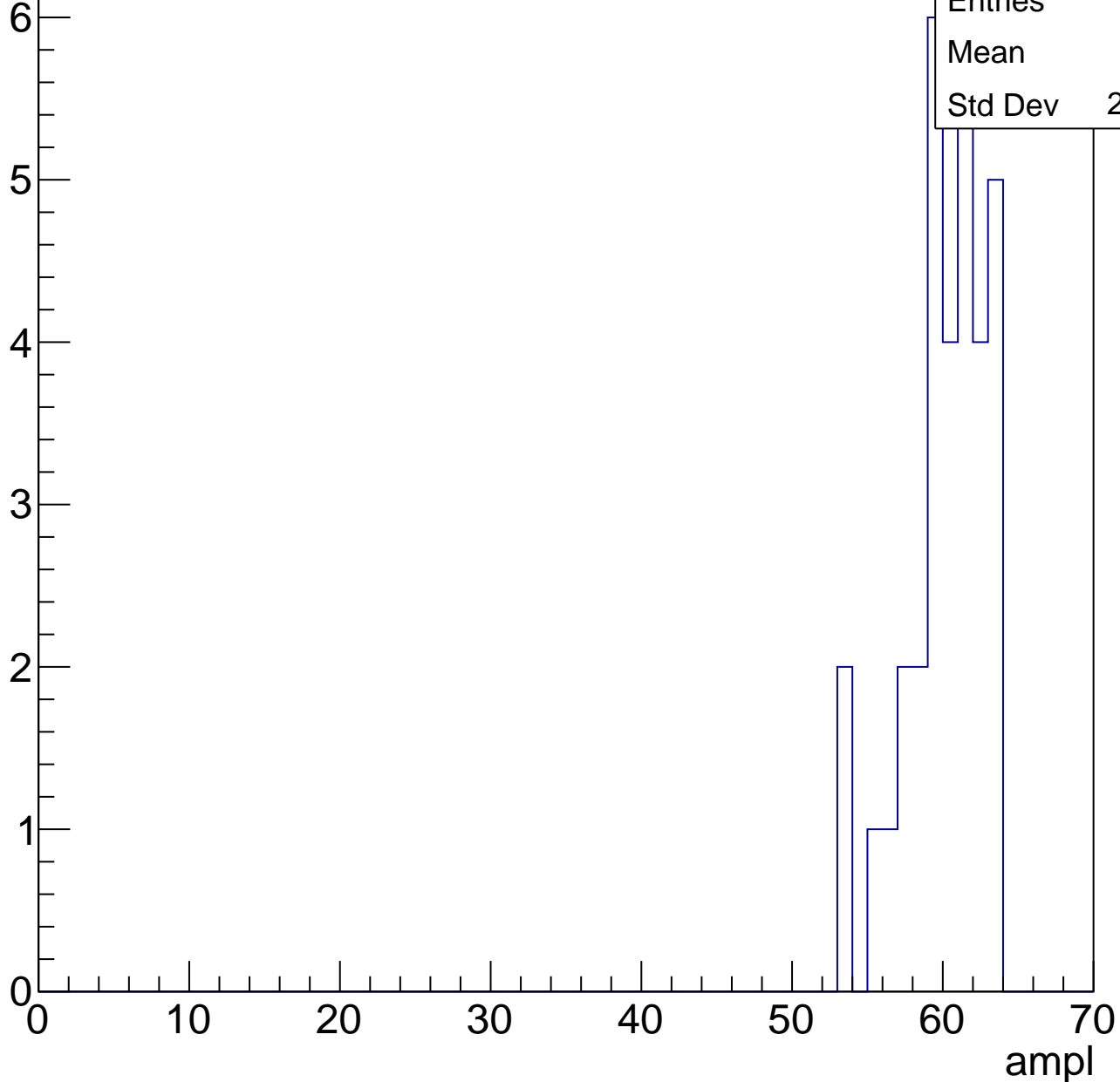
Entries	65
Mean	55.35
Std Dev	7.659



B0L001S, U15-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

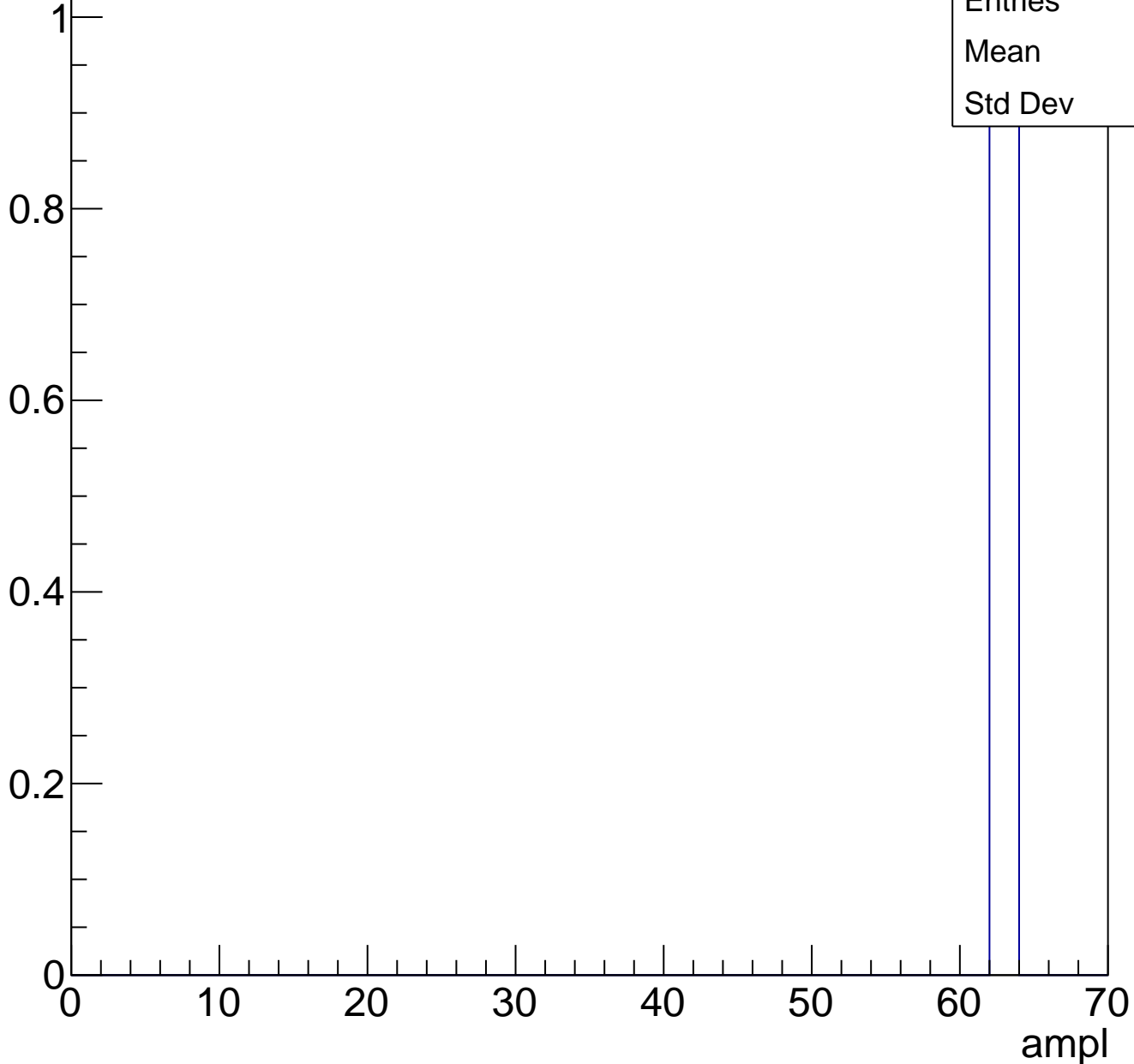
Entry



B0L001S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch103, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	29.47
Std Dev	3.42

Gaus mean : 30.0491

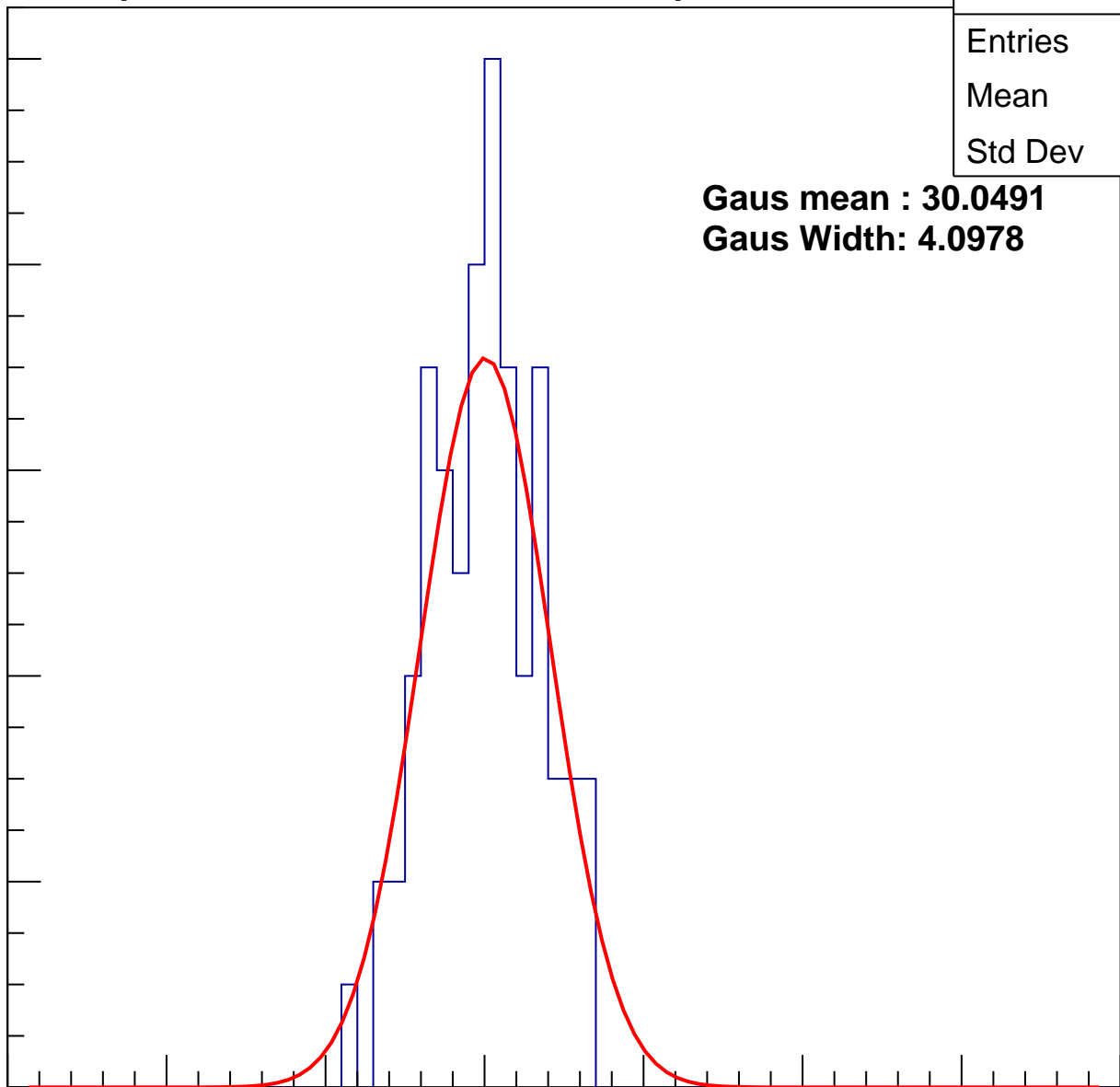
Gaus Width: 4.0978

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch103, adc1

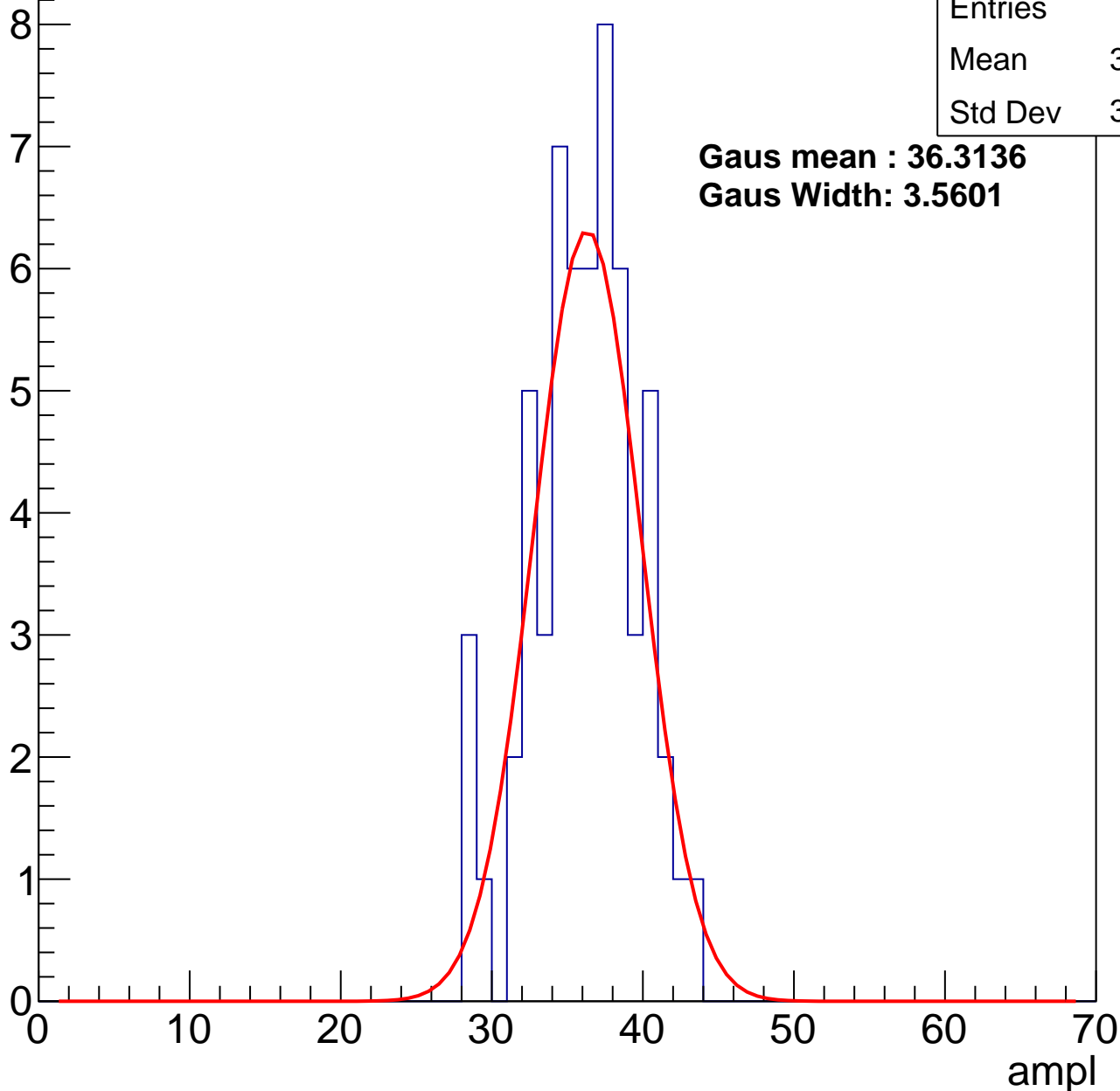
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	35.69
Std Dev	3.446

Gaus mean : 36.3136

Gaus Width: 3.5601



B0L001S, U15-ch103, adc2

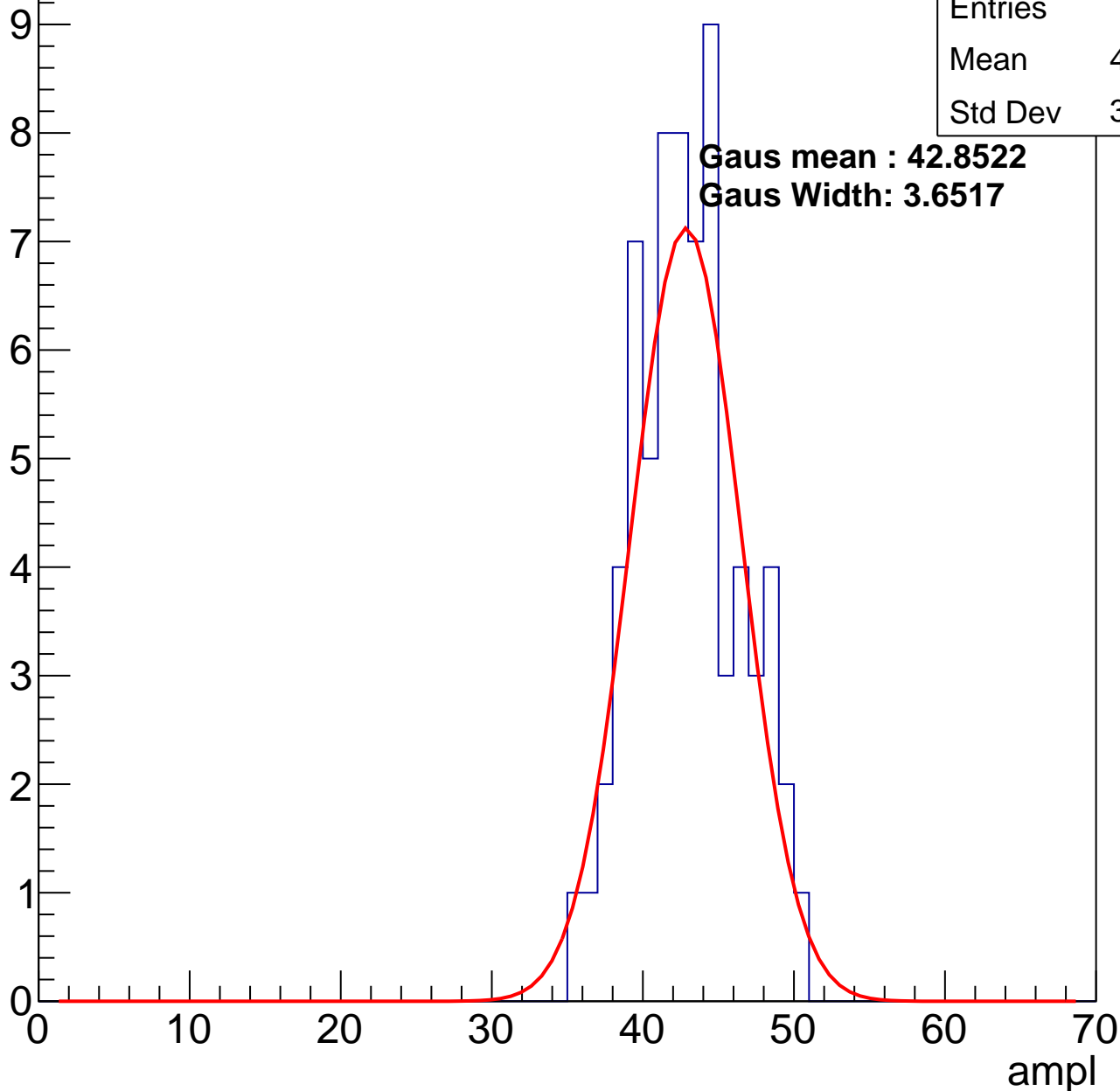
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.48
Std Dev	3.382

Gaus mean : 42.8522

Gaus Width: 3.6517

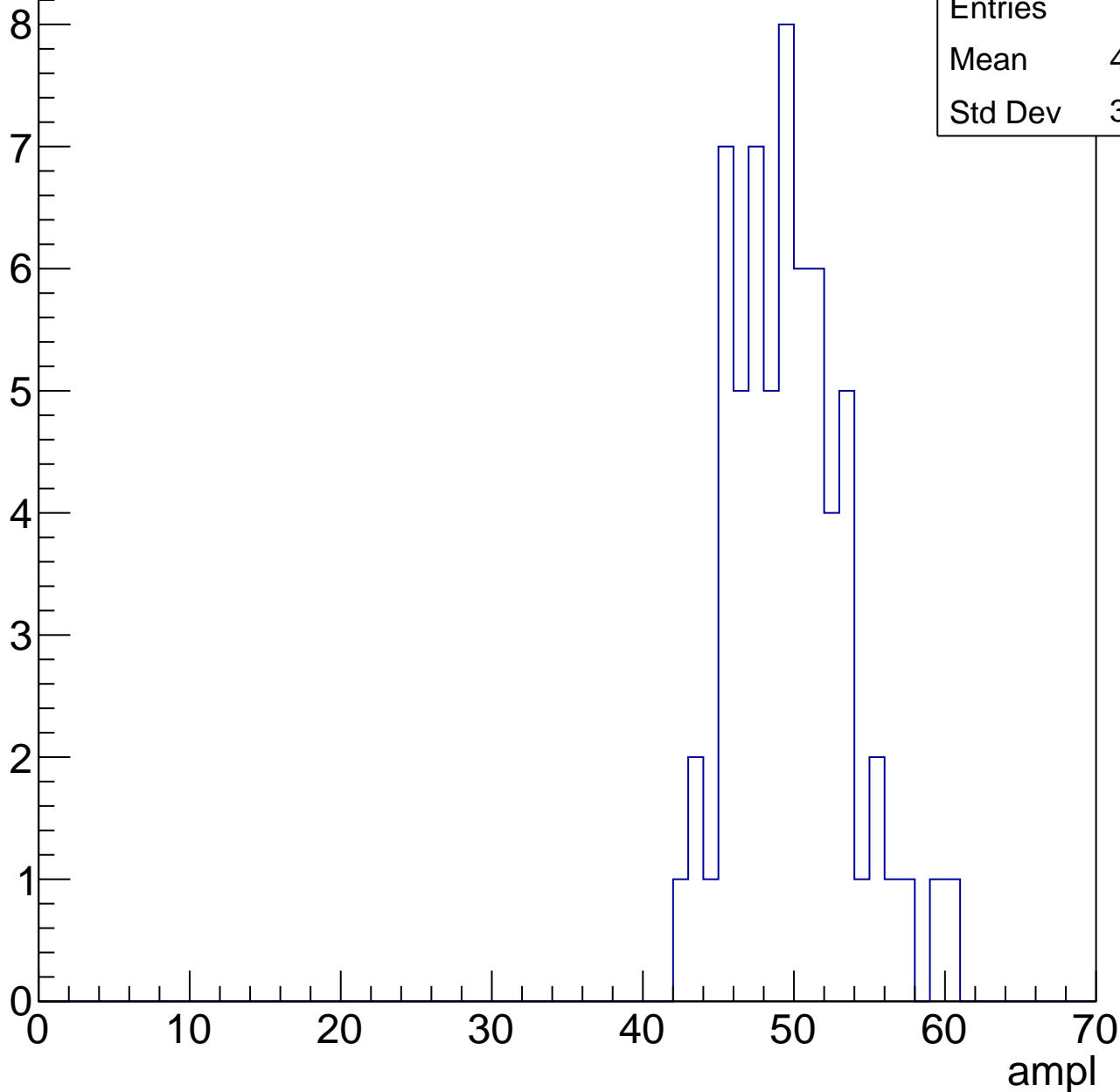


B0L001S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.27
Std Dev	3.768

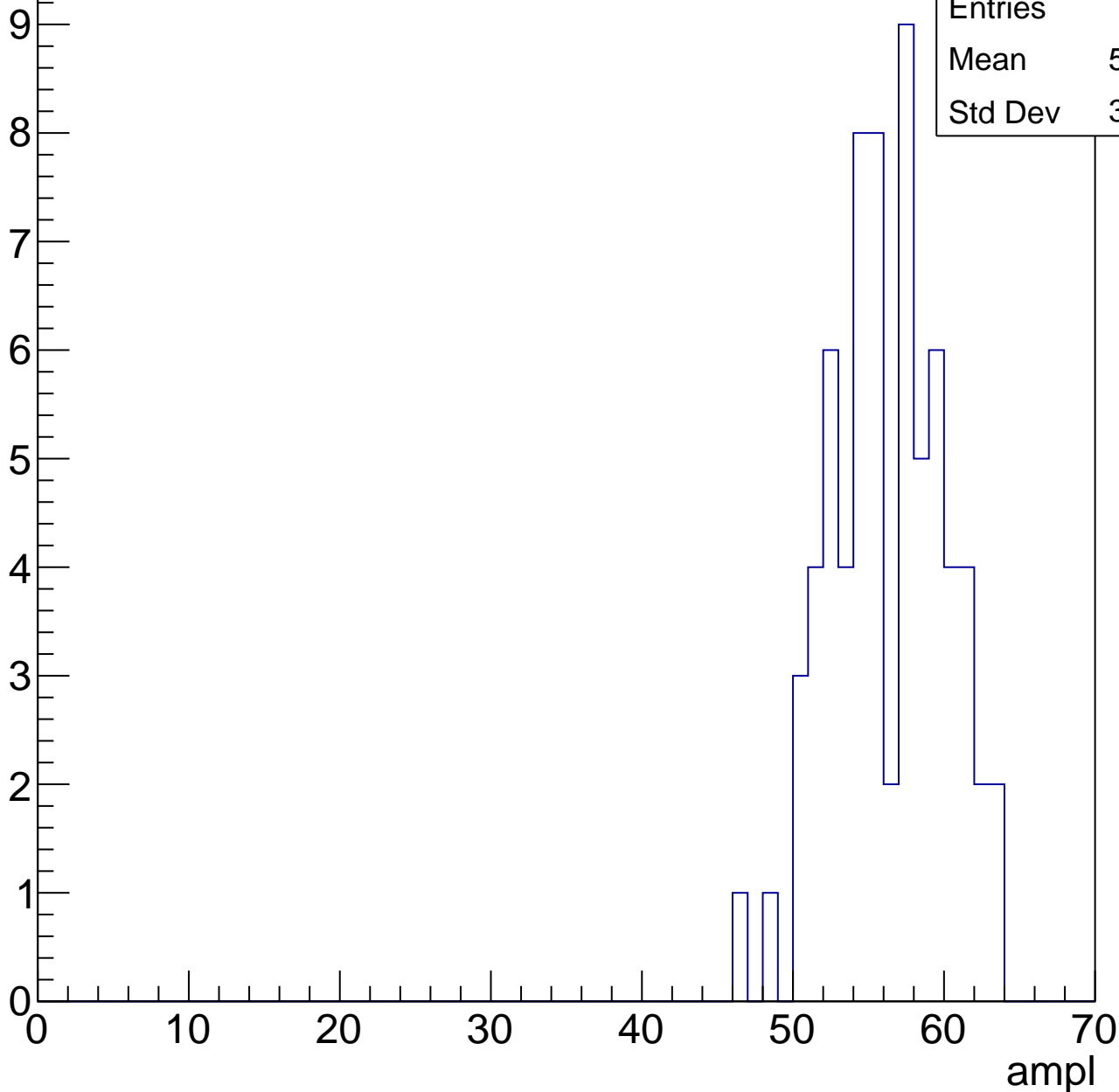


B0L001S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	55.75
Std Dev	3.724

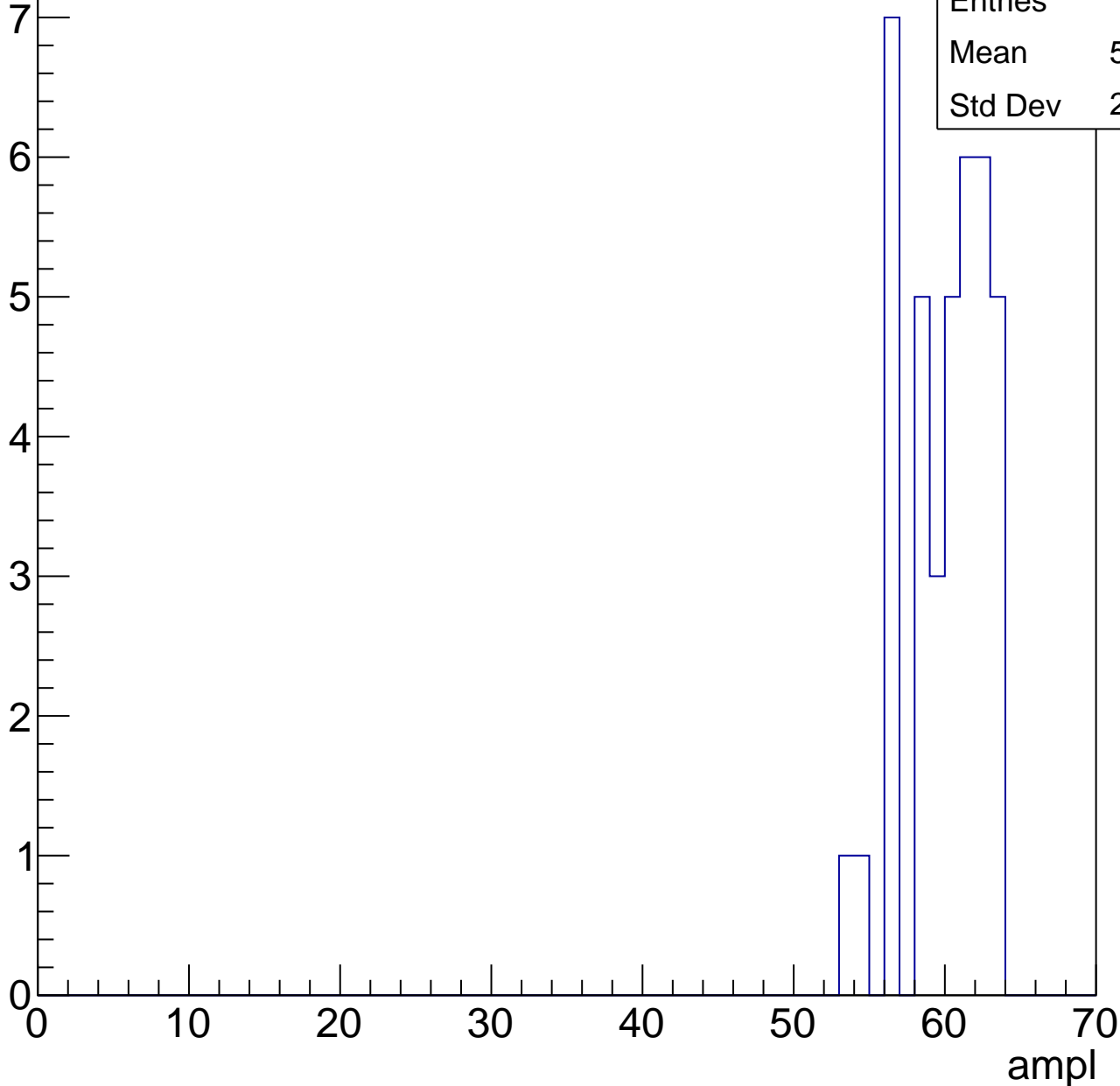


B0L001S, U15-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

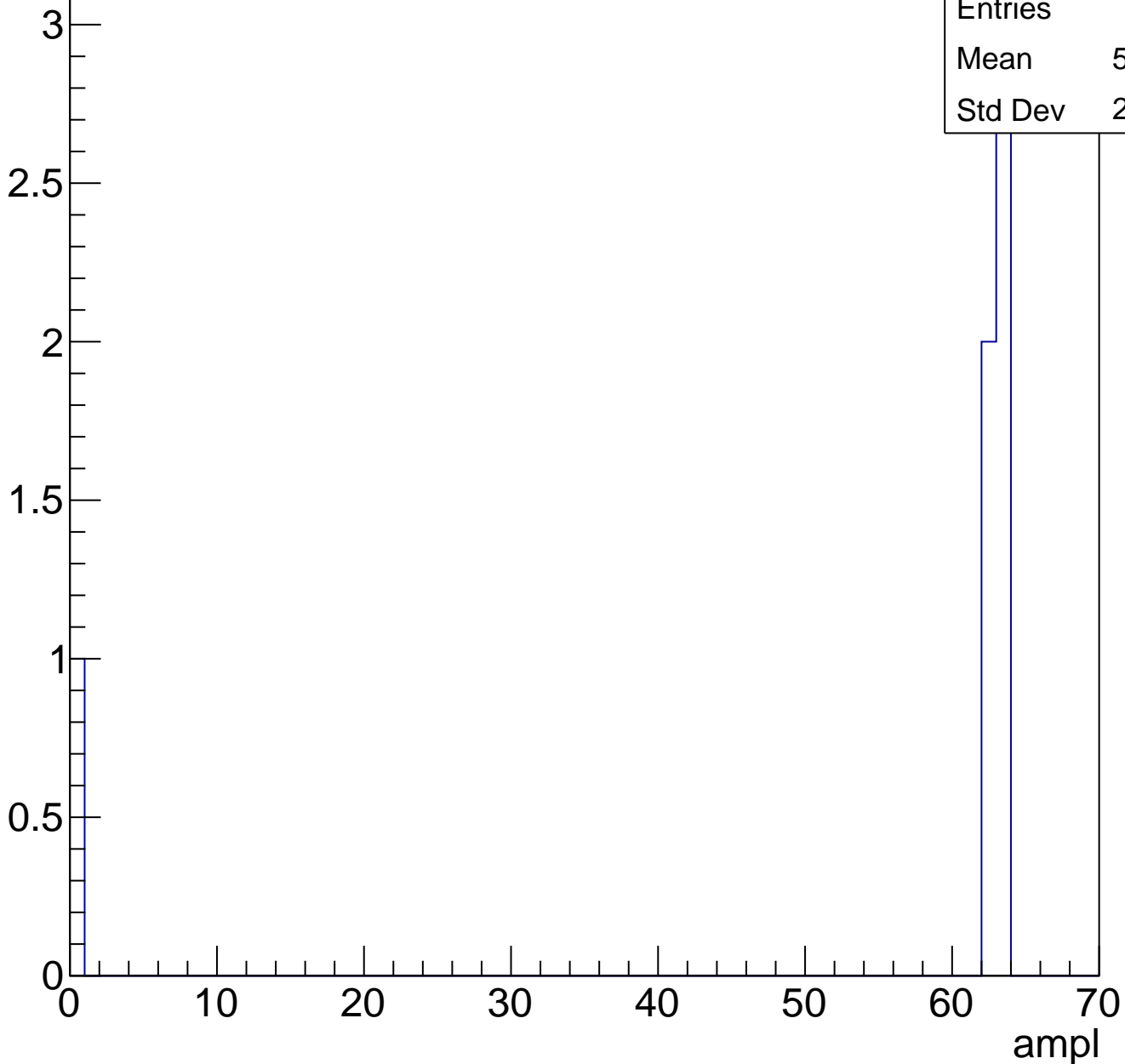
Entries	39
Mean	59.46
Std Dev	2.697



B0L001S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch104, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	27.35
Std Dev	8.117

Gaus mean : 30.4174

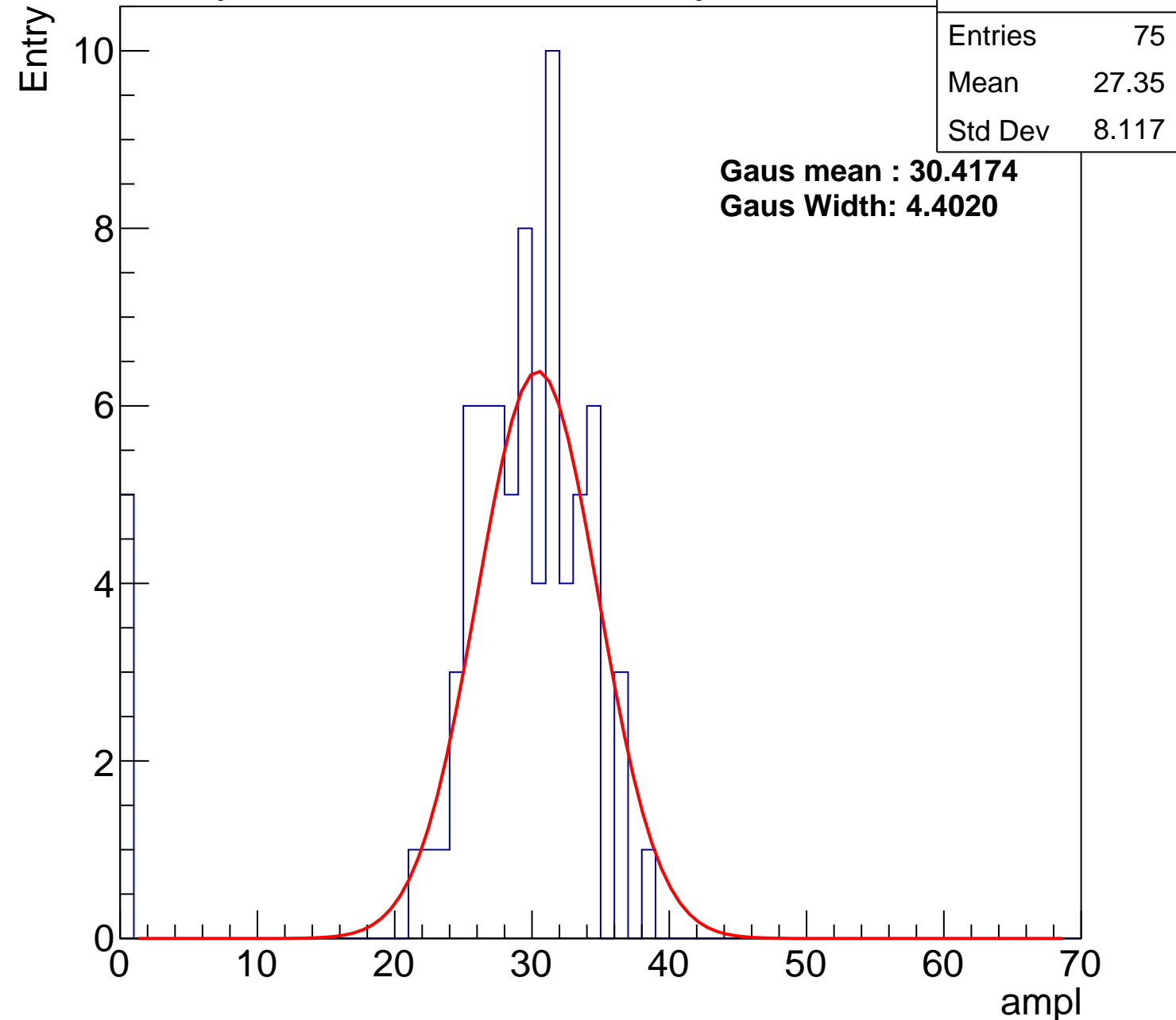
Gaus Width: 4.4020

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch104, adc1

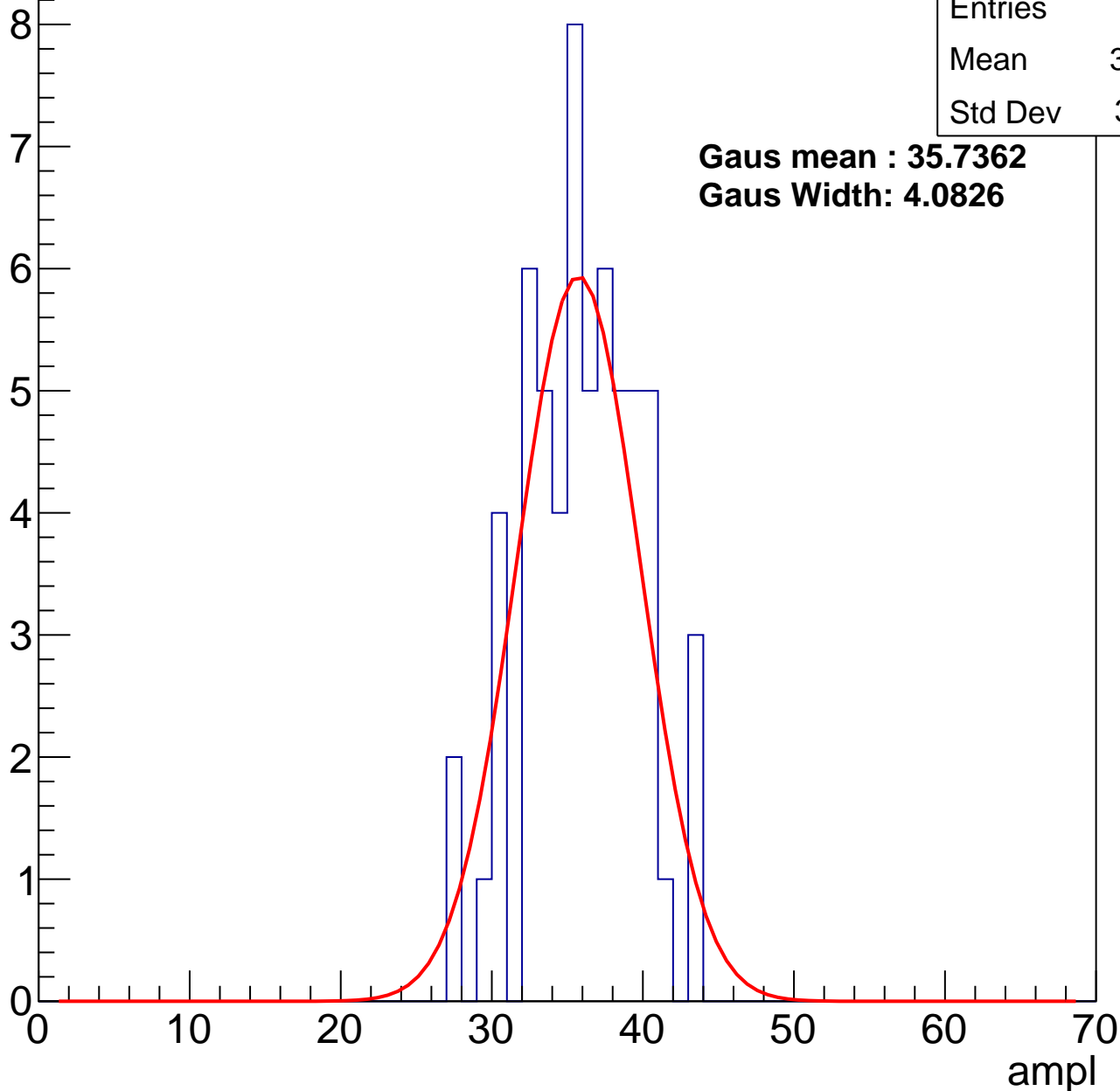
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	35.55
Std Dev	3.721

Gaus mean : 35.7362

Gaus Width: 4.0826



B0L001S, U15-ch104, adc2

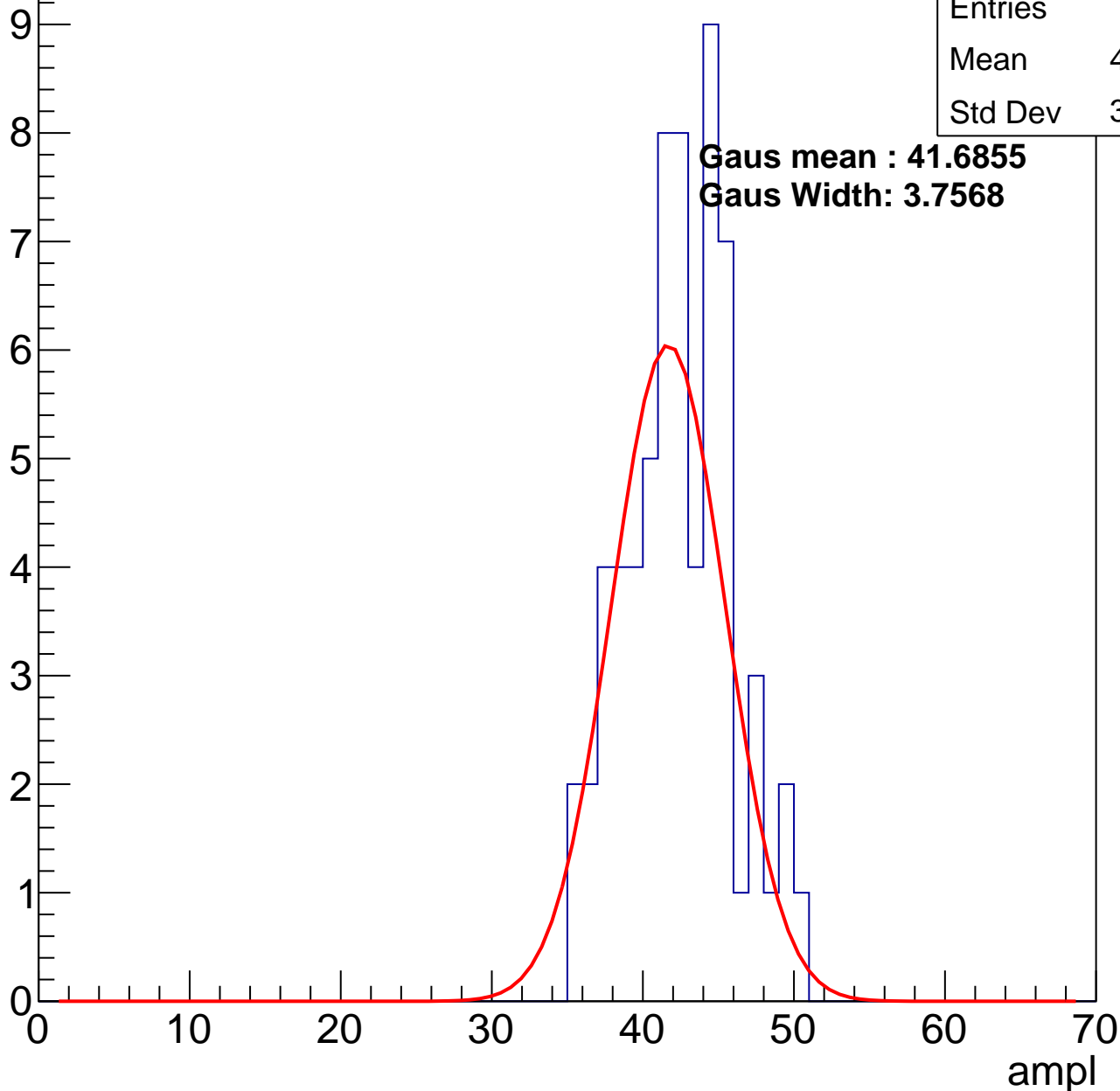
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	41.97
Std Dev	3.486

Gaus mean : 41.6855

Gaus Width: 3.7568

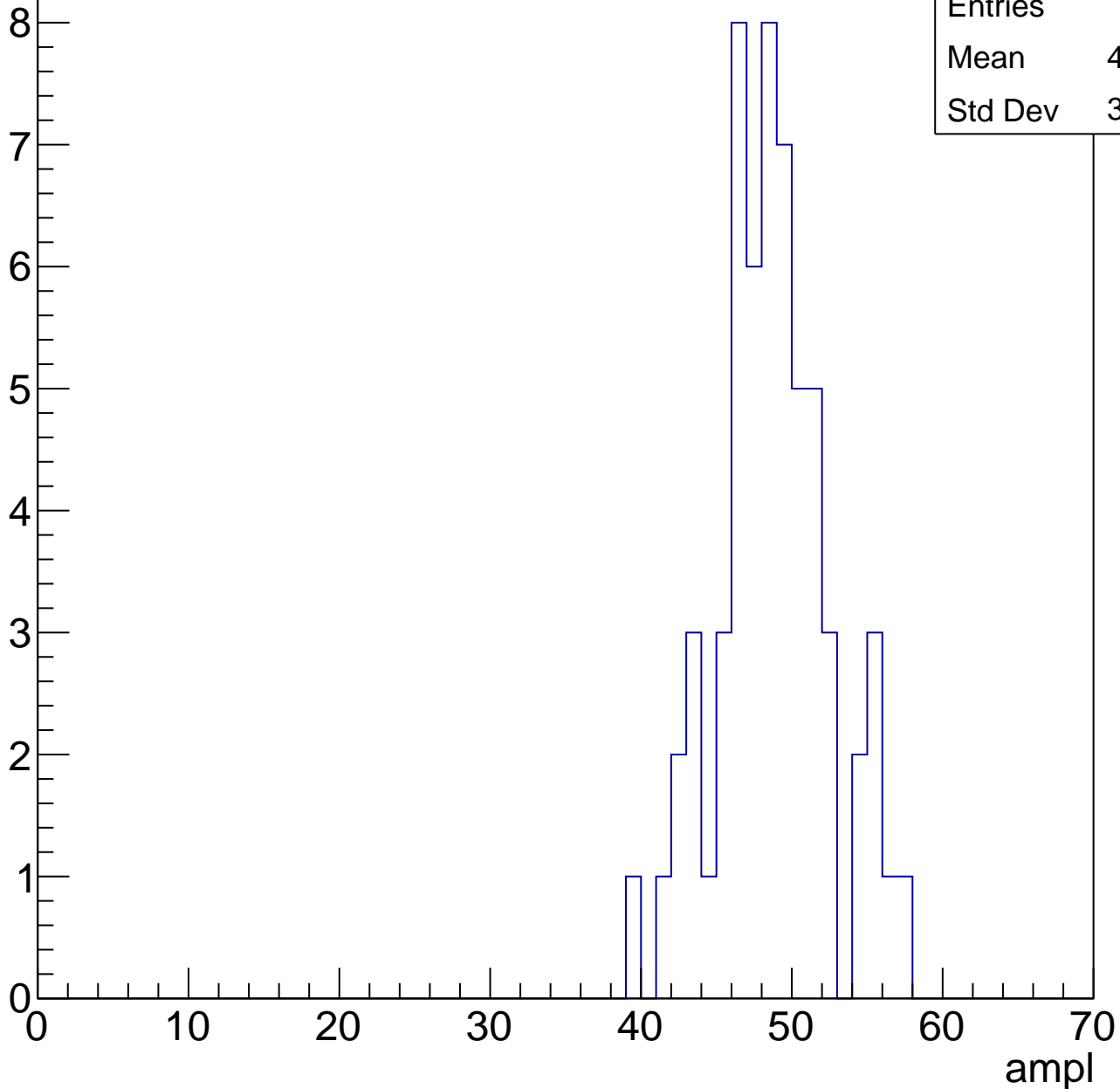


B0L001S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	48.27
Std Dev	3.737

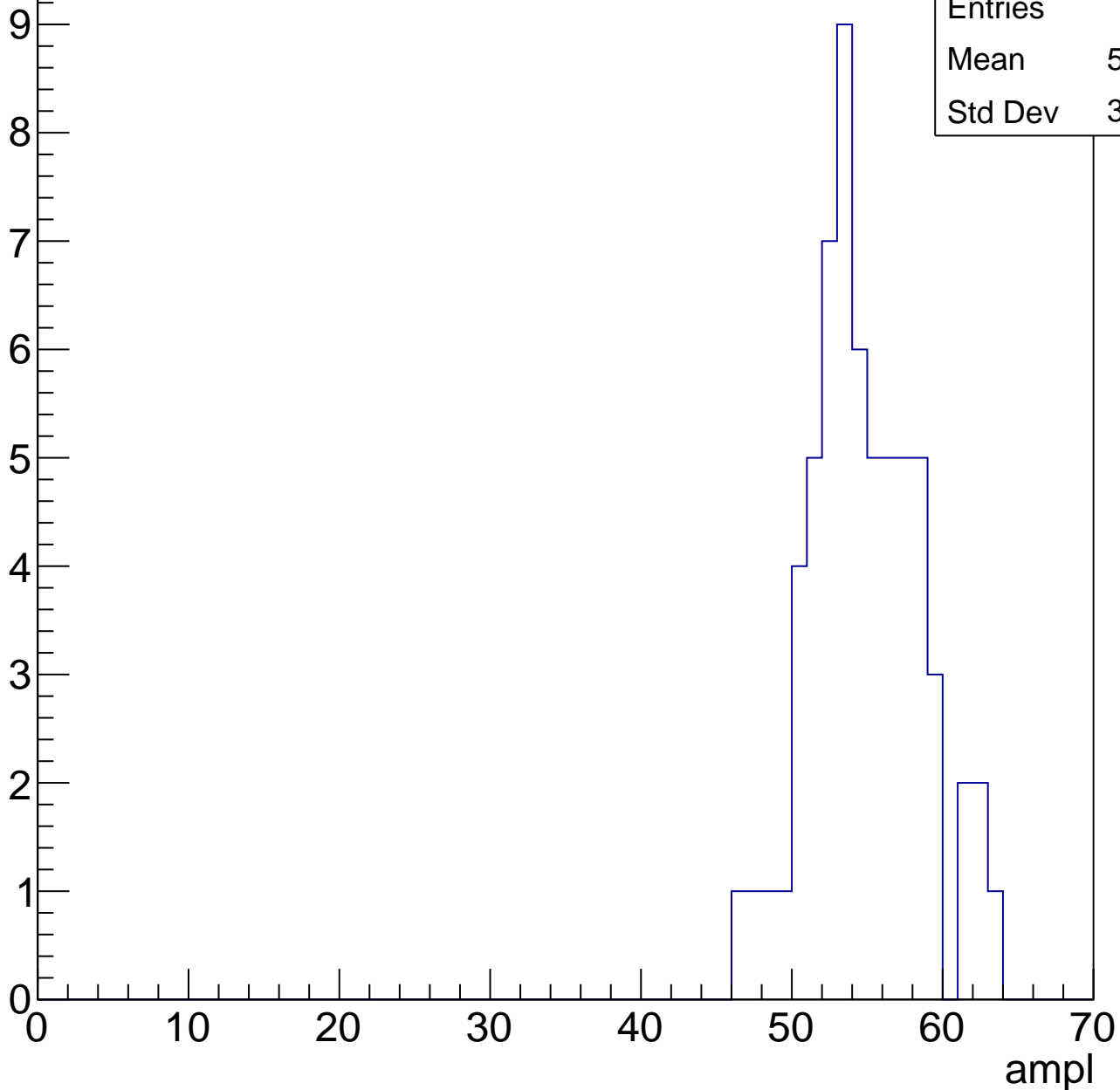


B0L001S, U15-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

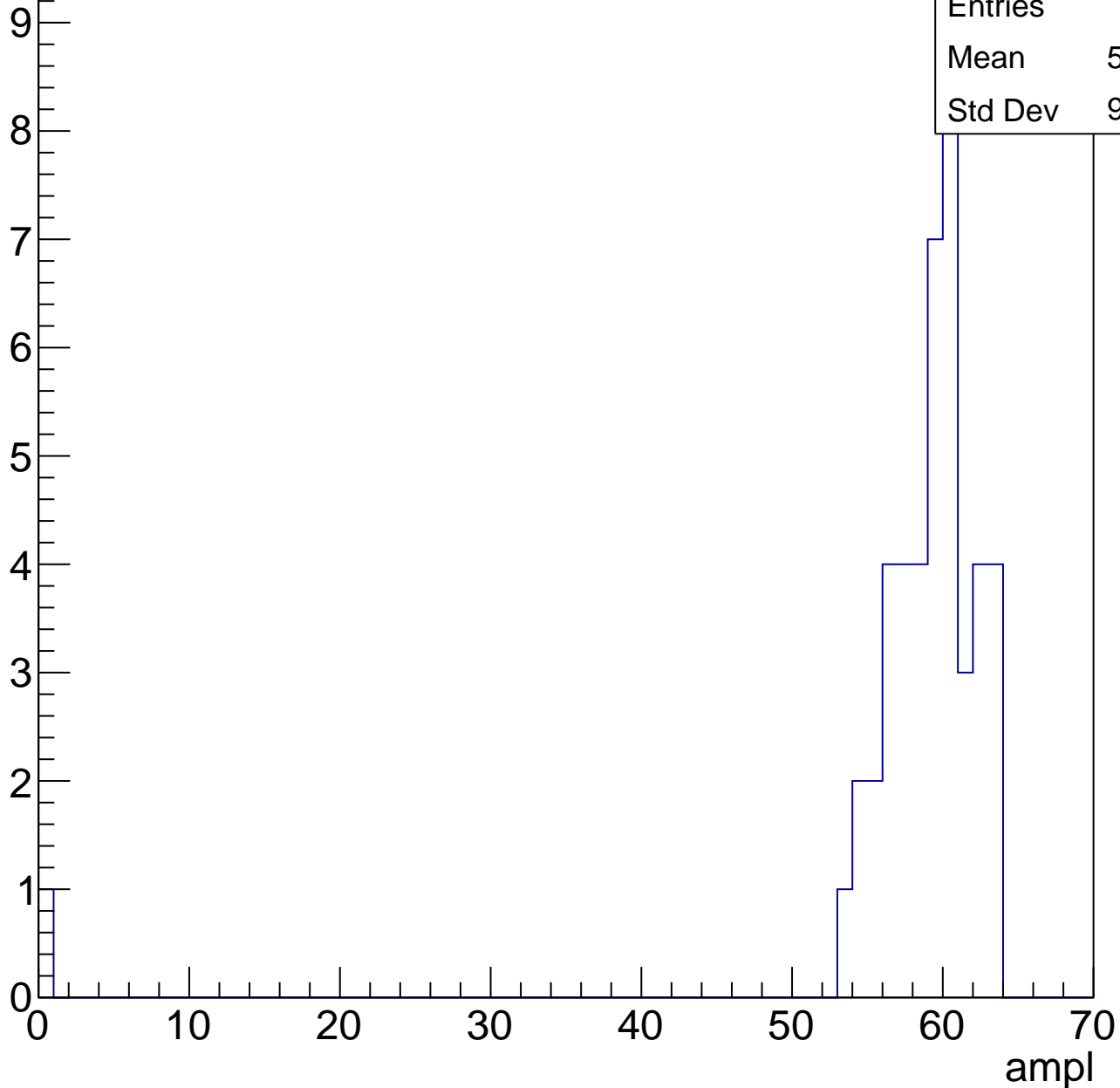
Entries	63
Mean	54.38
Std Dev	3.658



B0L001S, U15-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

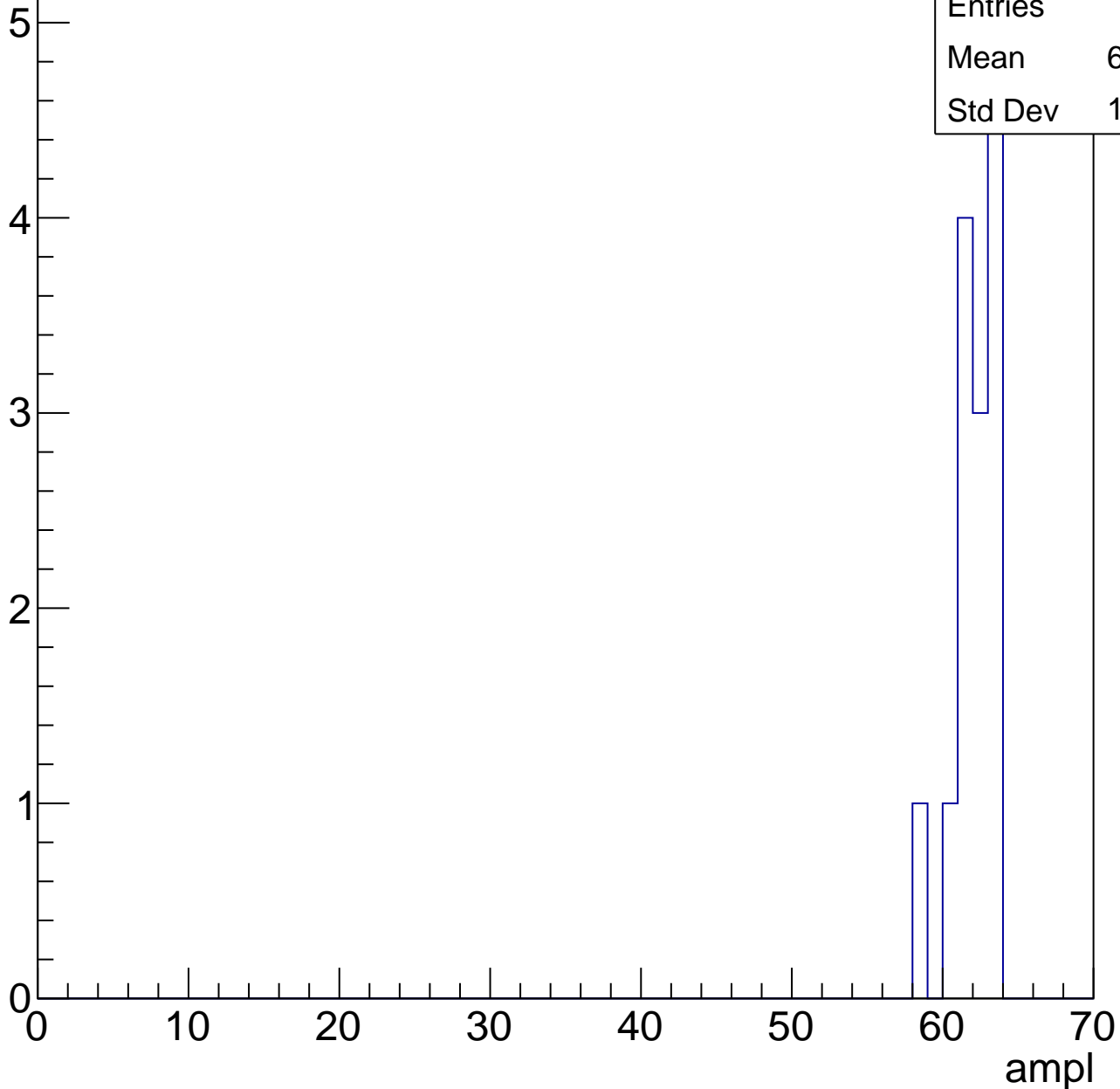


B0L001S, U15-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

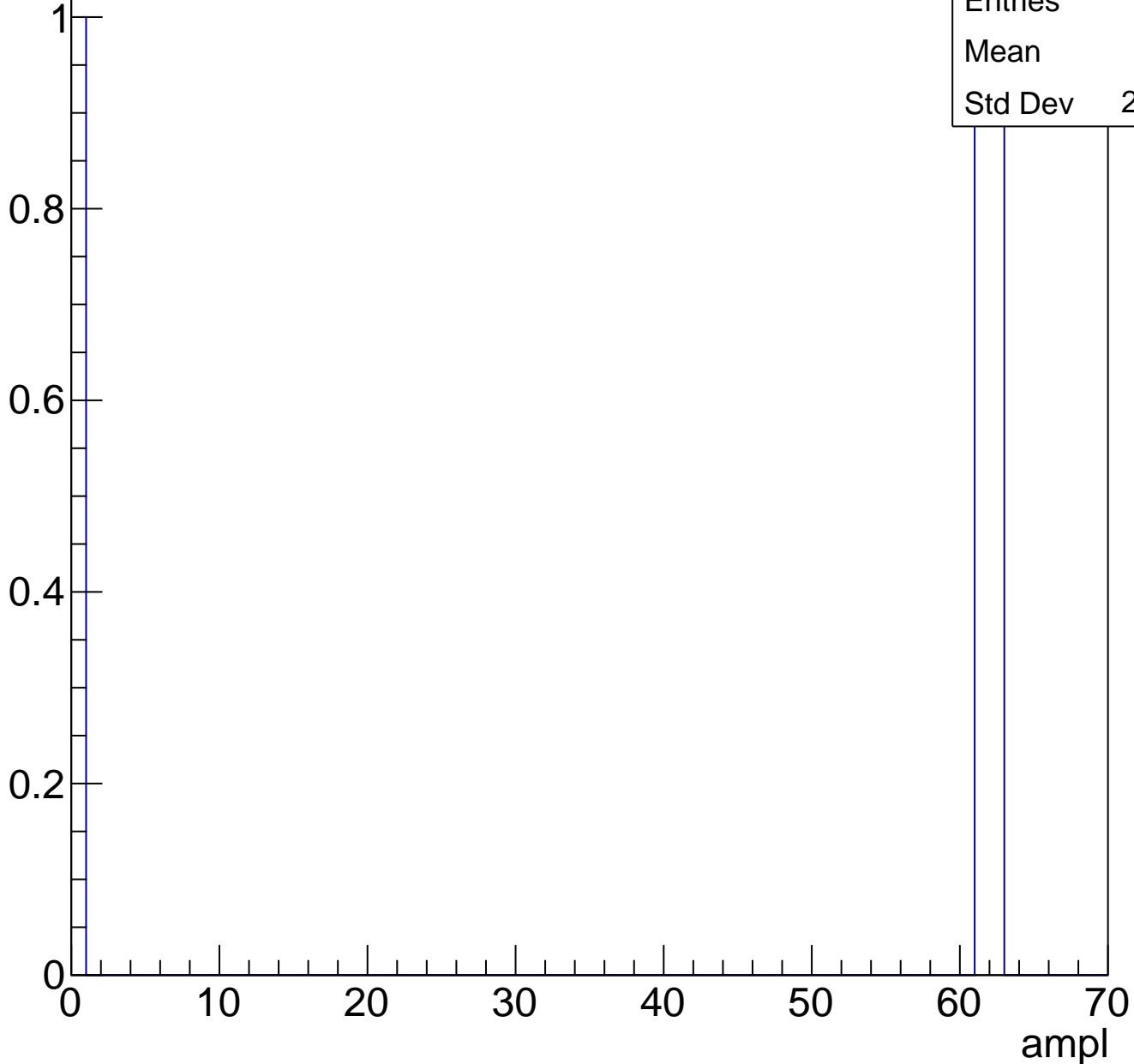
Entries	14
Mean	61.64
Std Dev	1.394



B0L001S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch105, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.18
Std Dev	3.68

Gaus mean : 30.5903

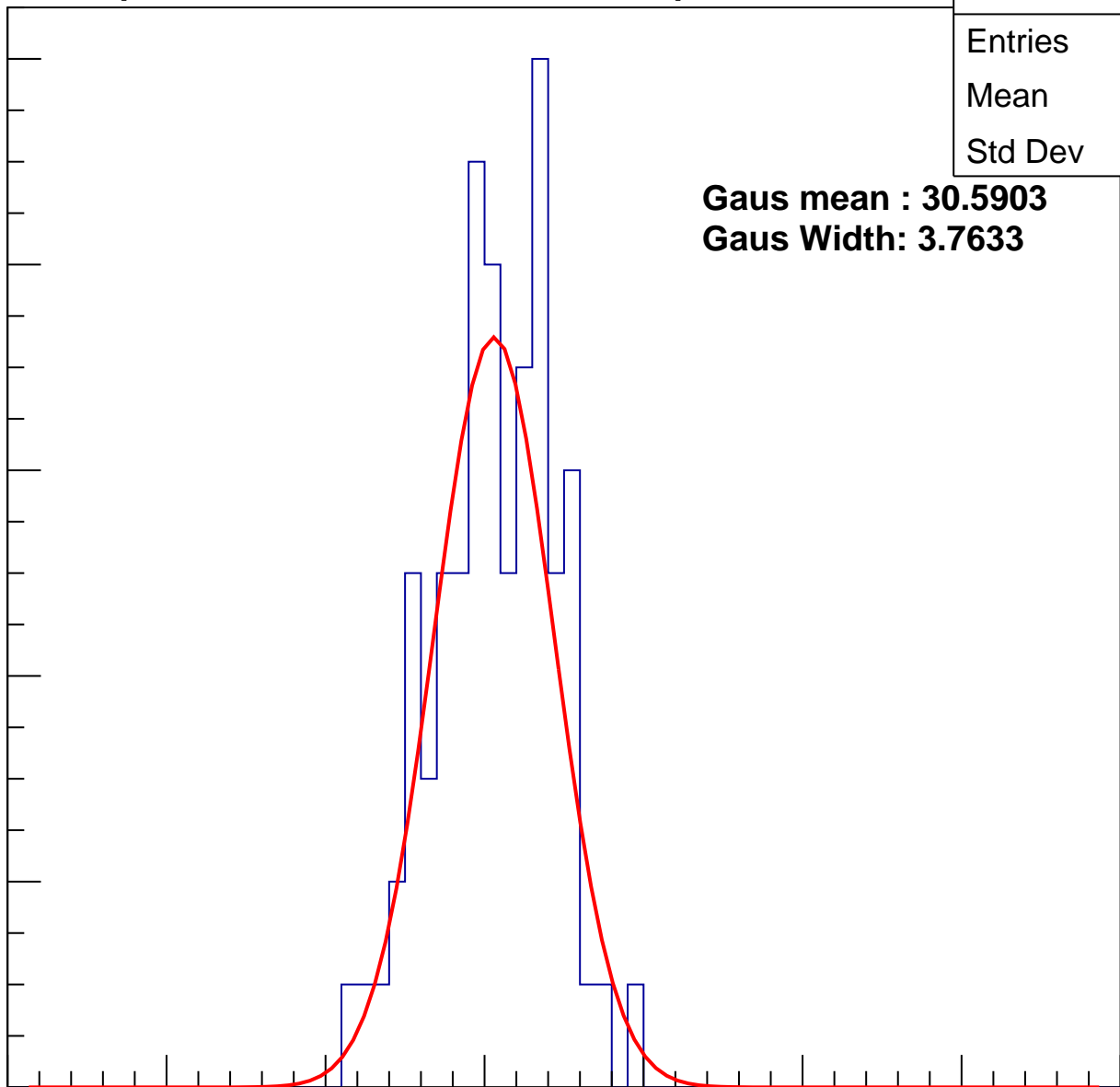
Gaus Width: 3.7633

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch105, adc1

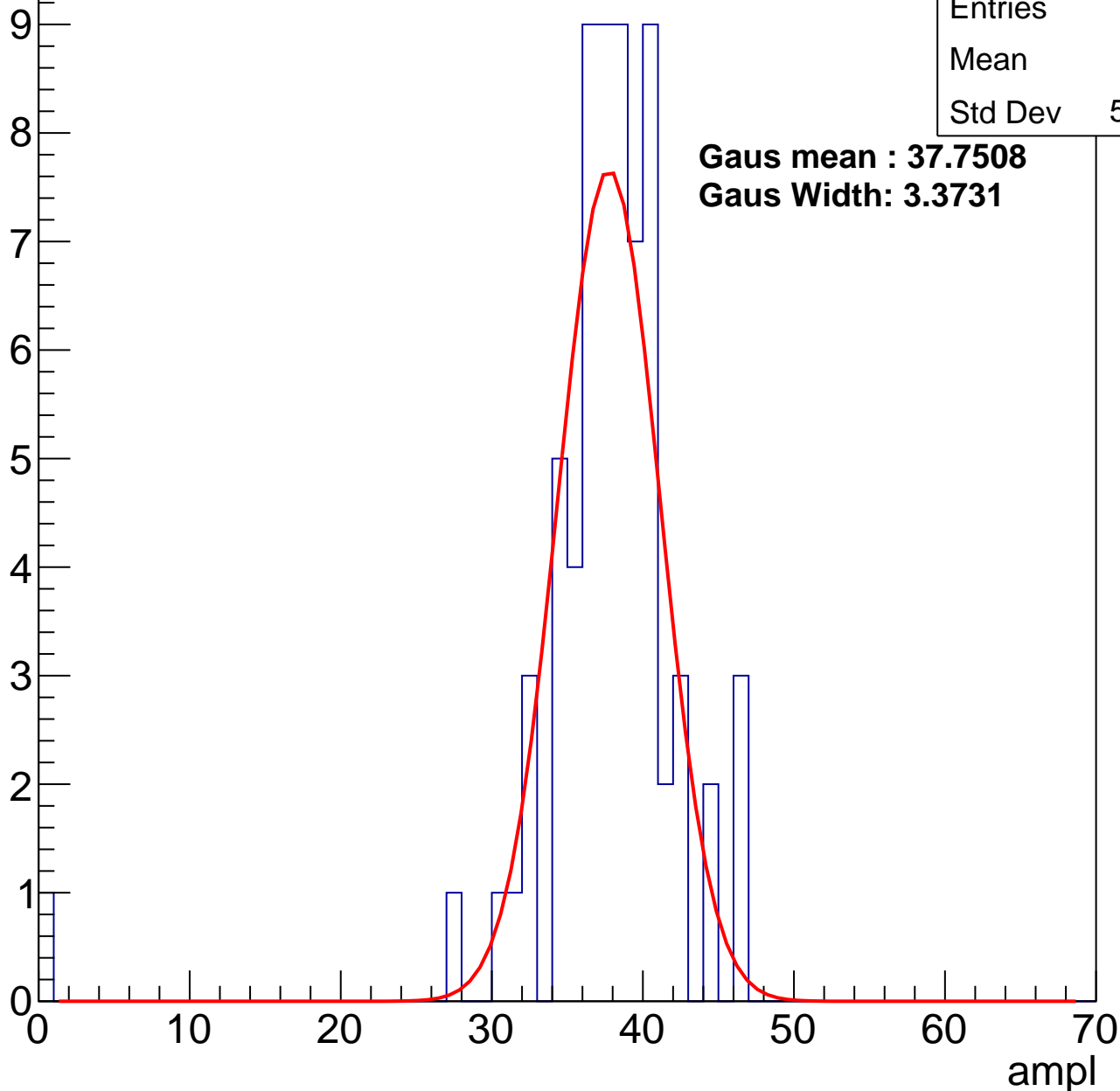
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.1
Std Dev	5.729

Gaus mean : 37.7508

Gaus Width: 3.3731



B0L001S, U15-ch105, adc2

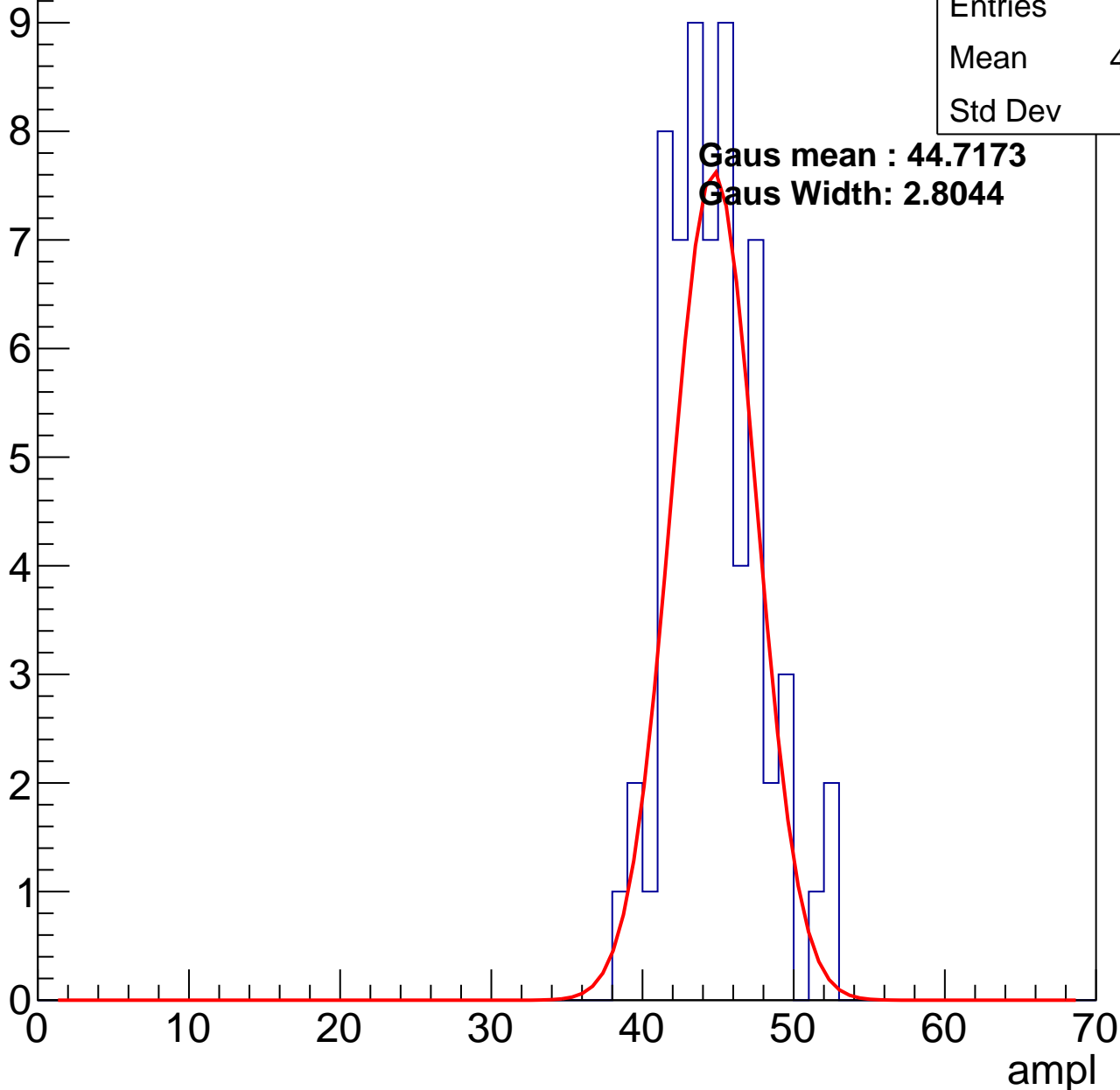
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.27
Std Dev	3.03

Gaus mean : 44.7173

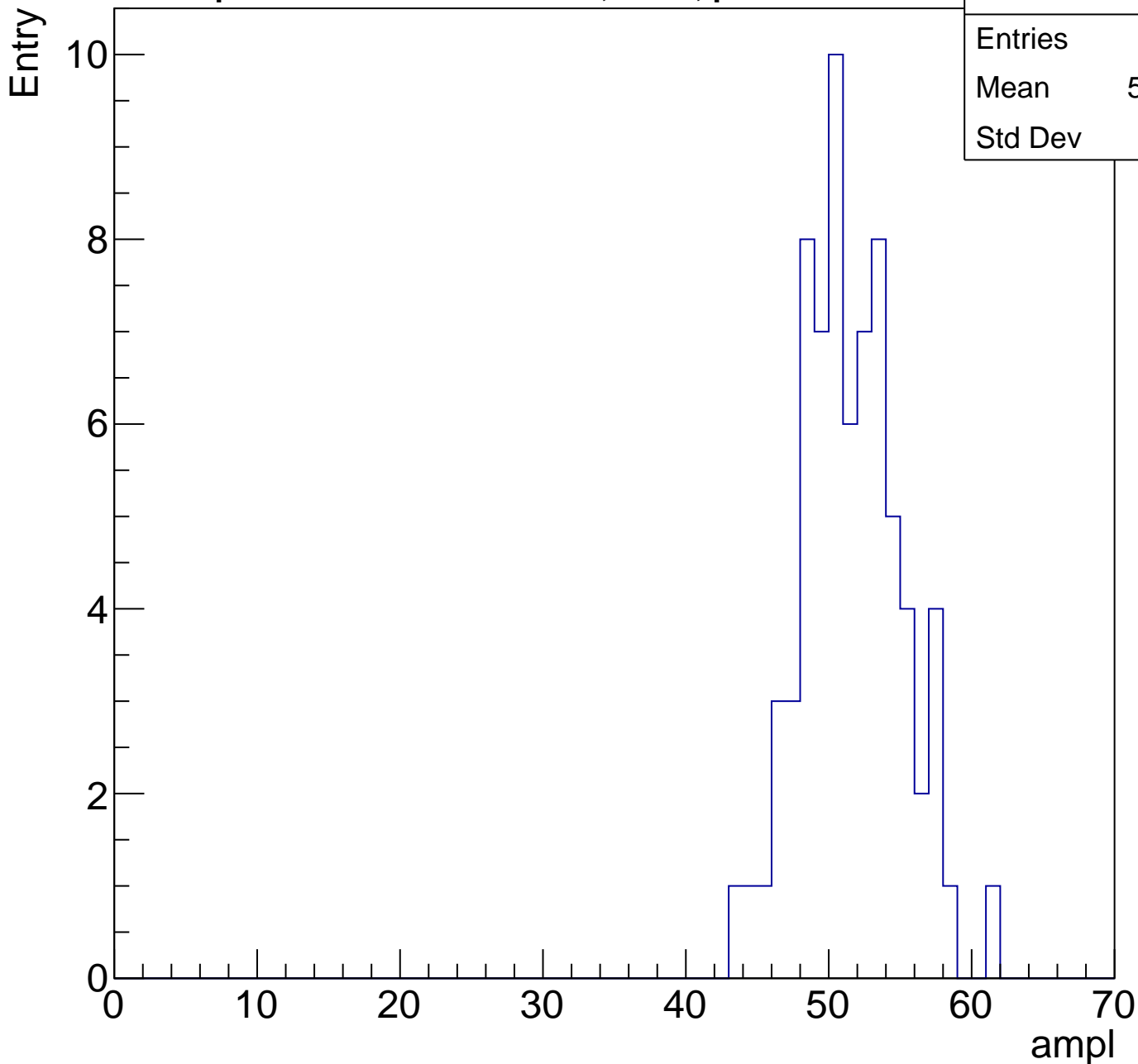
Gaus Width: 2.8044



B0L001S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	51.12
Std Dev	3.48

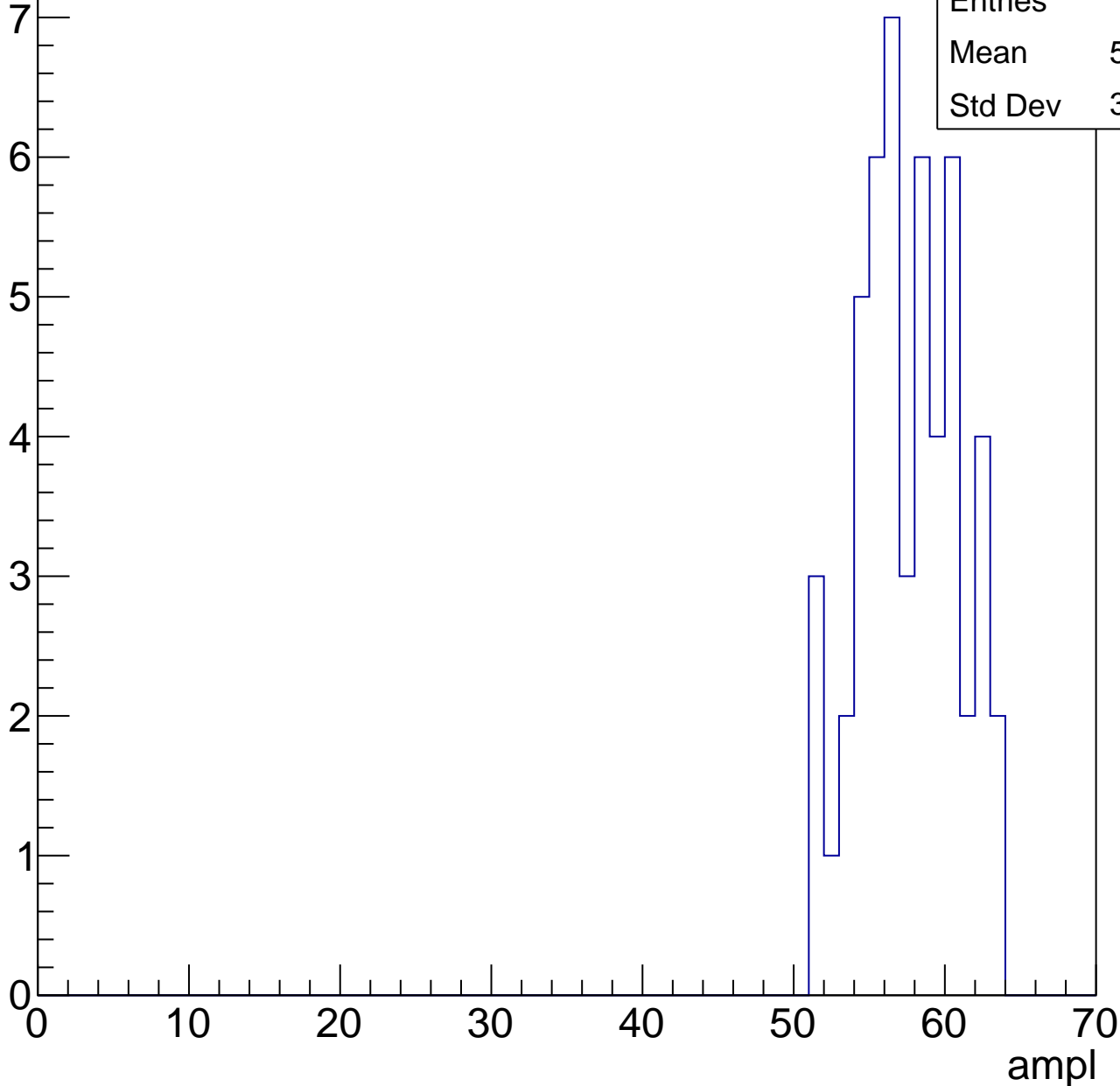


B0L001S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.14
Std Dev	3.193

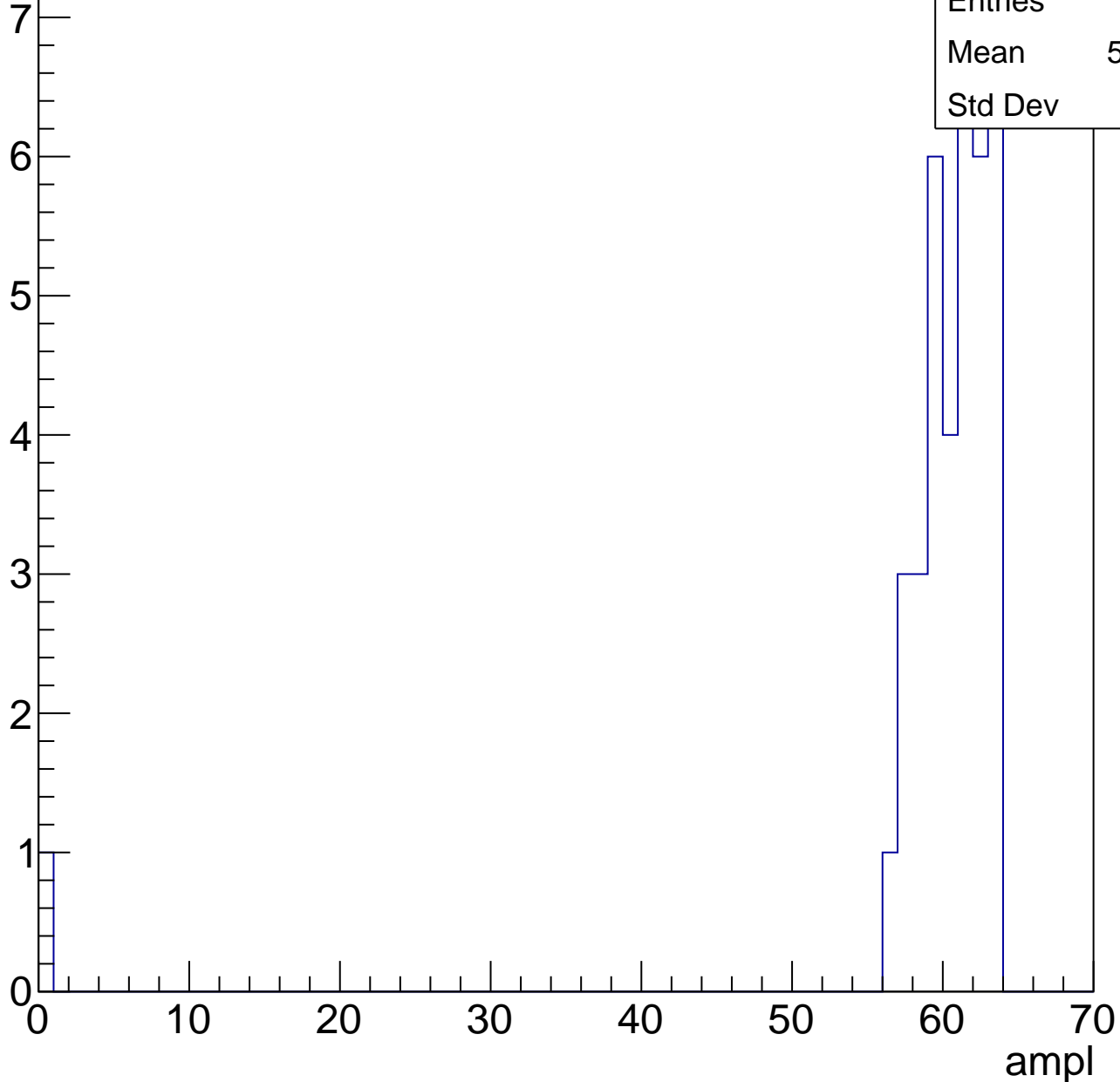


B0L001S, U15-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.82
Std Dev	9.87



B0L001S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch106, adc0

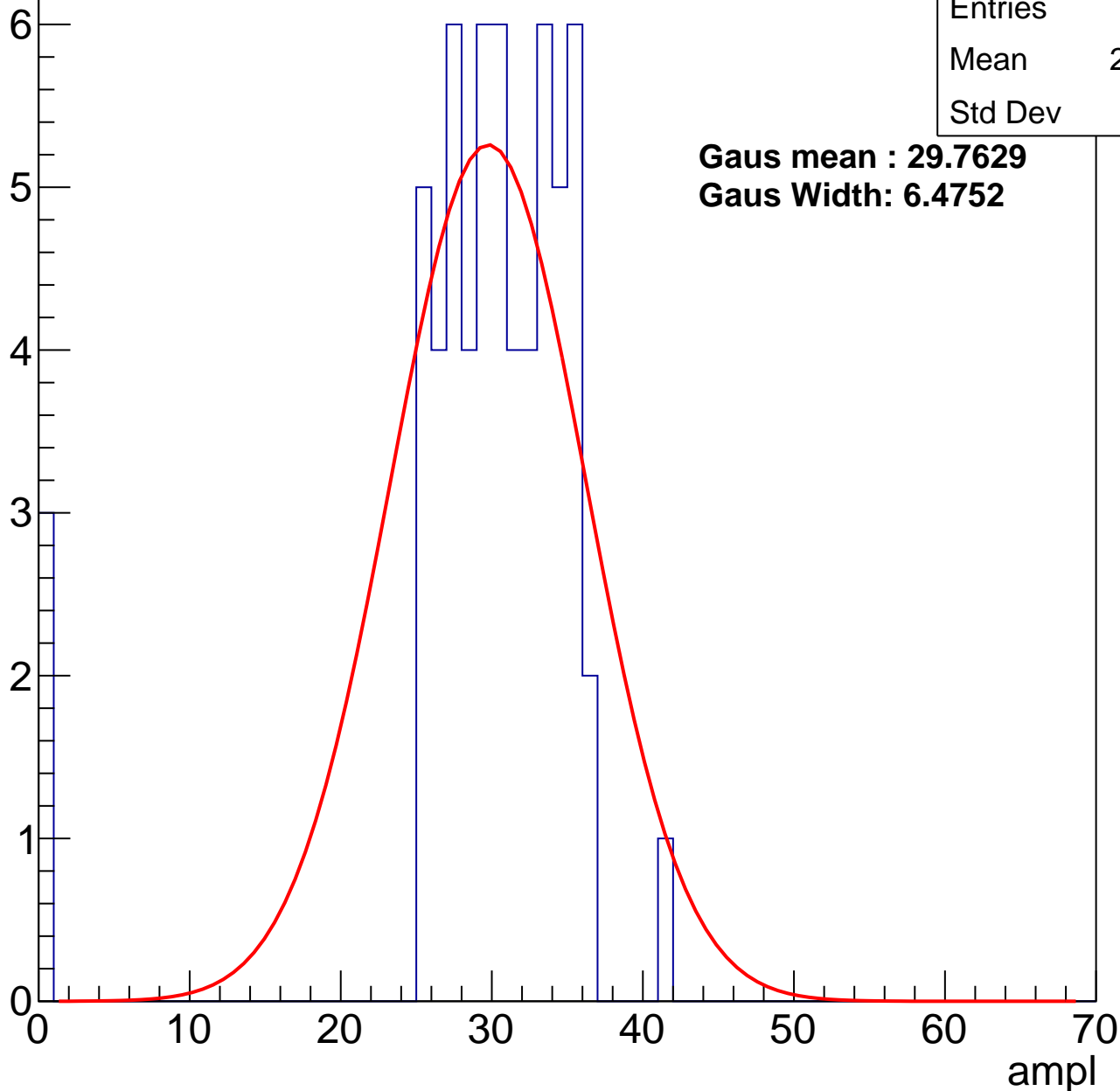
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.03
Std Dev	7.41

Gaus mean : 29.7629

Gaus Width: 6.4752



B0L001S, U15-ch106, adc1

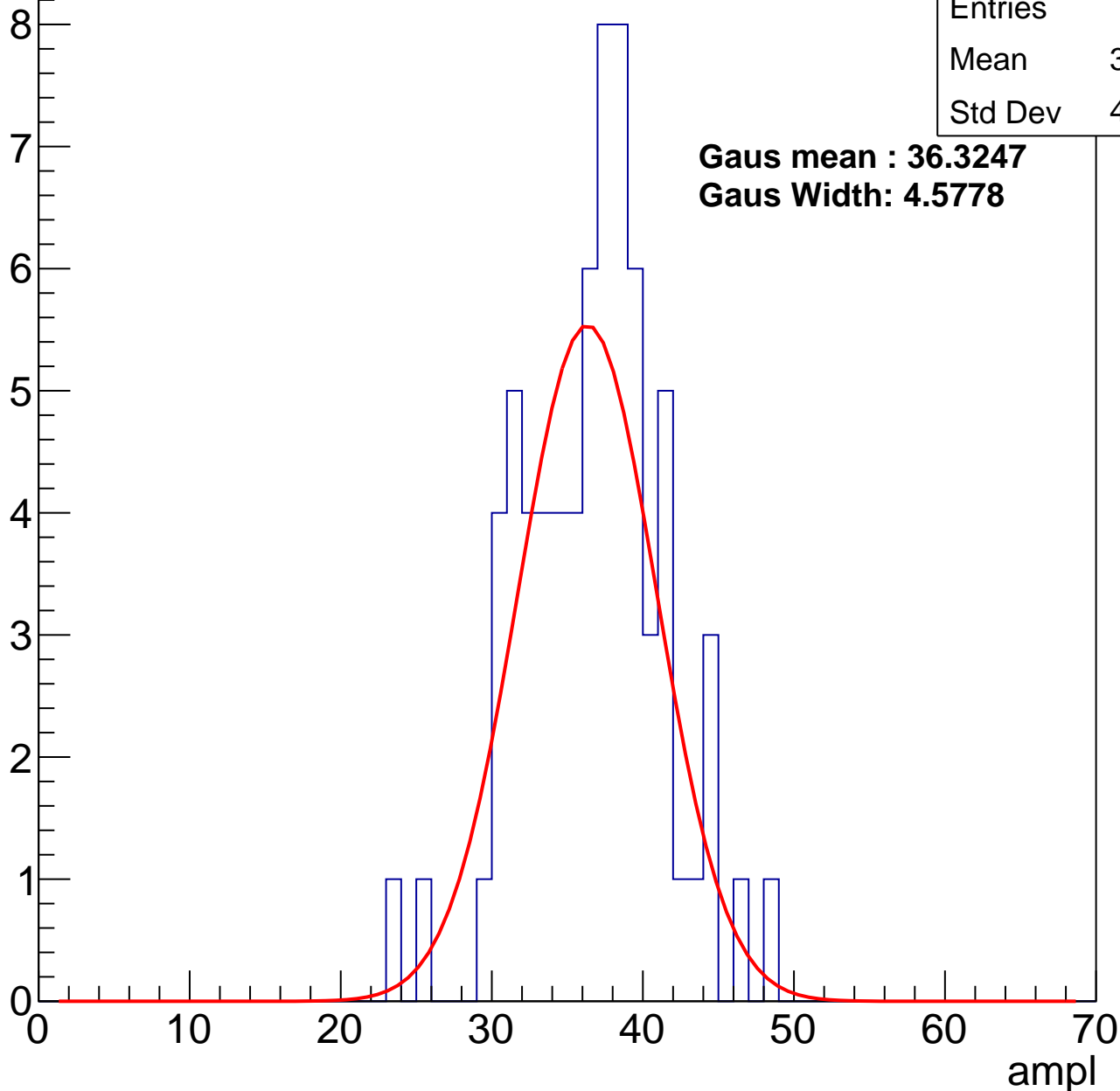
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.25
Std Dev	4.614

Gaus mean : 36.3247

Gaus Width: 4.5778



B0L001S, U15-ch106, adc2

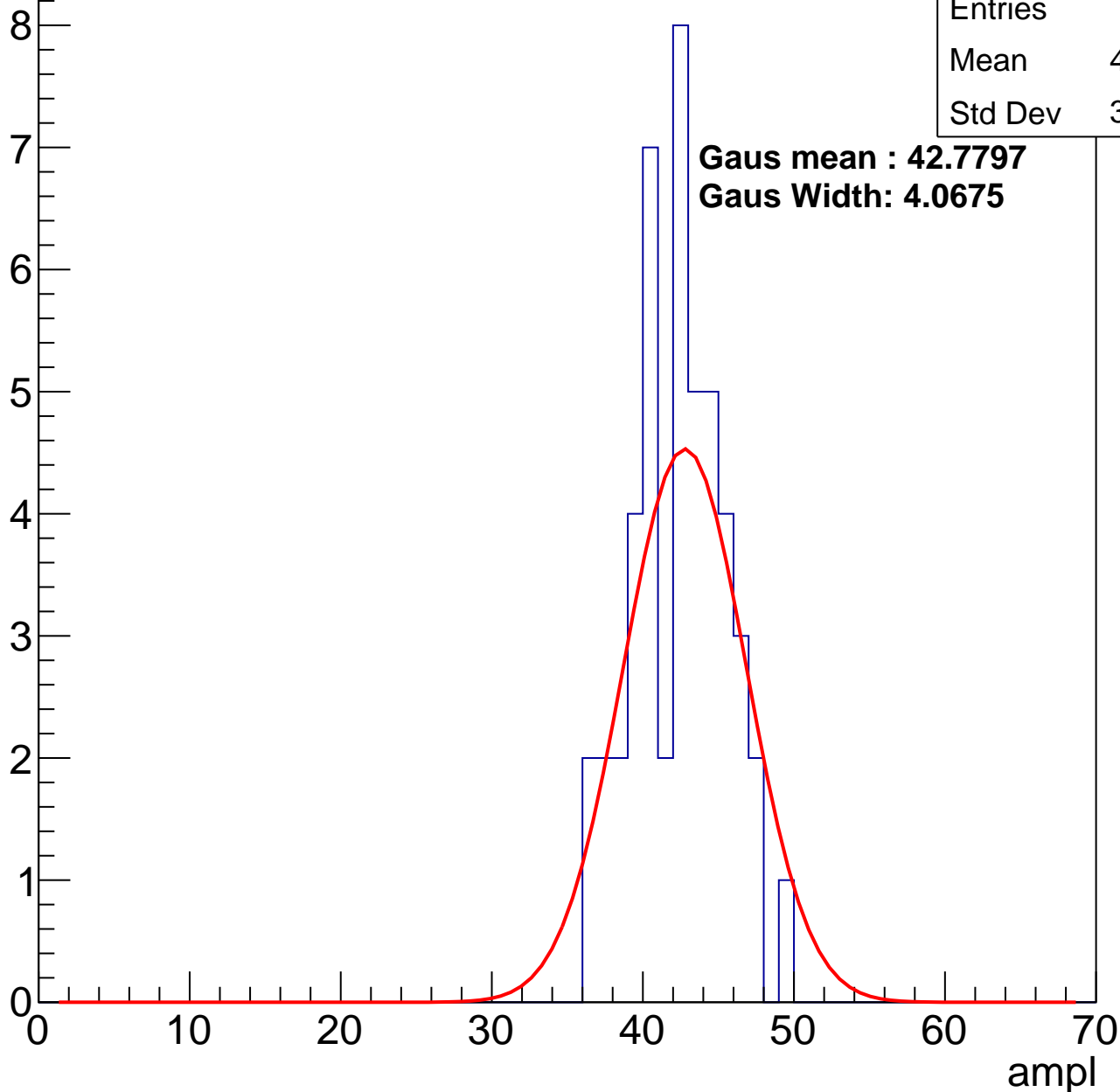
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	41.96
Std Dev	3.017

Gaus mean : 42.7797

Gaus Width: 4.0675

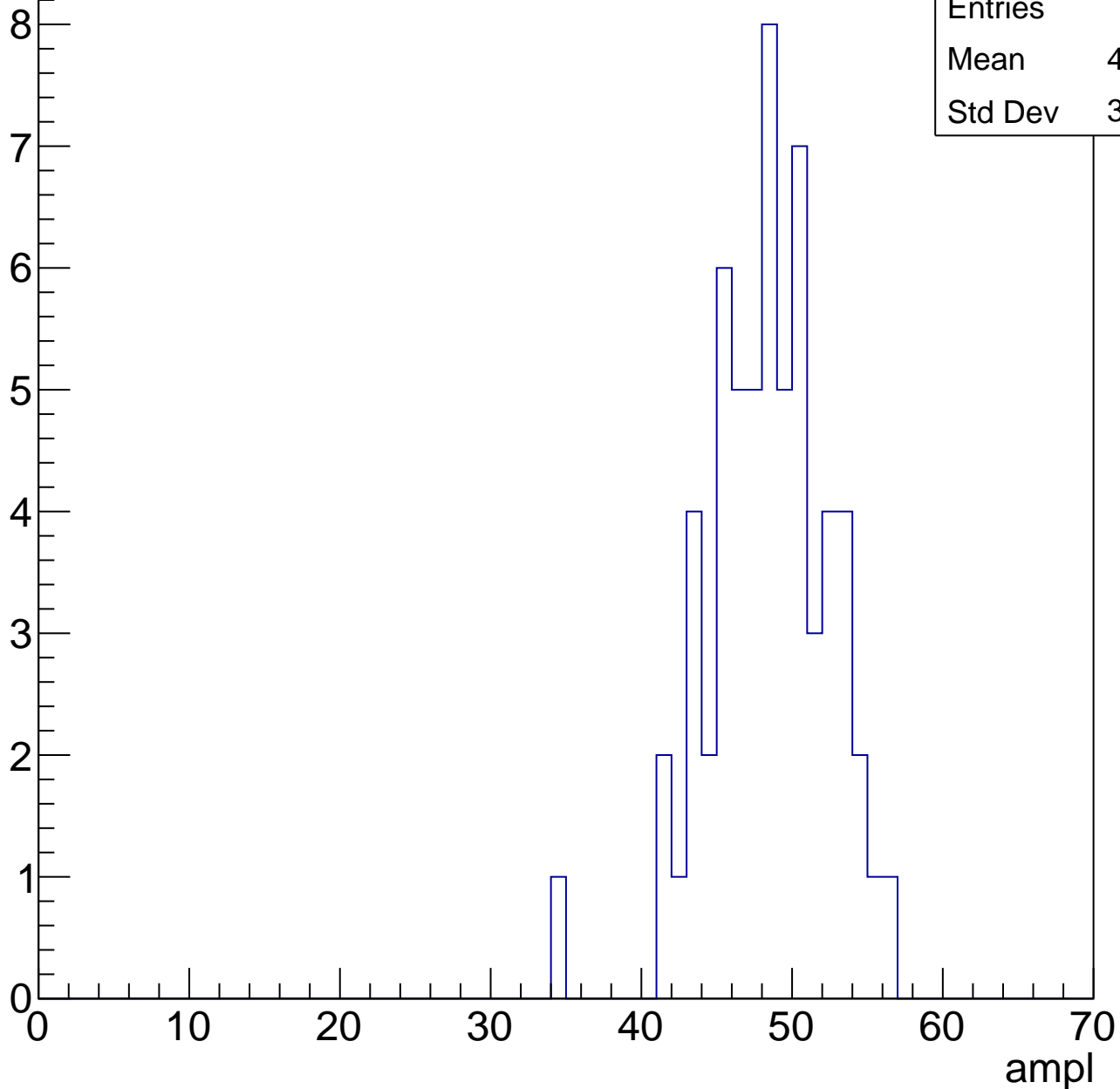


B0L001S, U15-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	47.93
Std Dev	3.946

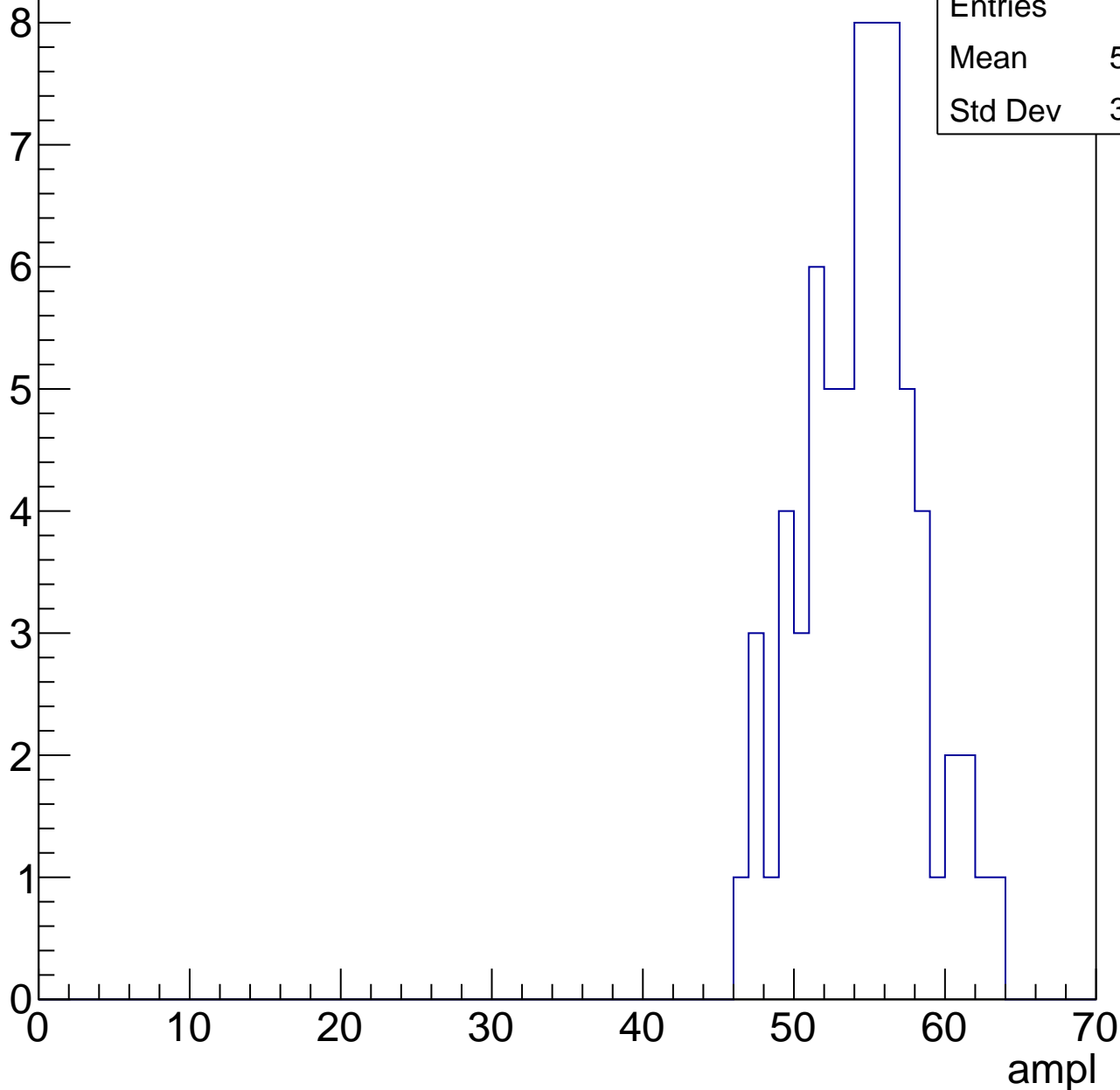


B0L001S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	54.04
Std Dev	3.763

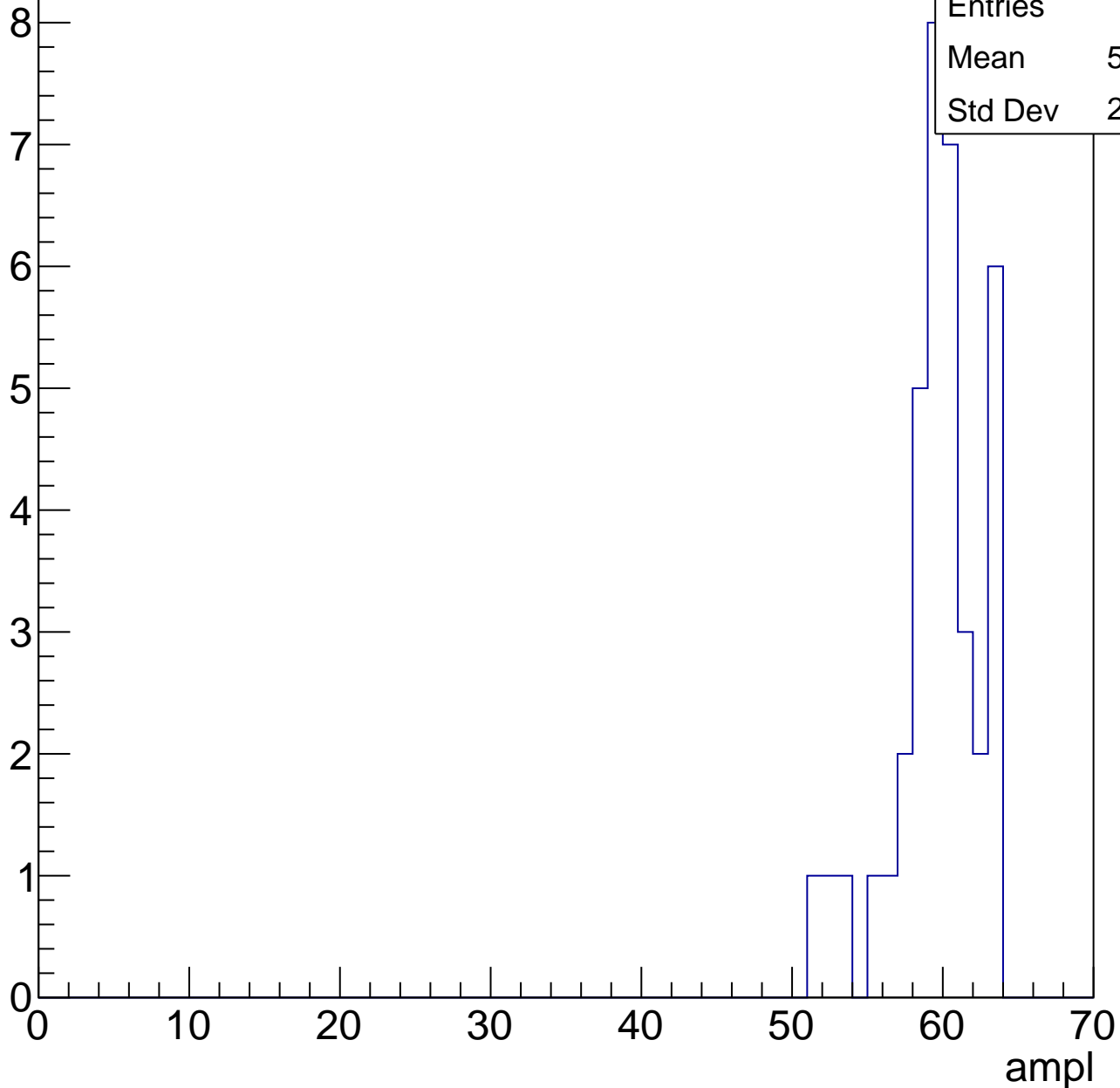


B0L001S, U15-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	59.16
Std Dev	2.898

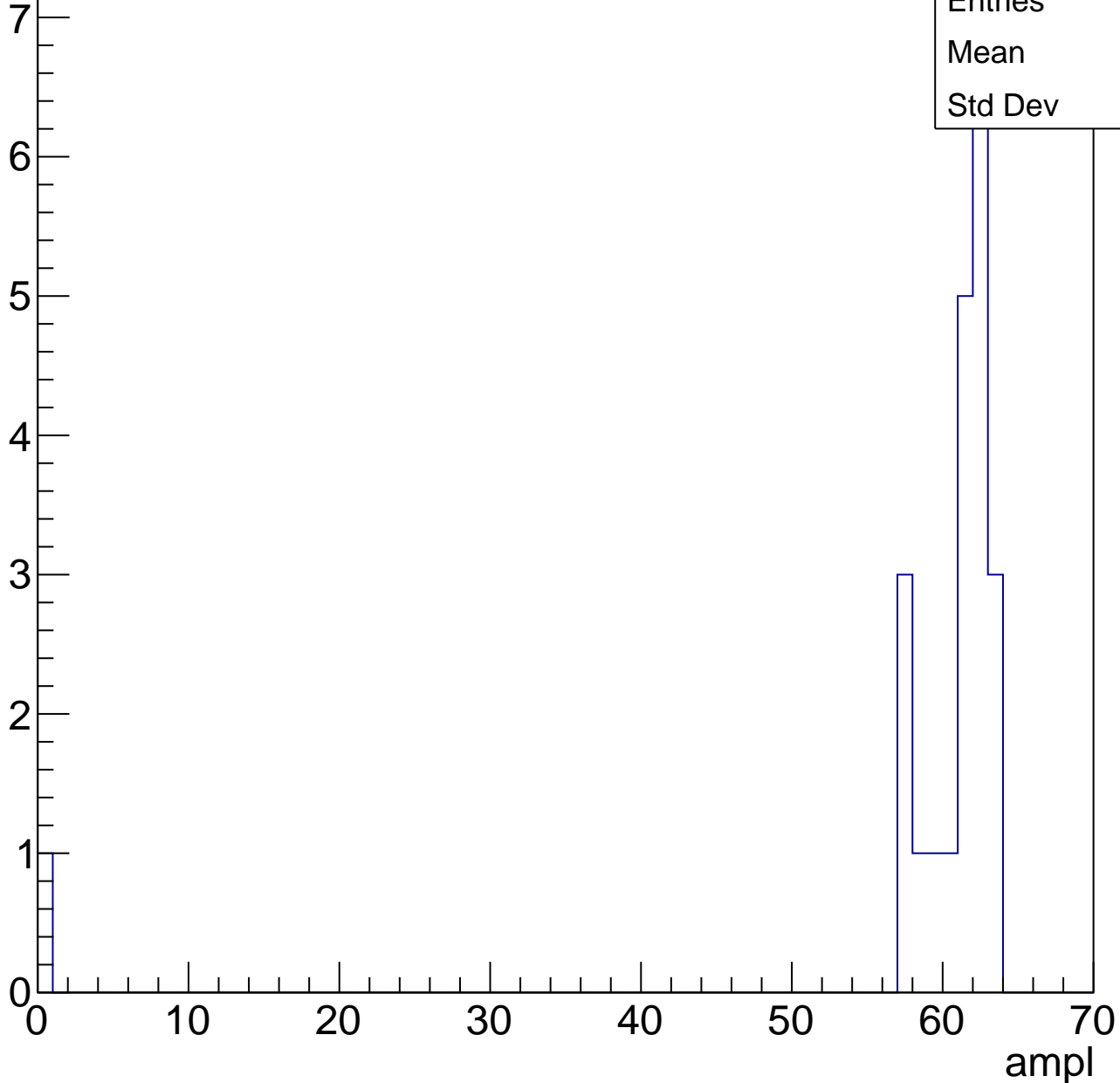


B0L001S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58
Std Dev	12.8



B0L001S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch107, adc0

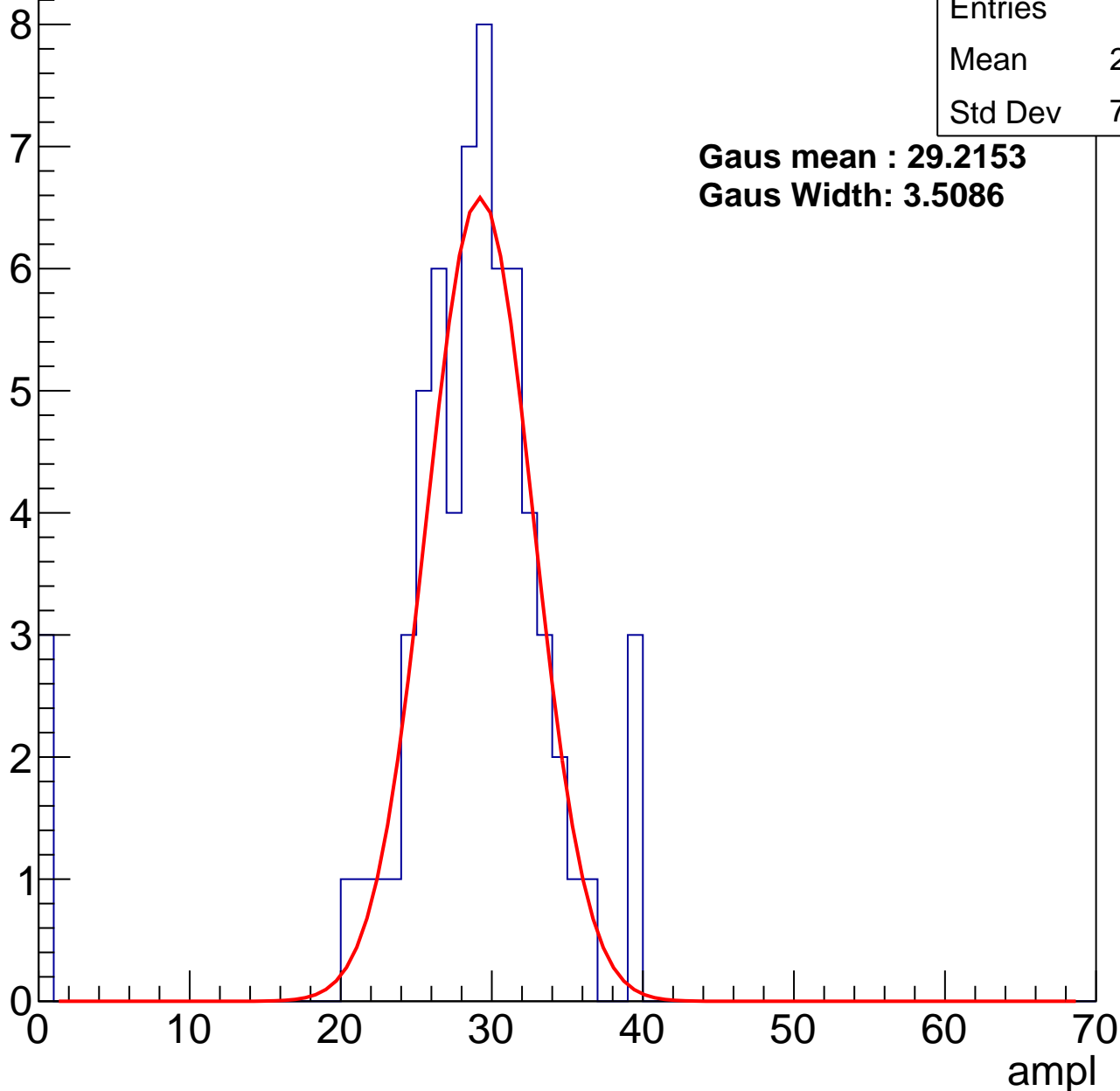
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	27.64
Std Dev	7.189

Gaus mean : 29.2153

Gaus Width: 3.5086



B0L001S, U15-ch107, adc1

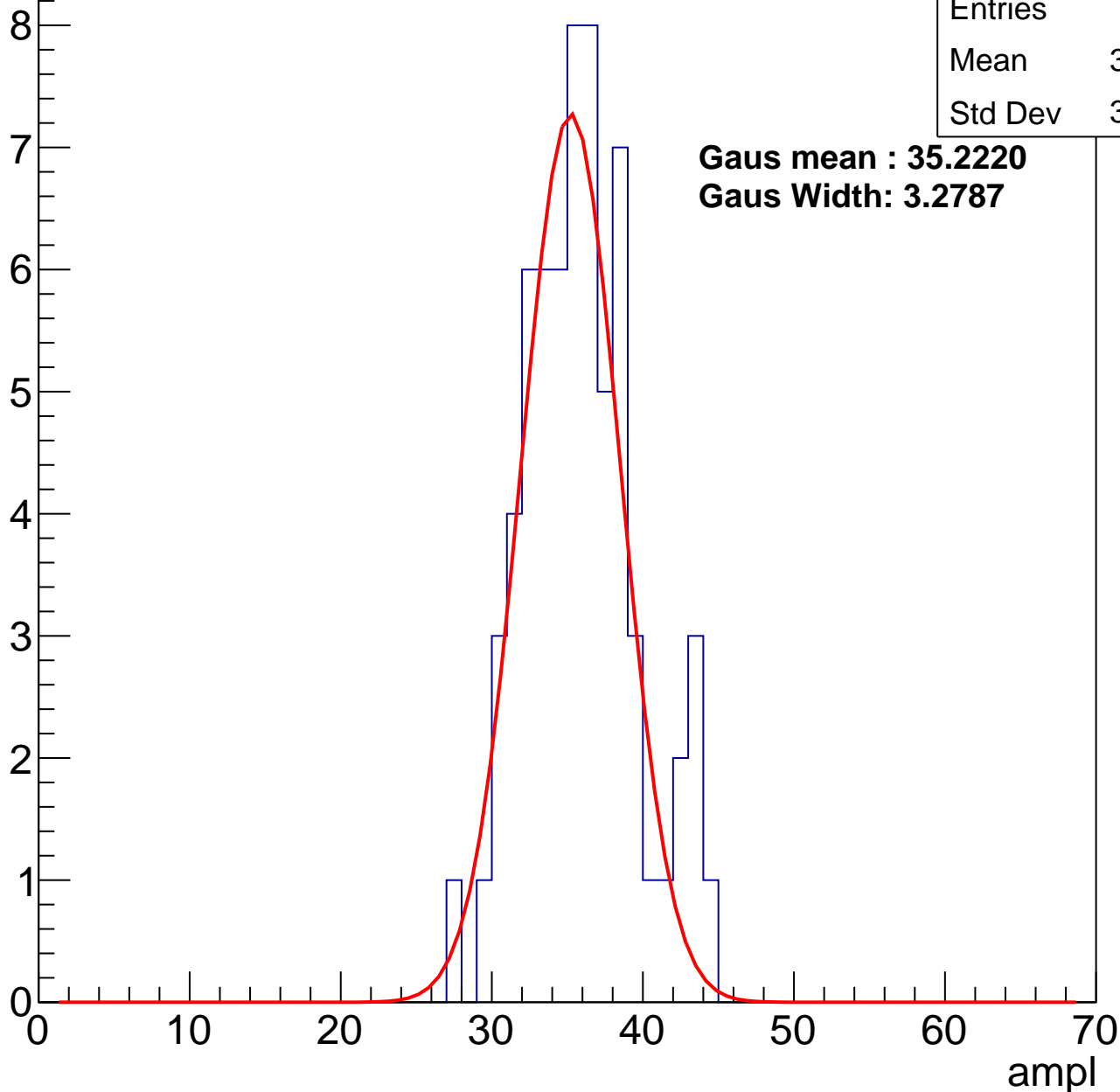
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	35.42
Std Dev	3.656

Gaus mean : 35.2220

Gaus Width: 3.2787



B0L001S, U15-ch107, adc2

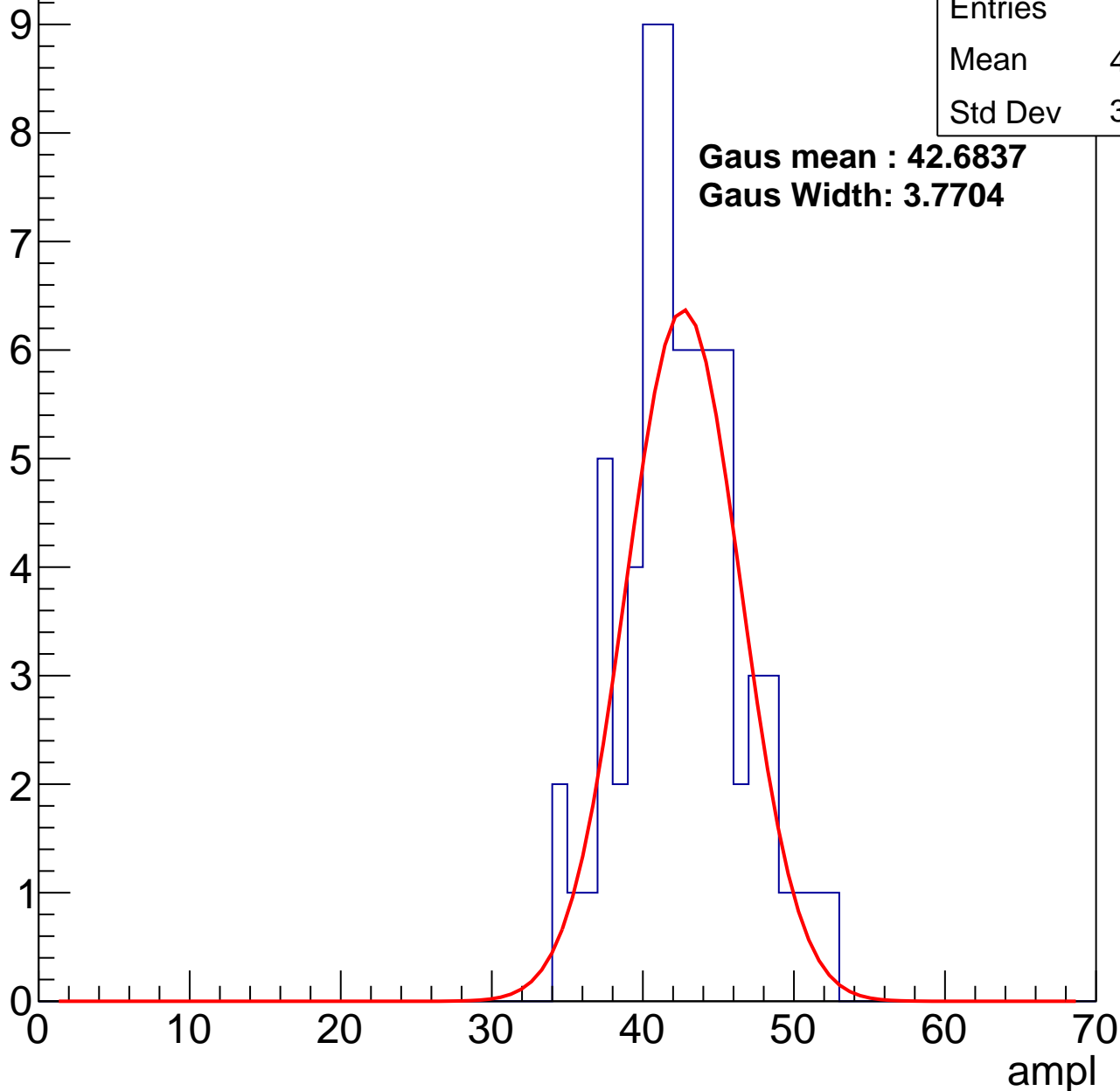
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.14
Std Dev	3.887

Gaus mean : 42.6837

Gaus Width: 3.7704

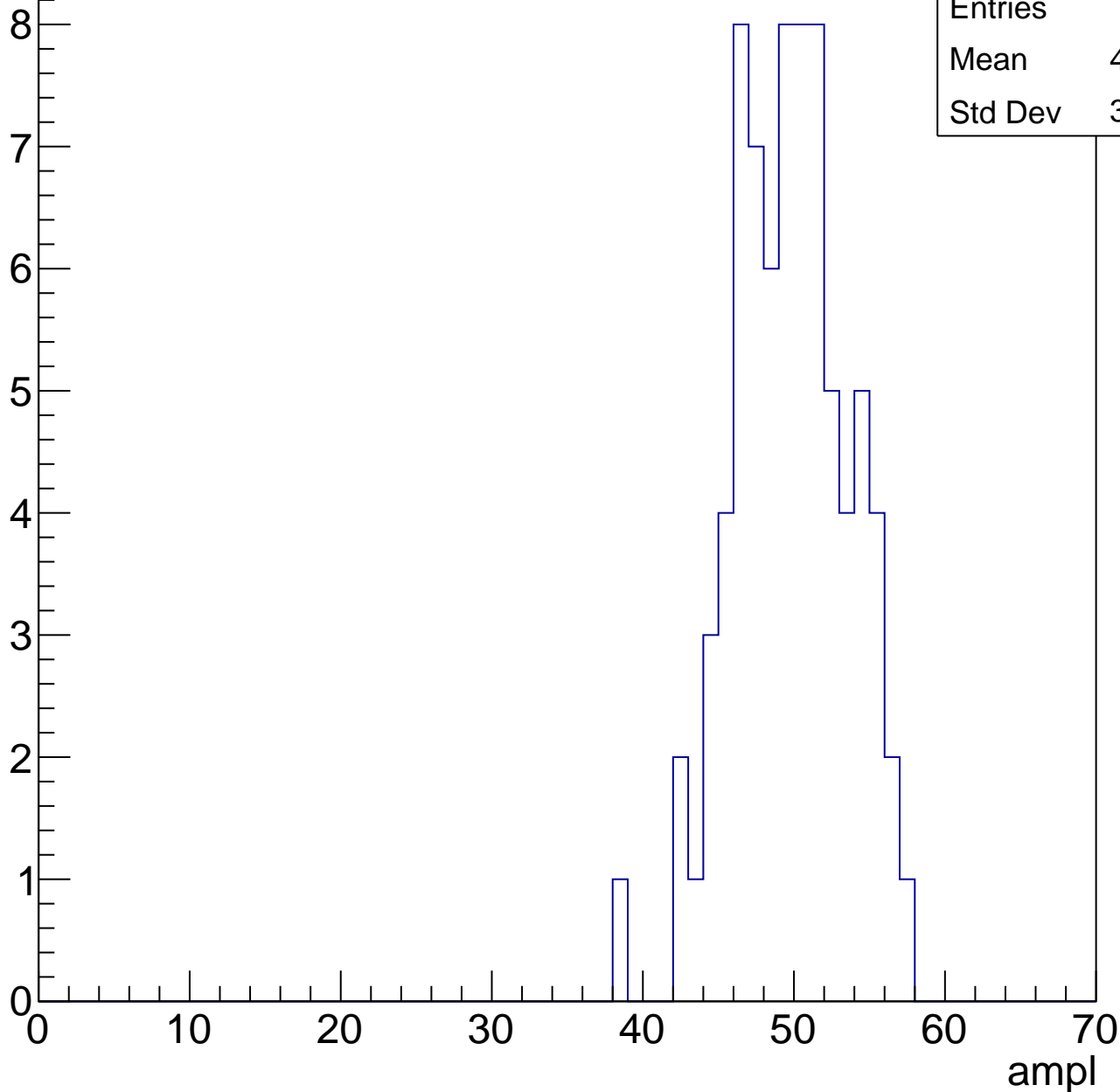


B0L001S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	49.26
Std Dev	3.736



B0L001S, U15-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

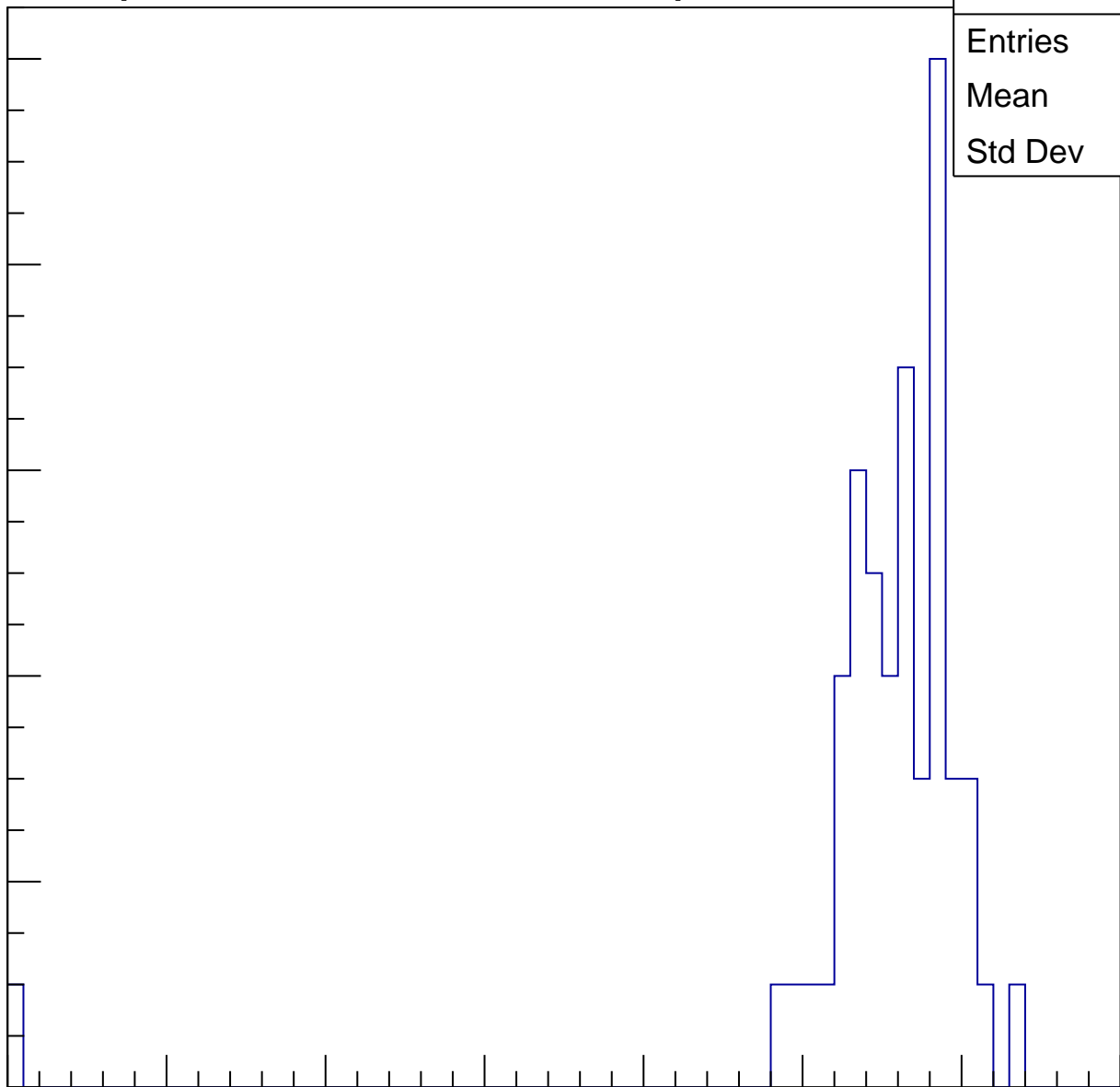
Entries	52
Mean	54.58
Std Dev	8.249

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

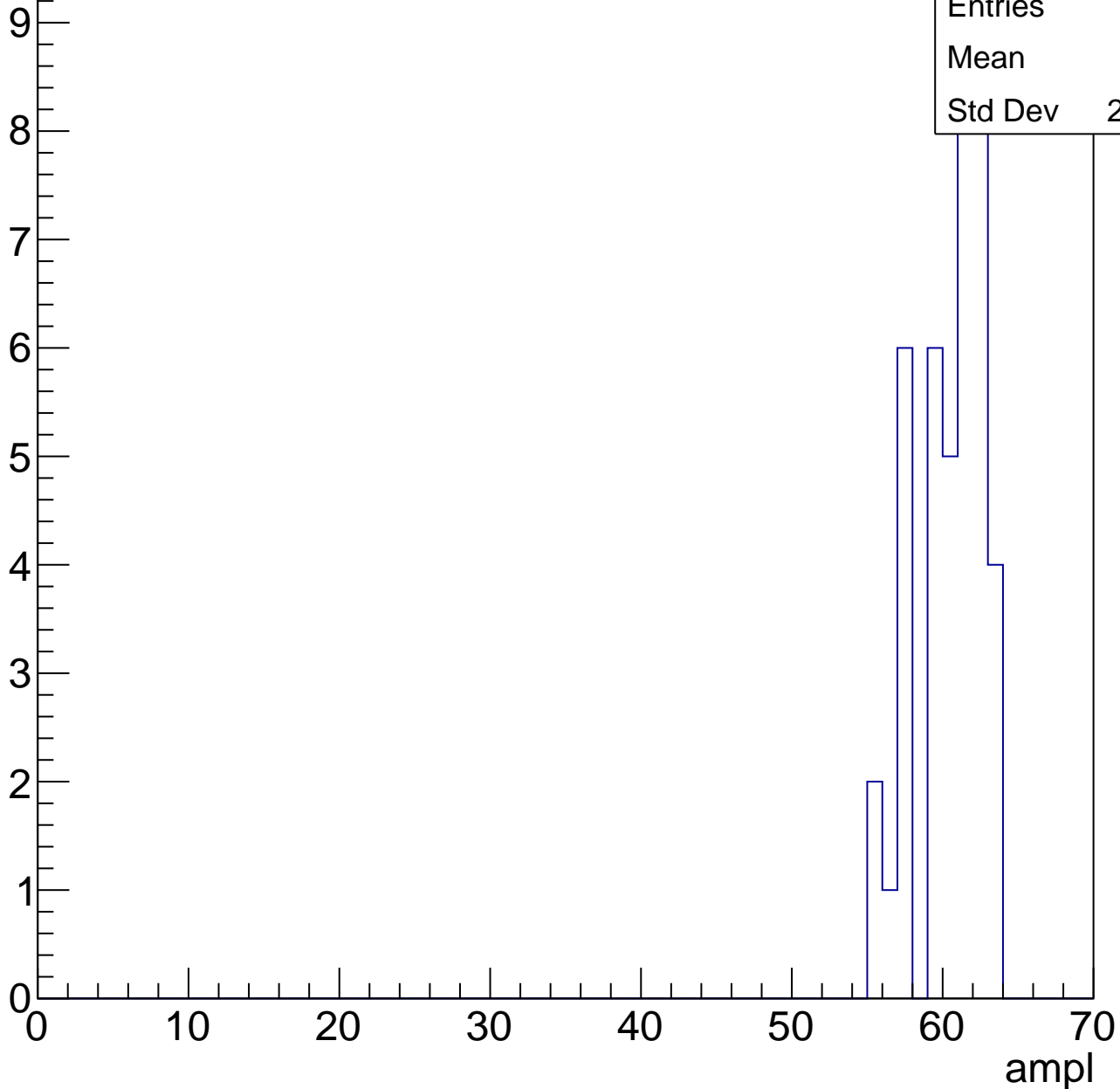


B0L001S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	60
Std Dev	2.242

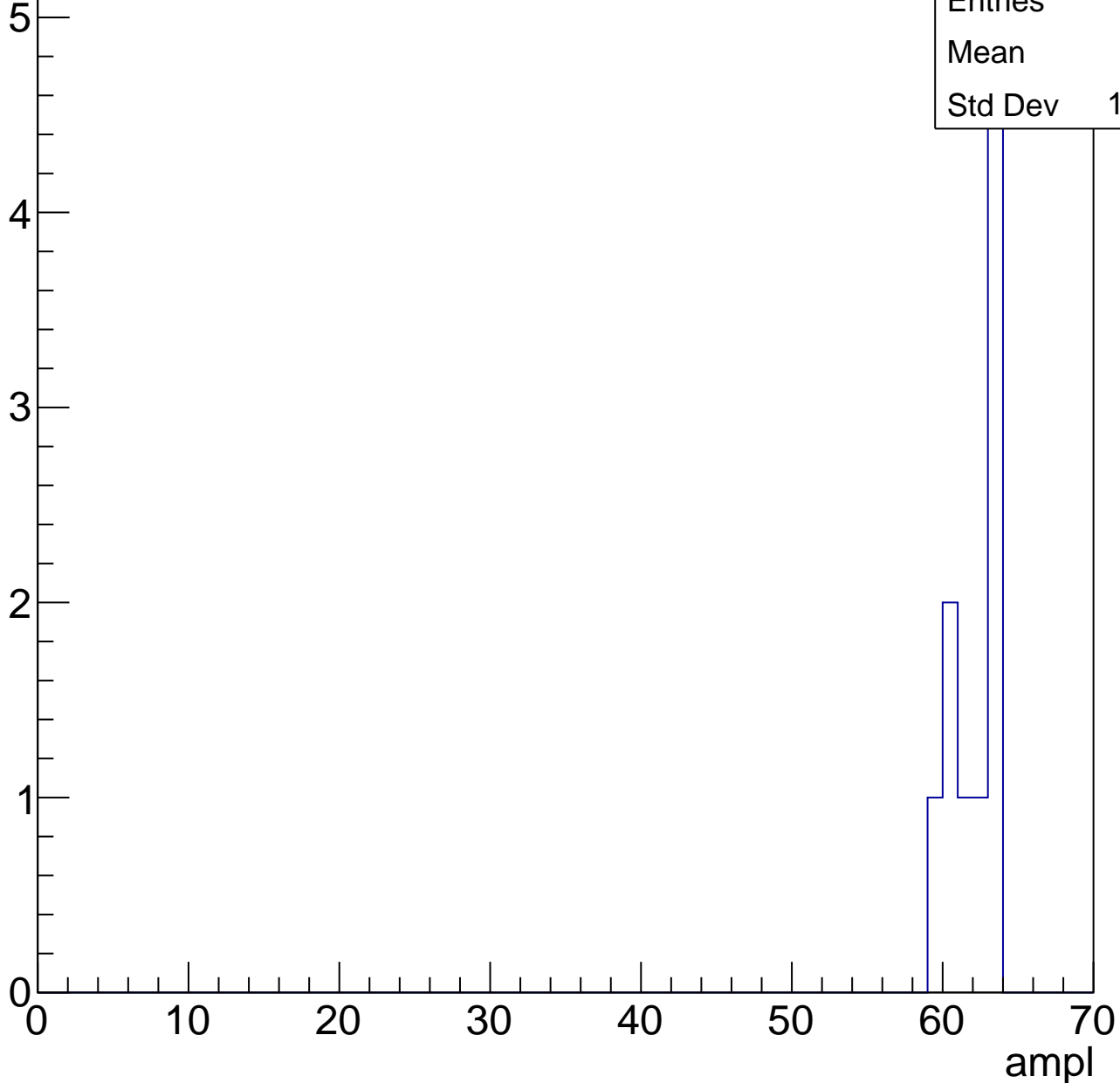


B0L001S, U15-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	10
Mean	61.7
Std Dev	1.487



B0L001S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch108, adc0

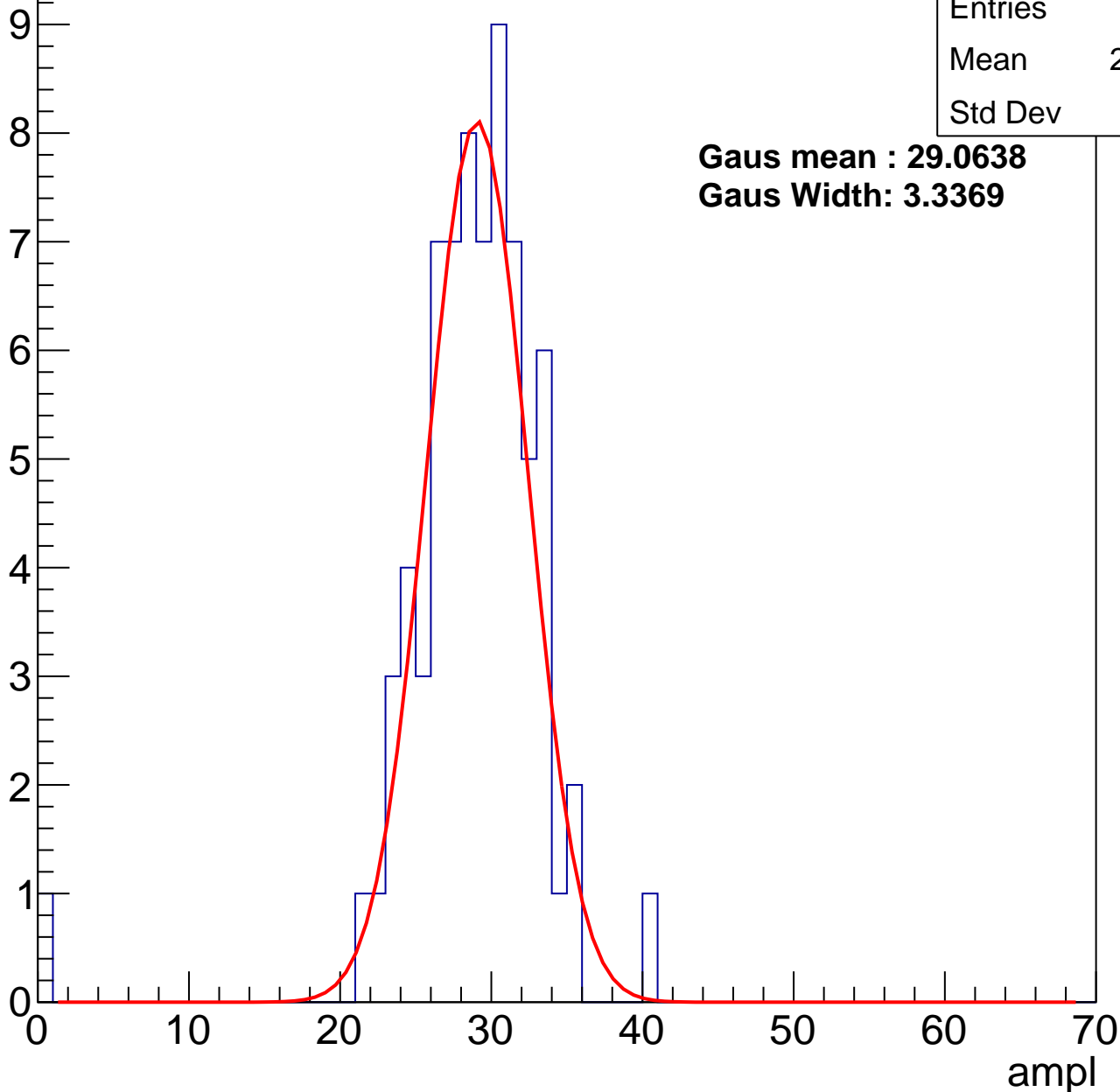
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	28.36
Std Dev	4.79

Gaus mean : 29.0638

Gaus Width: 3.3369



B0L001S, U15-ch108, adc1

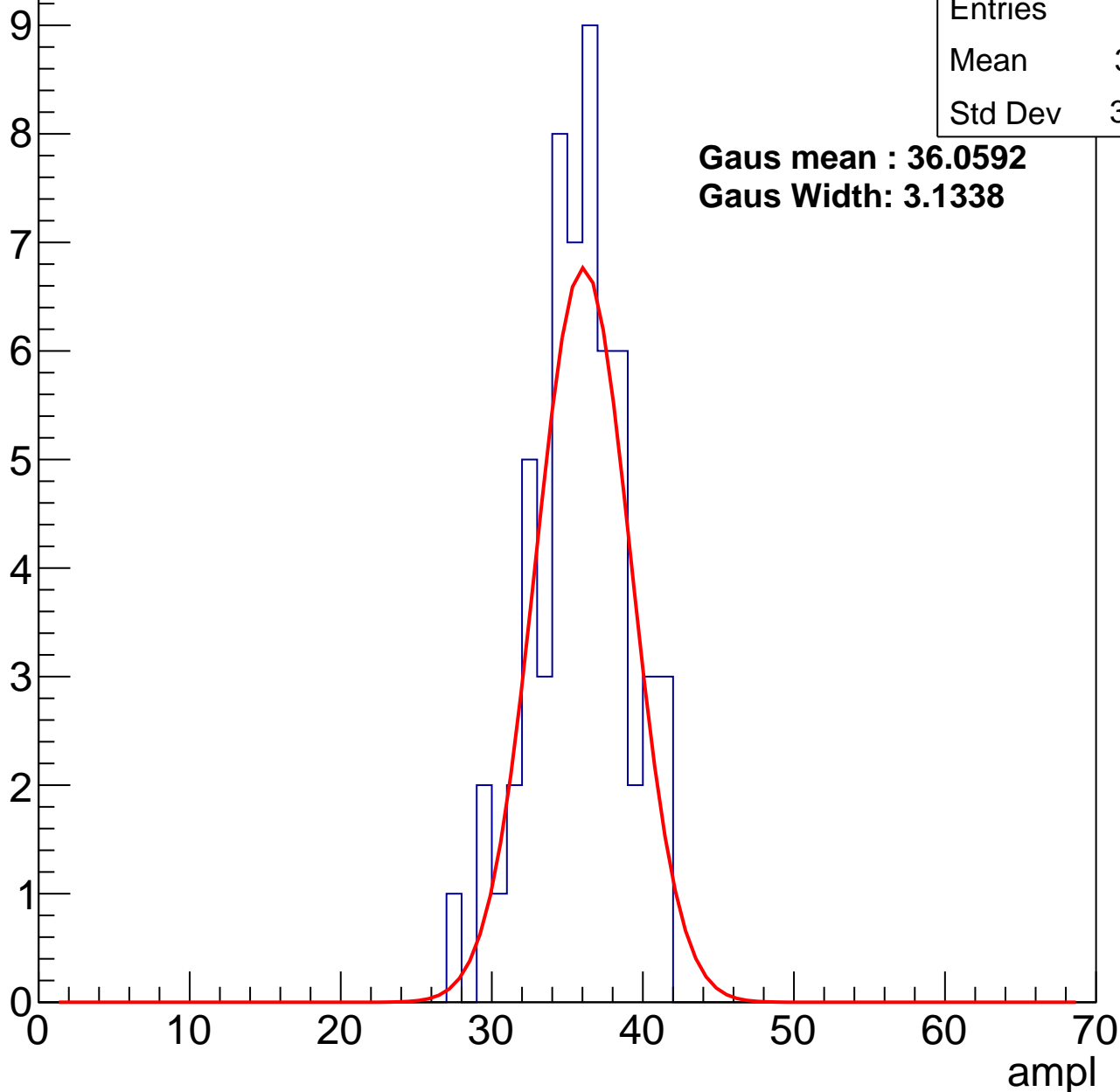
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	35.31
Std Dev	3.092

Gaus mean : 36.0592

Gaus Width: 3.1338



B0L001S, U15-ch108, adc2

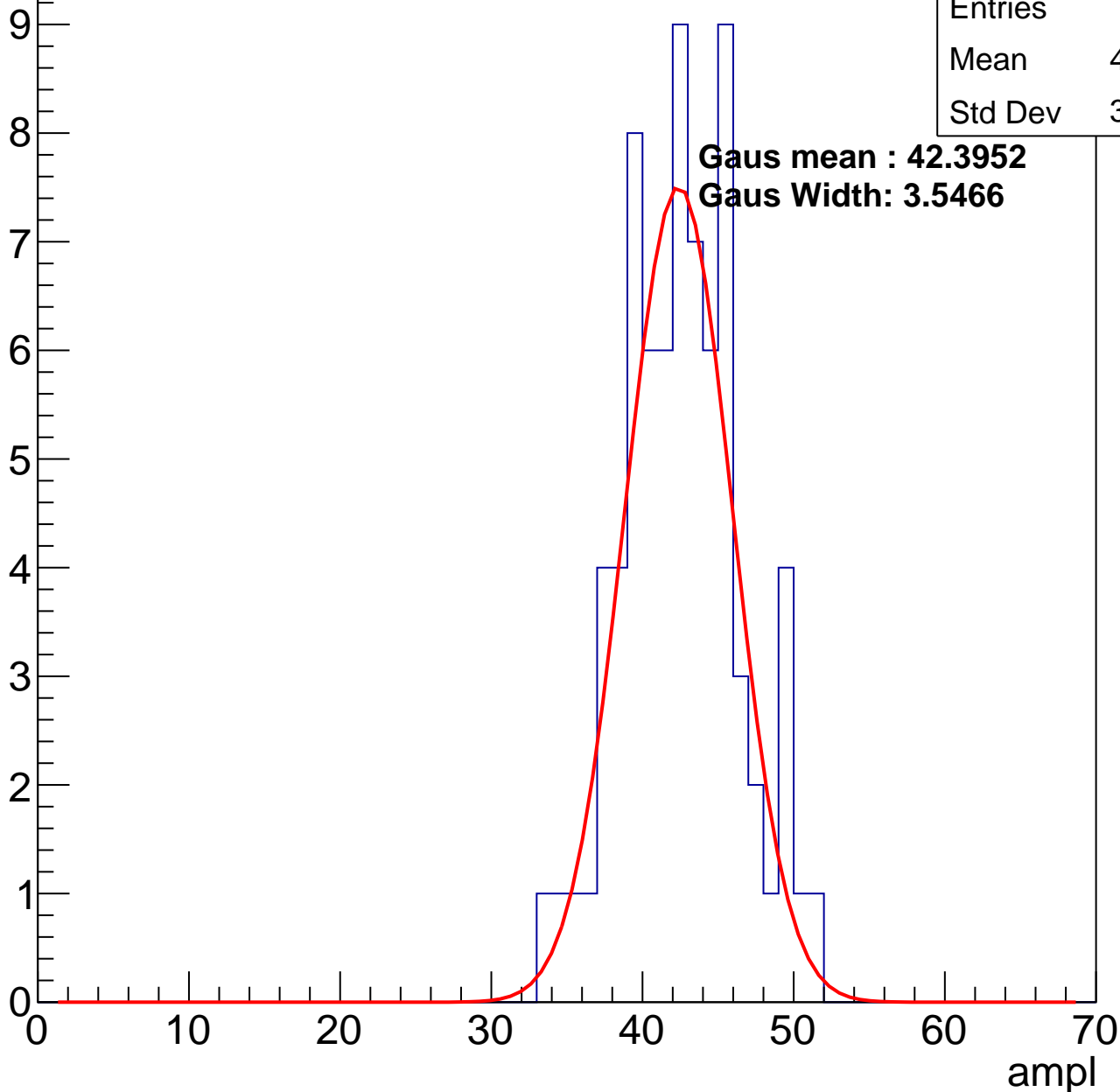
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	42.15
Std Dev	3.797

Gaus mean : 42.3952

Gaus Width: 3.5466

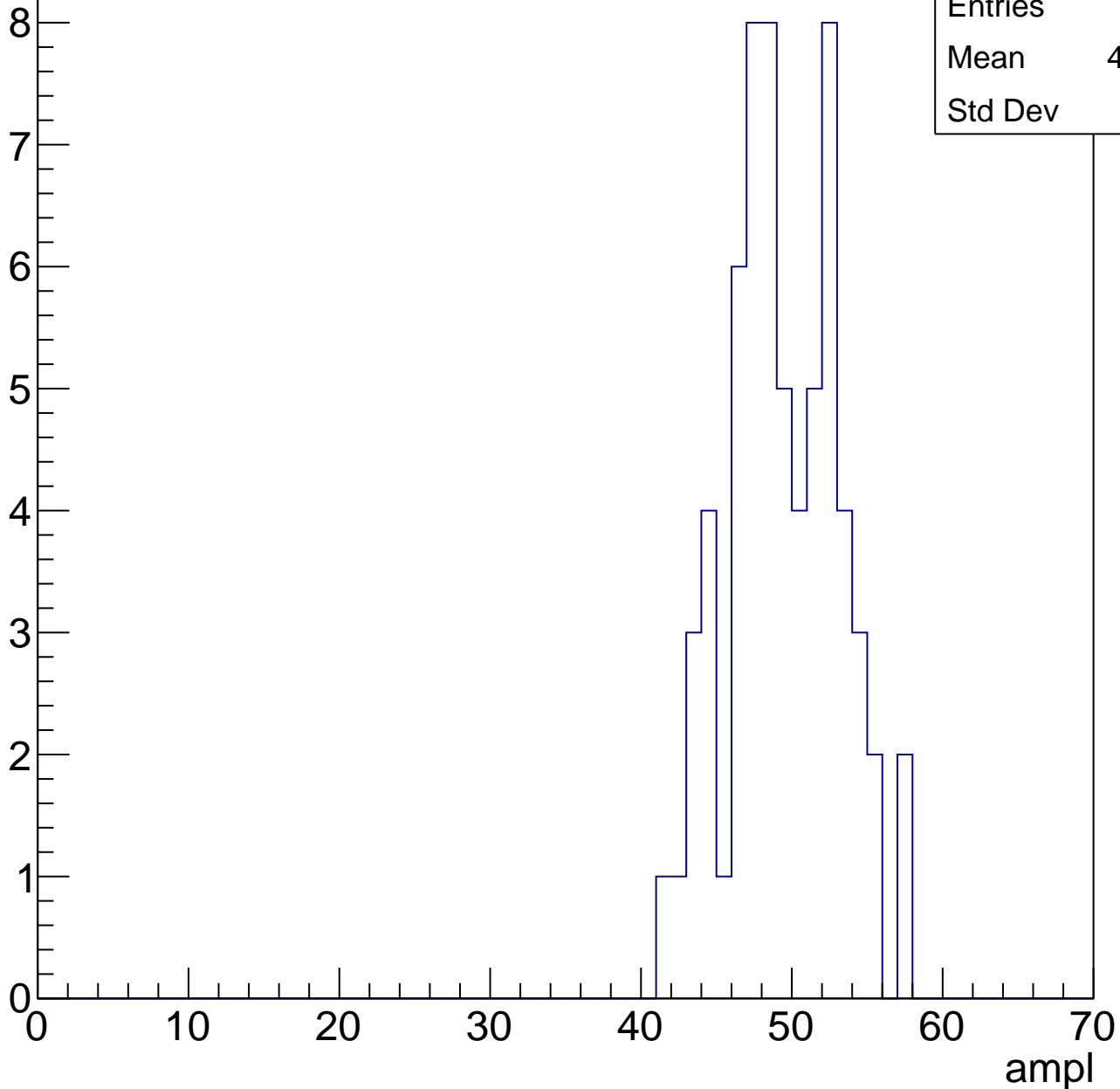


B0L001S, U15-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	48.97
Std Dev	3.65

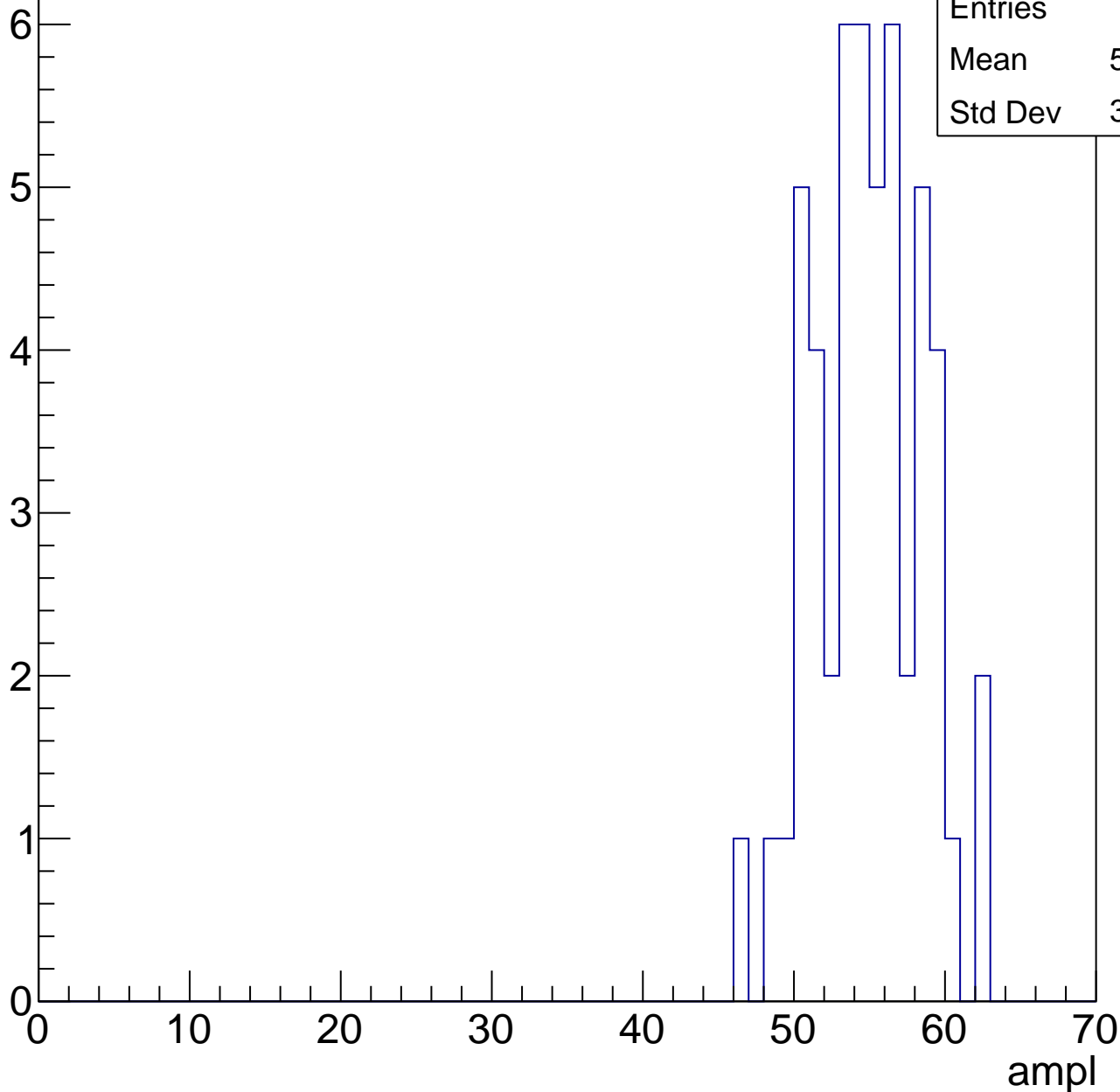


B0L001S, U15-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	54.47
Std Dev	3.528

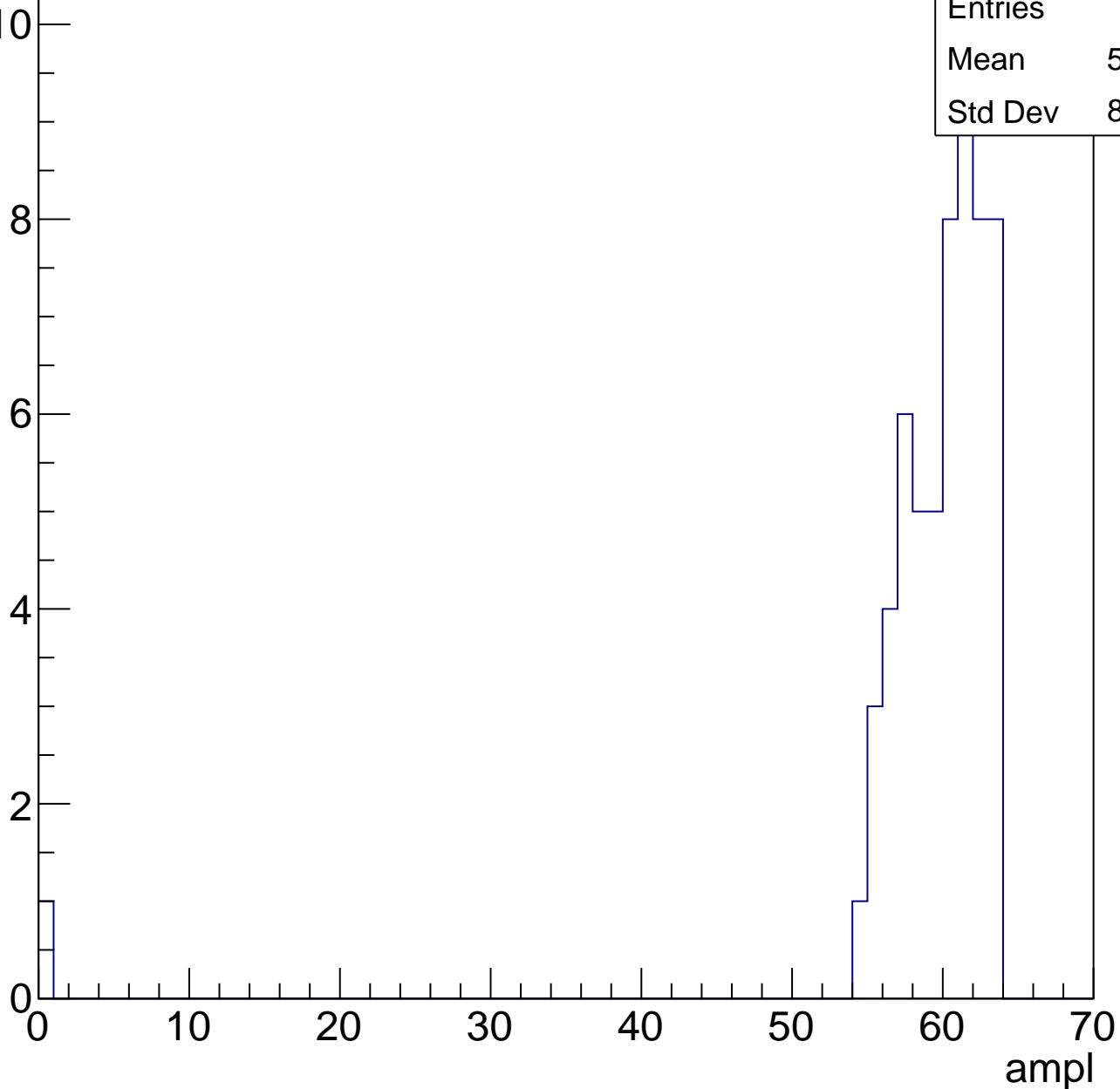


B0L001S, U15-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	58.64
Std Dev	8.088



B0L001S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.67
Std Dev	1.7

ampl

B0L001S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch109, adc0

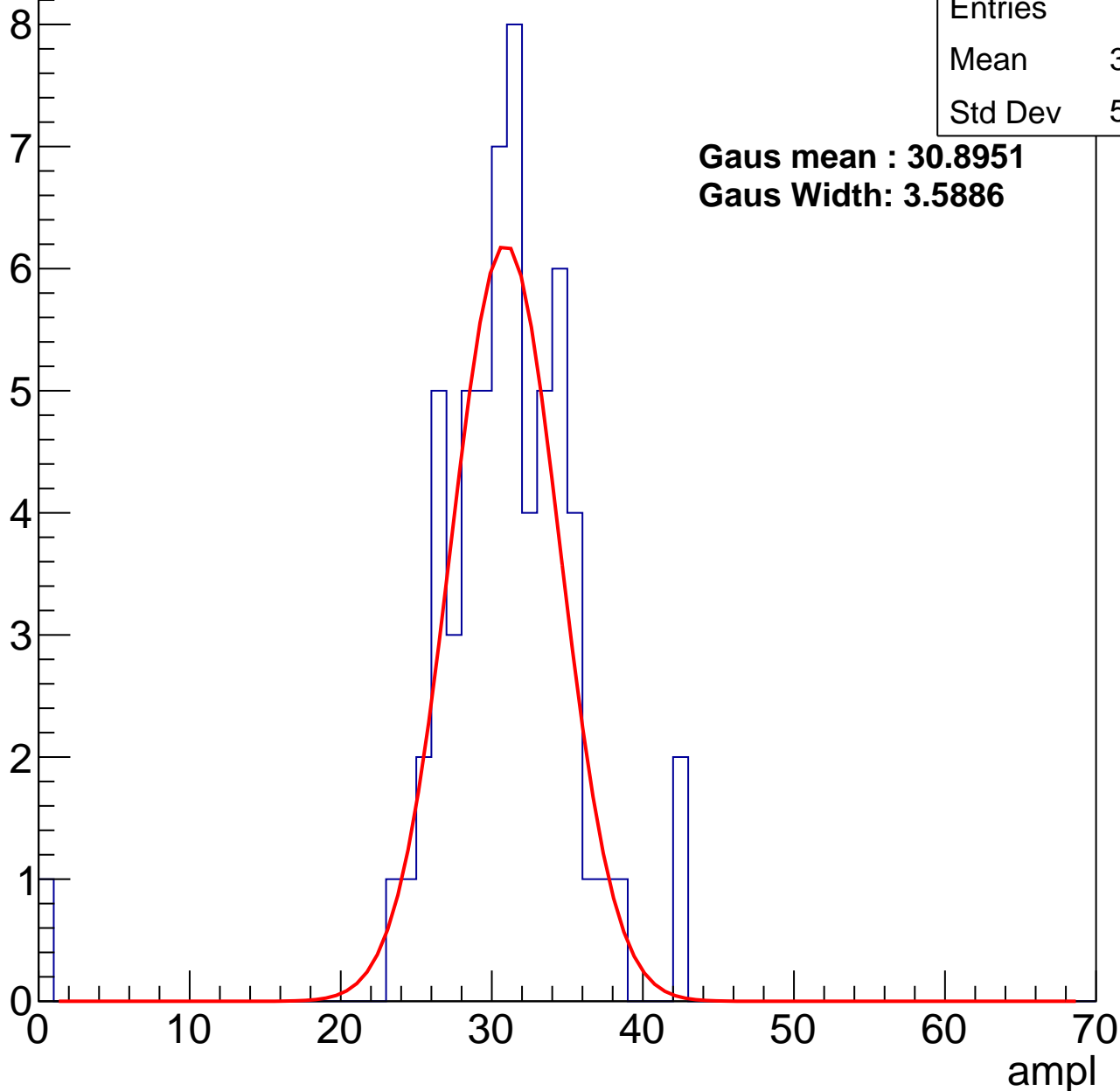
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.37
Std Dev	5.478

Gaus mean : 30.8951

Gaus Width: 3.5886



B0L001S, U15-ch109, adc1

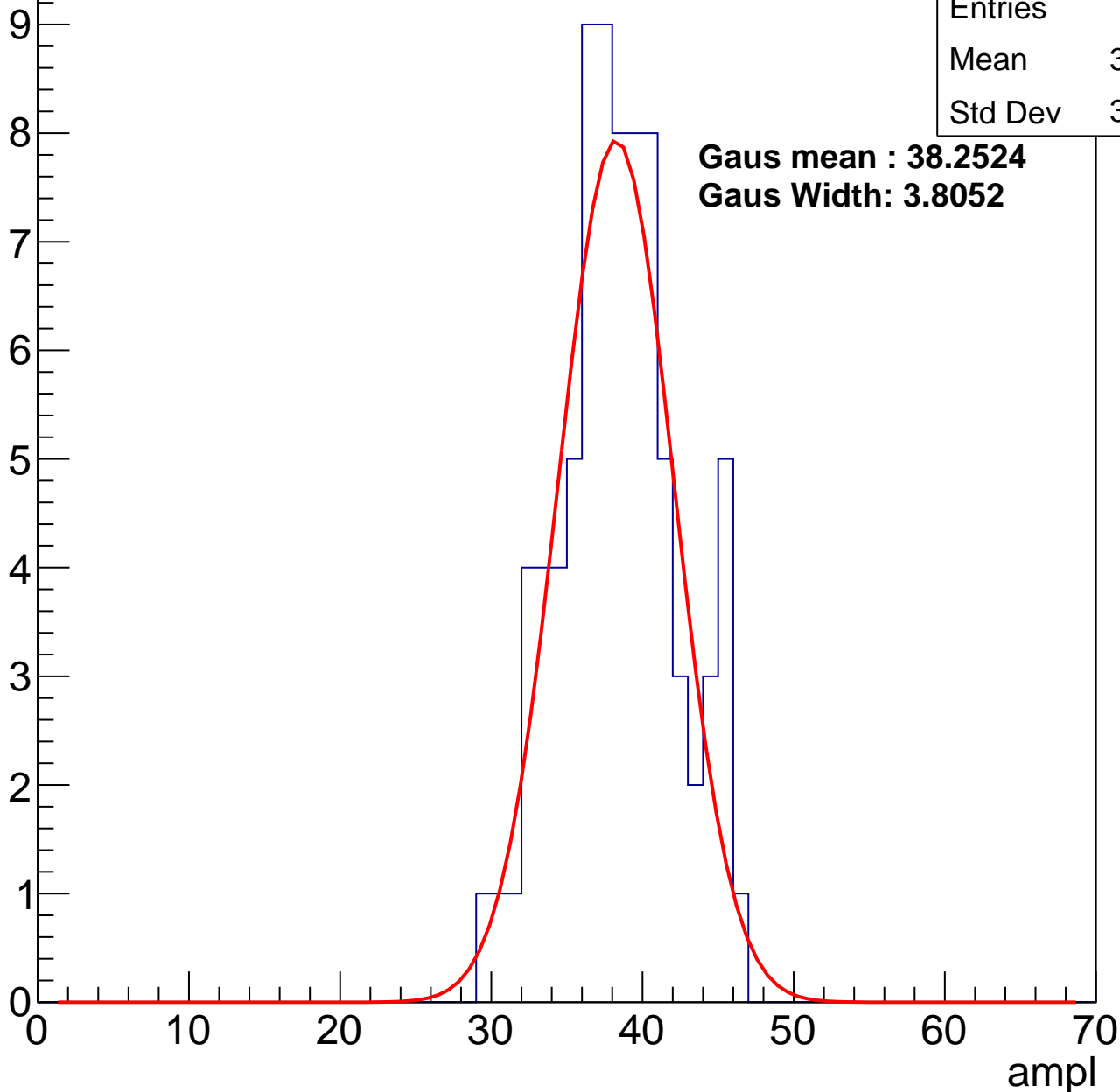
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.95
Std Dev	3.836

Gaus mean : 38.2524

Gaus Width: 3.8052



B0L001S, U15-ch109, adc2

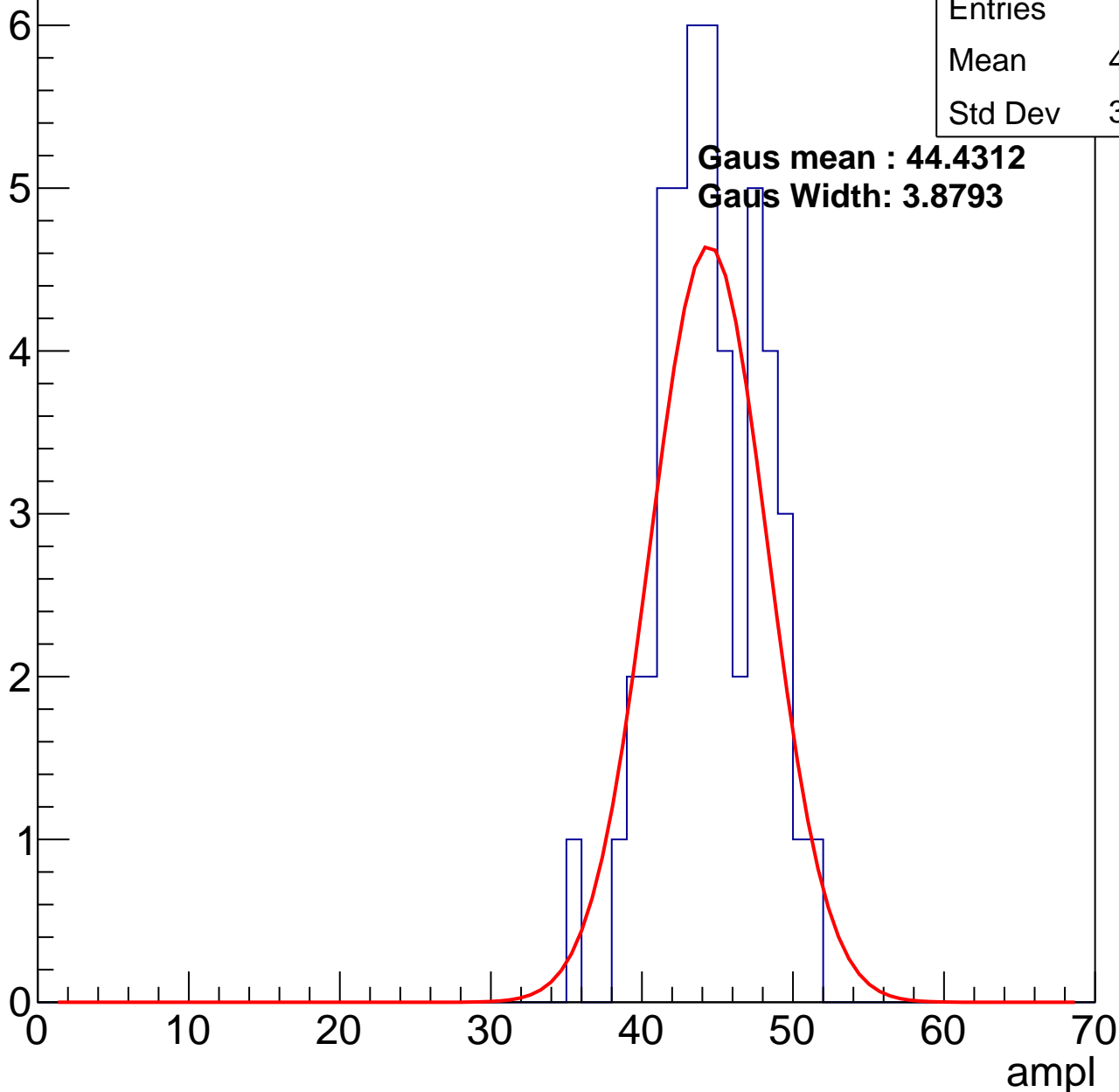
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	44.06
Std Dev	3.388

Gaus mean : 44.4312

Gaus Width: 3.8793

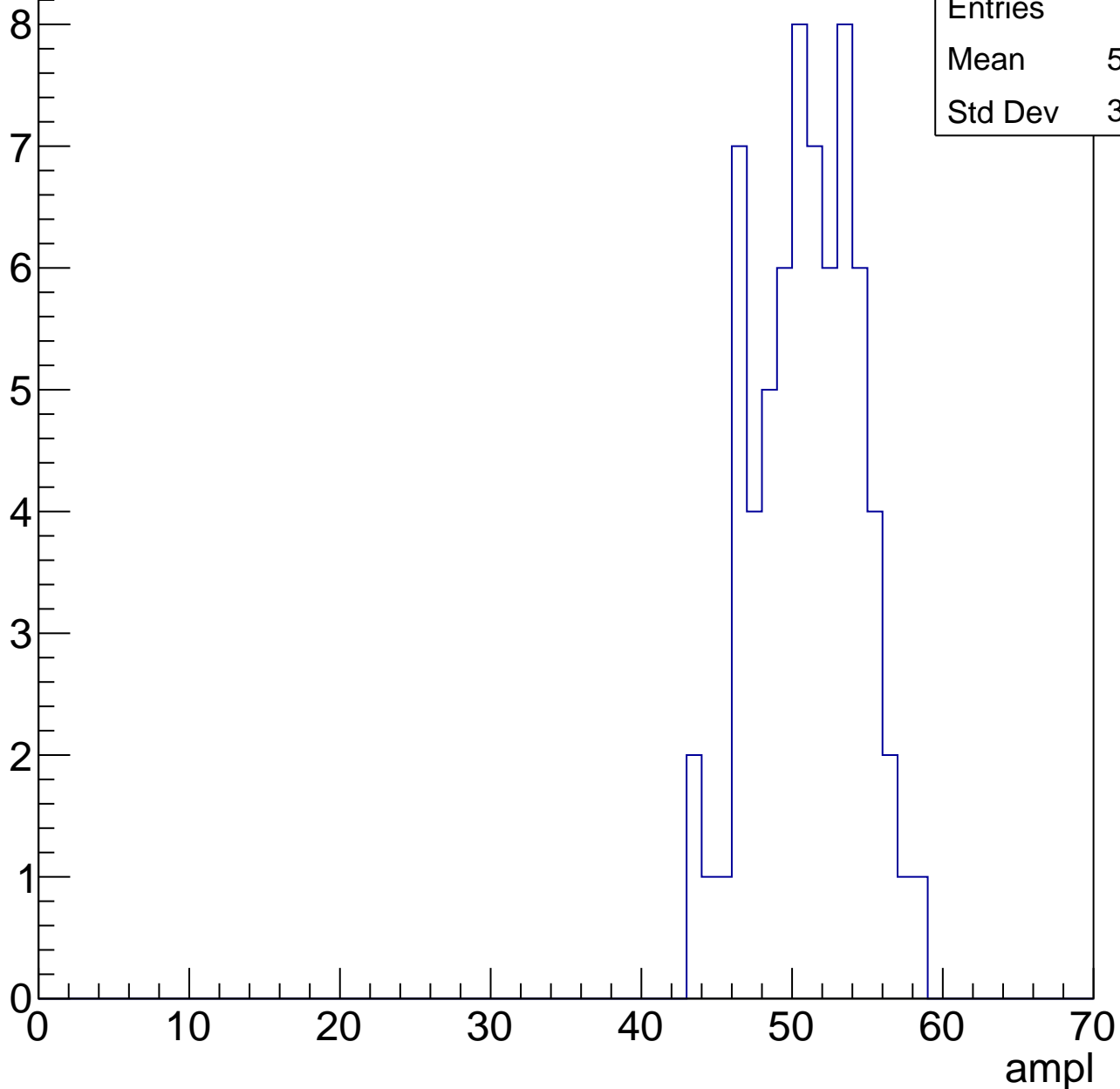


B0L001S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.48
Std Dev	3.408

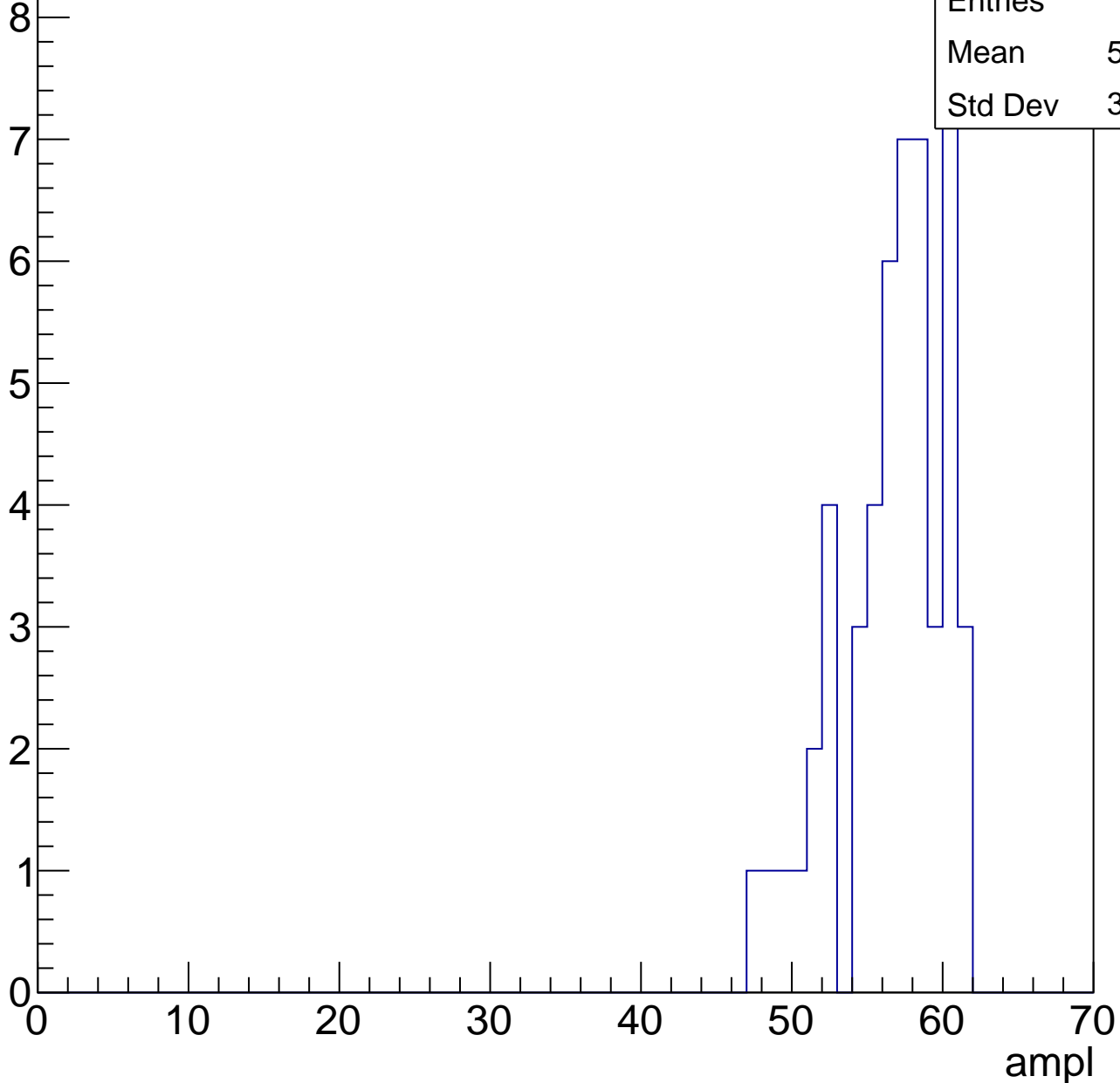


B0L001S, U15-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

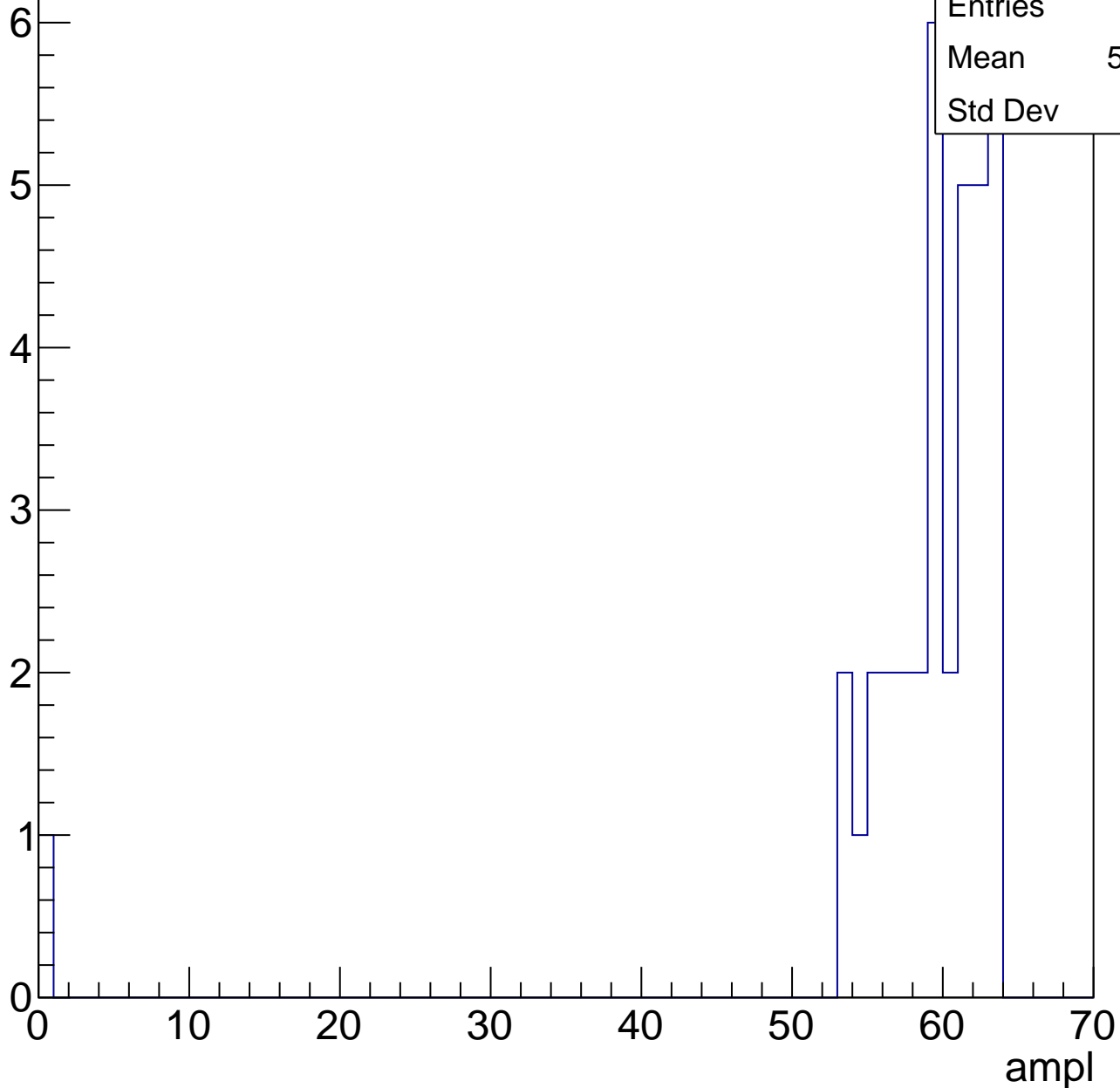
Entries	51
Mean	56.22
Std Dev	3.494



B0L001S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

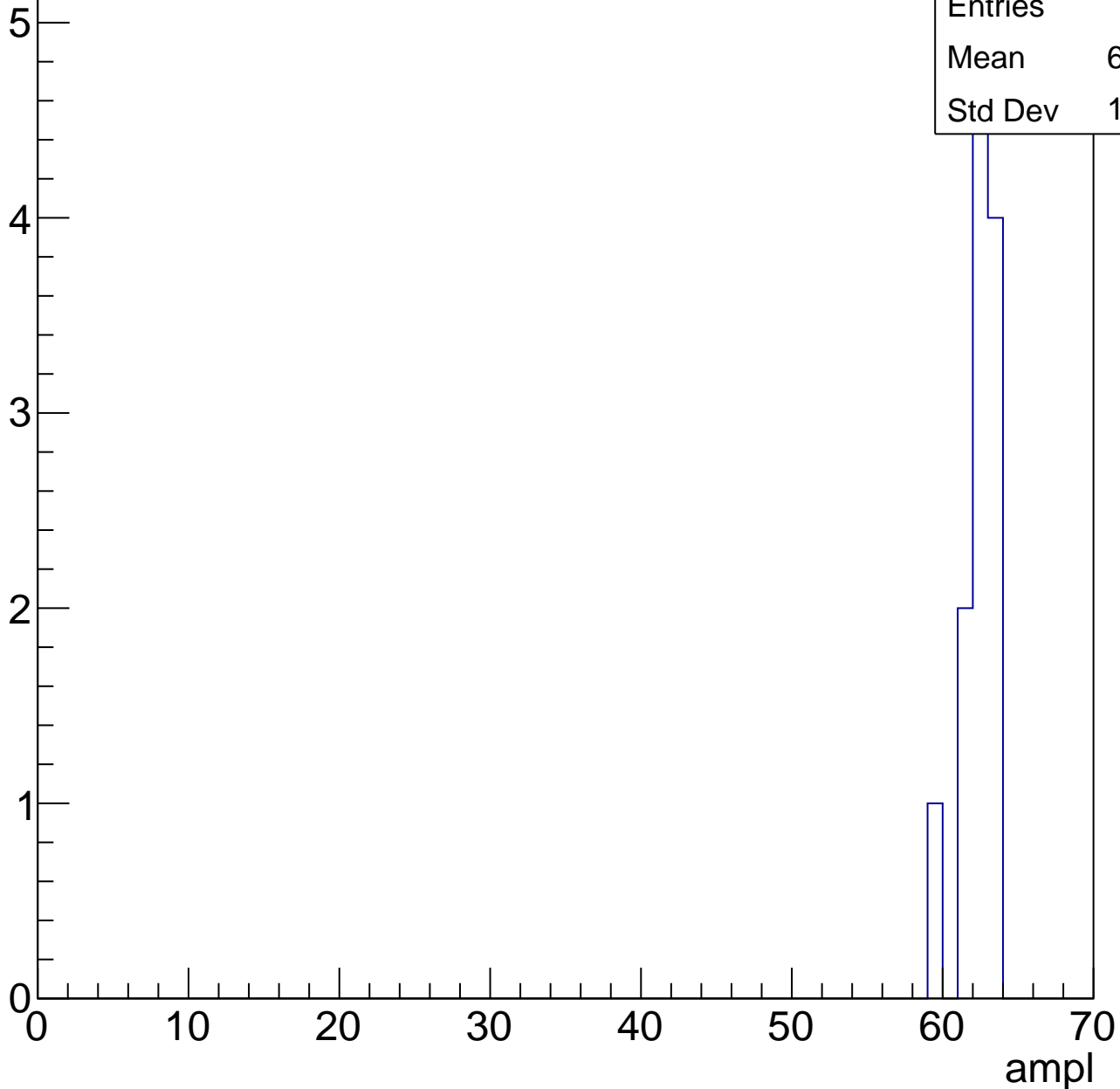


B0L001S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	12
Mean	61.92
Std Dev	1.115



B0L001S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch110, adc0

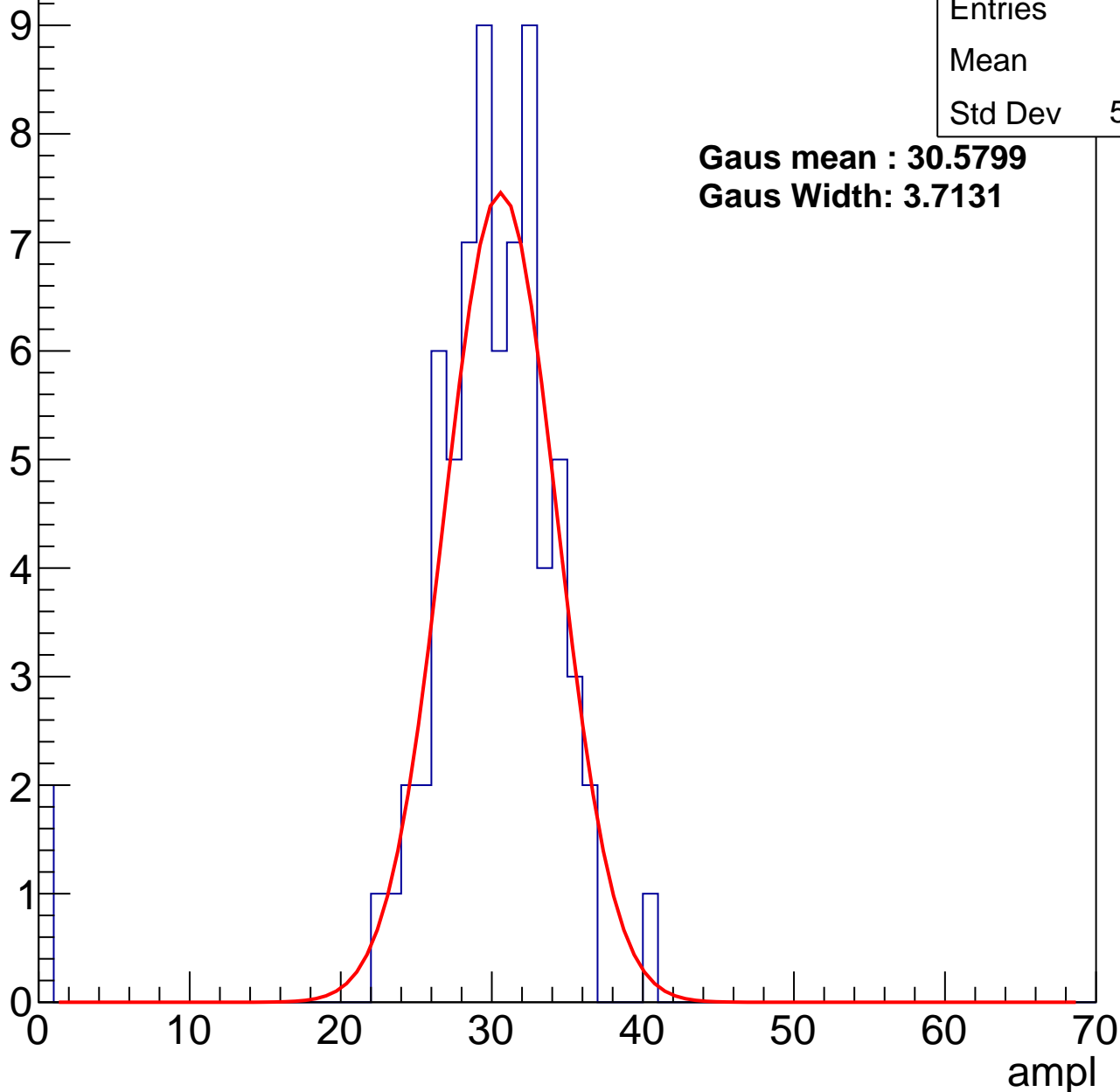
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.1
Std Dev	5.963

Gaus mean : 30.5799

Gaus Width: 3.7131



B0L001S, U15-ch110, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	37.18
Std Dev	4.025

Gaus mean : 37.3846

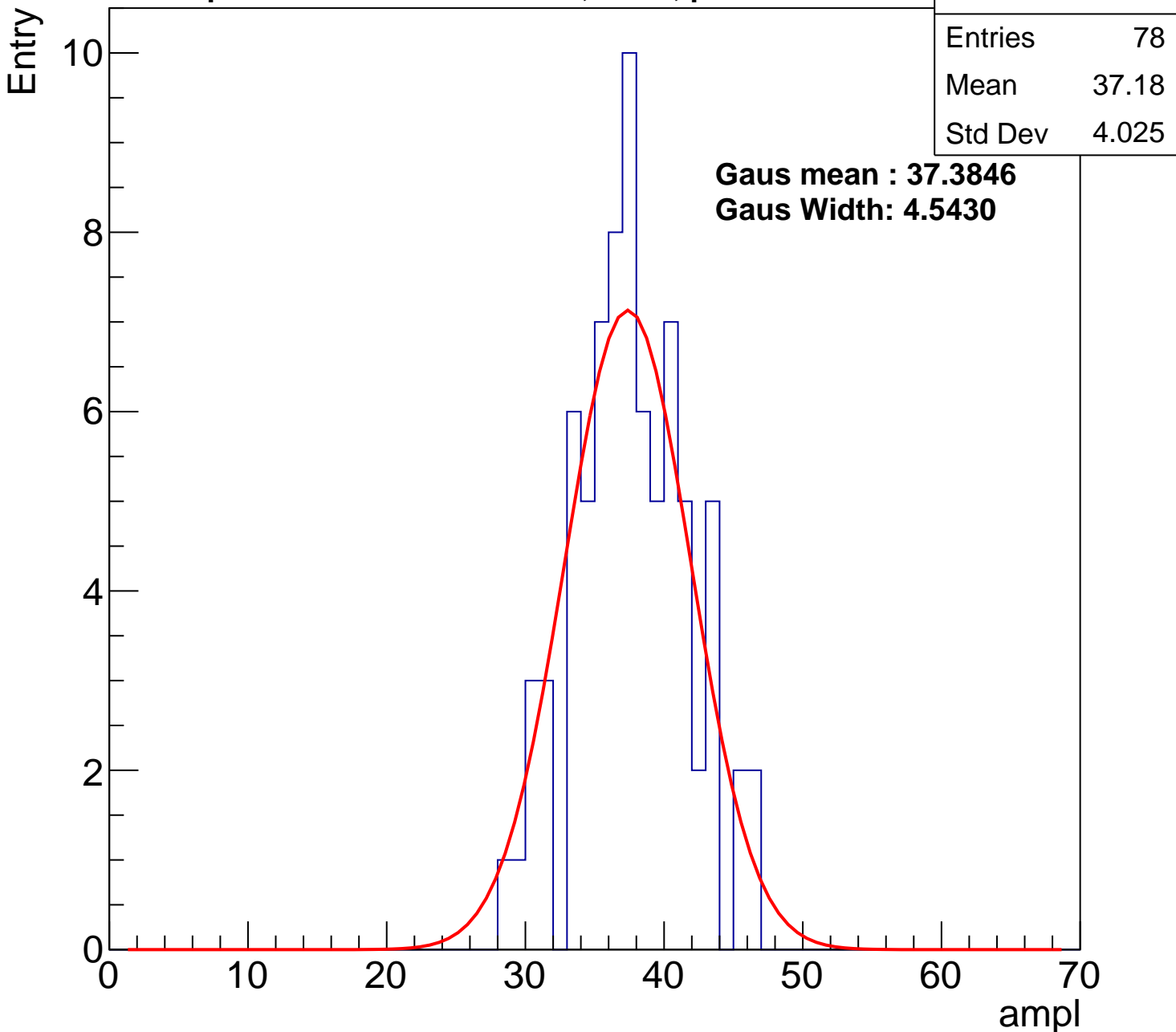
Gaus Width: 4.5430

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch110, adc2

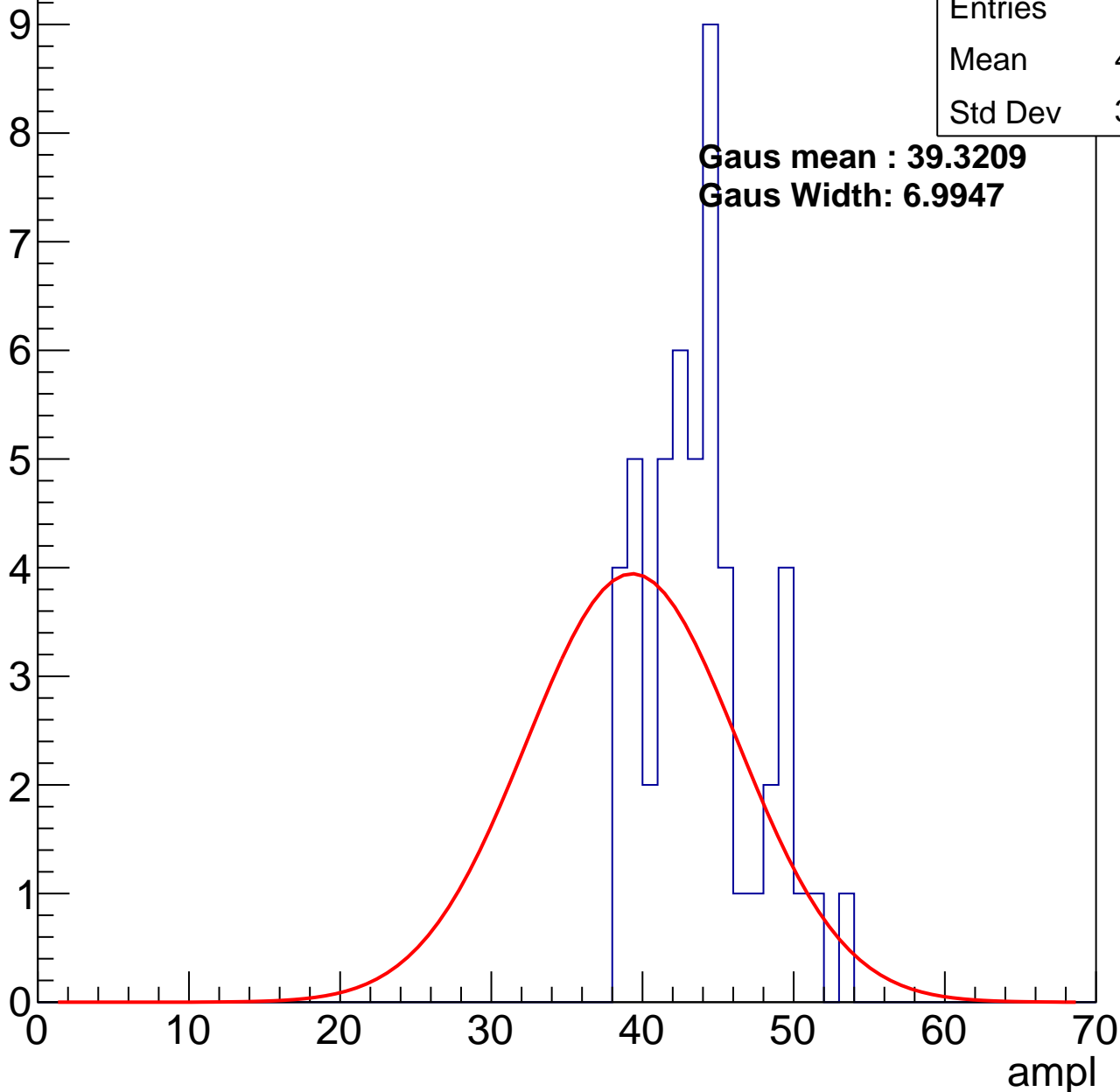
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	43.41
Std Dev	3.631

Gaus mean : 39.3209

Gaus Width: 6.9947

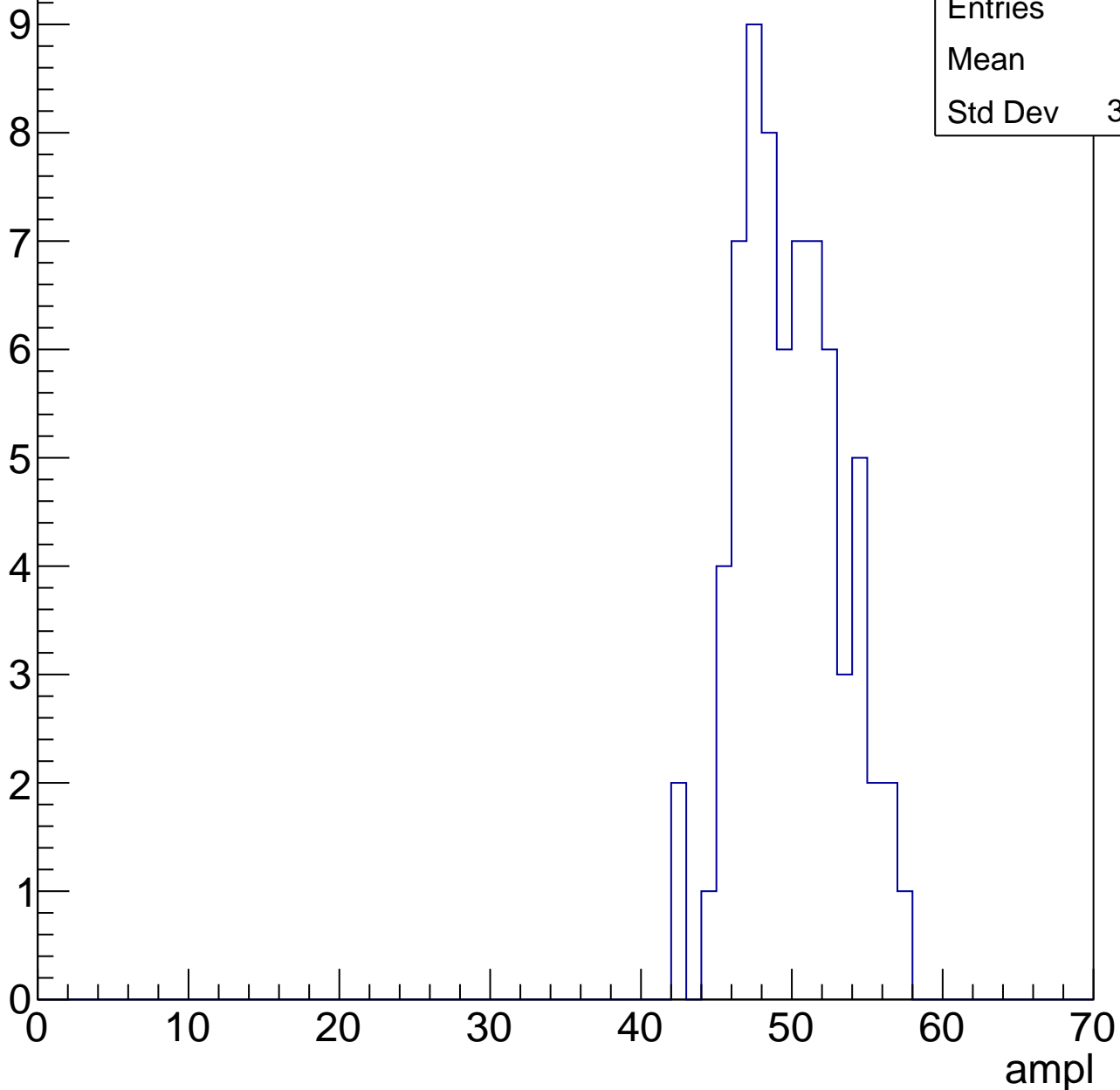


B0L001S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.4
Std Dev	3.336



B0L001S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6
5
4
3
2
1
0

Entries	49
Mean	55.71
Std Dev	3.057

ampl

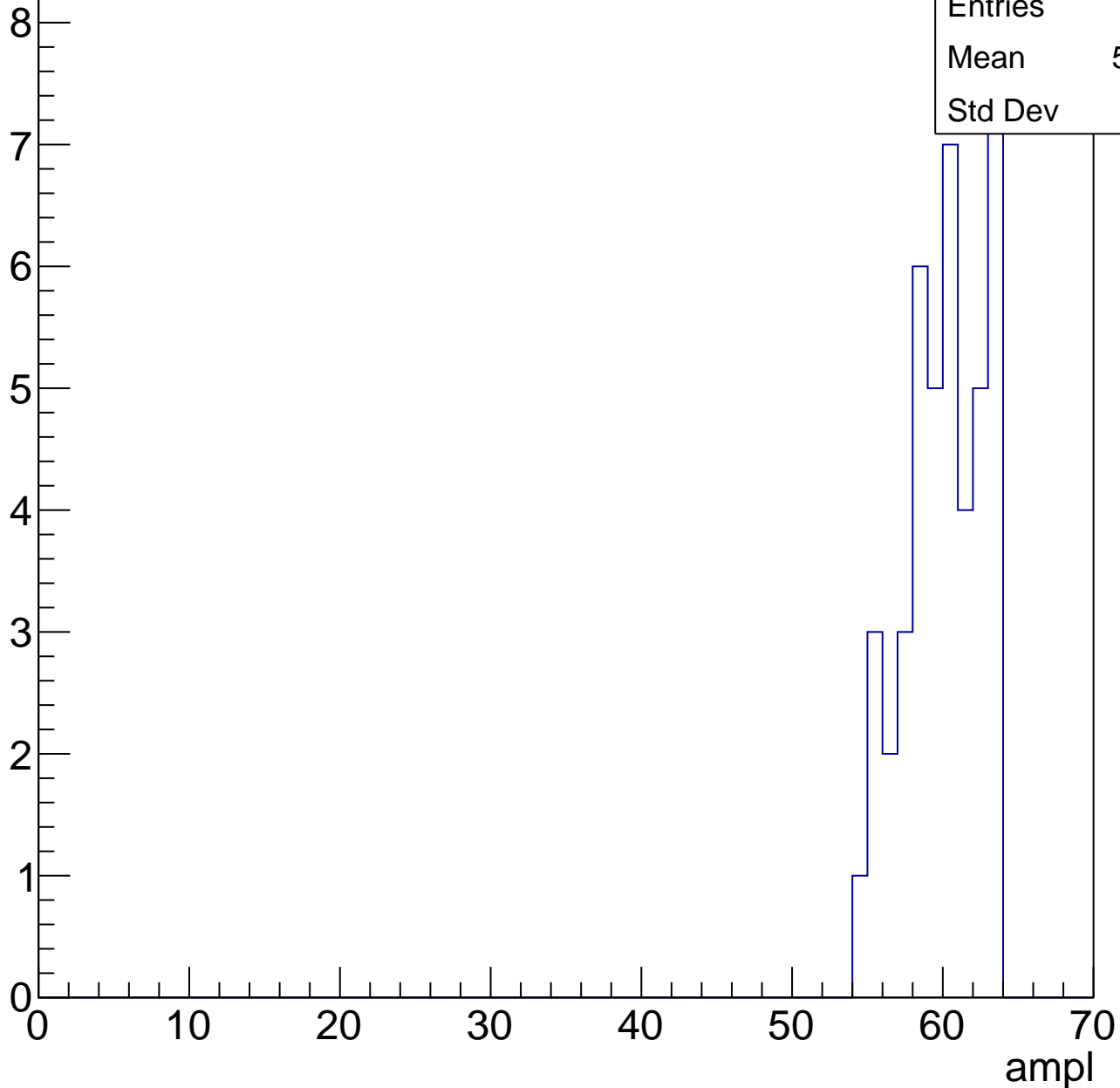
0 10 20 30 40 50 60 70

B0L001S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

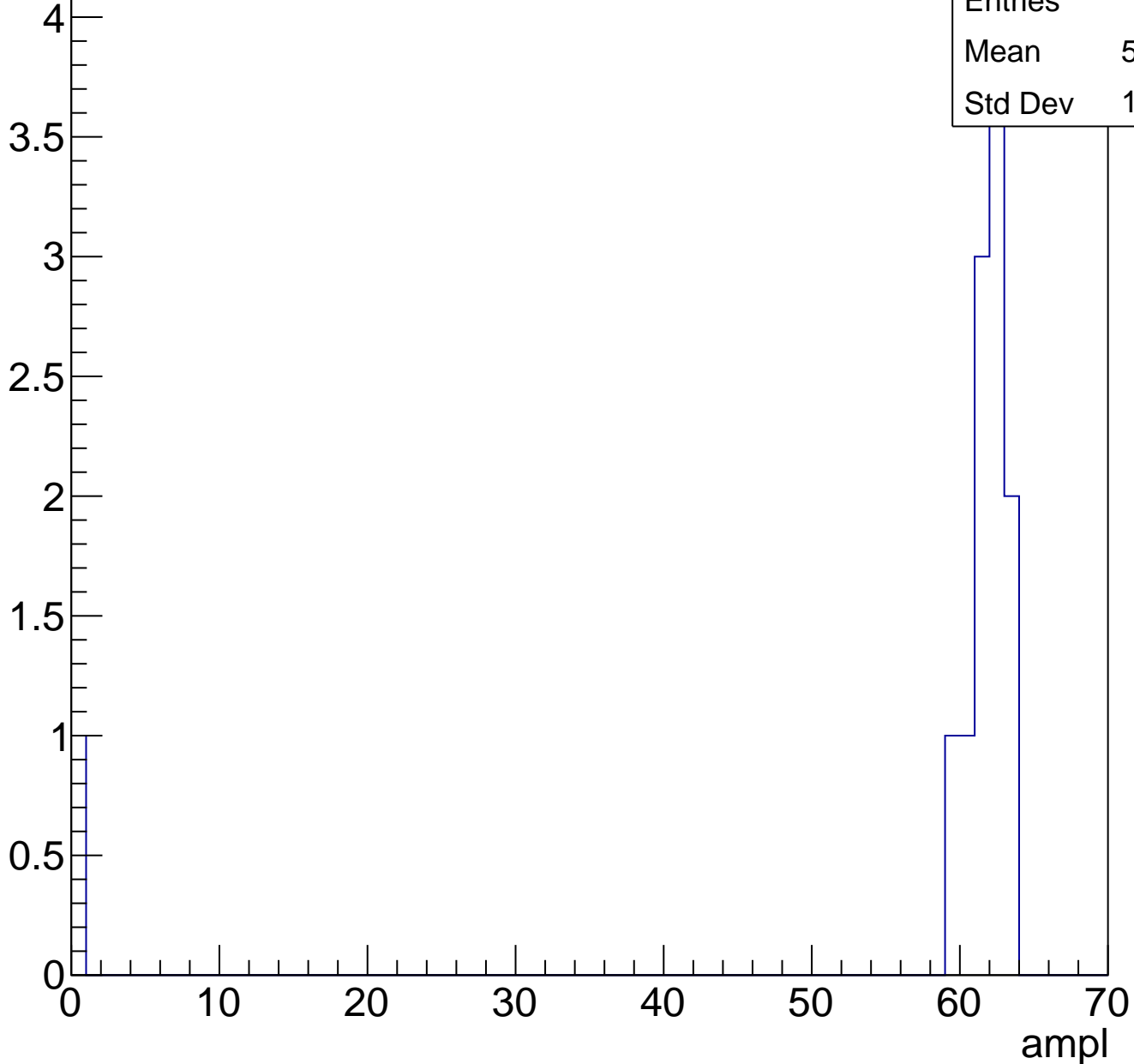
Entries	44
Mean	59.61
Std Dev	2.56



B0L001S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

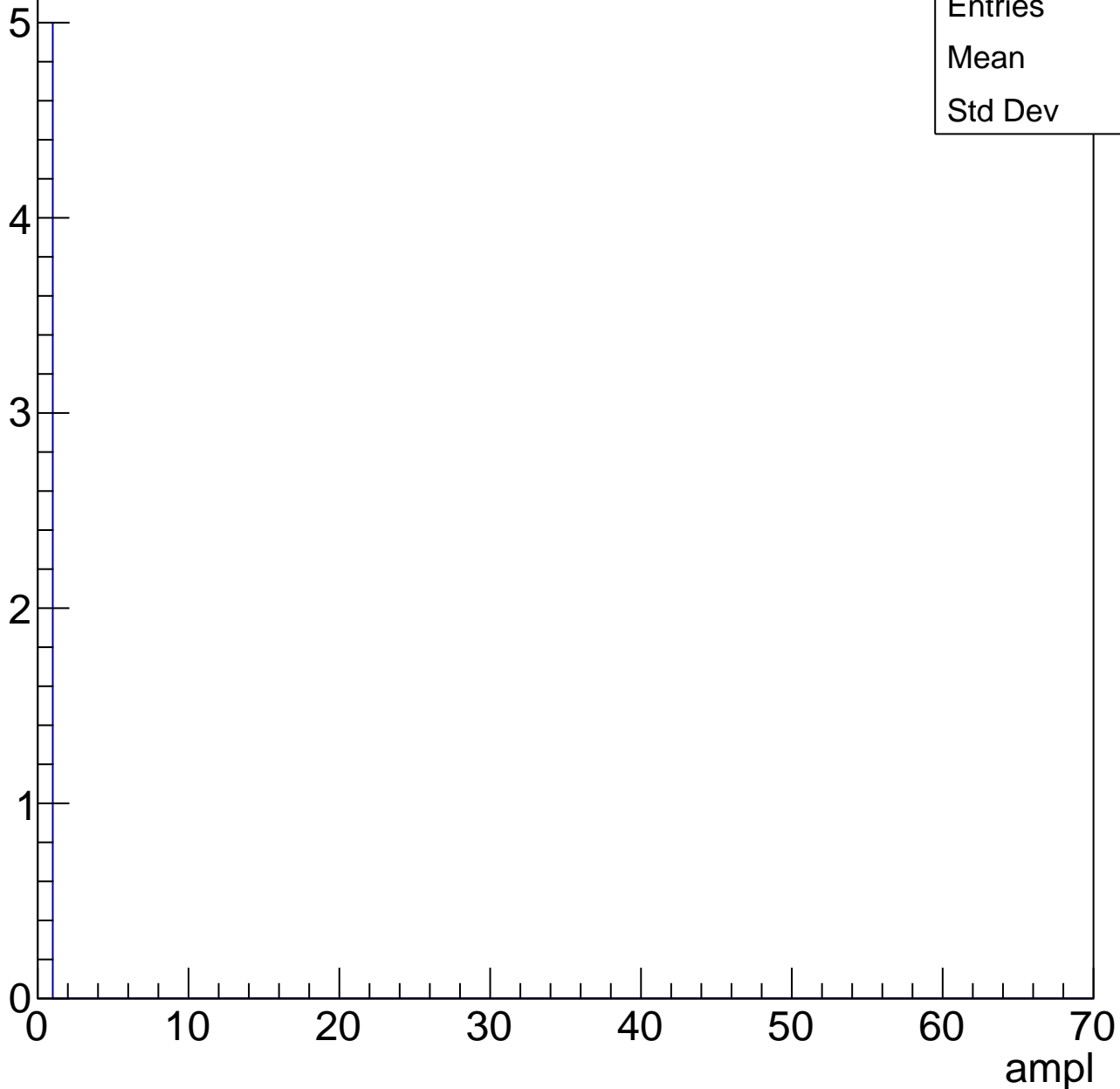


B0L001S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	5
Mean	0
Std Dev	0



B0L001S, U15-ch111, adc0

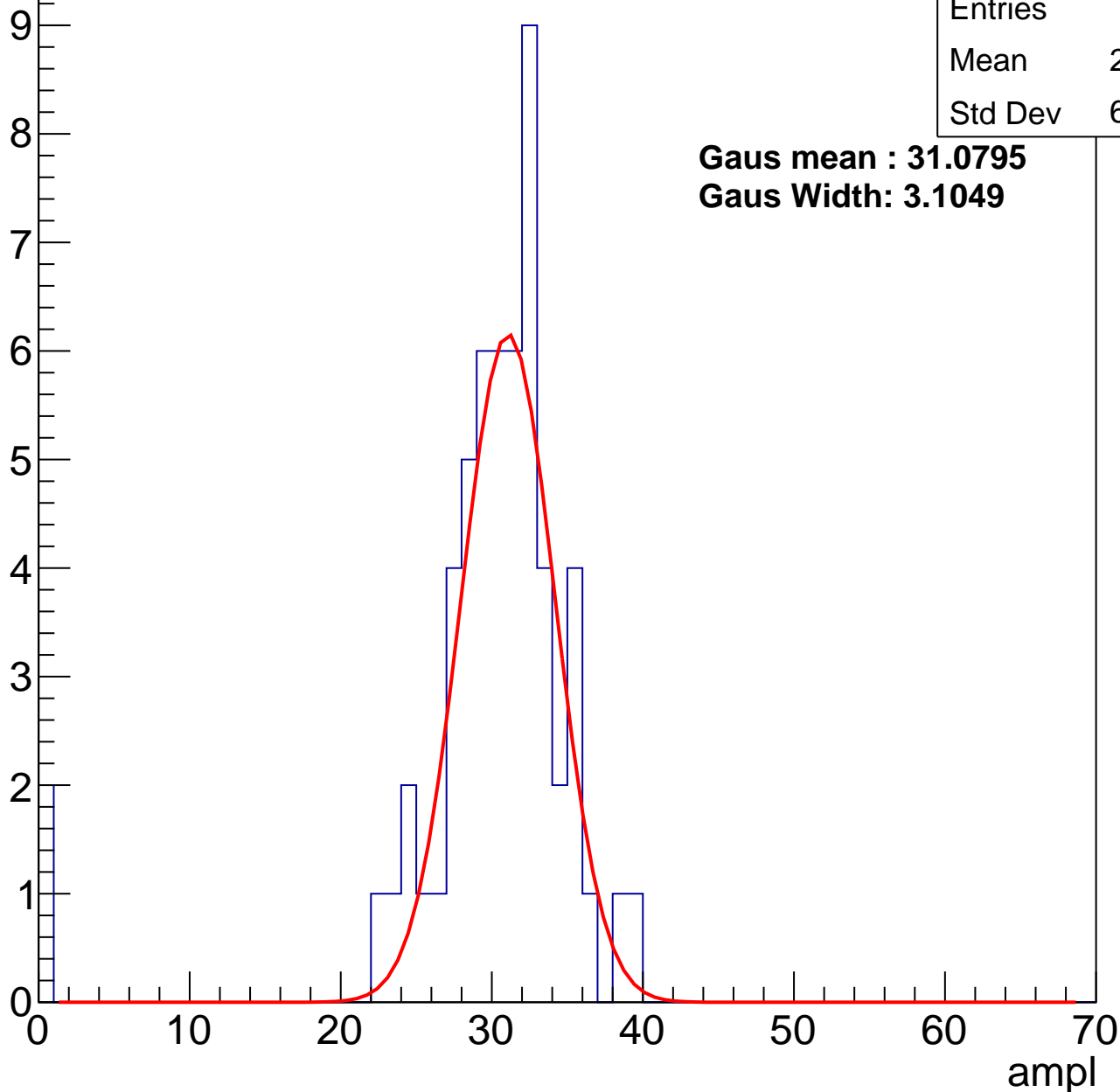
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.35
Std Dev	6.563

Gaus mean : 31.0795

Gaus Width: 3.1049



B0L001S, U15-ch111, adc1

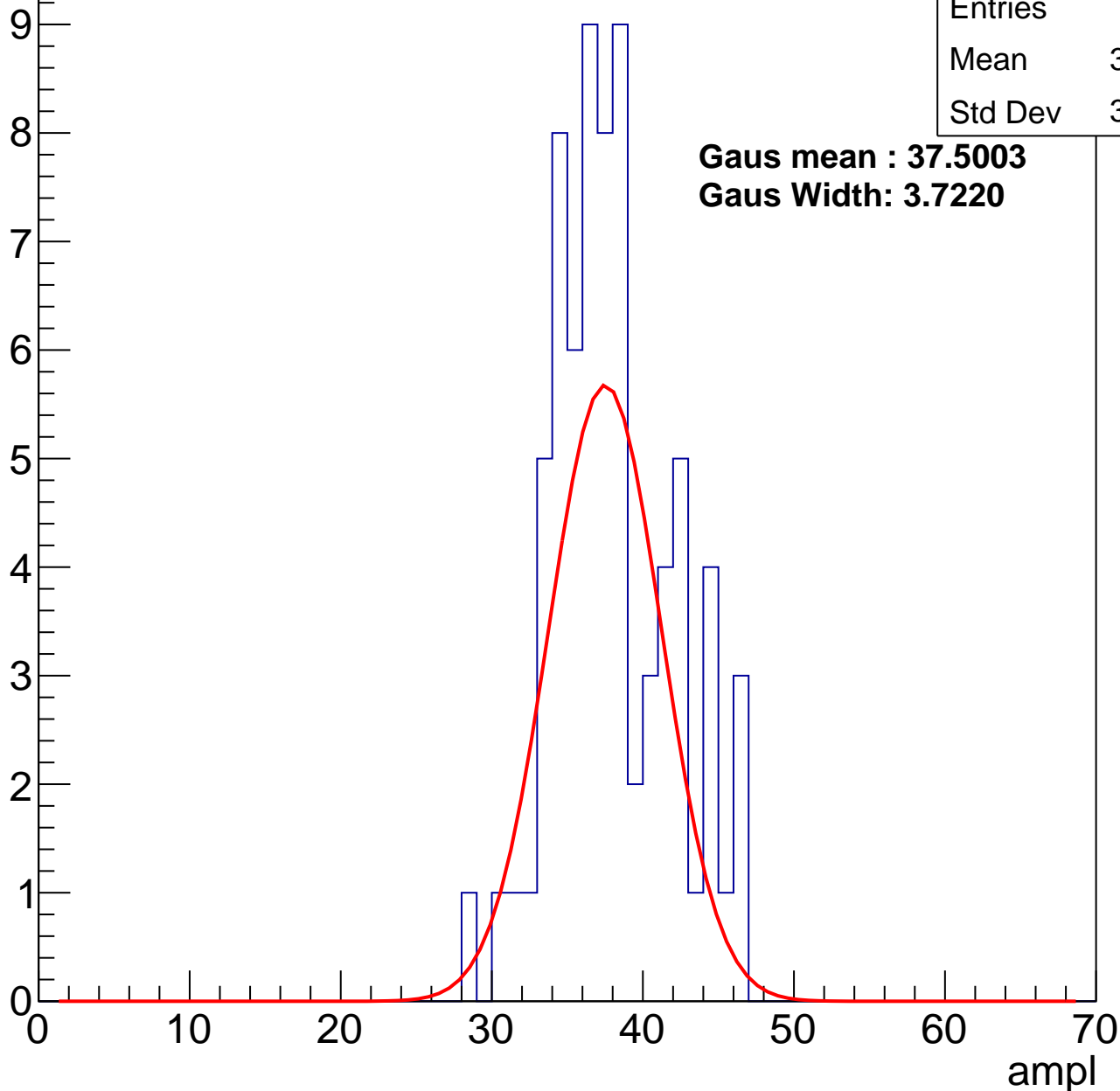
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.56
Std Dev	3.954

Gaus mean : 37.5003

Gaus Width: 3.7220



B0L001S, U15-ch111, adc2

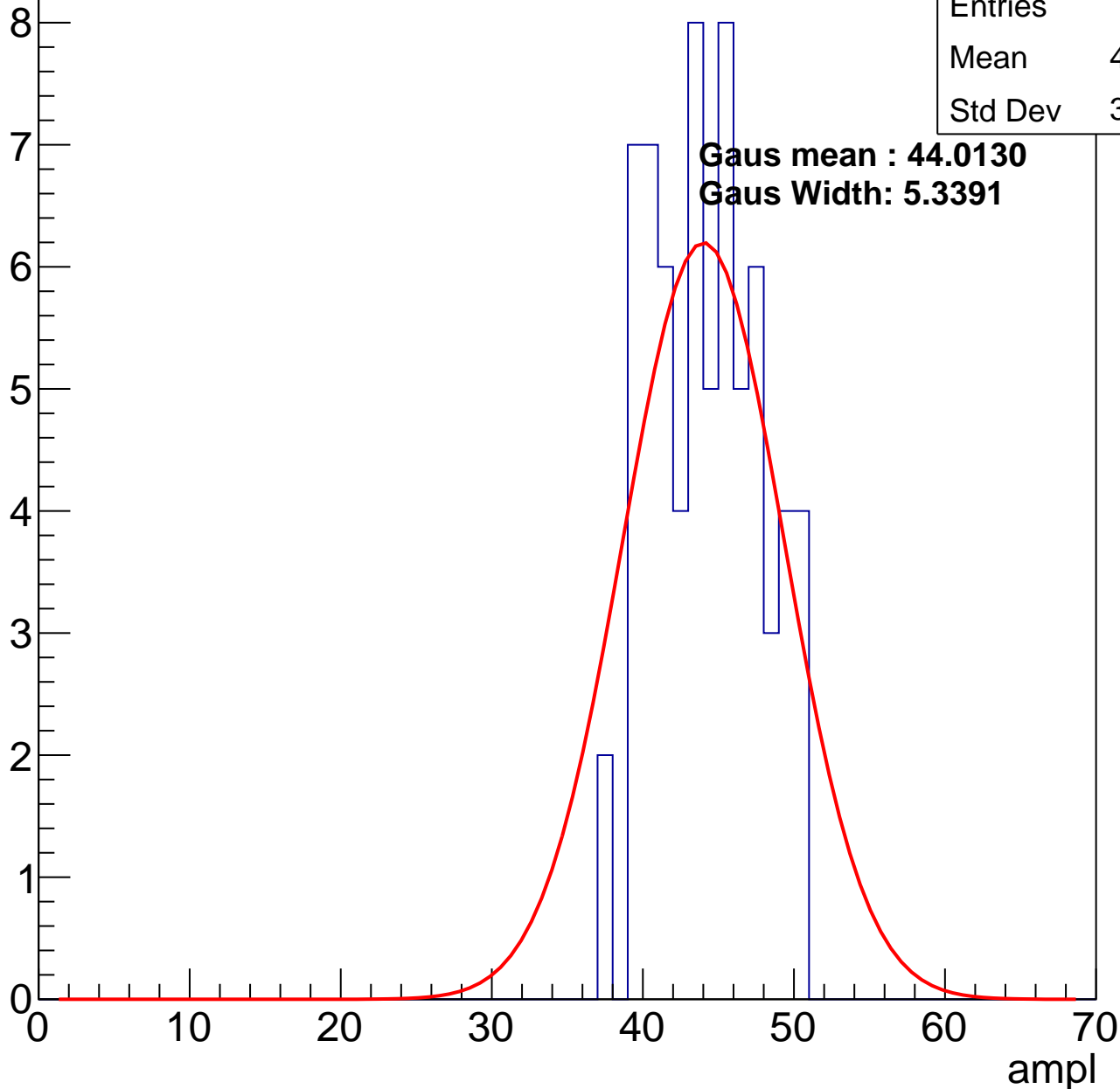
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.72
Std Dev	3.472

Gaus mean : 44.0130

Gaus Width: 5.3391

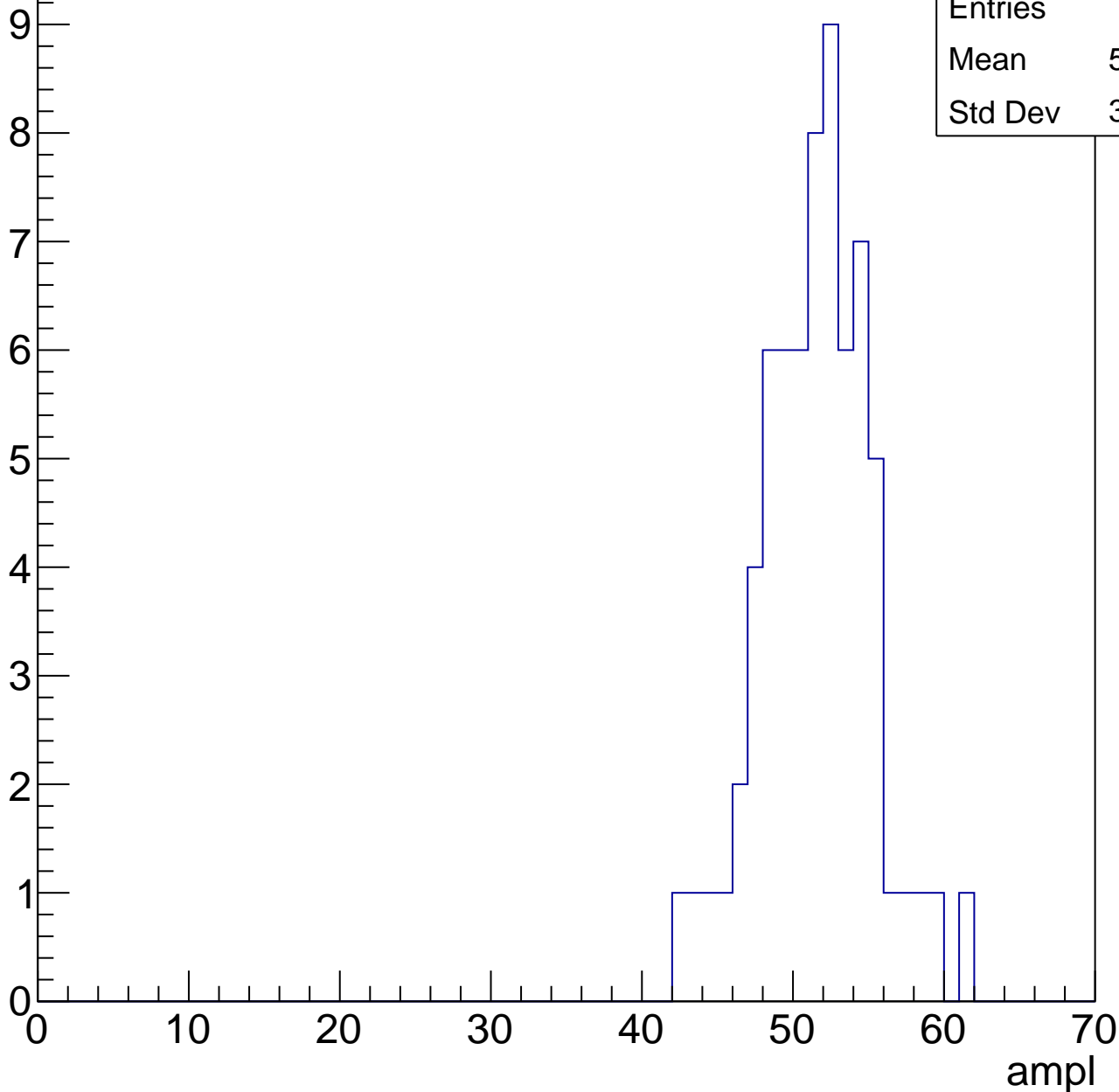


B0L001S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.09
Std Dev	3.592

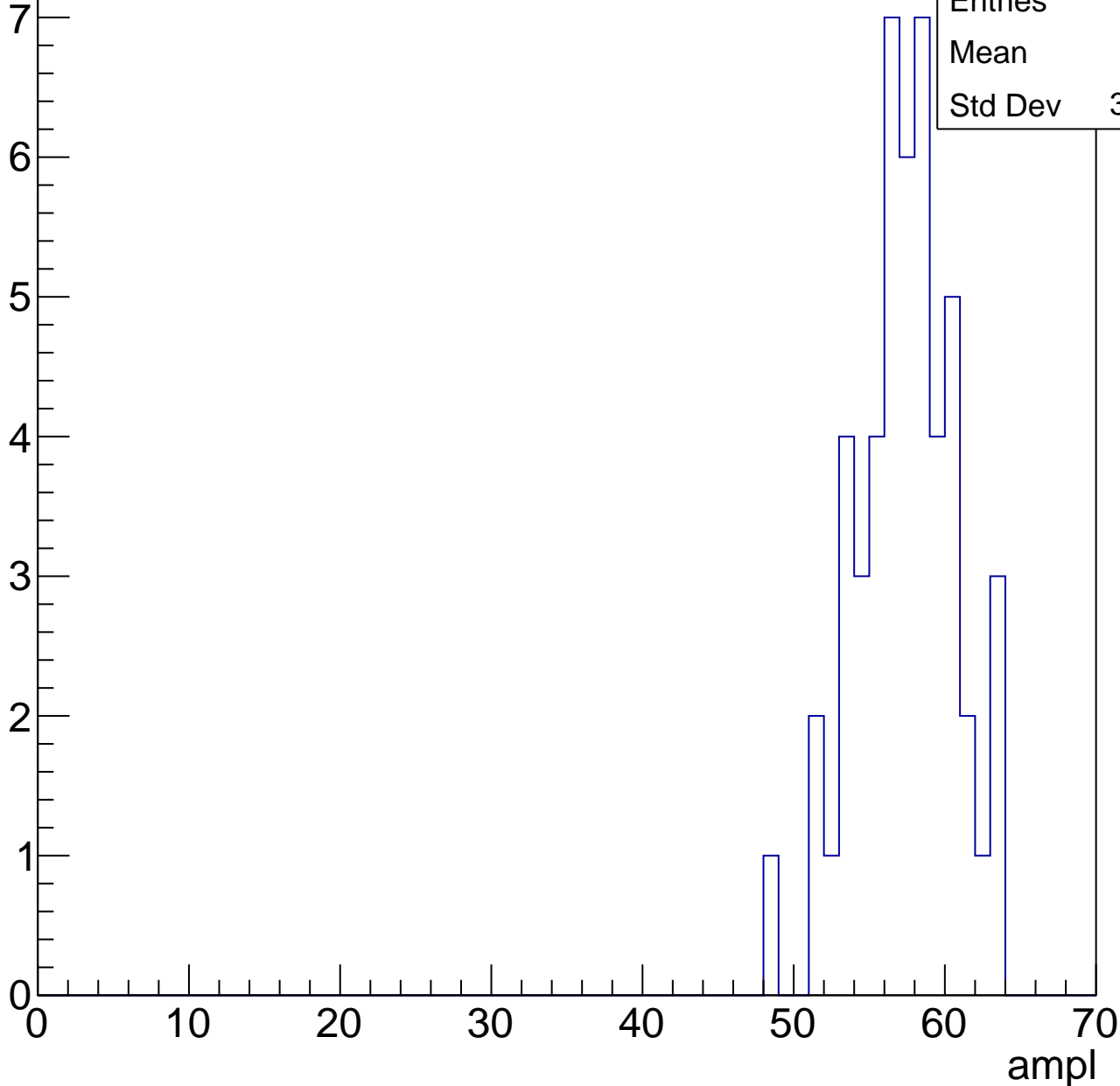


B0L001S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.9
Std Dev	3.239

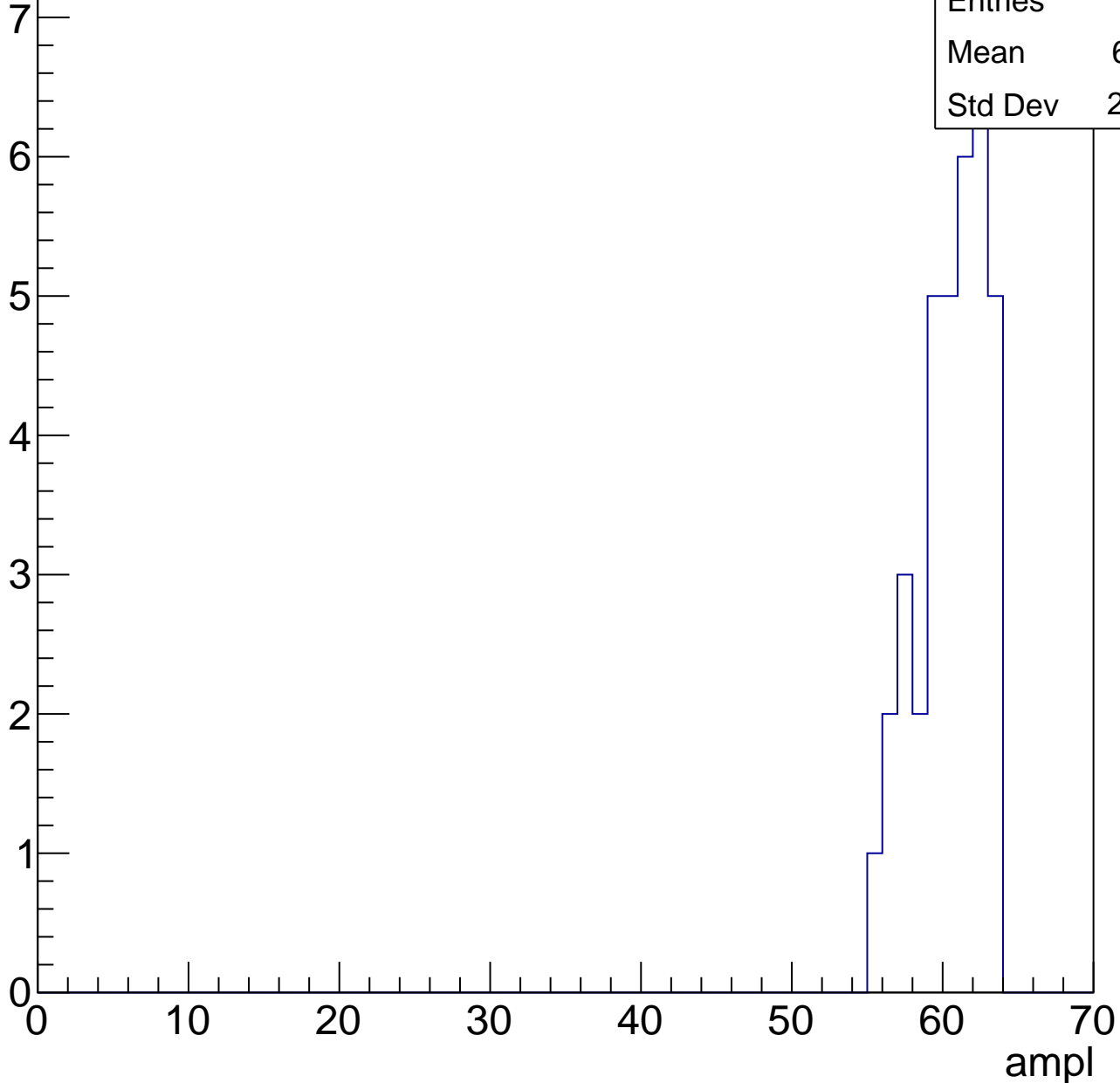


B0L001S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	60.11
Std Dev	2.208



B0L001S, U15-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

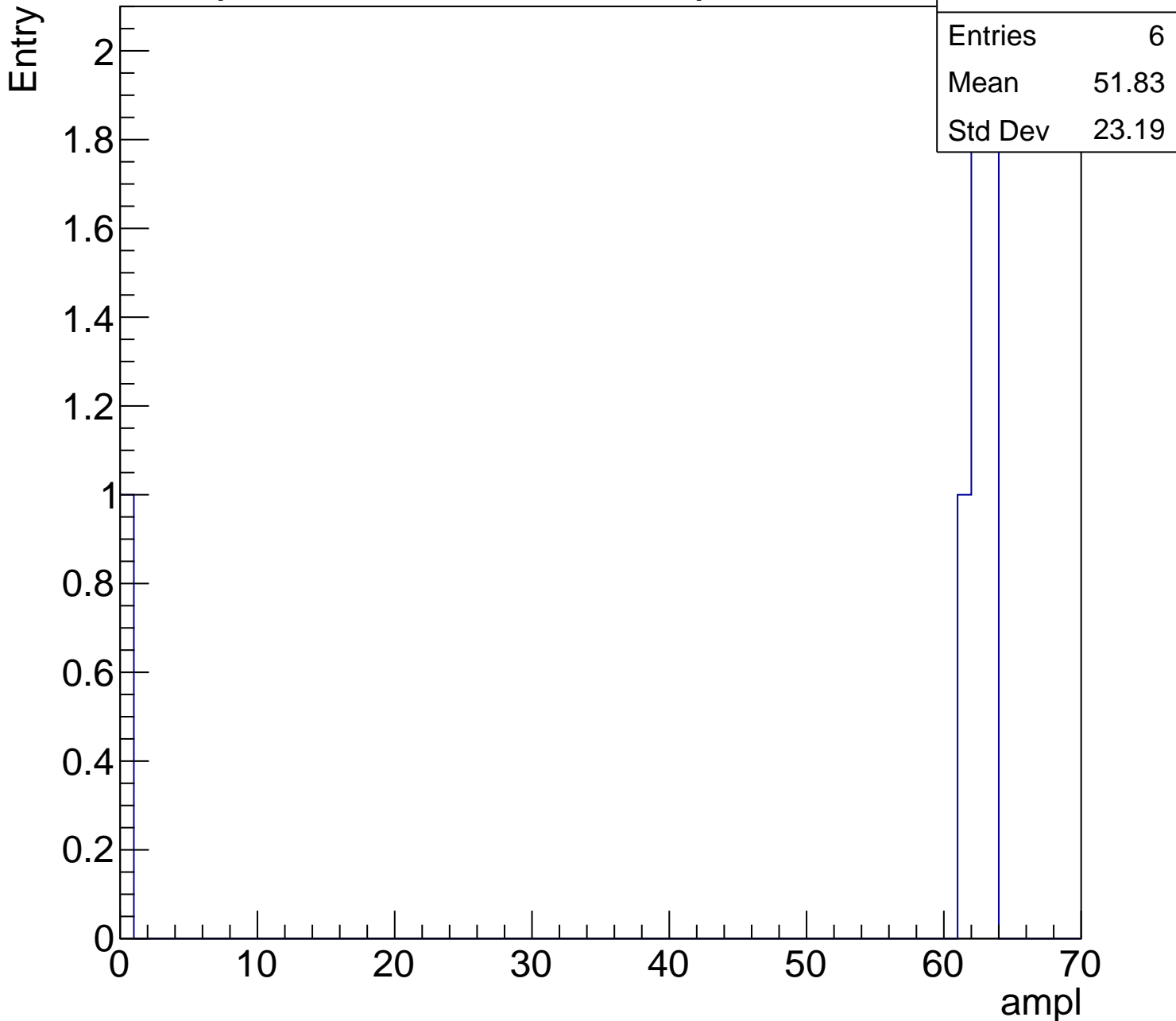
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U15-ch112, adc0

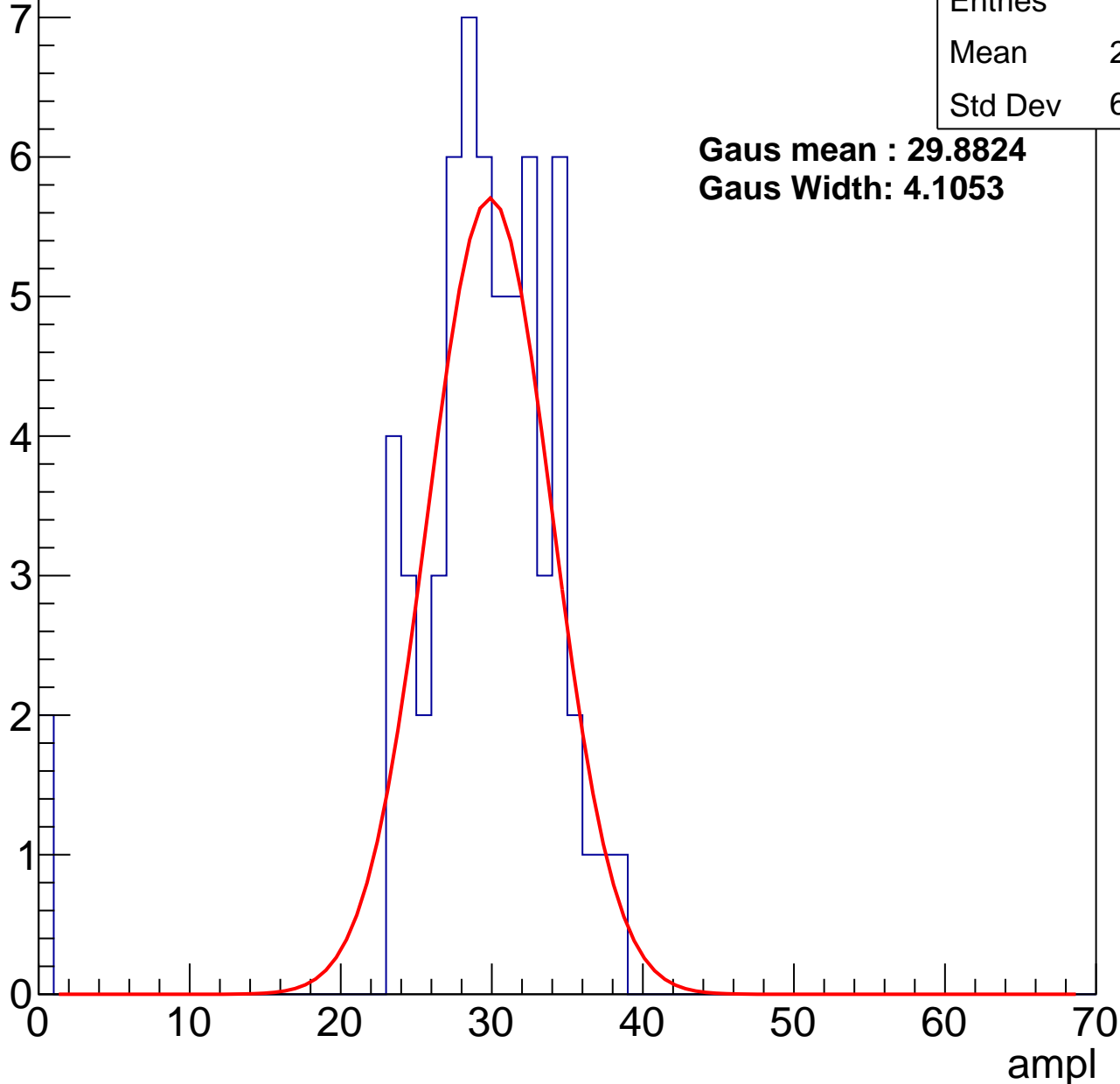
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	28.65
Std Dev	6.332

Gaus mean : 29.8824

Gaus Width: 4.1053



B0L001S, U15-ch112, adc1

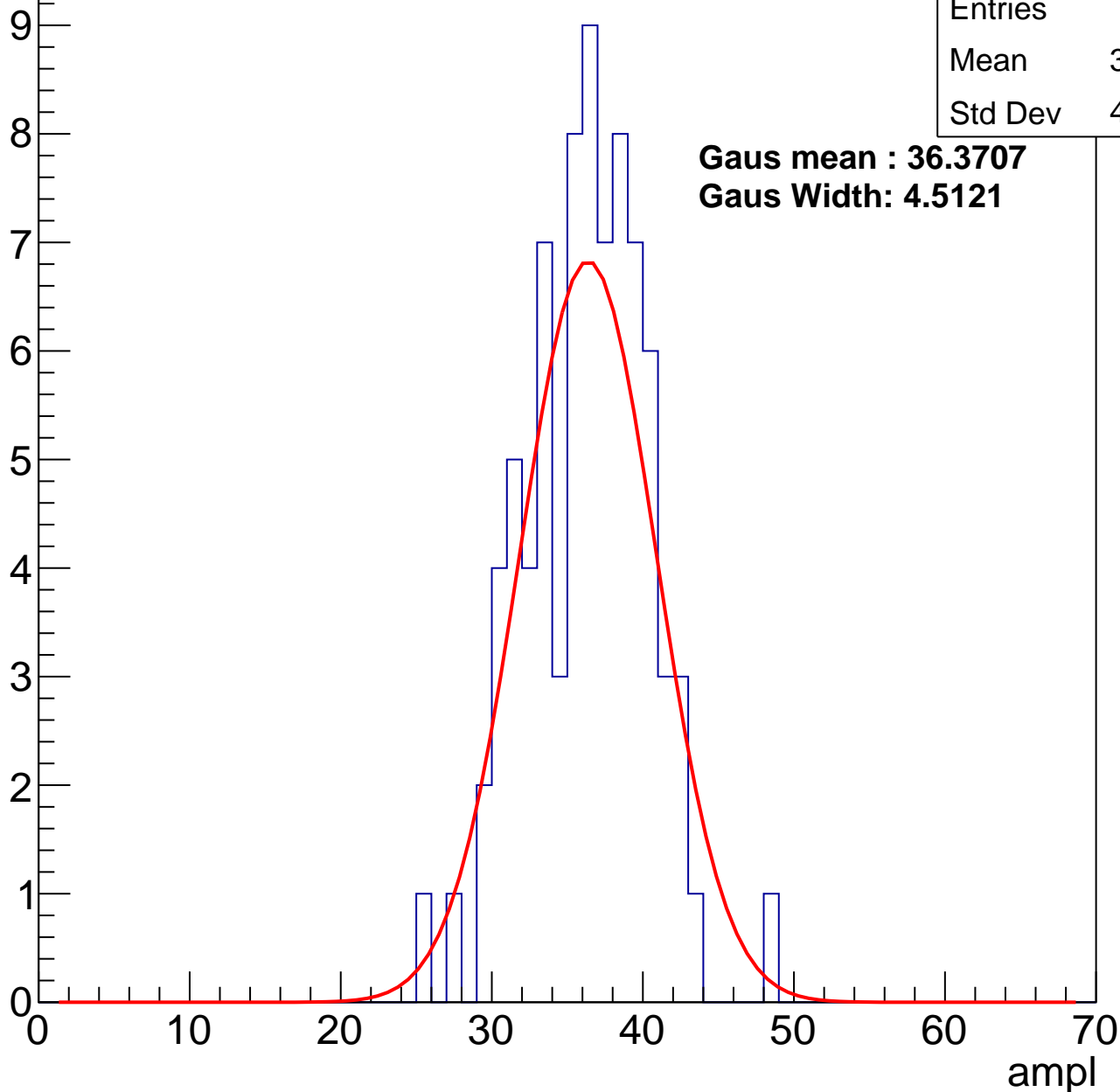
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	35.83
Std Dev	4.012

Gaus mean : 36.3707

Gaus Width: 4.5121



B0L001S, U15-ch112, adc2

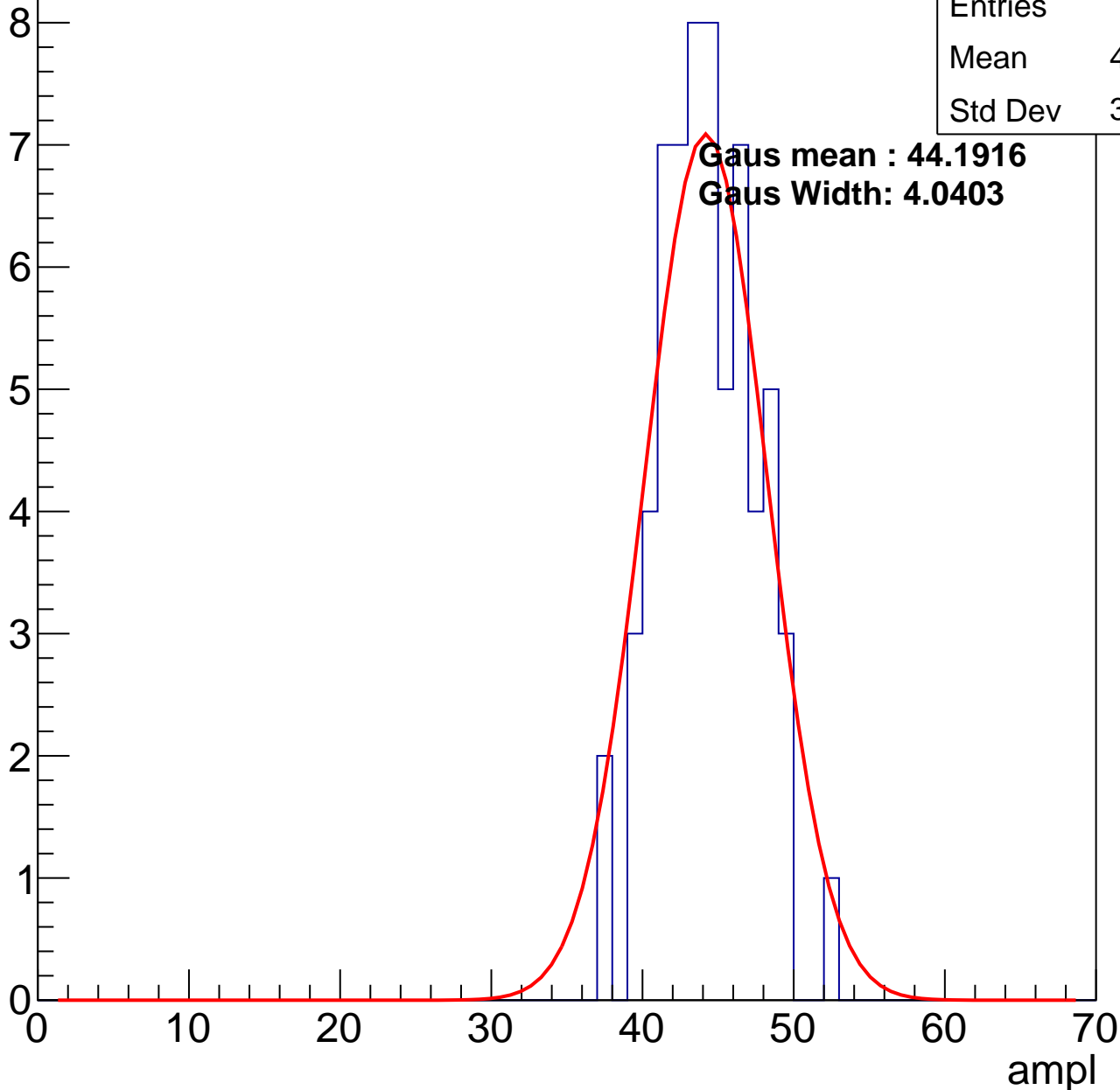
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.78
Std Dev	3.115

Gaus mean : 44.1916

Gaus Width: 4.0403

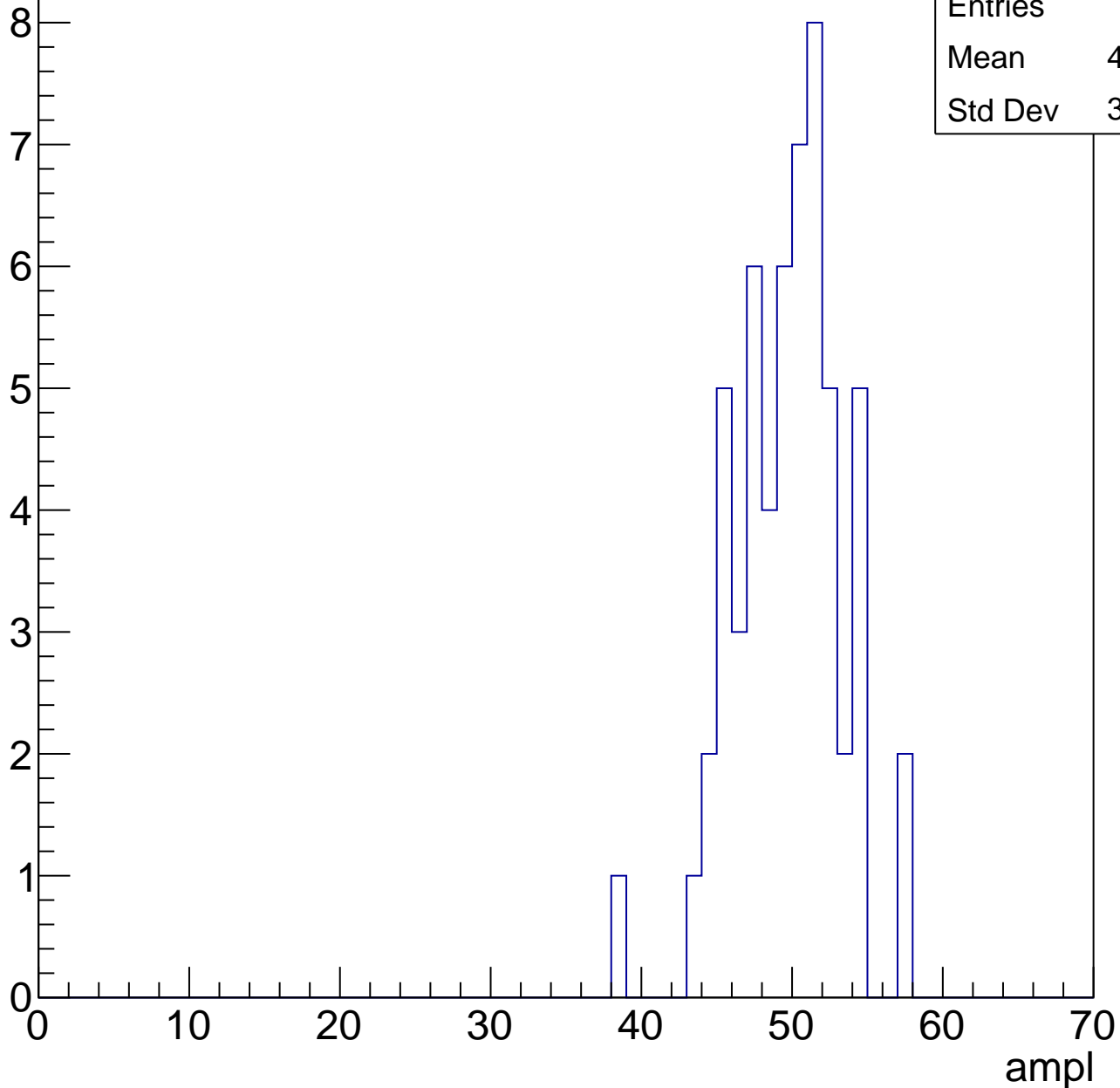


B0L001S, U15-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

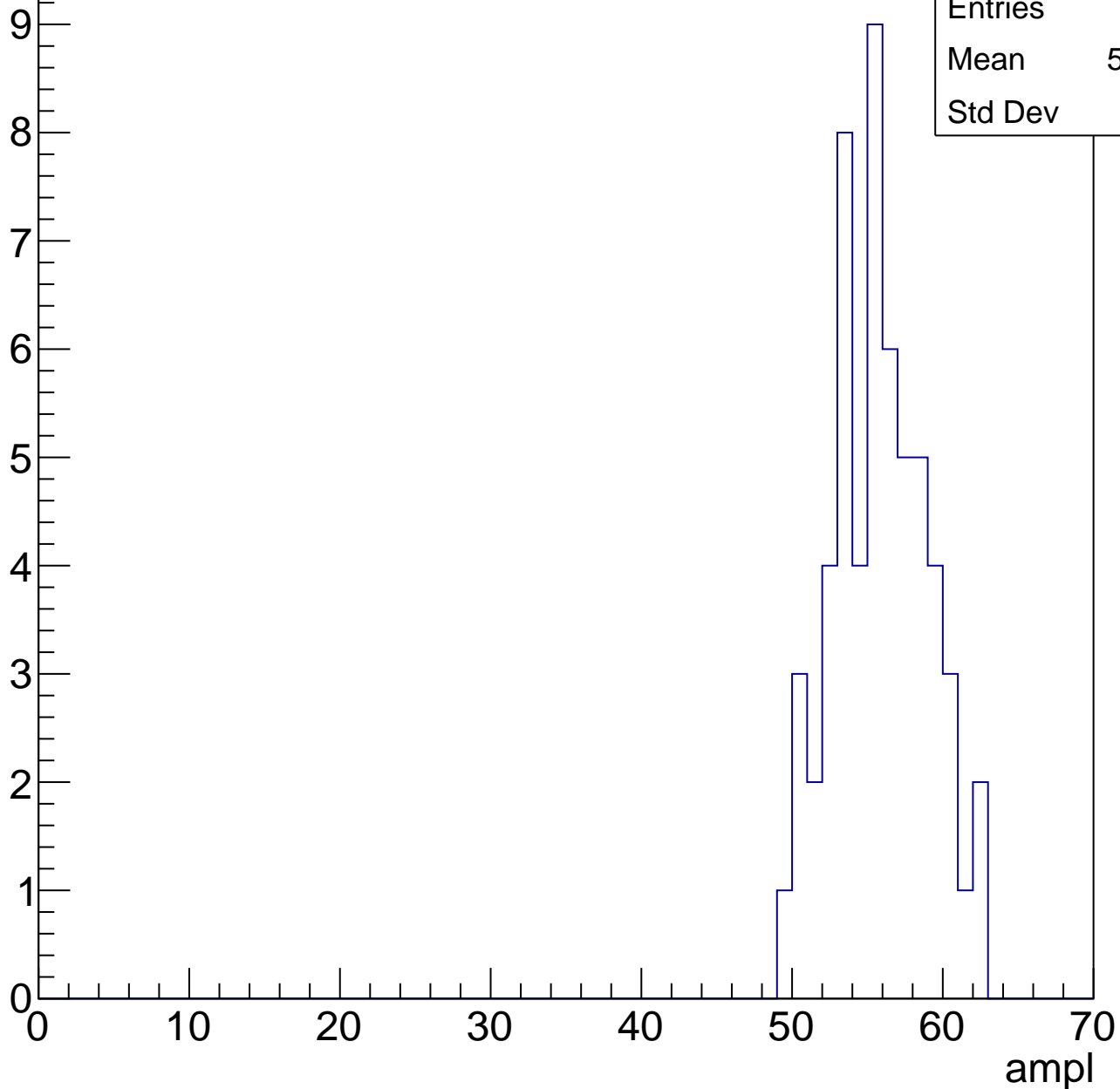
Entries	57
Mean	49.26
Std Dev	3.522



B0L001S, U15-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



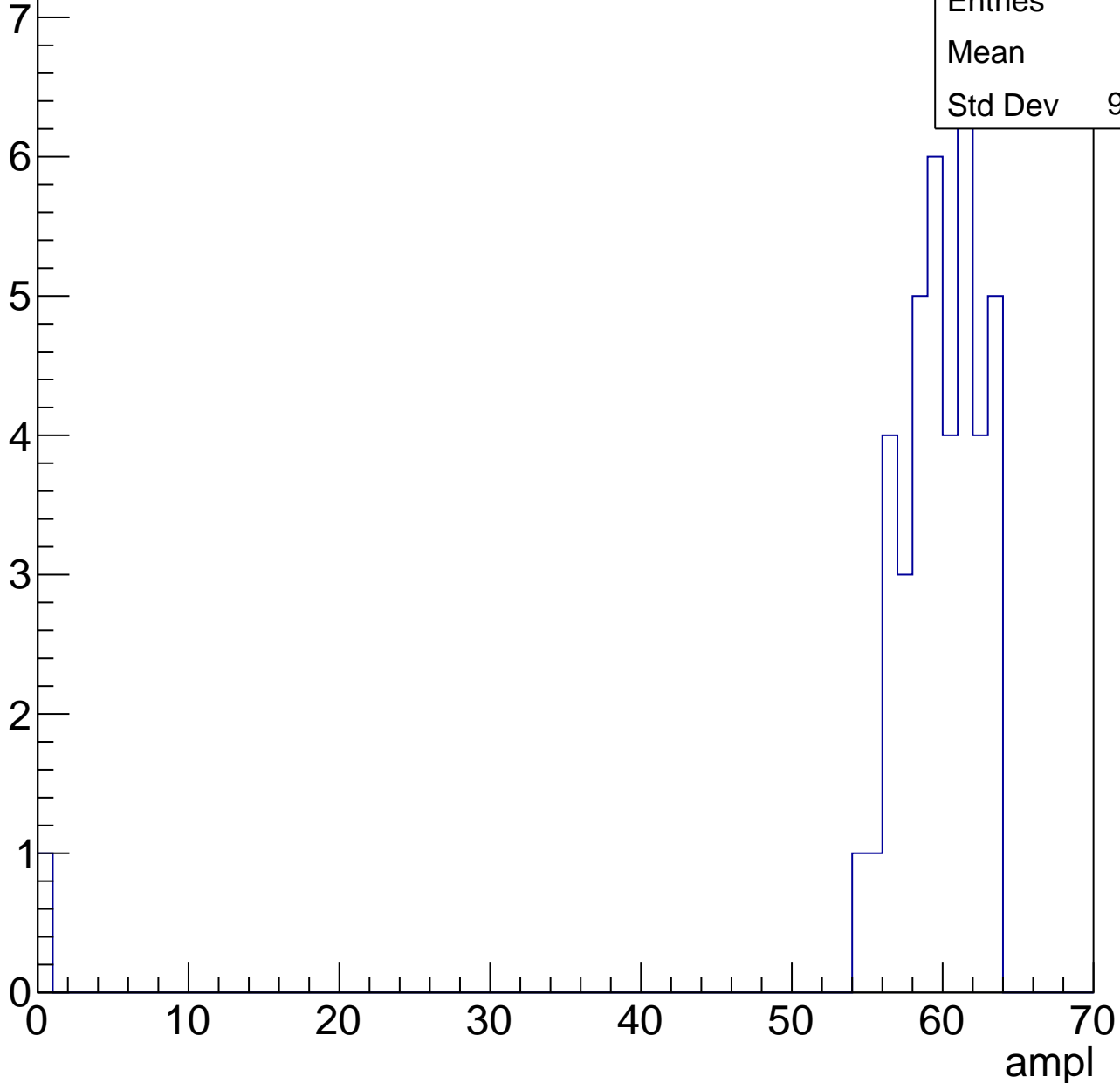
Entries	57
Mean	55.37
Std Dev	3.11

B0L001S, U15-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58
Std Dev	9.477

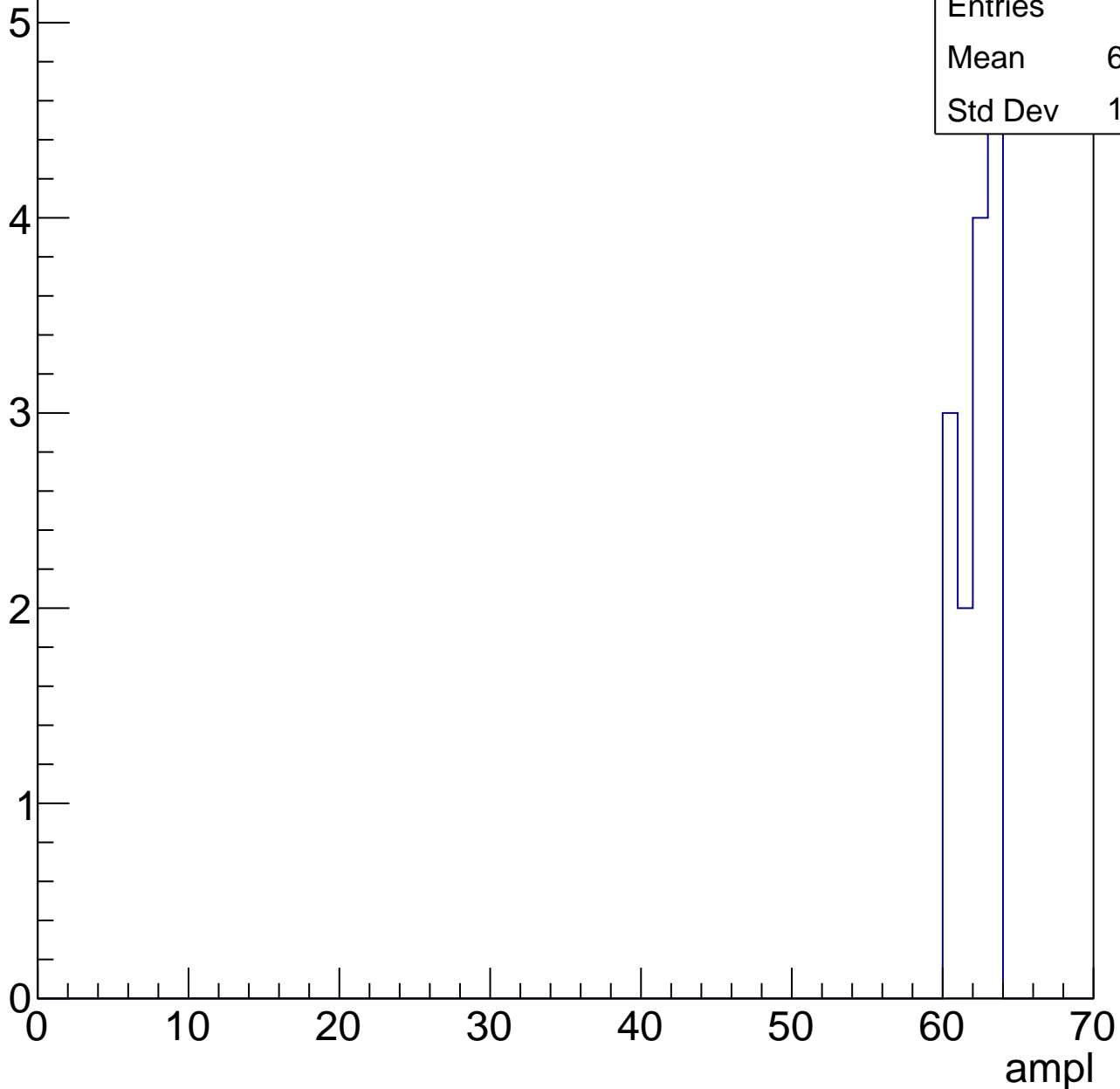


B0L001S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	61.79
Std Dev	1.145



B0L001S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch113, adc0

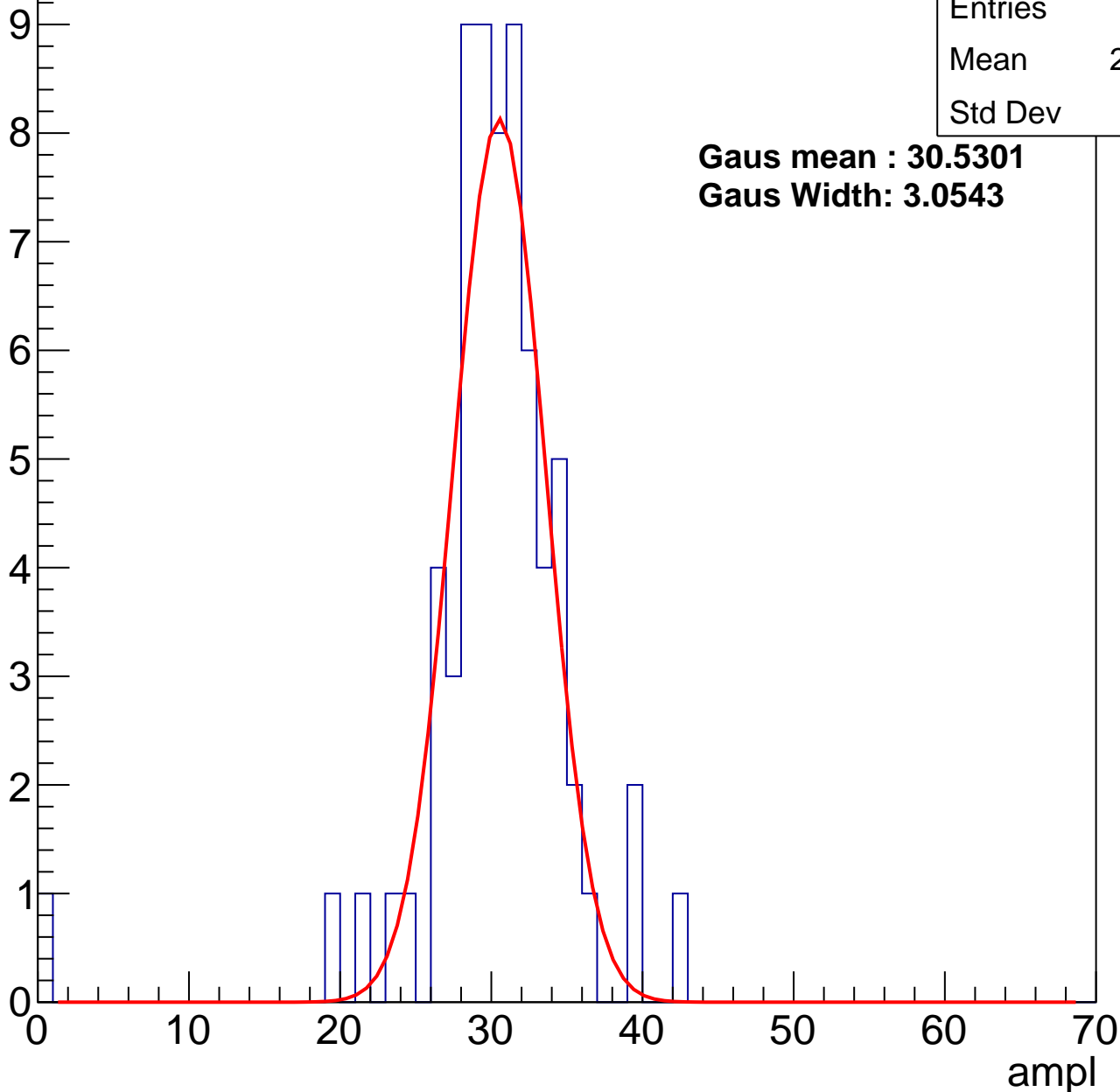
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.76
Std Dev	5.23

Gaus mean : 30.5301

Gaus Width: 3.0543



B0L001S, U15-ch113, adc1

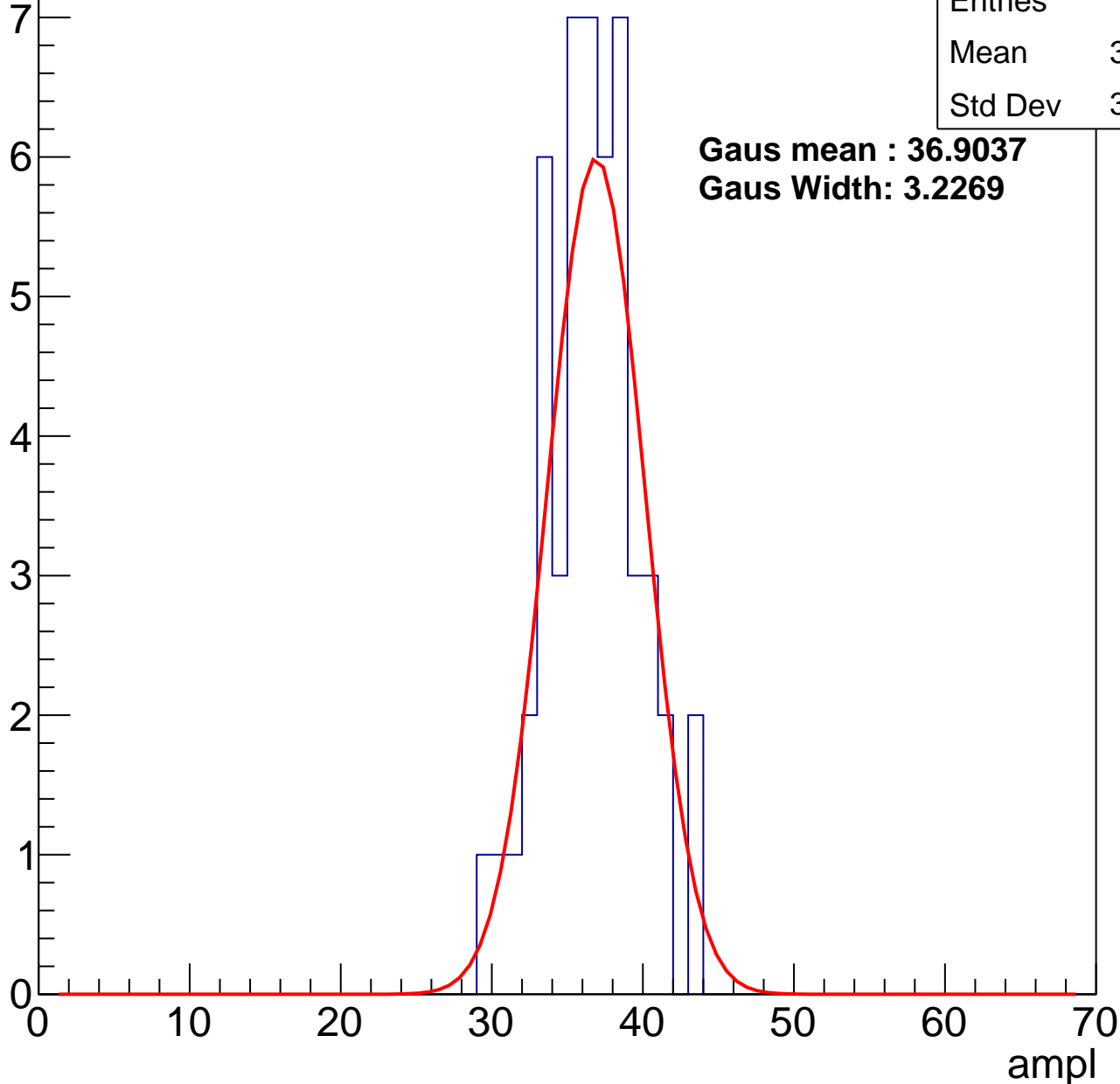
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	36.16
Std Dev	3.038

Gaus mean : 36.9037

Gaus Width: 3.2269



B0L001S, U15-ch113, adc2

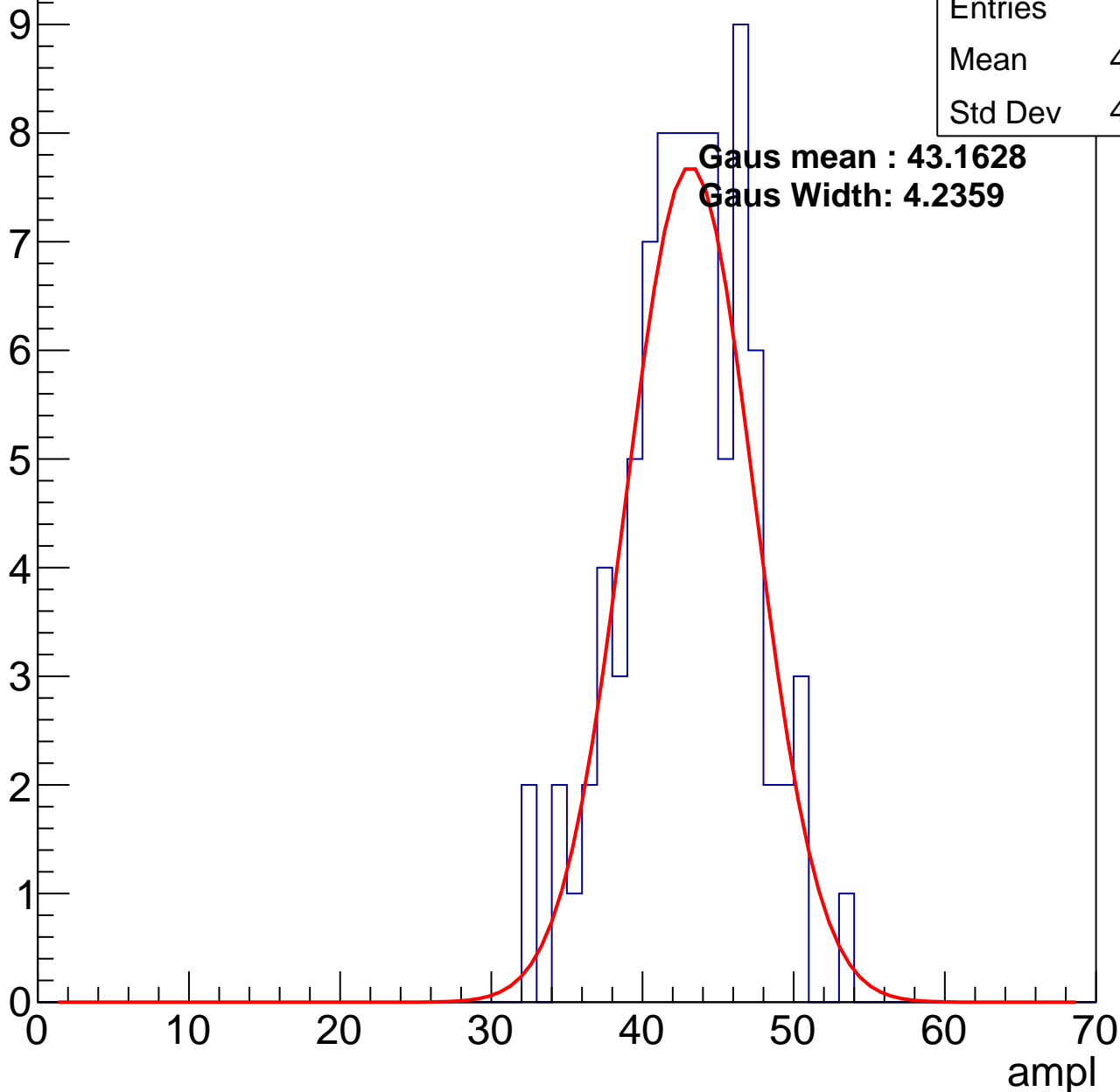
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	42.49
Std Dev	4.203

Gaus mean : 43.1628

Gaus Width: 4.2359

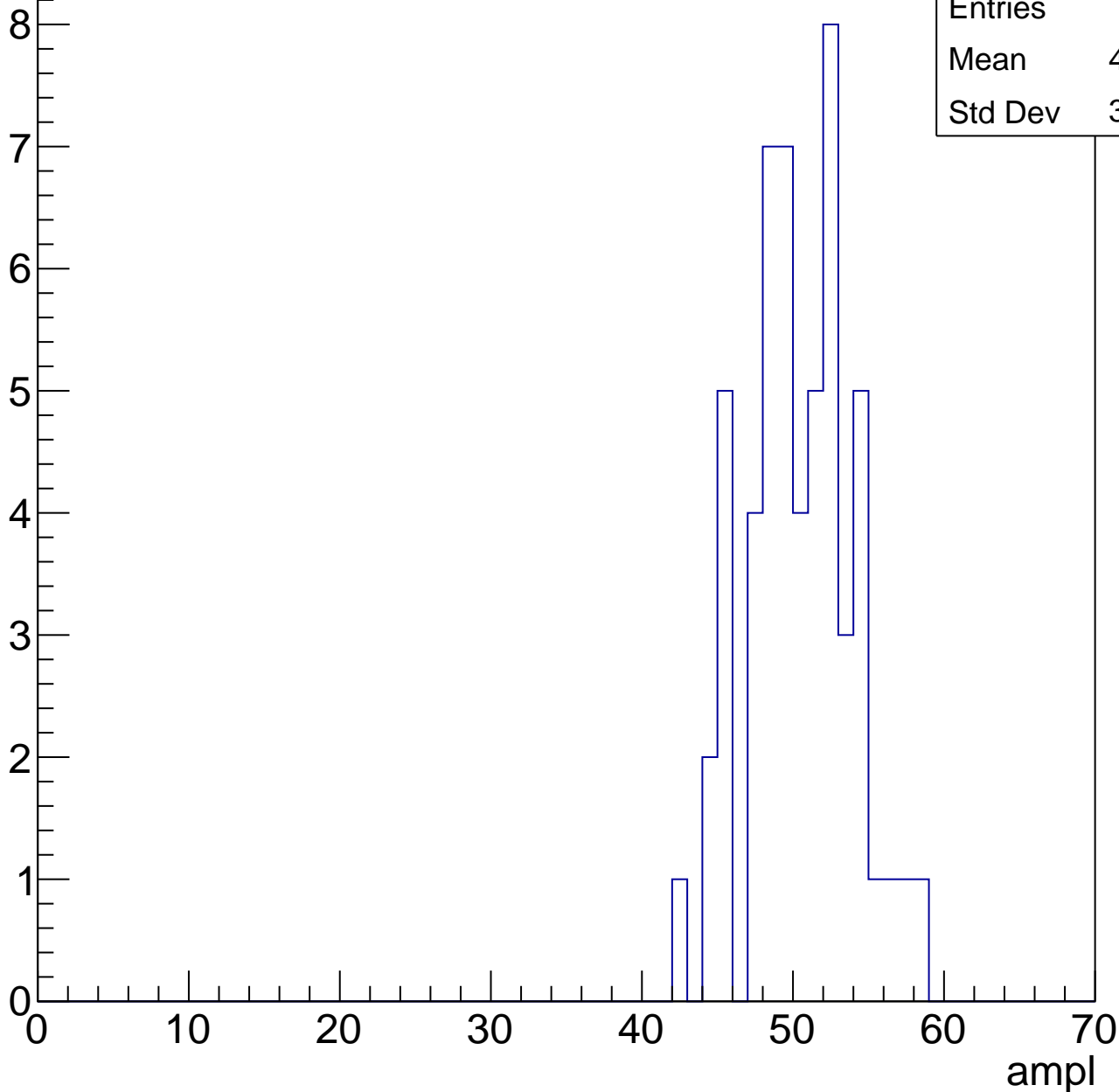


B0L001S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.96
Std Dev	3.438

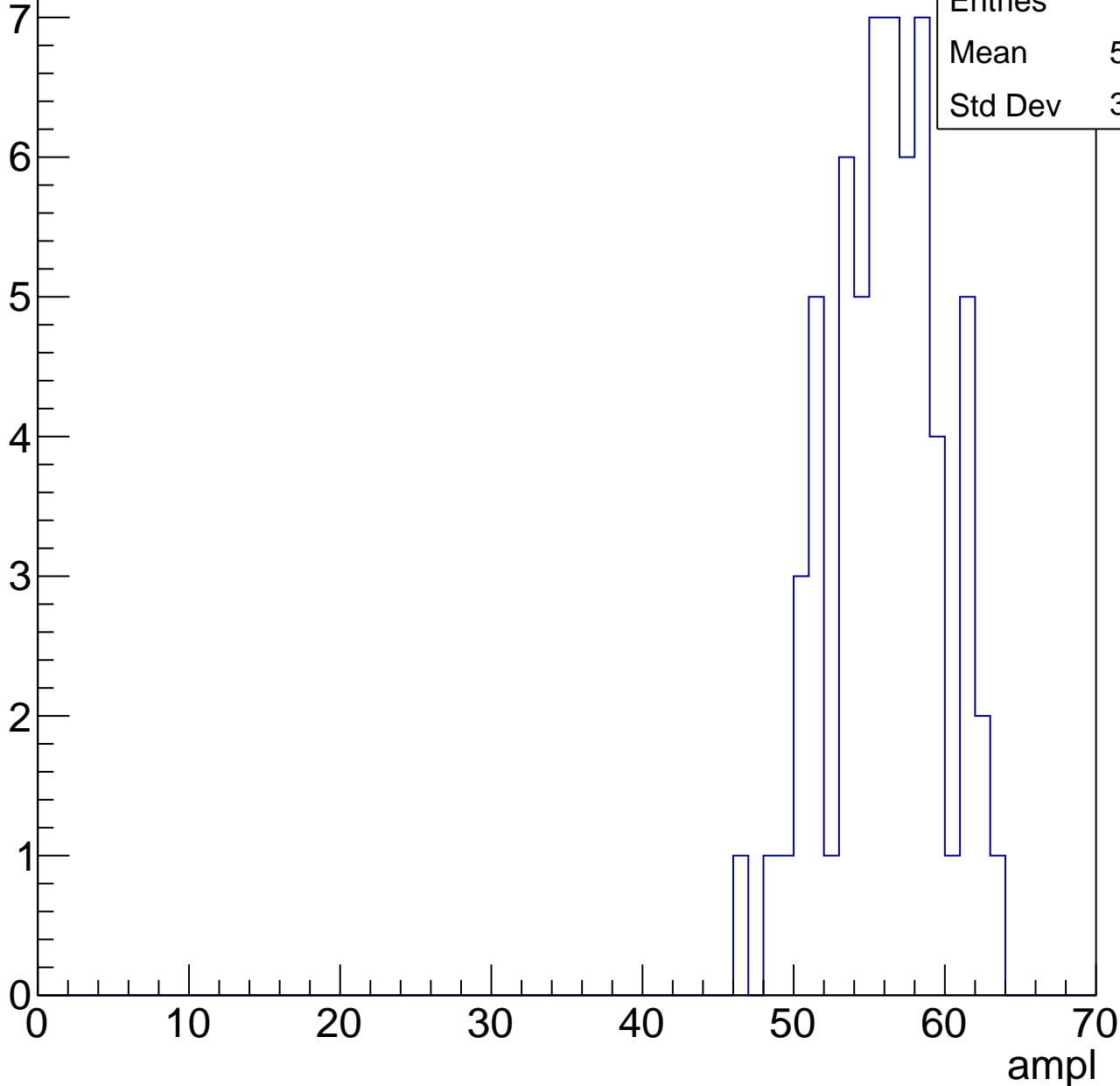


B0L001S, U15-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	55.57
Std Dev	3.702

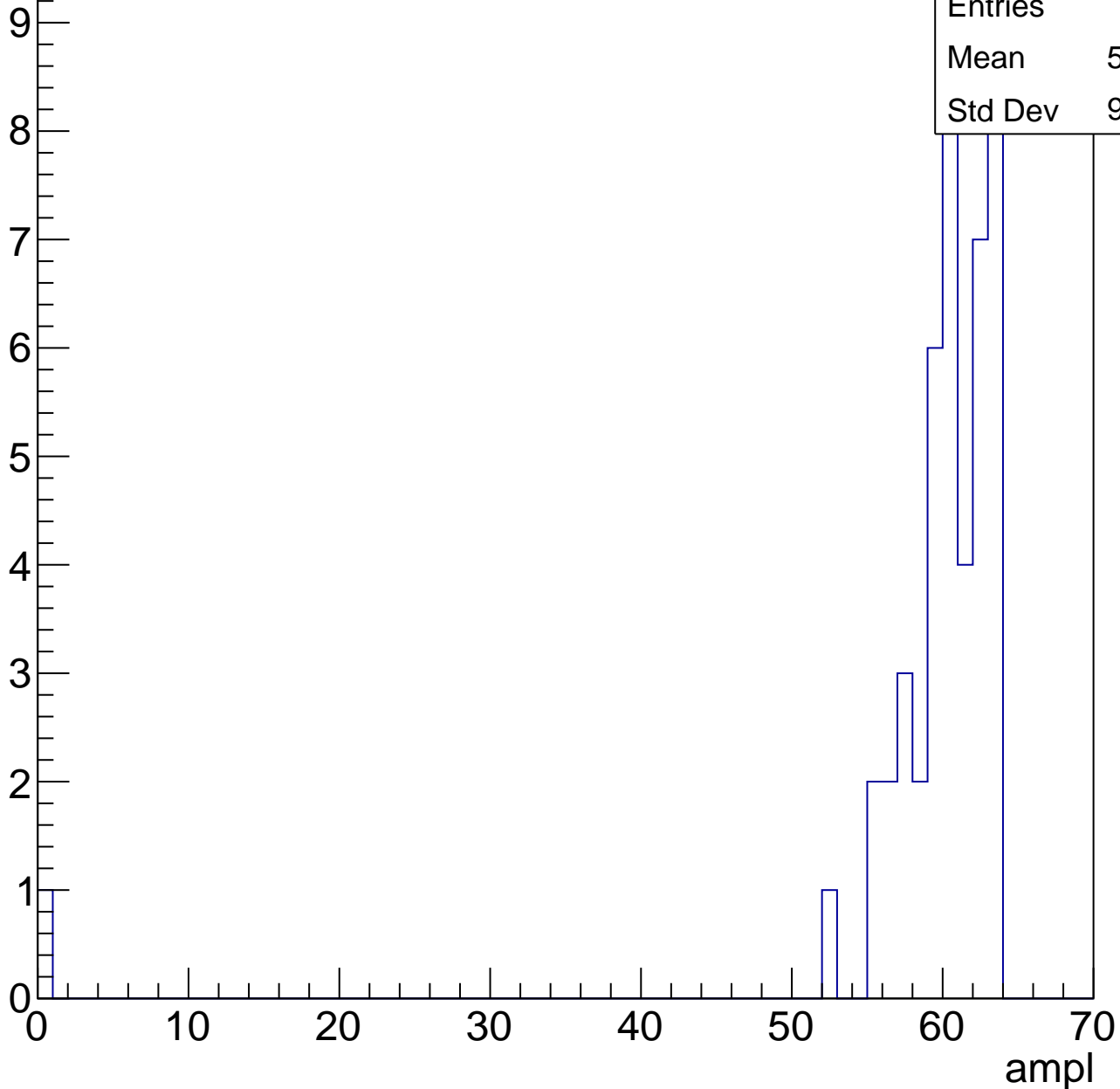


B0L001S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.67
Std Dev	9.214



B0L001S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch114, adc0

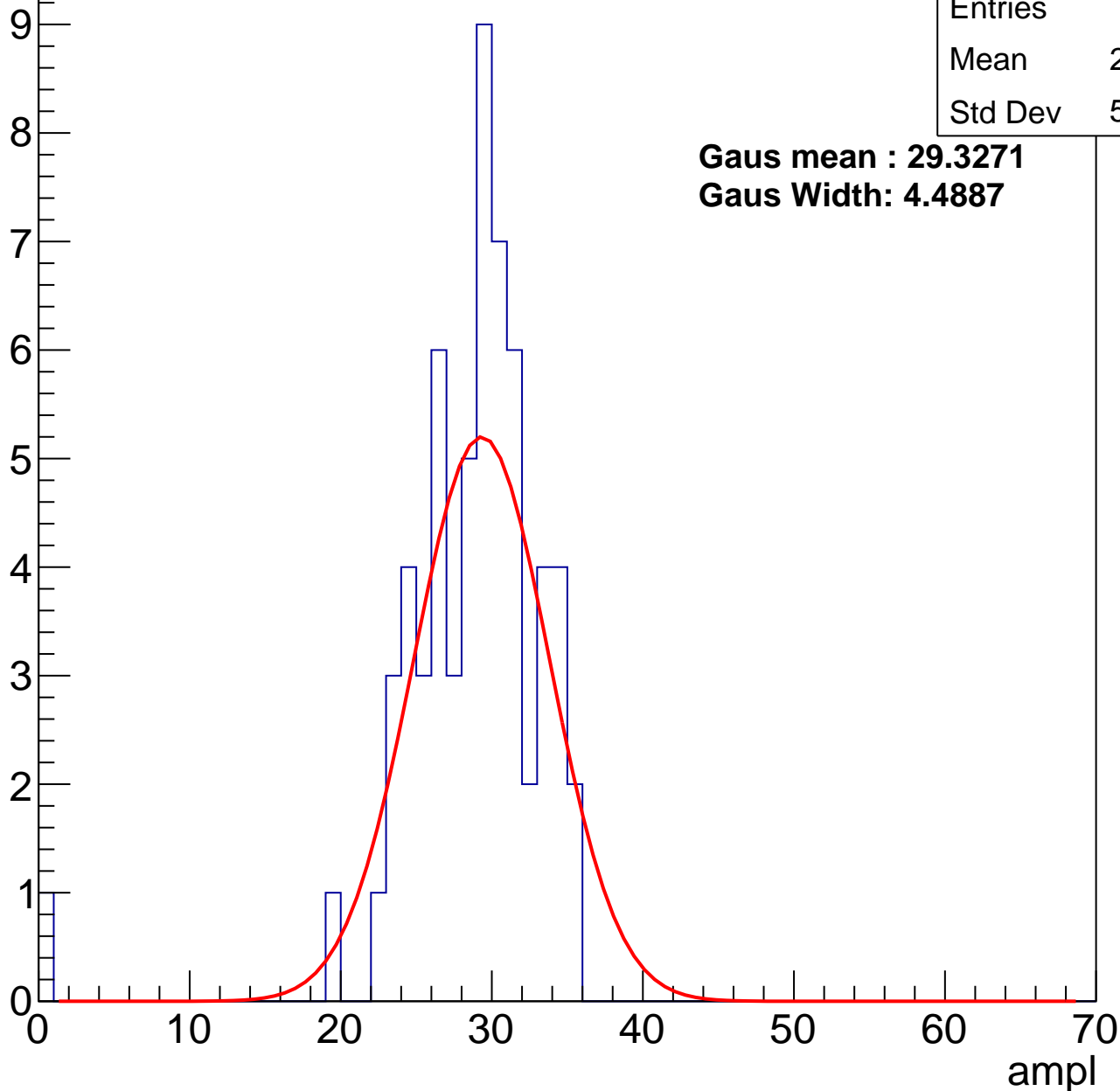
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.15
Std Dev	5.053

Gaus mean : 29.3271

Gaus Width: 4.4887



B0L001S, U15-ch114, adc1

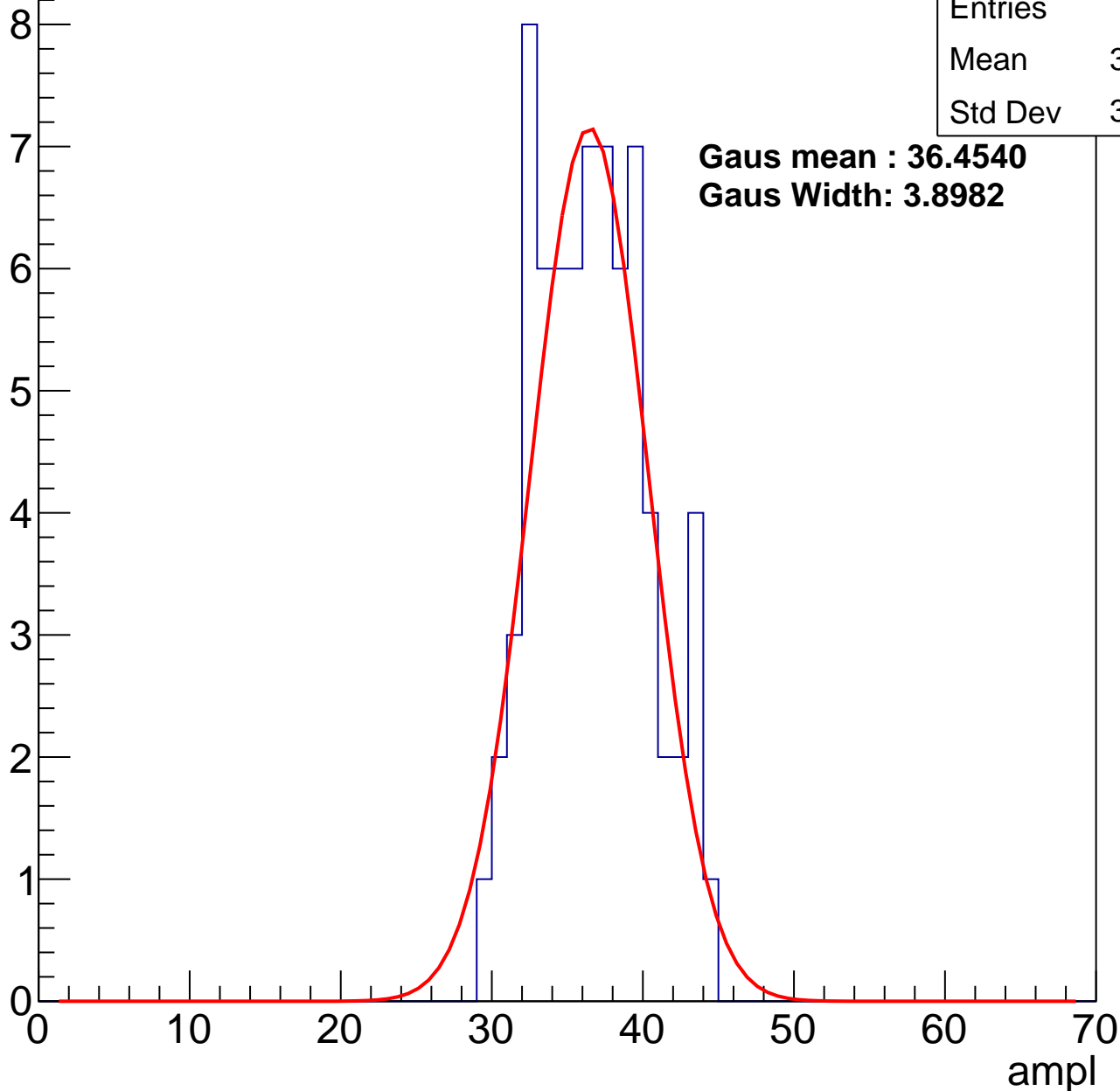
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.17
Std Dev	3.625

Gaus mean : 36.4540

Gaus Width: 3.8982



B0L001S, U15-ch114, adc2

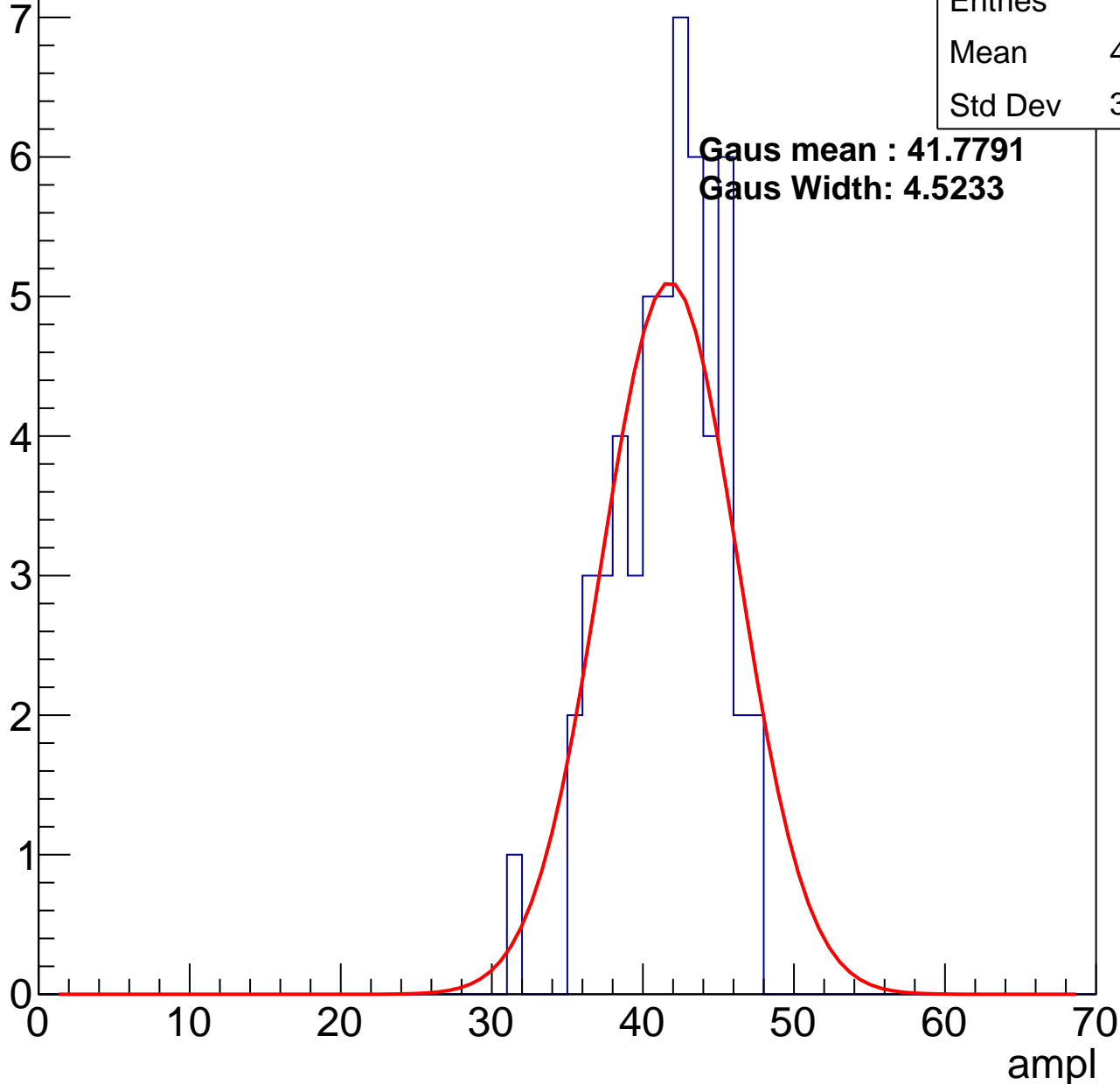
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	41.09
Std Dev	3.455

Gaus mean : 41.7791

Gaus Width: 4.5233

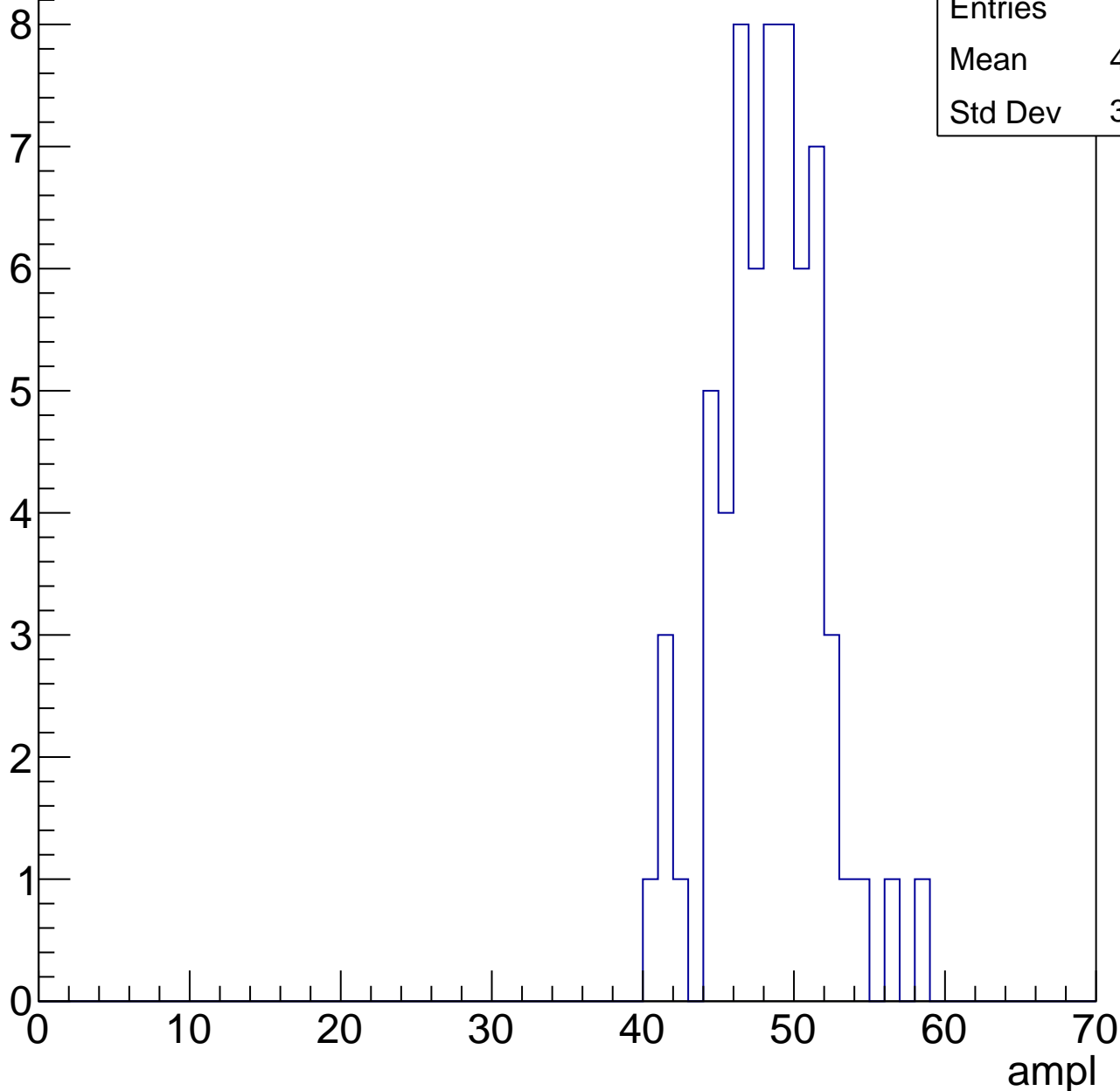


B0L001S, U15-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	47.89
Std Dev	3.465

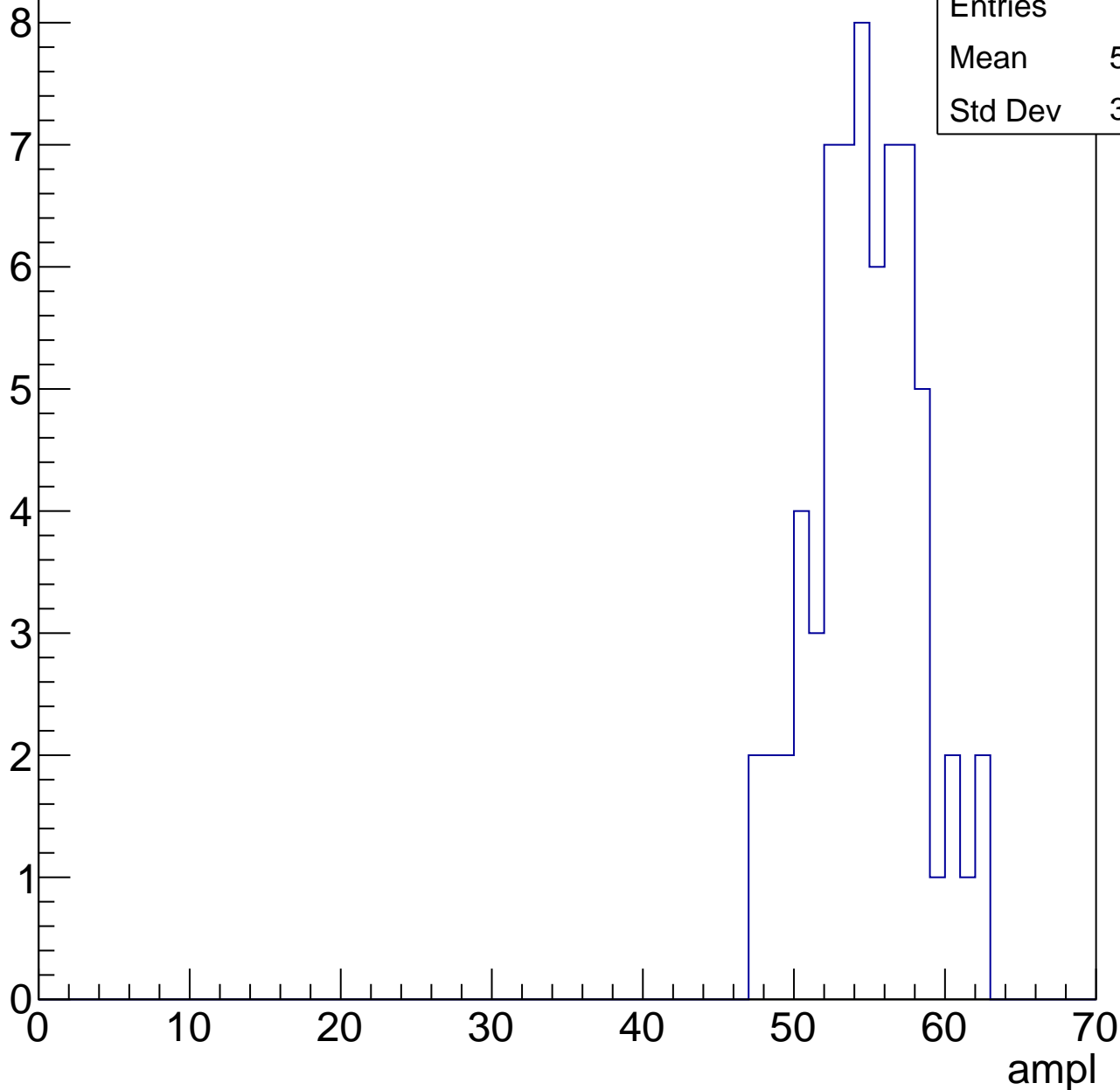


B0L001S, U15-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	54.29
Std Dev	3.463

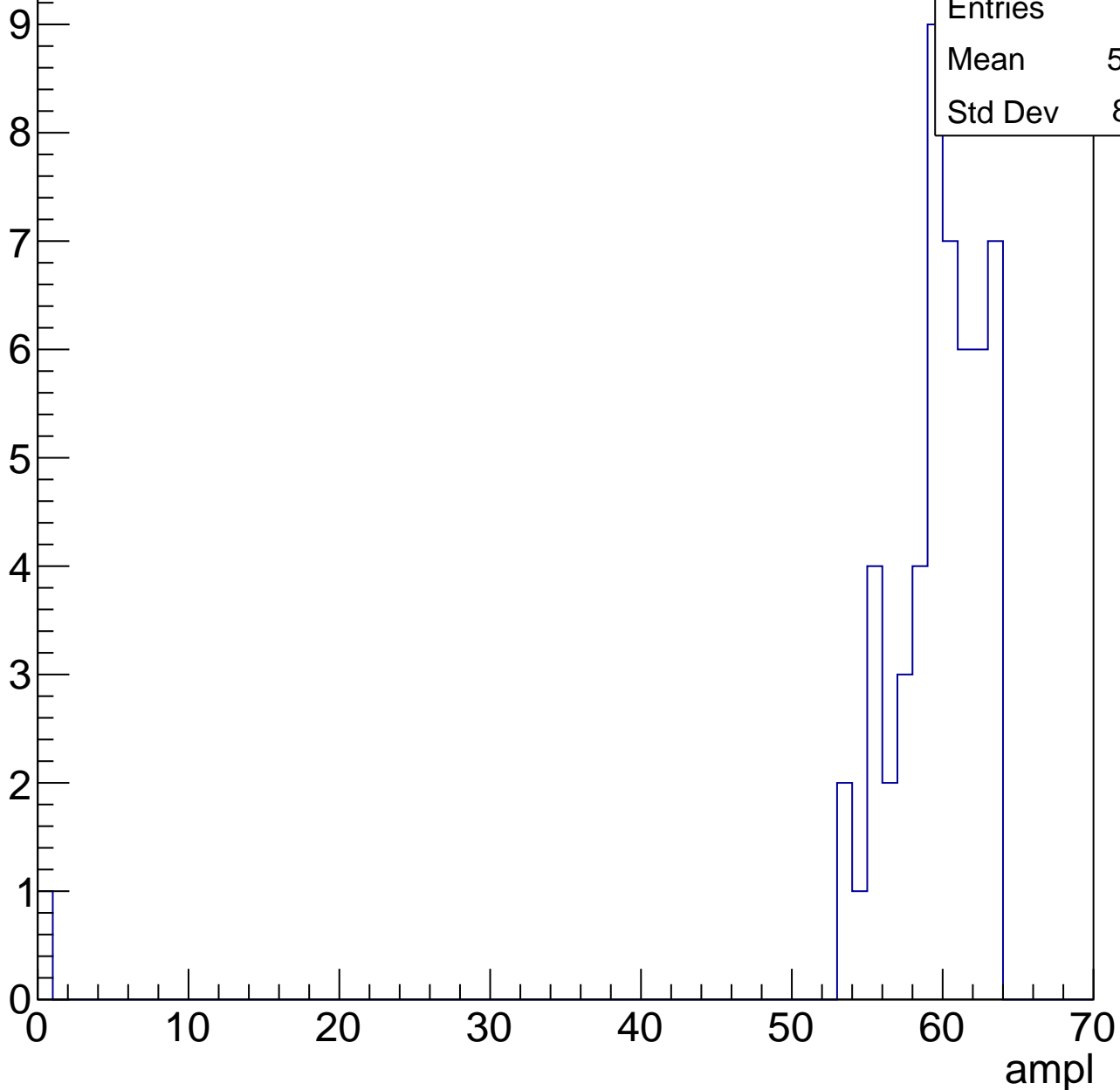


B0L001S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

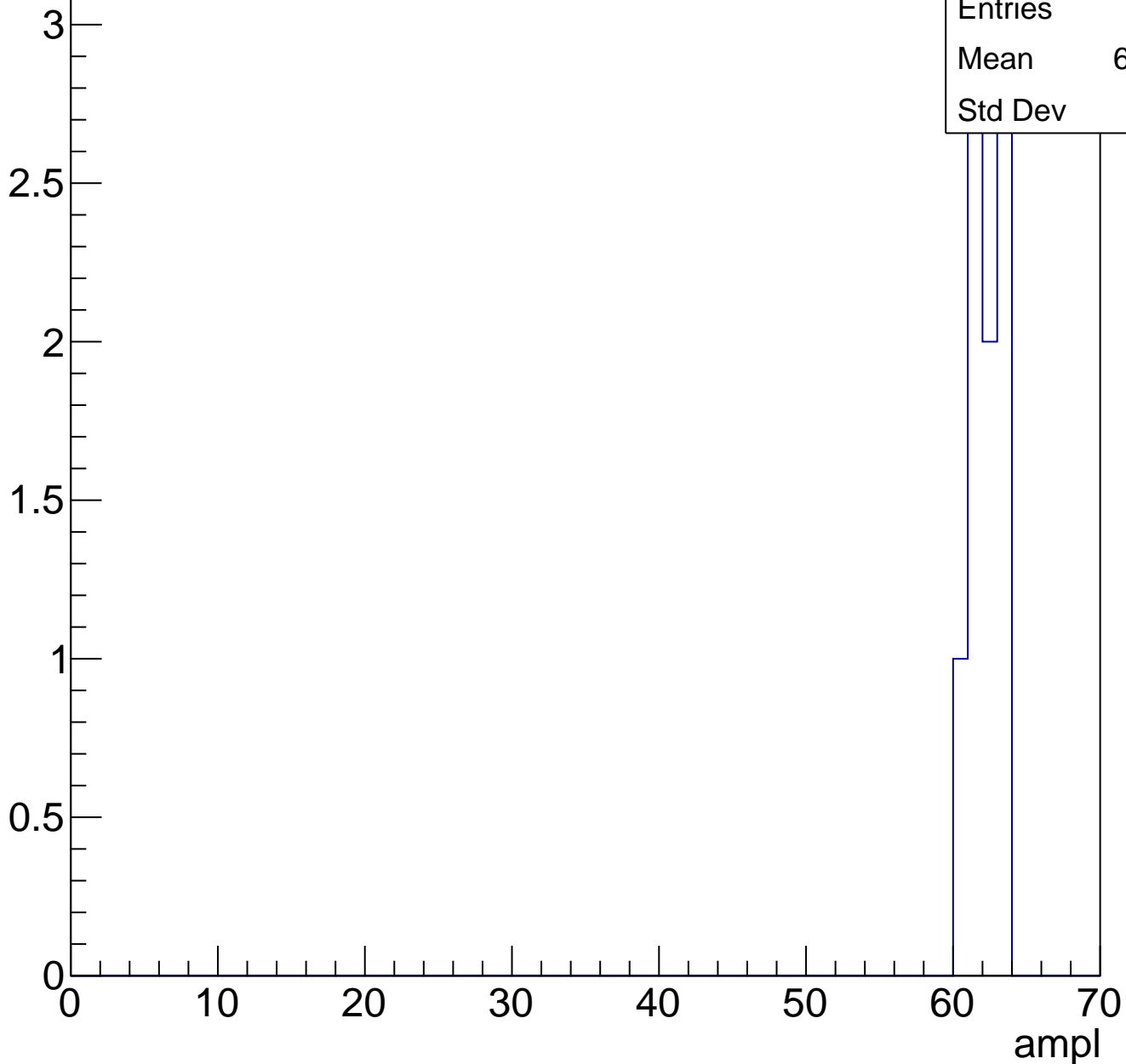
Entries	52
Mean	58.17
Std Dev	8.591



B0L001S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch115, adc0

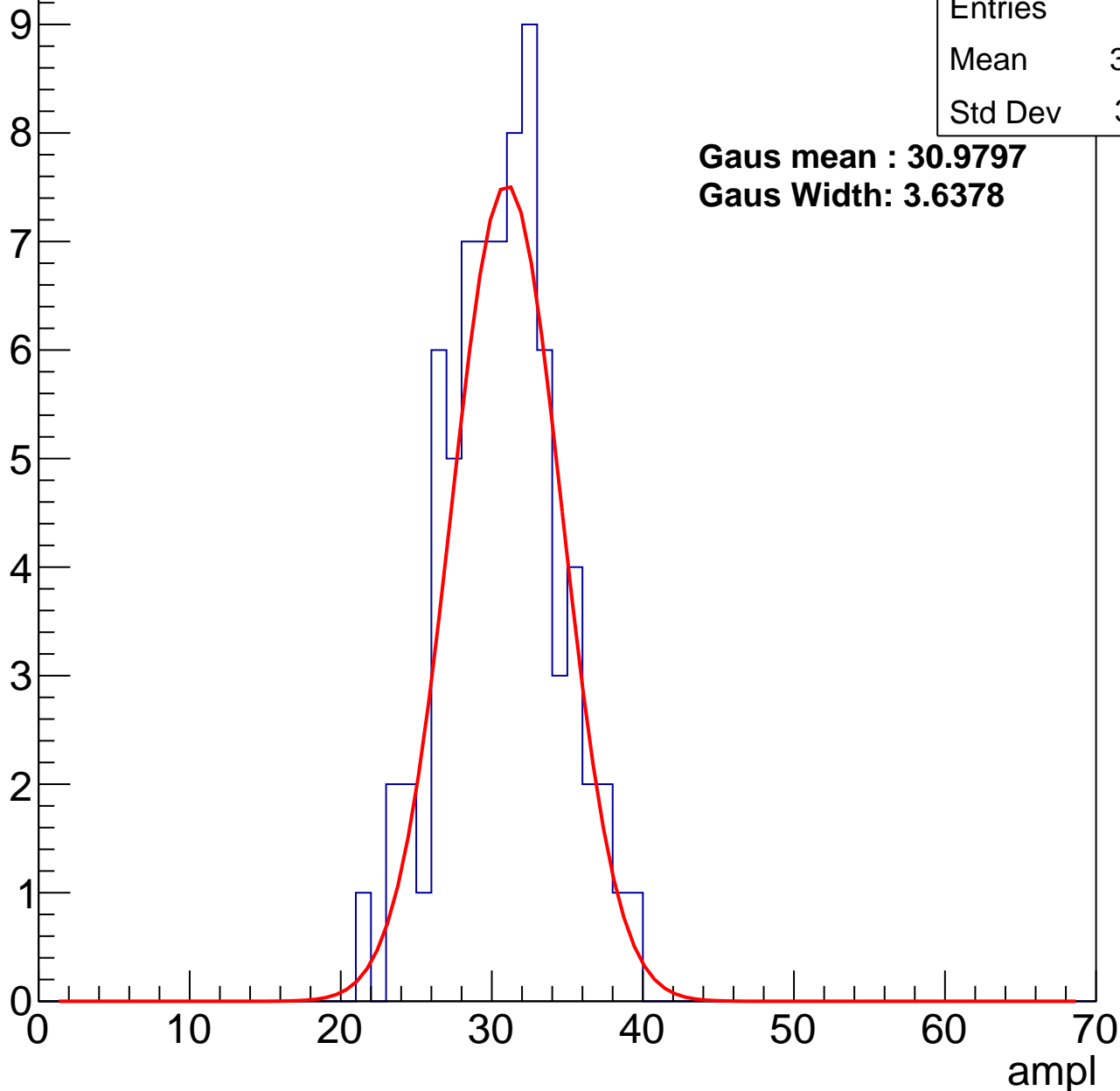
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.26
Std Dev	3.691

Gaus mean : 30.9797

Gaus Width: 3.6378



B0L001S, U15-ch115, adc1

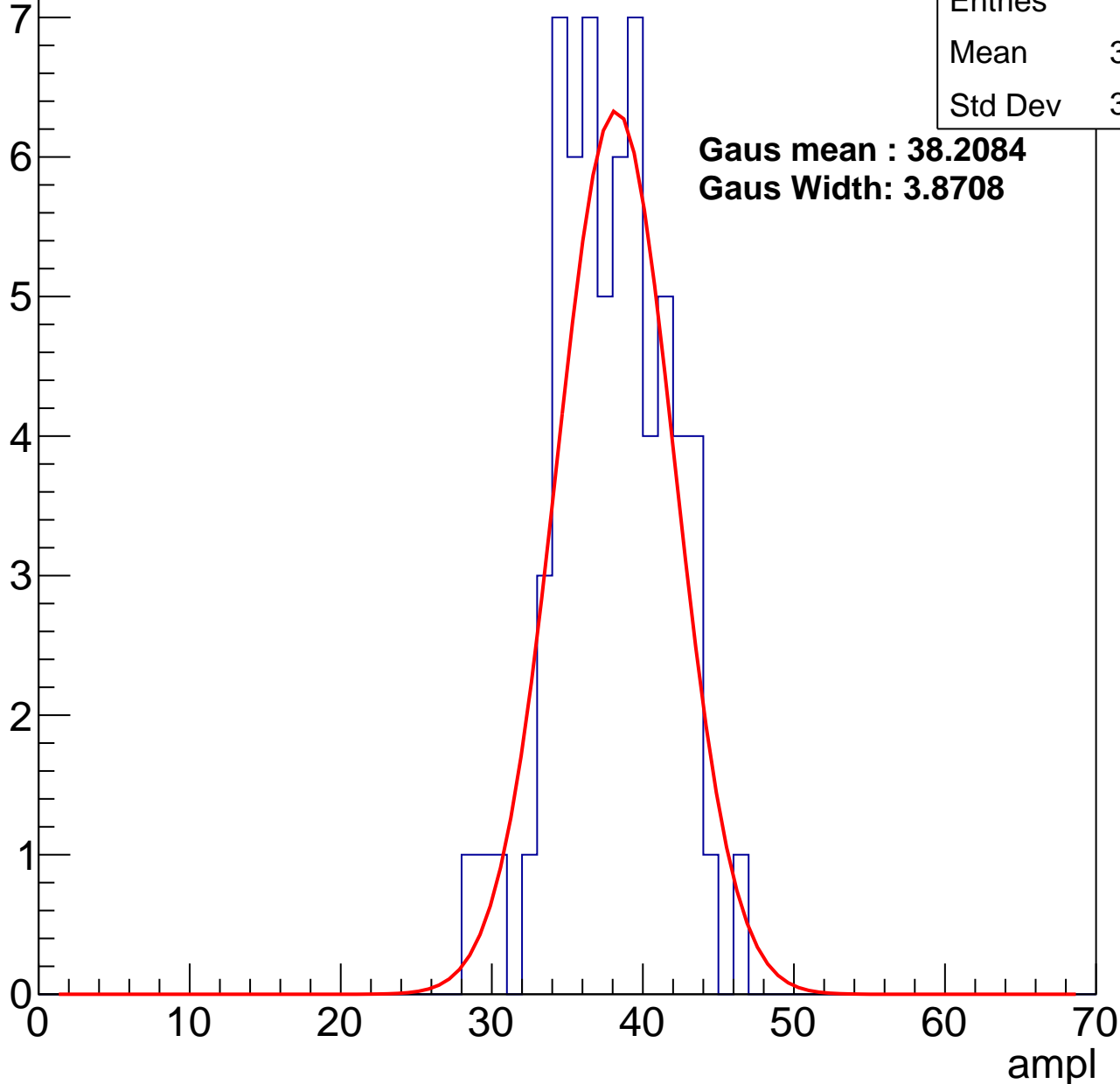
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.48
Std Dev	3.687

Gaus mean : 38.2084

Gaus Width: 3.8708



B0L001S, U15-ch115, adc2

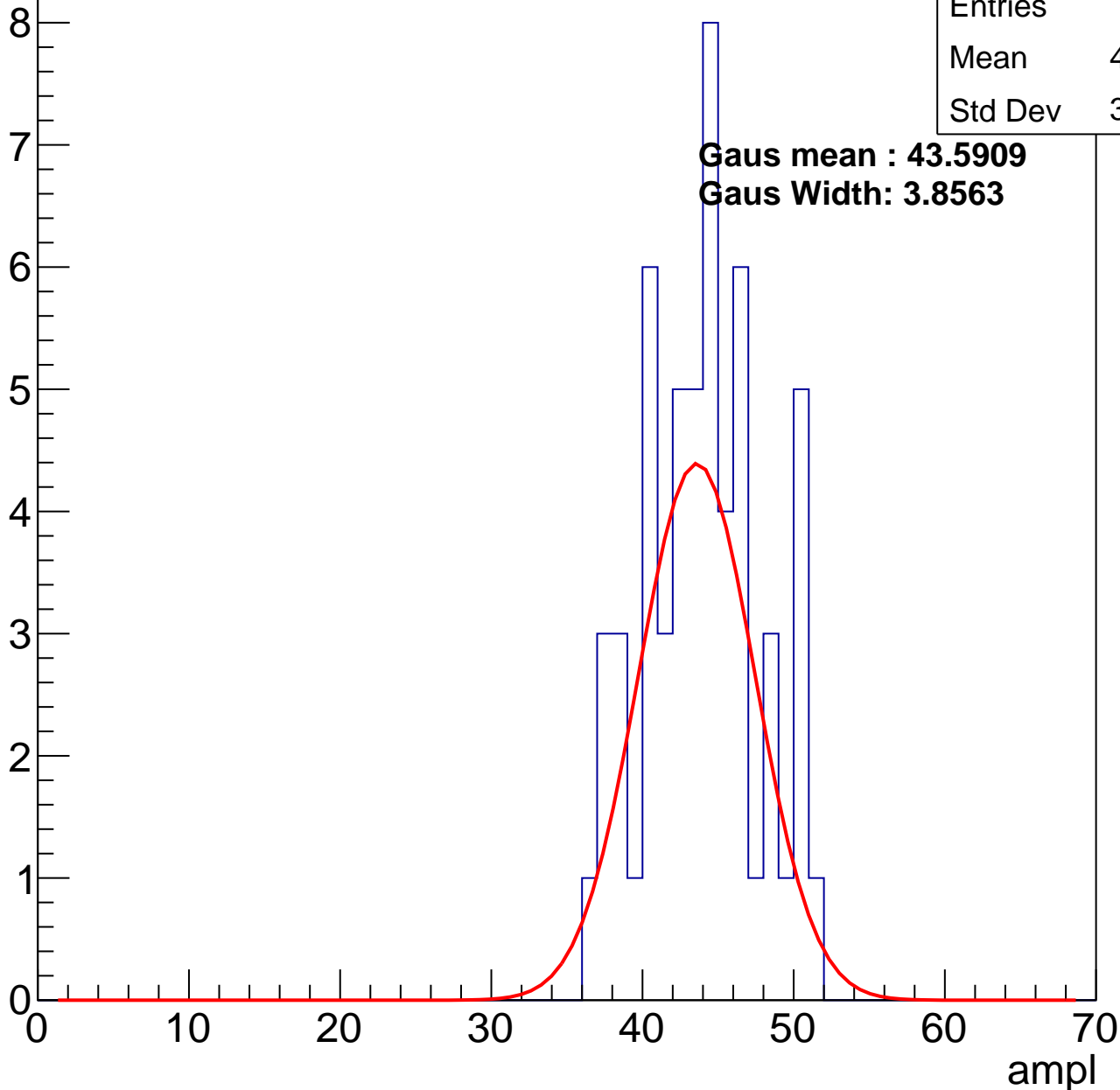
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.52
Std Dev	3.822

Gaus mean : 43.5909

Gaus Width: 3.8563

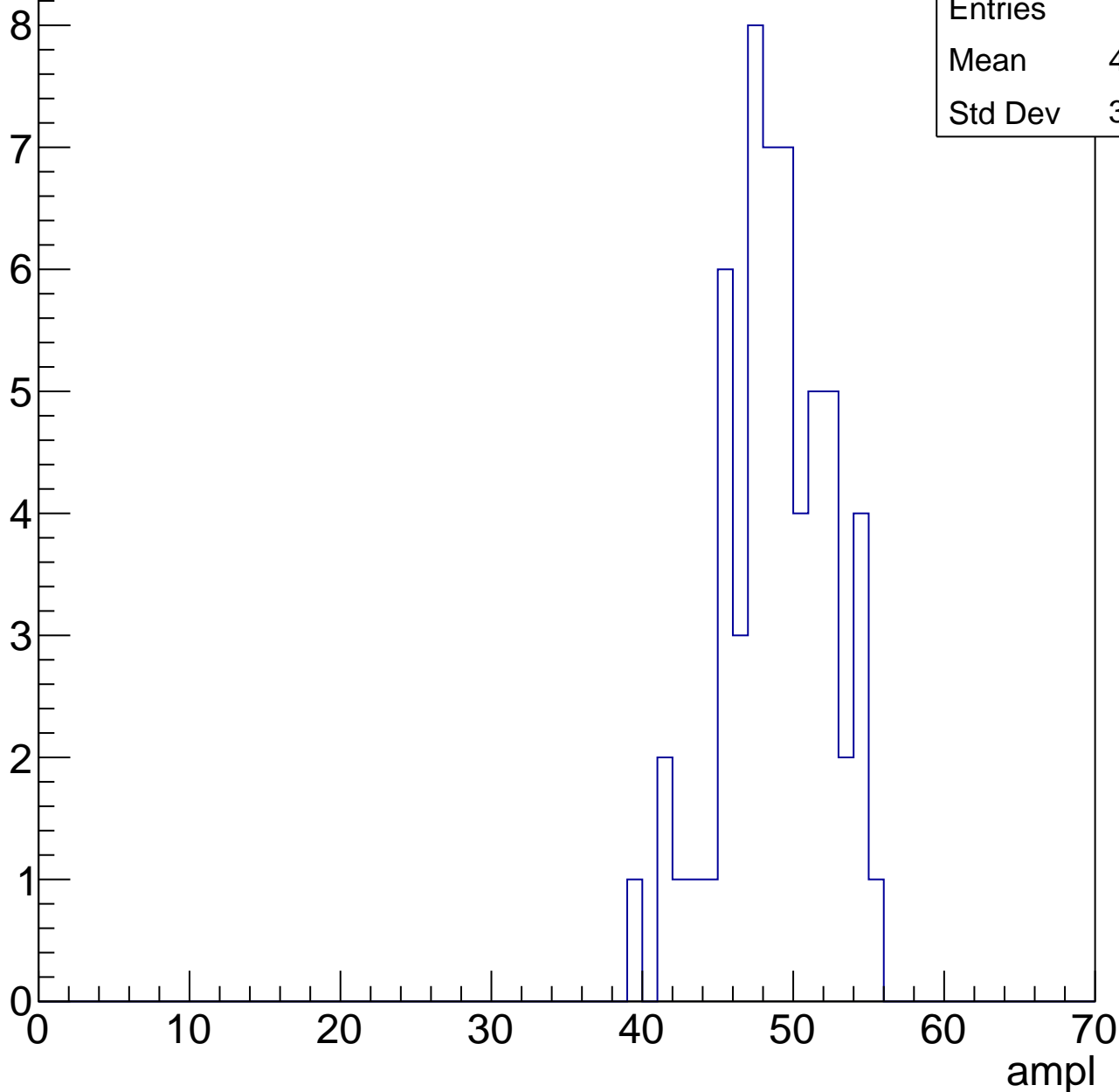


B0L001S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	48.36
Std Dev	3.507

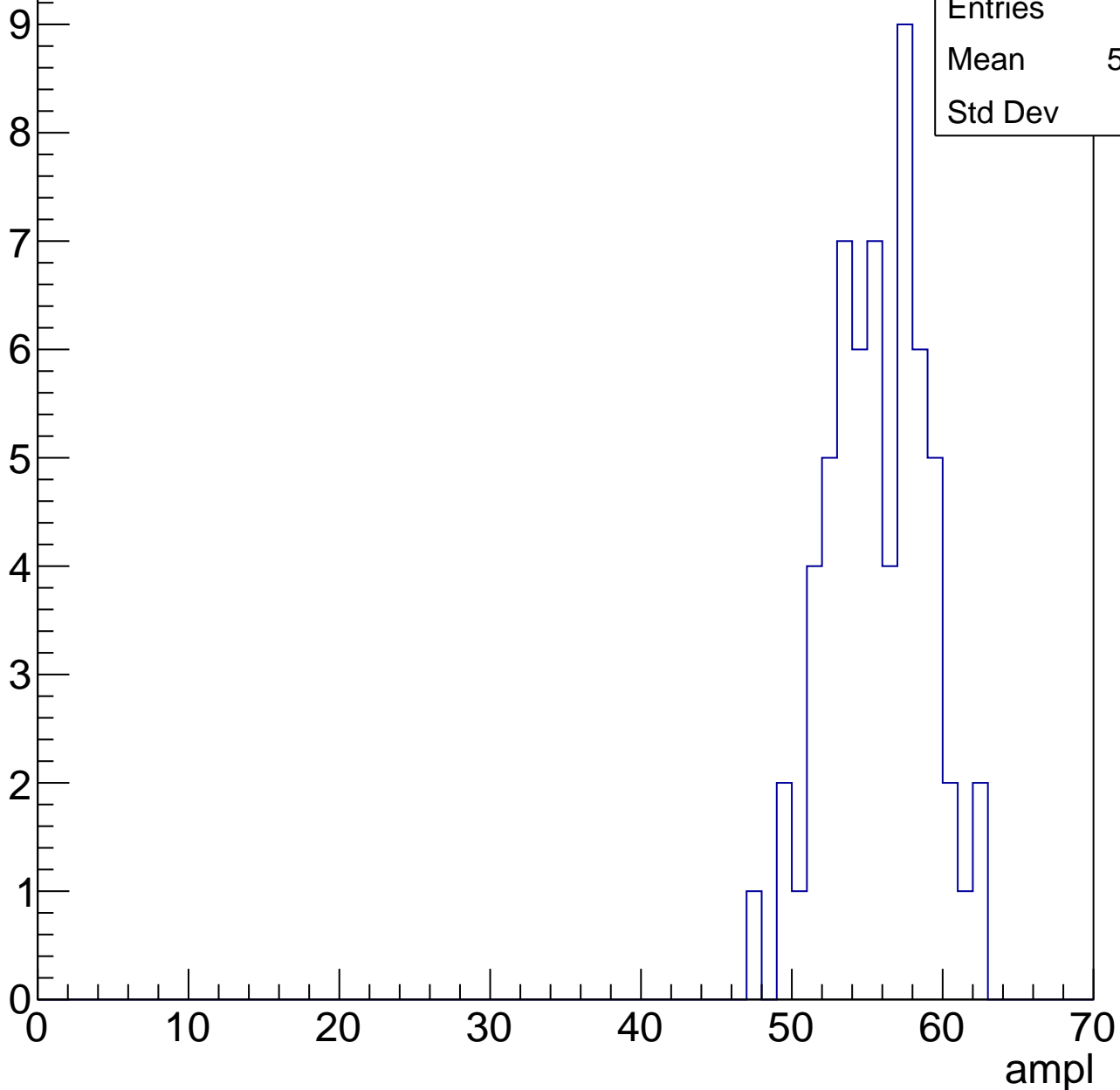


B0L001S, U15-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	55.23
Std Dev	3.26

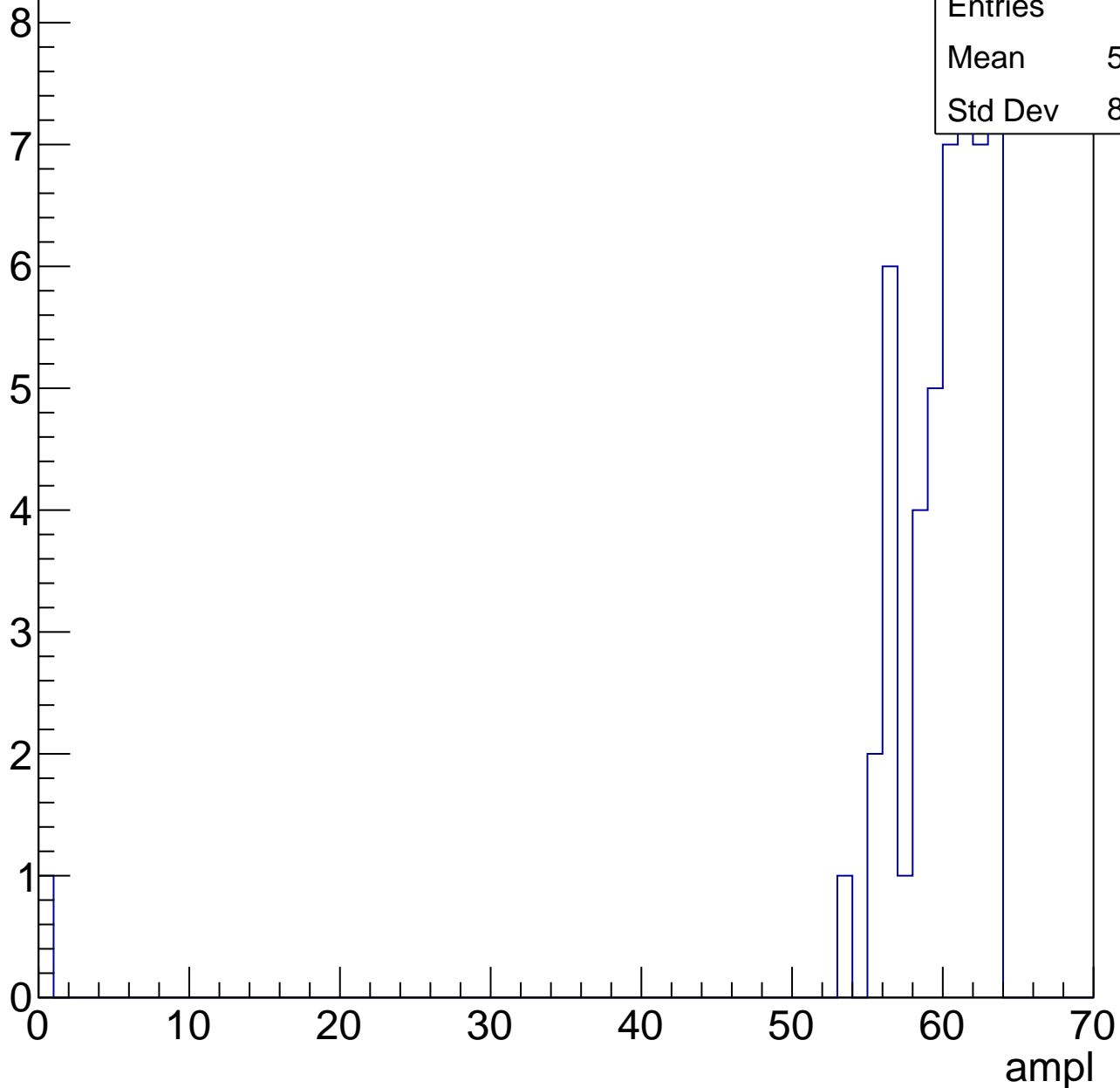


B0L001S, U15-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	58.58
Std Dev	8.755



B0L001S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B0L001S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch116, adc0

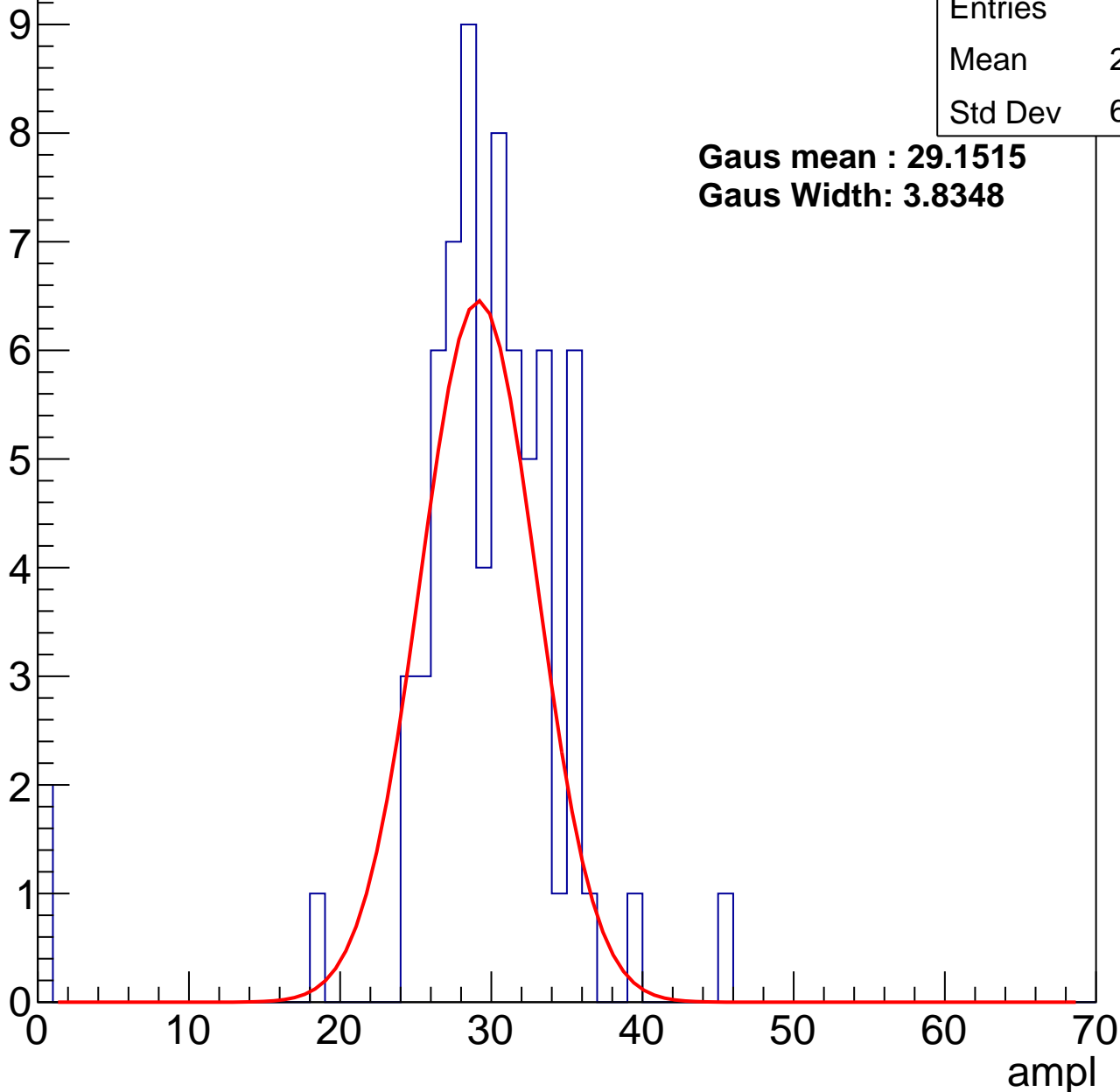
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	28.94
Std Dev	6.365

Gaus mean : 29.1515

Gaus Width: 3.8348



B0L001S, U15-ch116, adc1

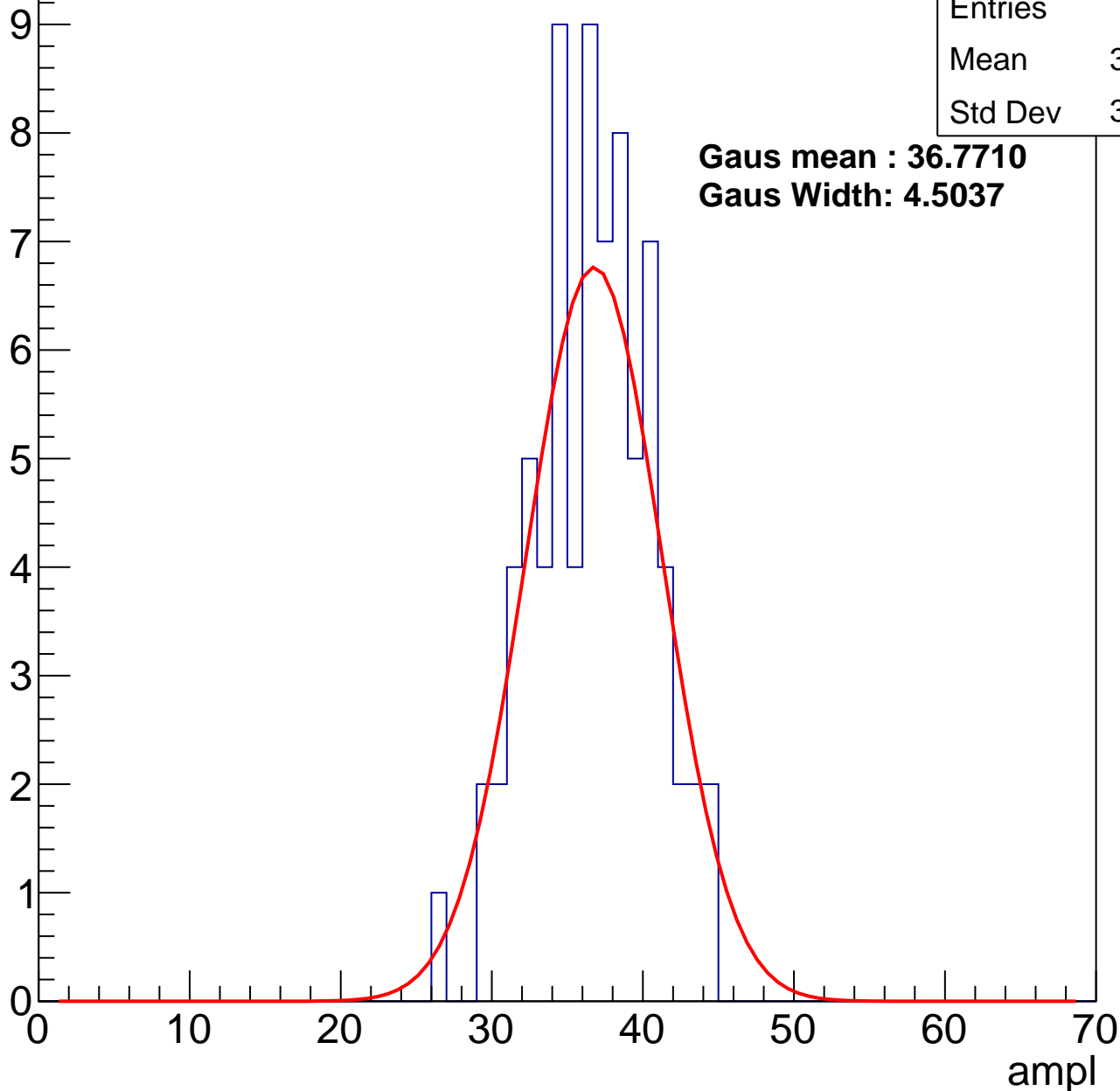
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.23
Std Dev	3.807

Gaus mean : 36.7710

Gaus Width: 4.5037



B0L001S, U15-ch116, adc2

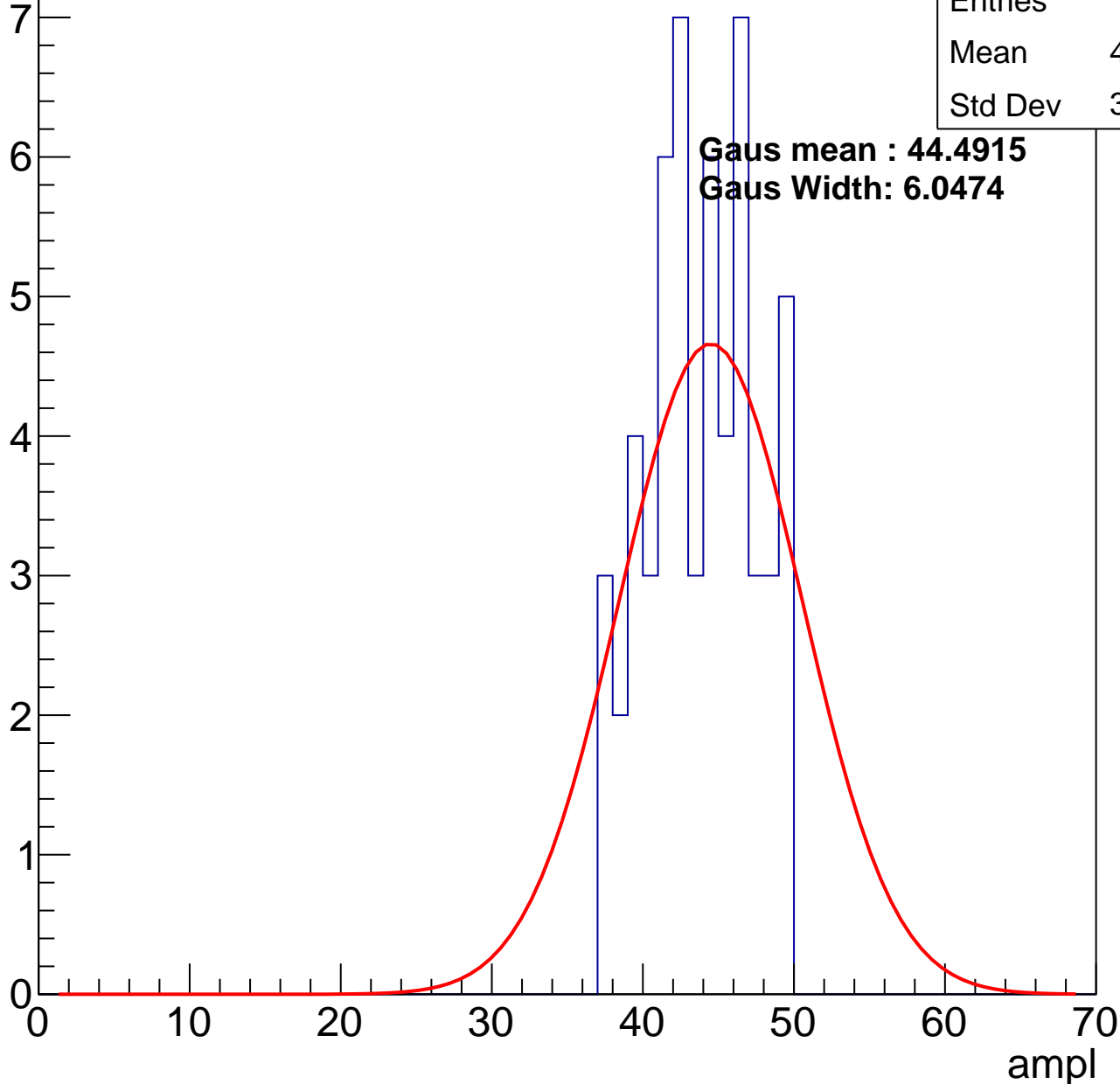
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.36
Std Dev	3.435

Gaus mean : 44.4915

Gaus Width: 6.0474

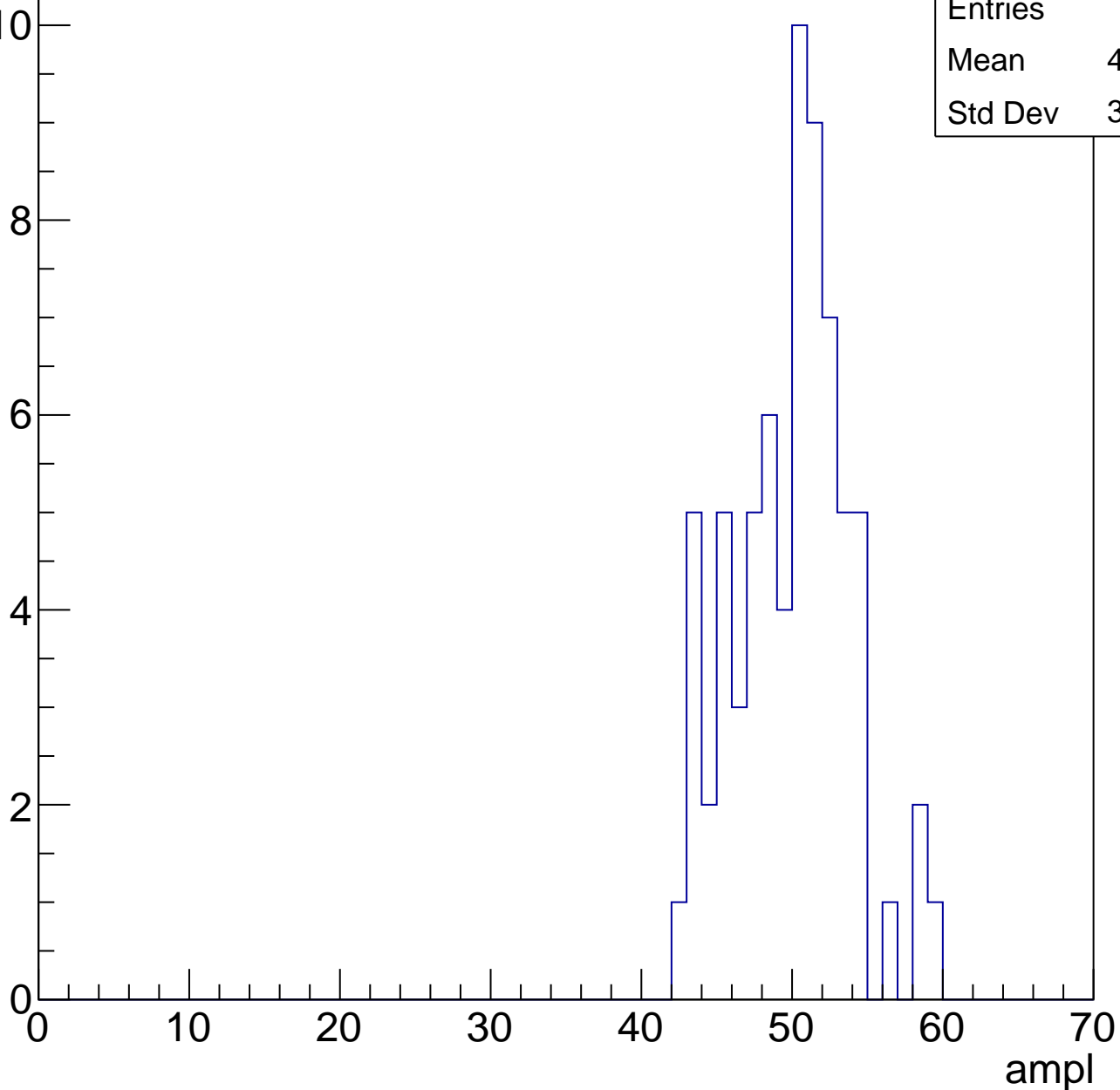


B0L001S, U15-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.52
Std Dev	3.786

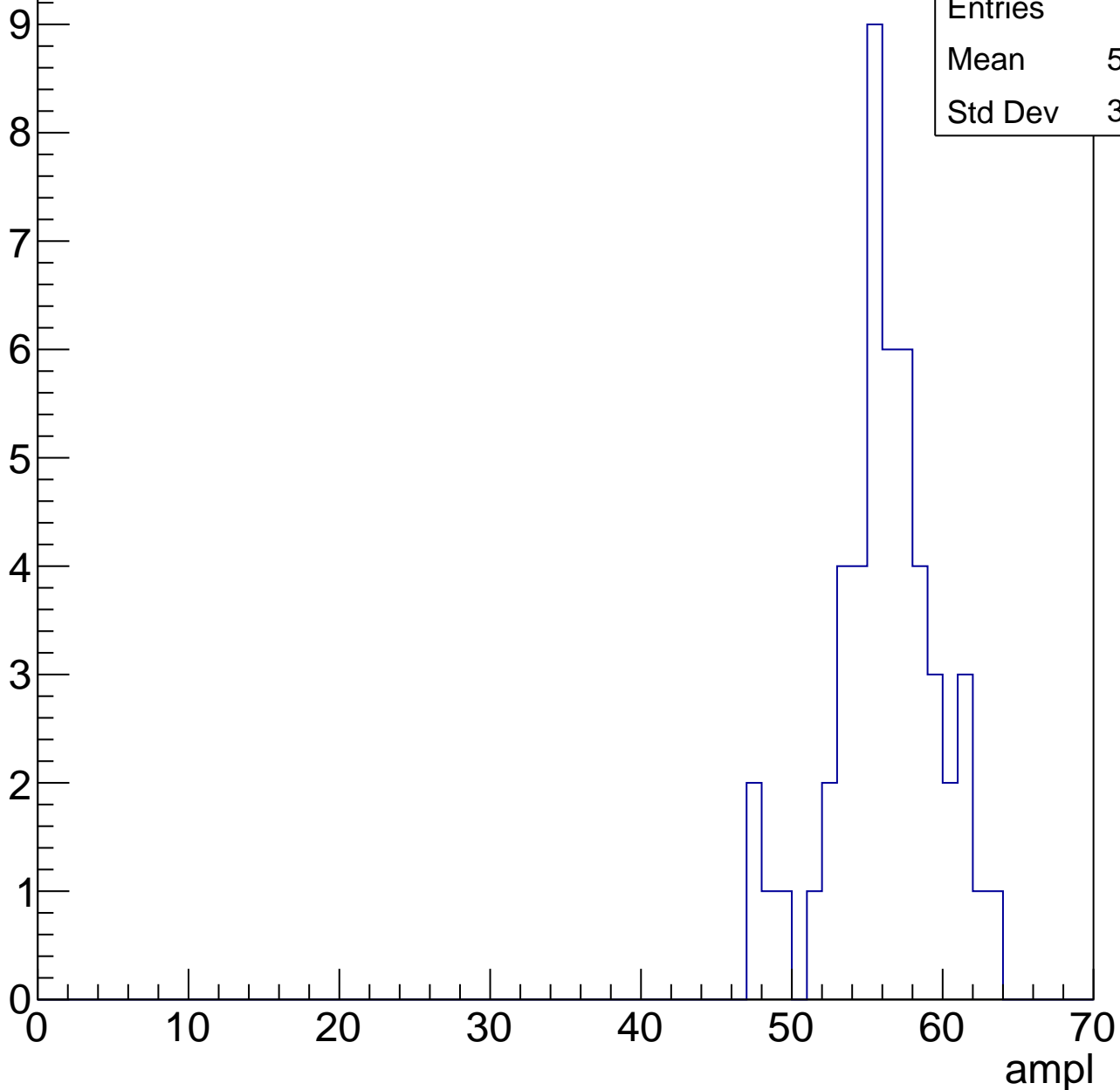


B0L001S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

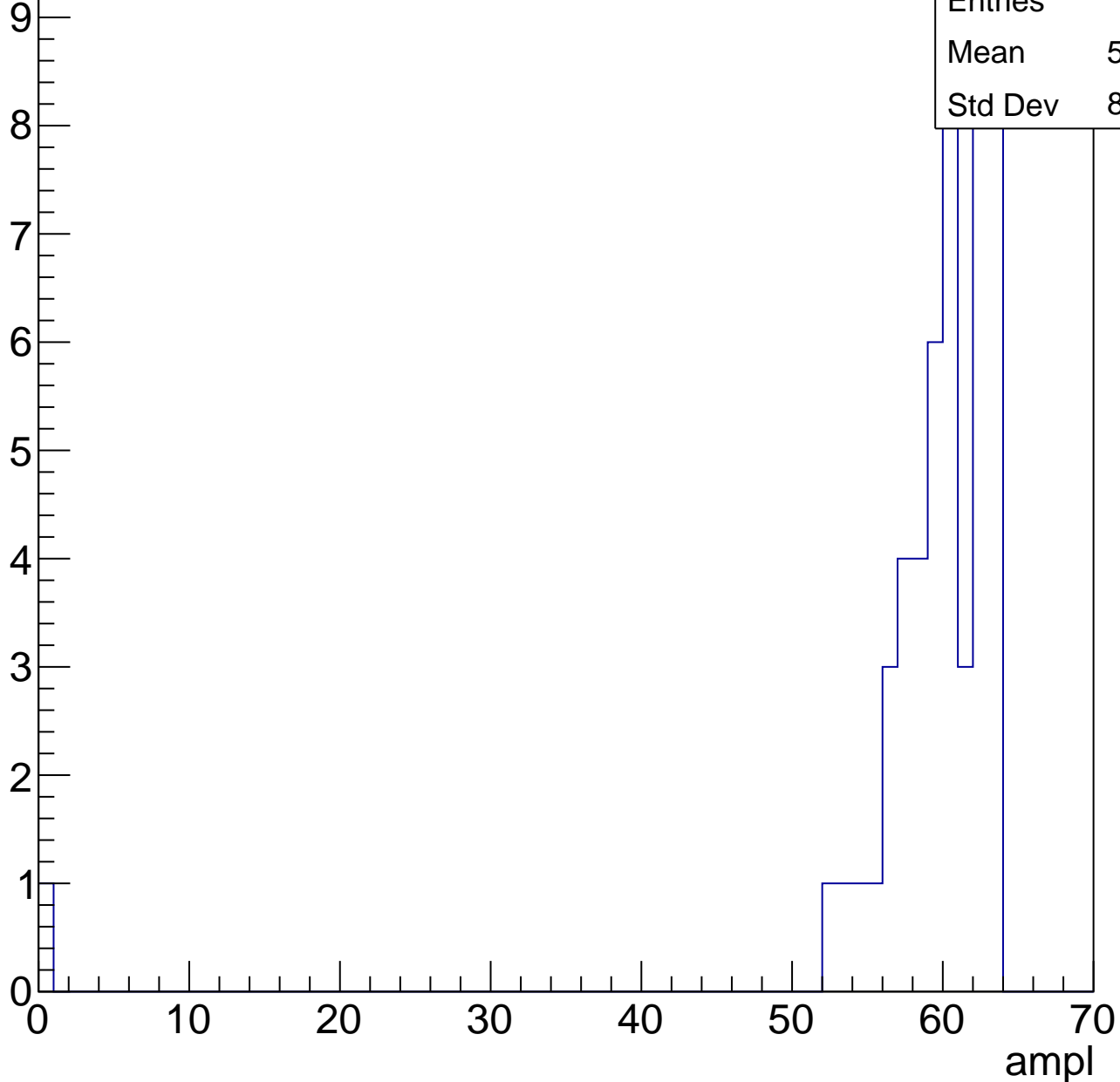
Entries	50
Mean	55.68
Std Dev	3.546



B0L001S, U15-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

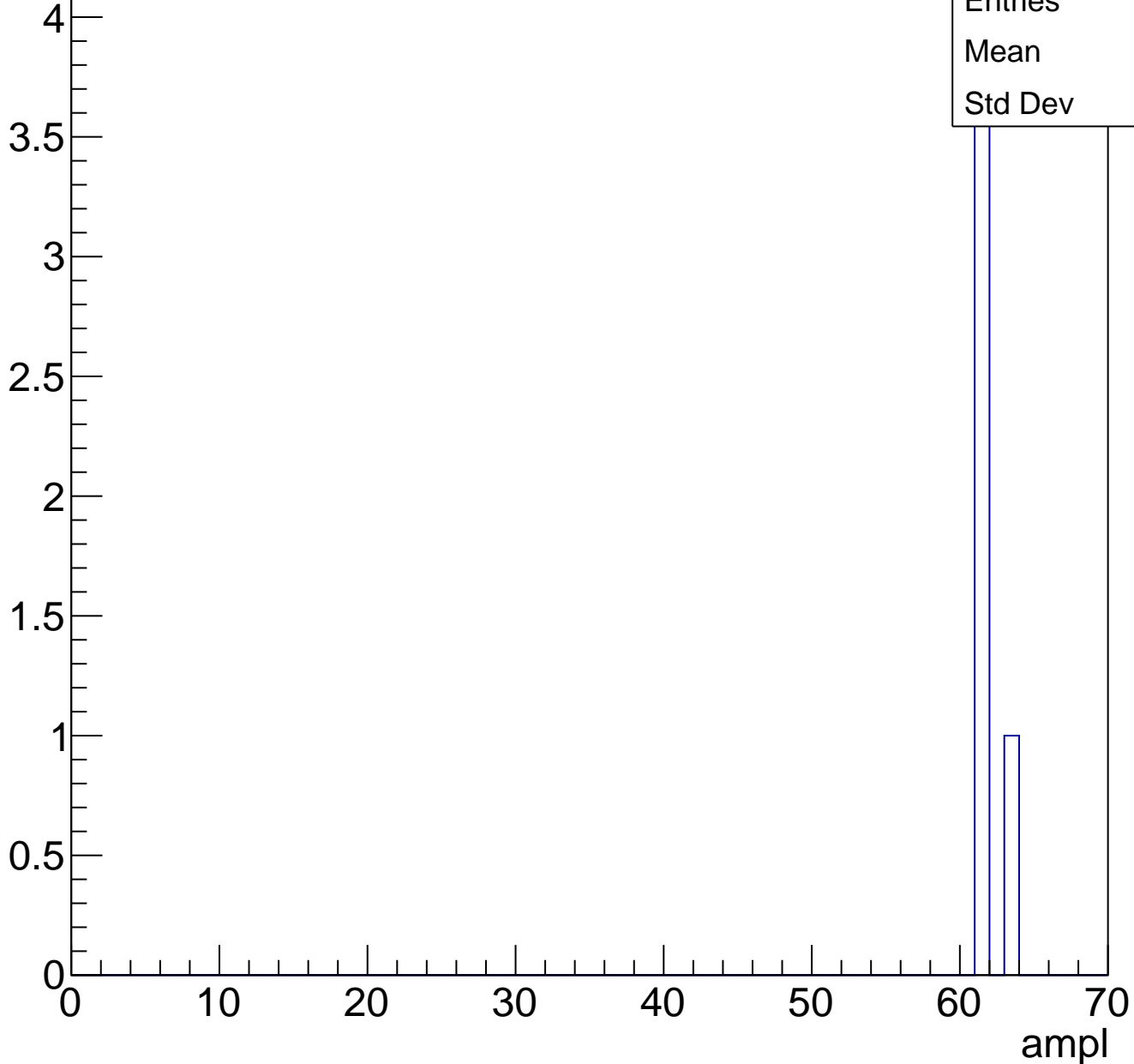
Entry



B0L001S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	61.4
Std Dev	0.8

B0L001S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch117, adc0

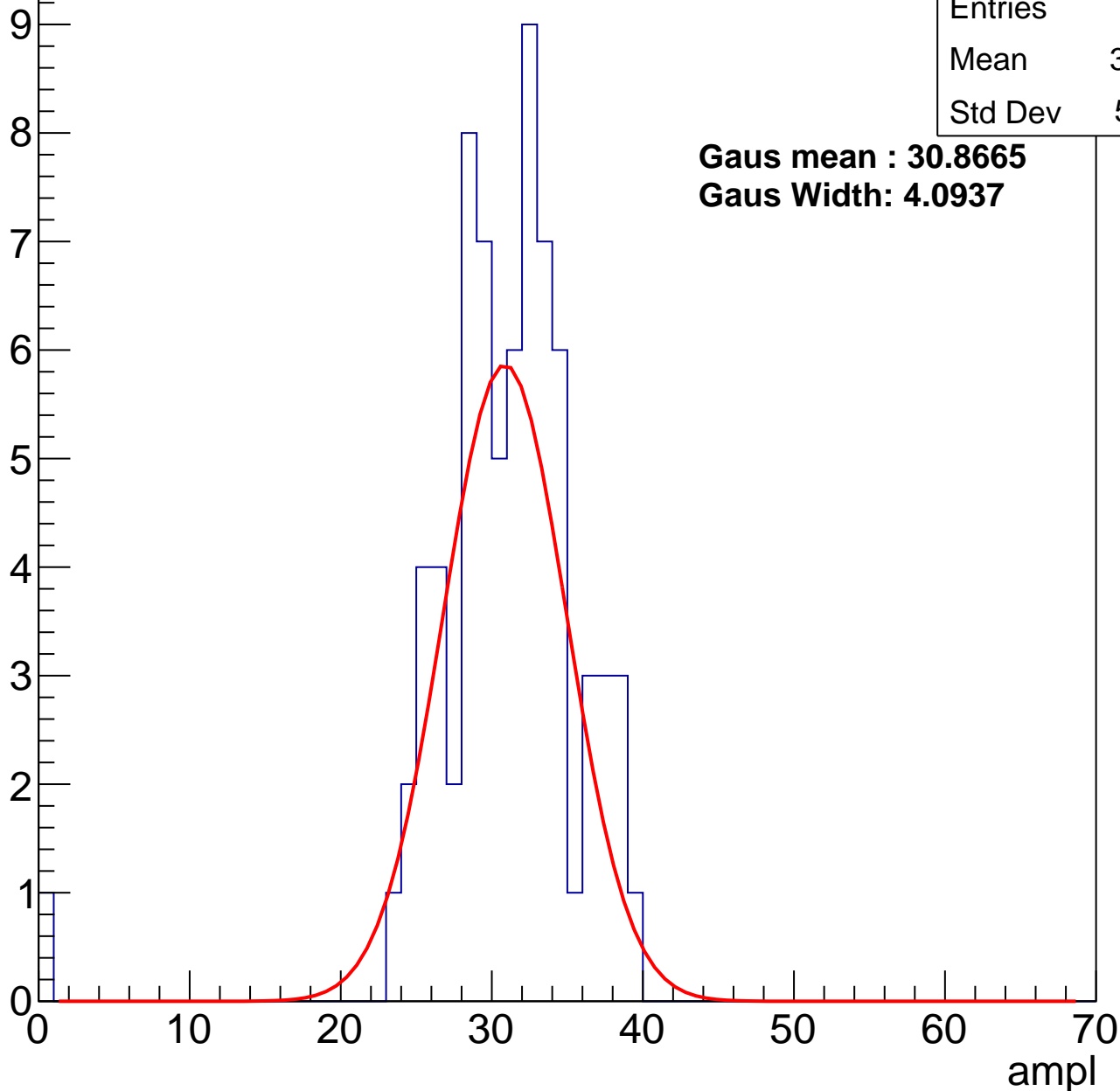
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.44
Std Dev	5.211

Gaus mean : 30.8665

Gaus Width: 4.0937



B0L001S, U15-ch117, adc1

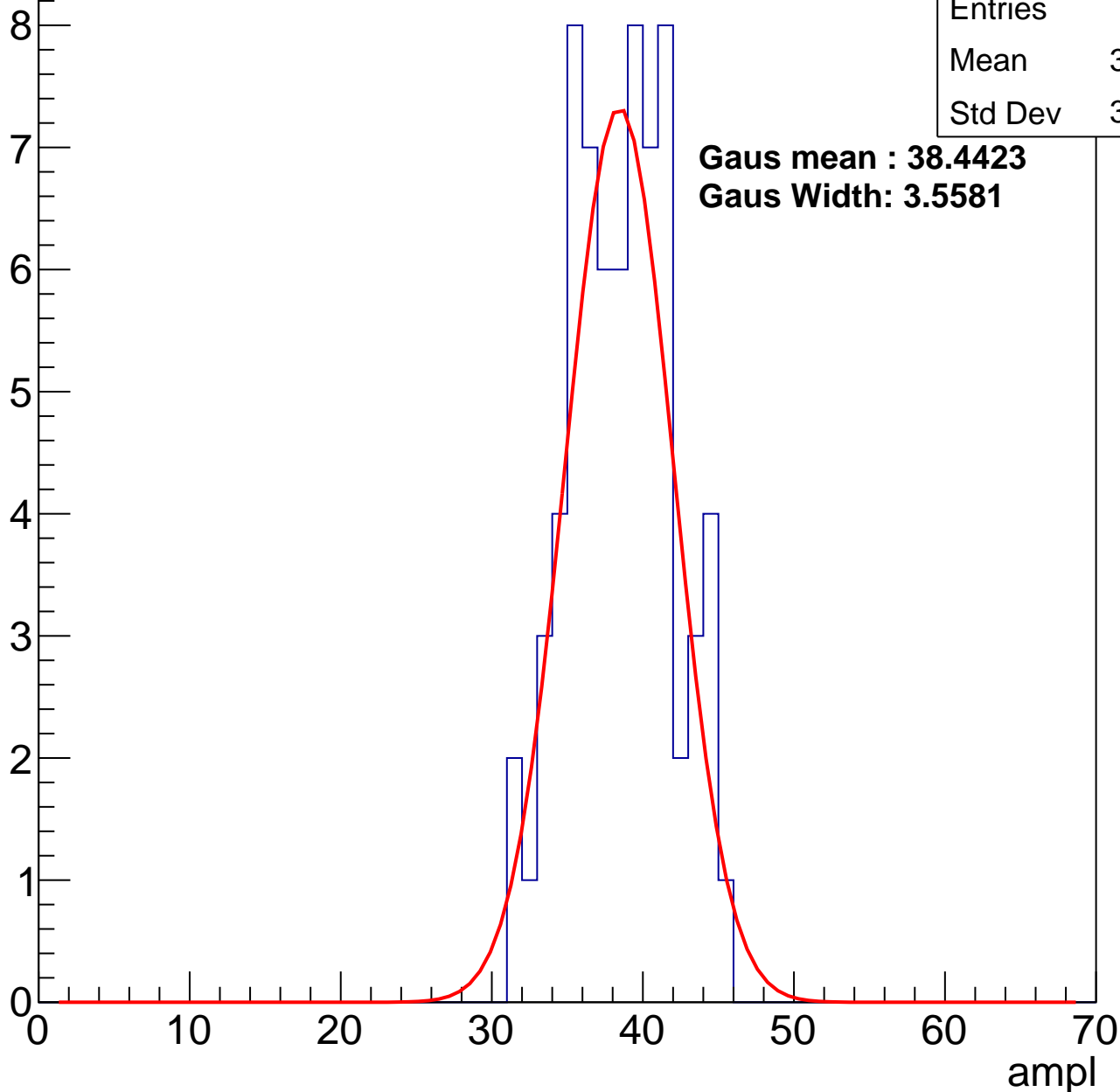
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.07
Std Dev	3.352

Gaus mean : 38.4423

Gaus Width: 3.5581



B0L001S, U15-ch117, adc2

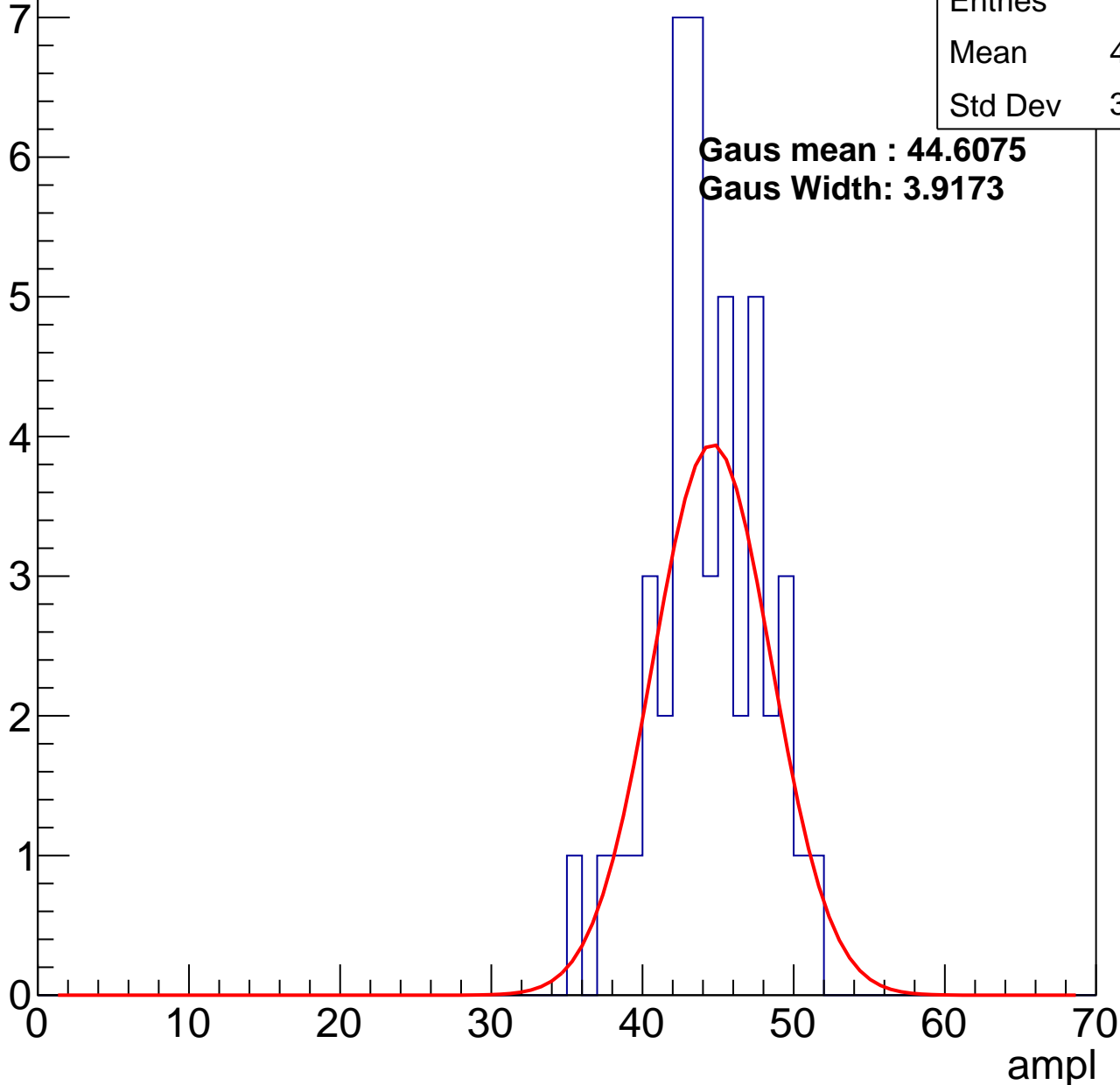
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	43.87
Std Dev	3.468

Gaus mean : 44.6075

Gaus Width: 3.9173

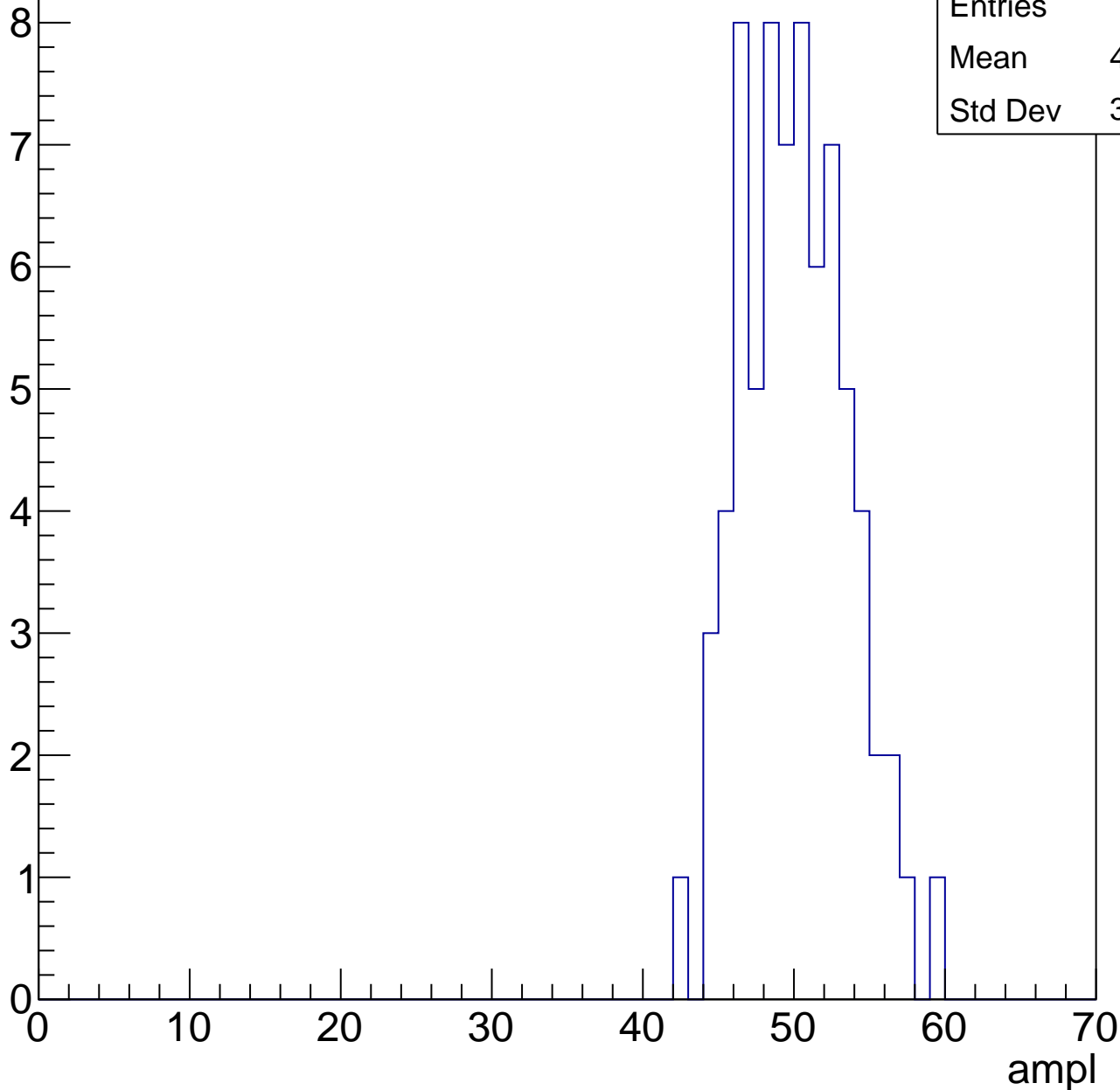


B0L001S, U15-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	49.62
Std Dev	3.474

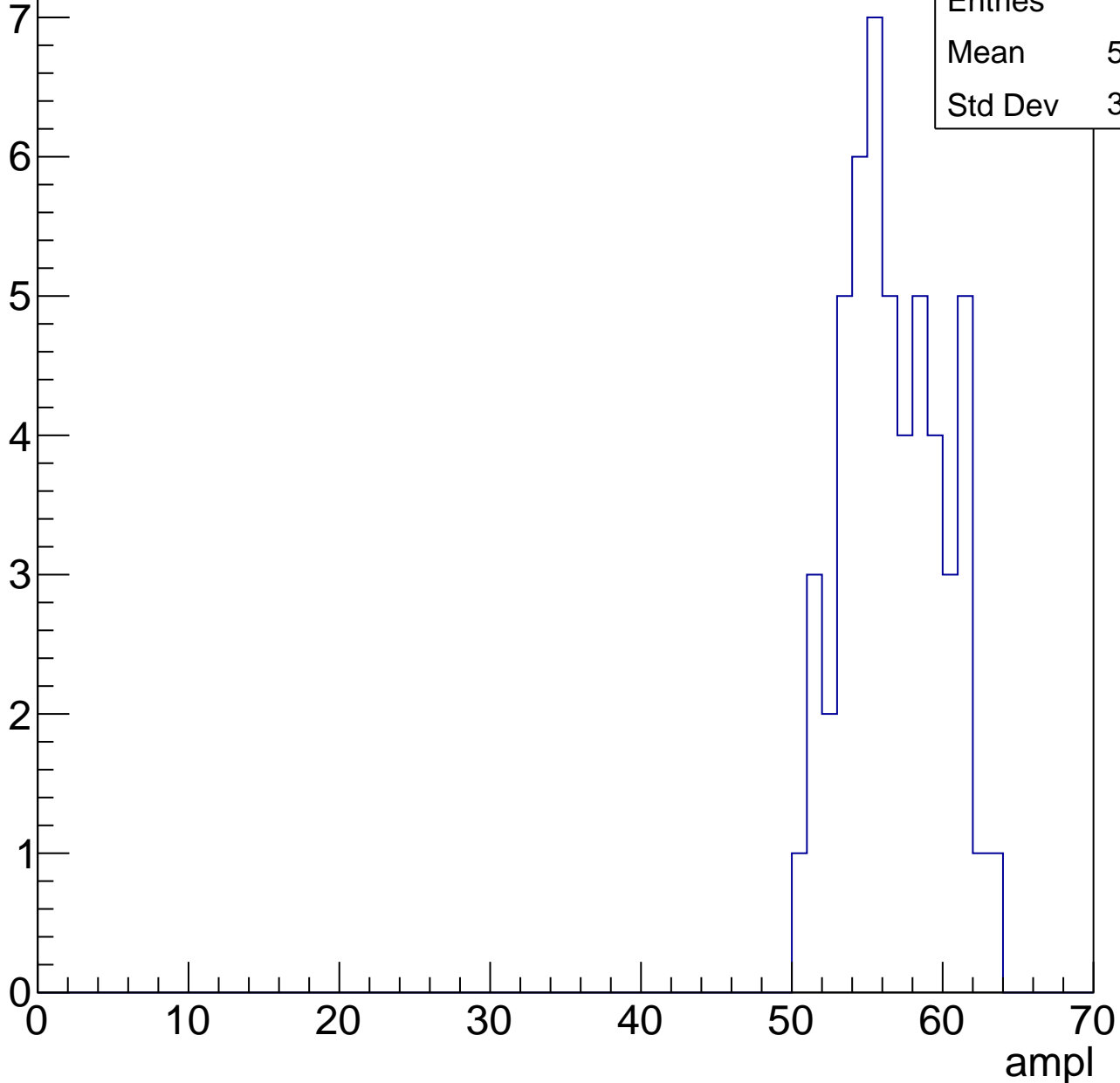


B0L001S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.25
Std Dev	3.204

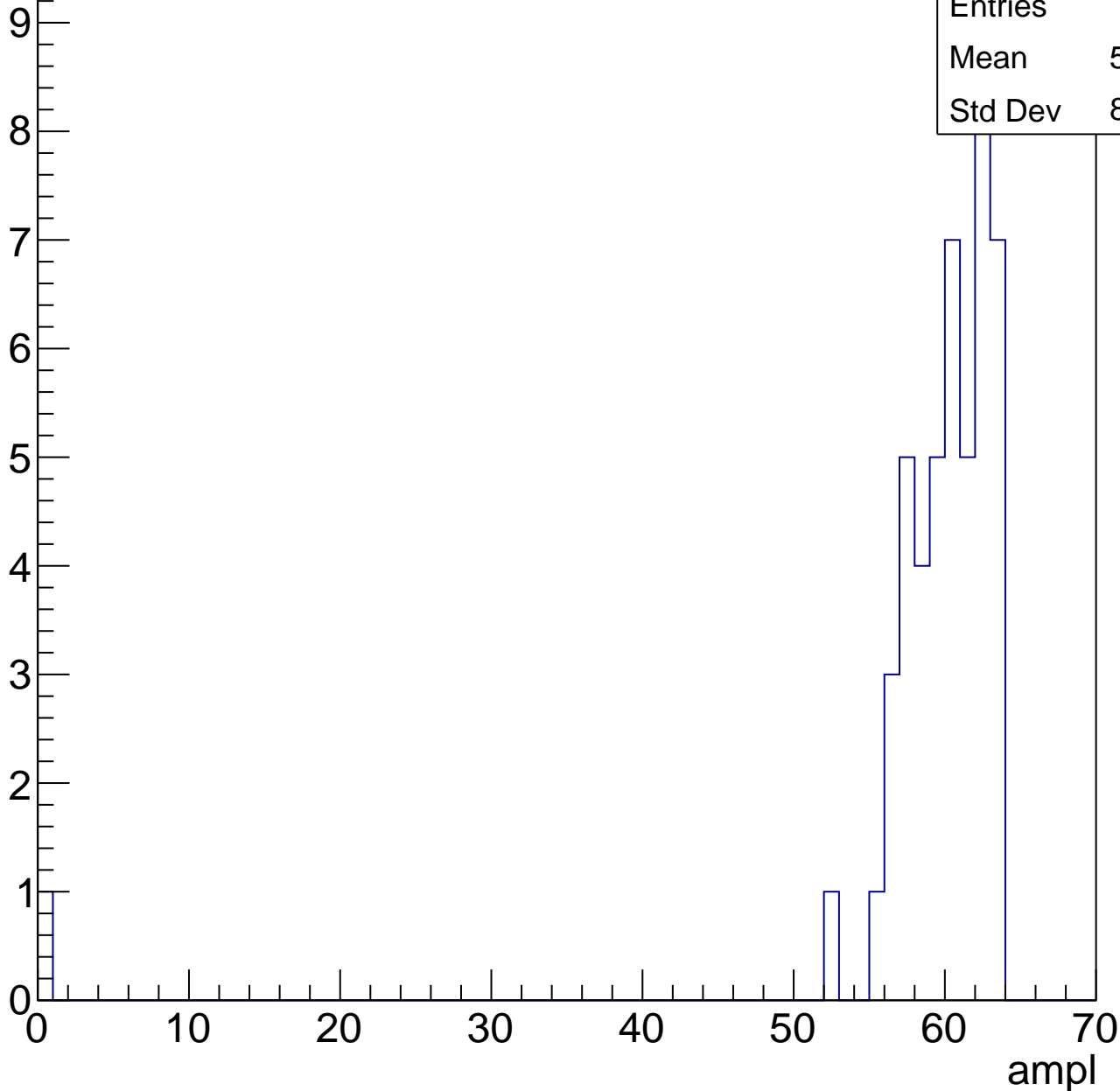


B0L001S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

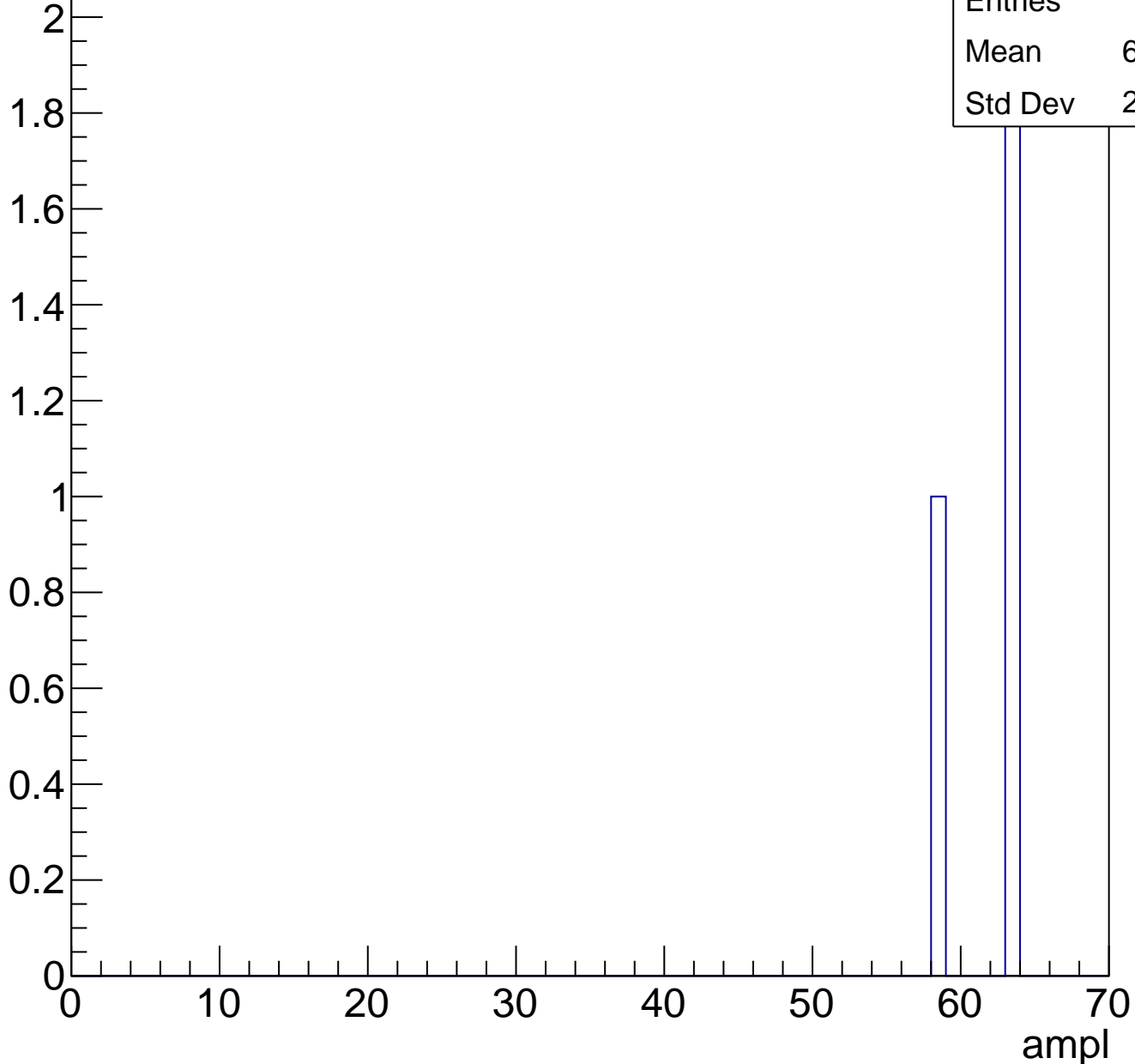
Entries	48
Mean	58.56
Std Dev	8.907



B0L001S, U15-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch118, adc0

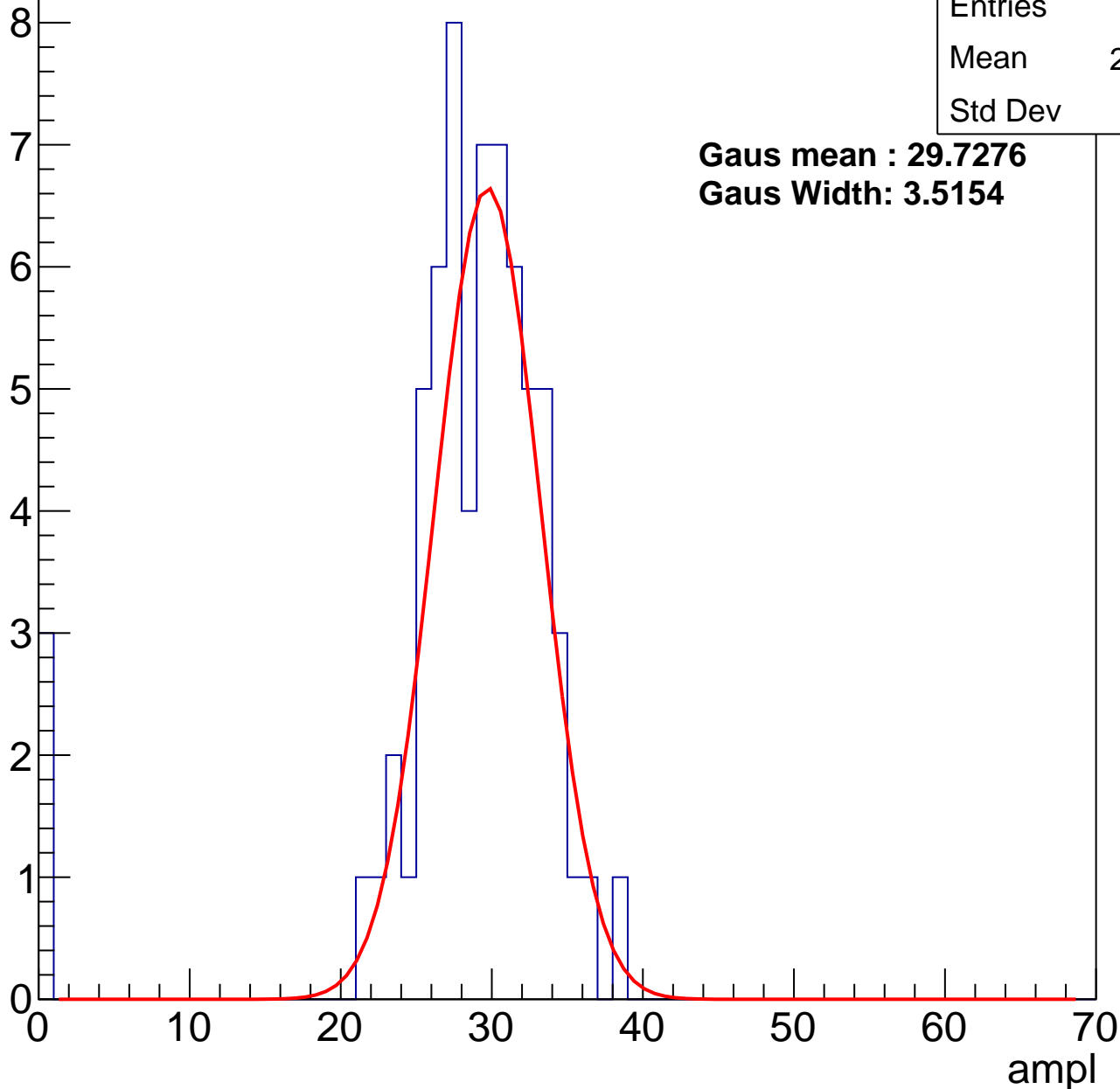
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	27.72
Std Dev	6.9

Gaus mean : 29.7276

Gaus Width: 3.5154



B0L001S, U15-ch118, adc1

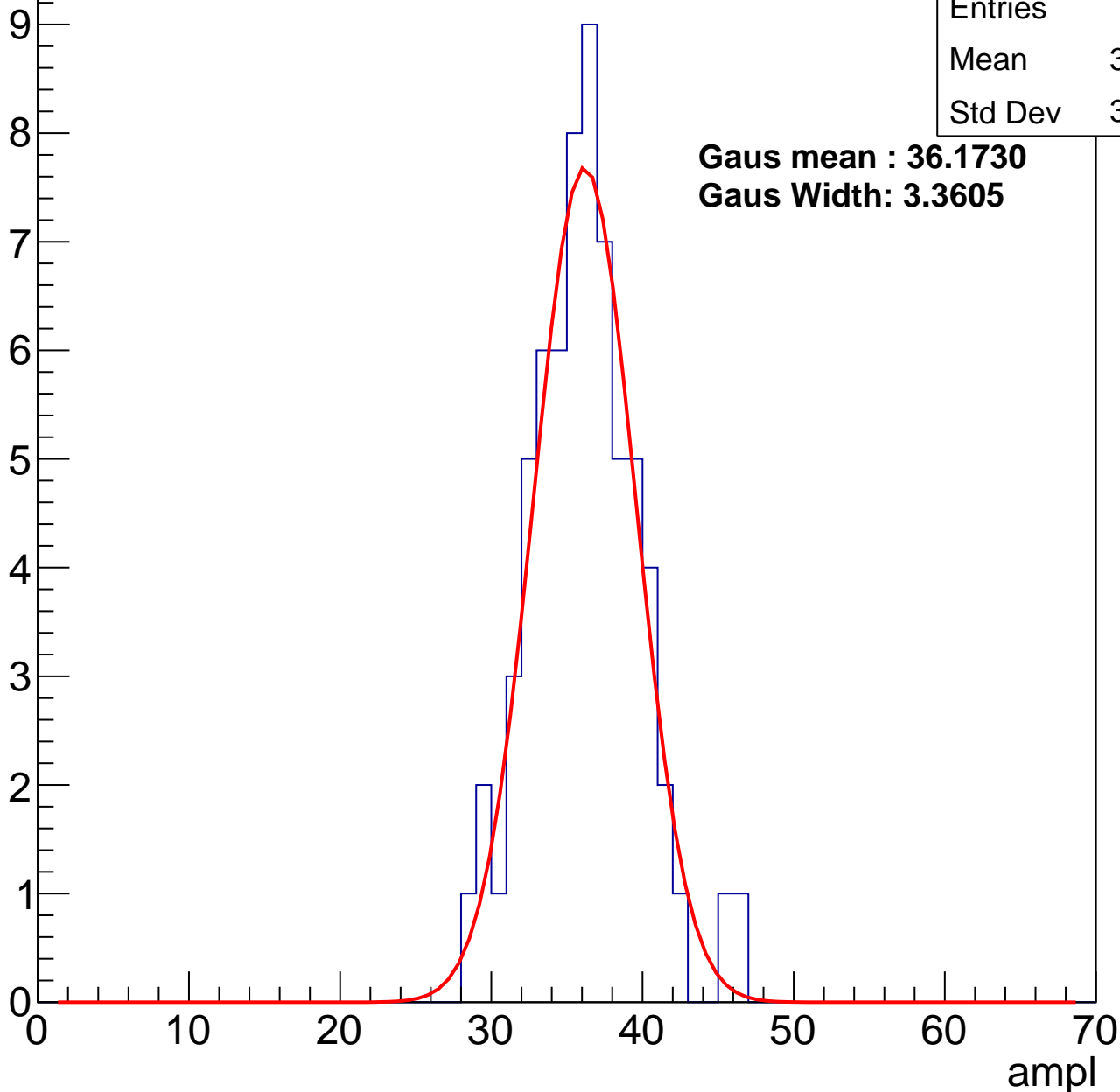
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	35.73
Std Dev	3.535

Gaus mean : 36.1730

Gaus Width: 3.3605



B0L001S, U15-ch118, adc2

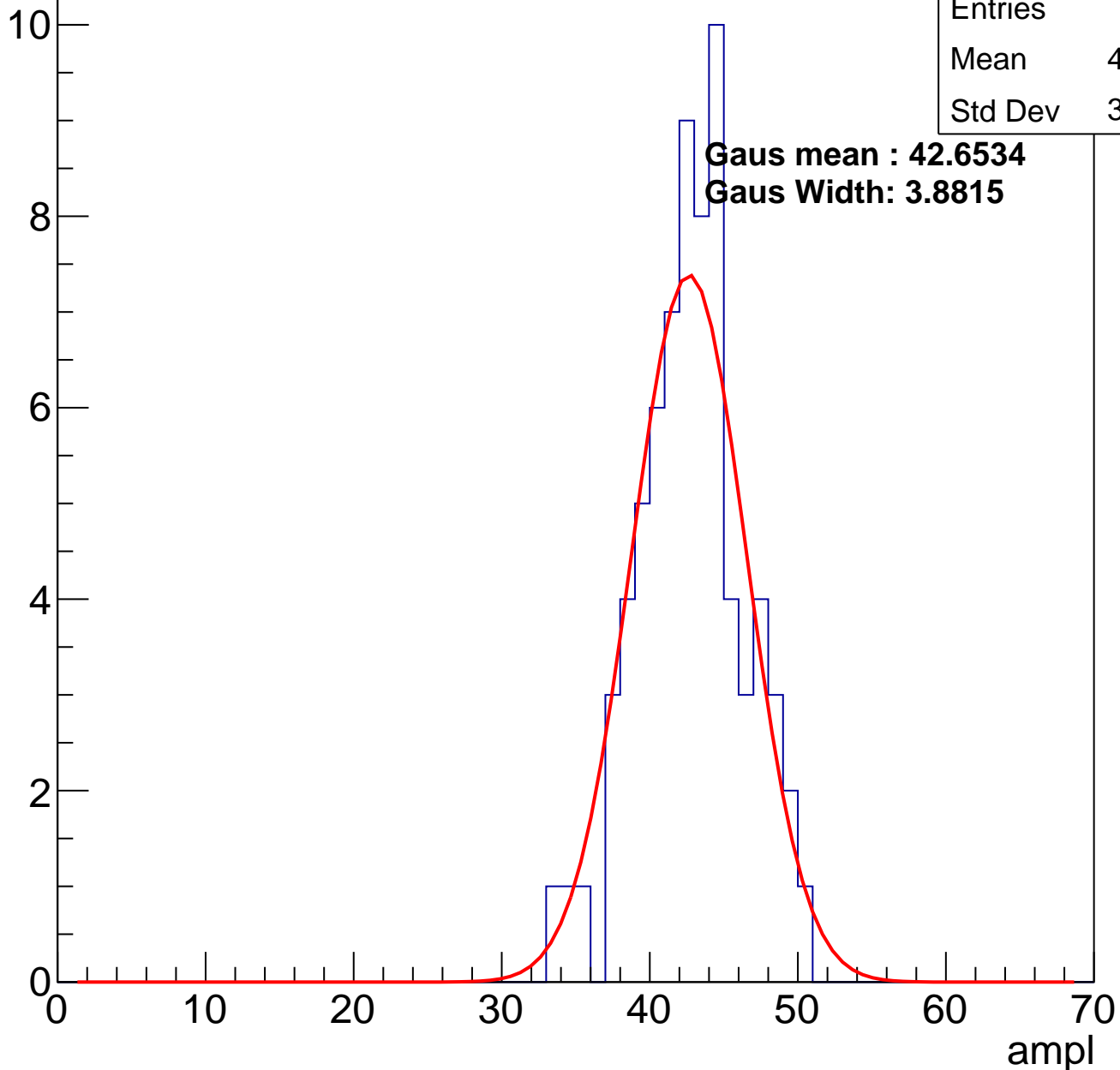
calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	42.32
Std Dev	3.543

Gaus mean : 42.6534

Gaus Width: 3.8815

Entry

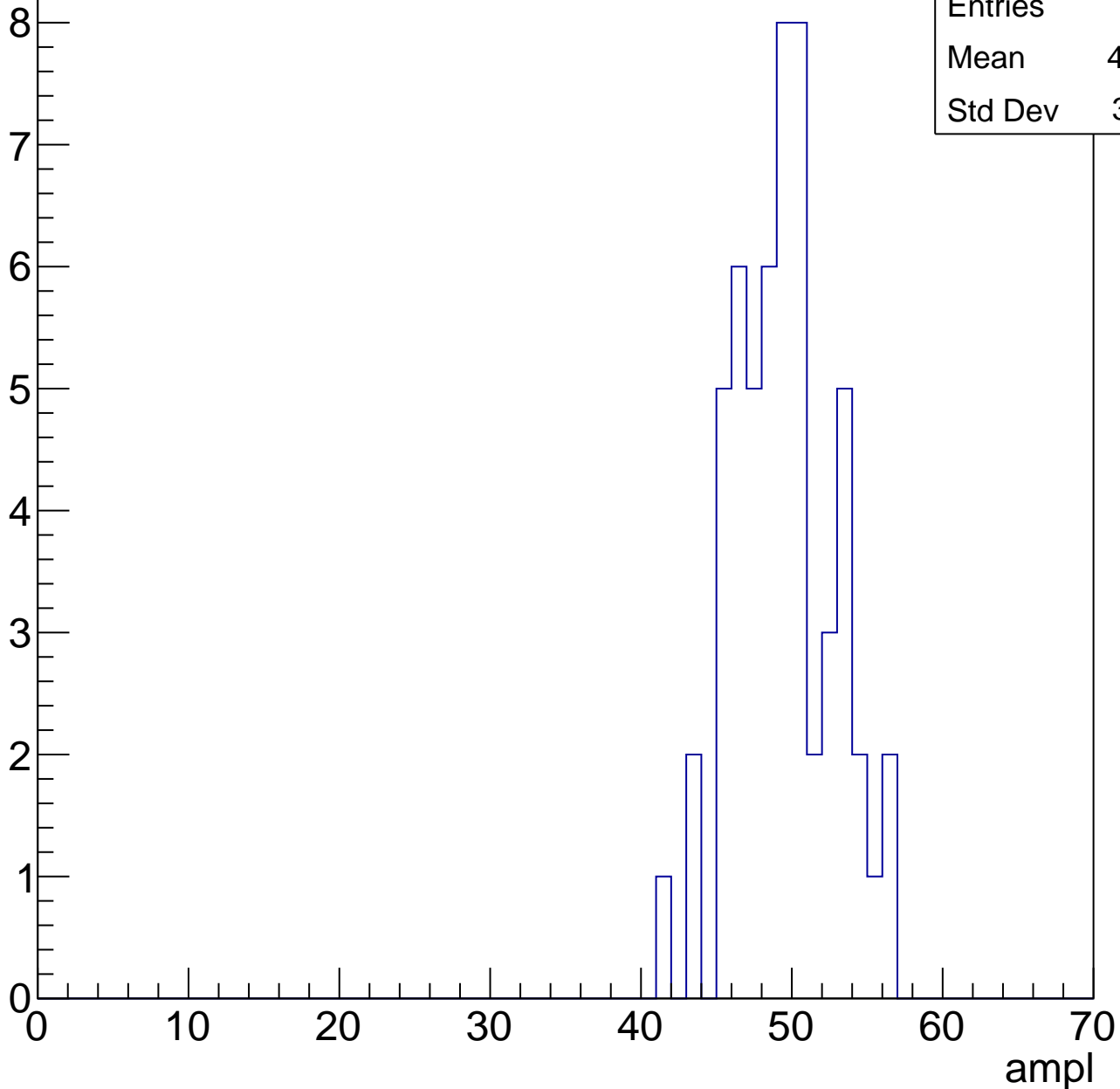


B0L001S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	48.95
Std Dev	3.281

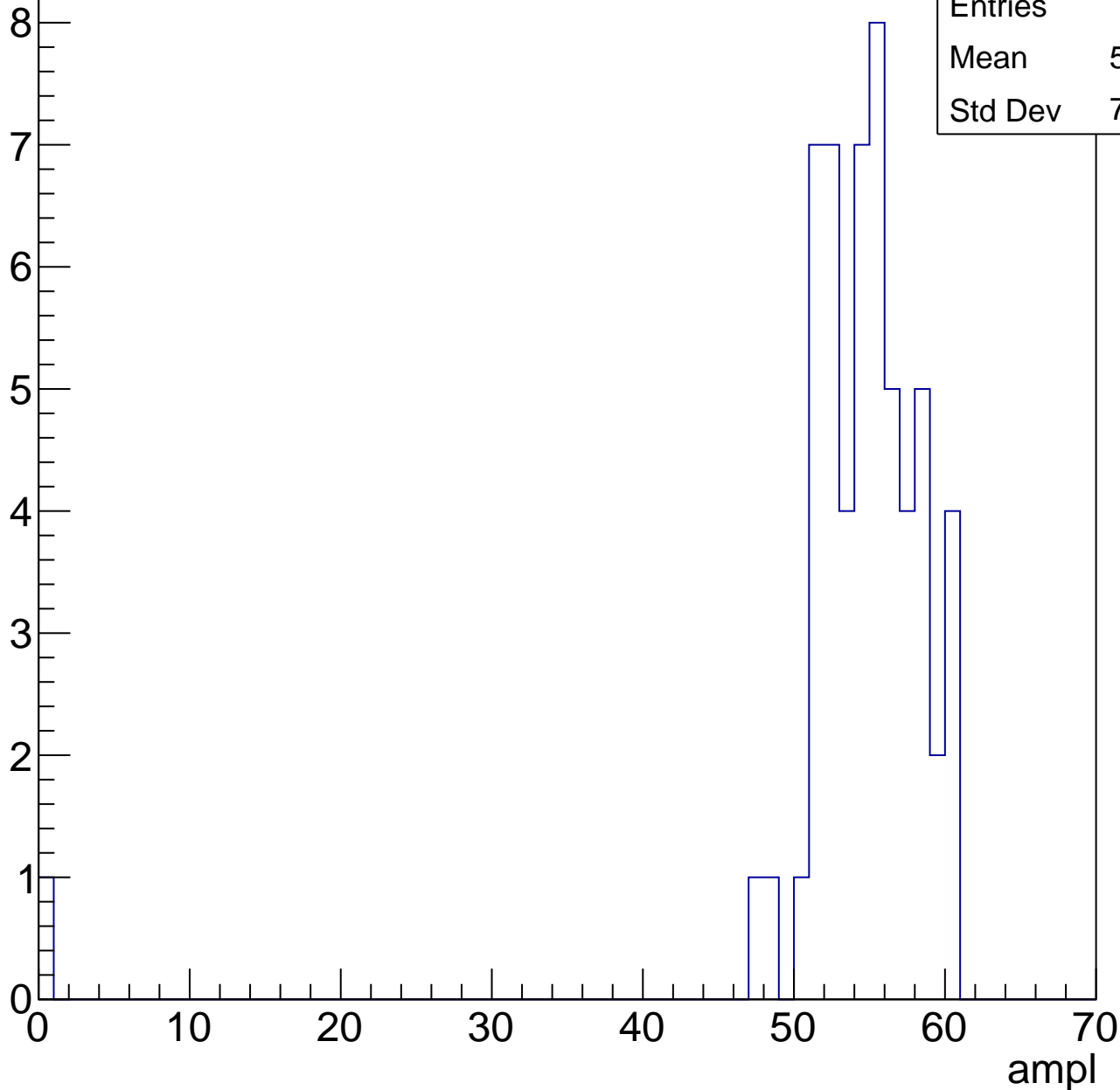


B0L001S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	53.54
Std Dev	7.766



B0L001S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	59.53
Std Dev	2.689

Entry

10

8

6

4

2

0

0

10

20

30

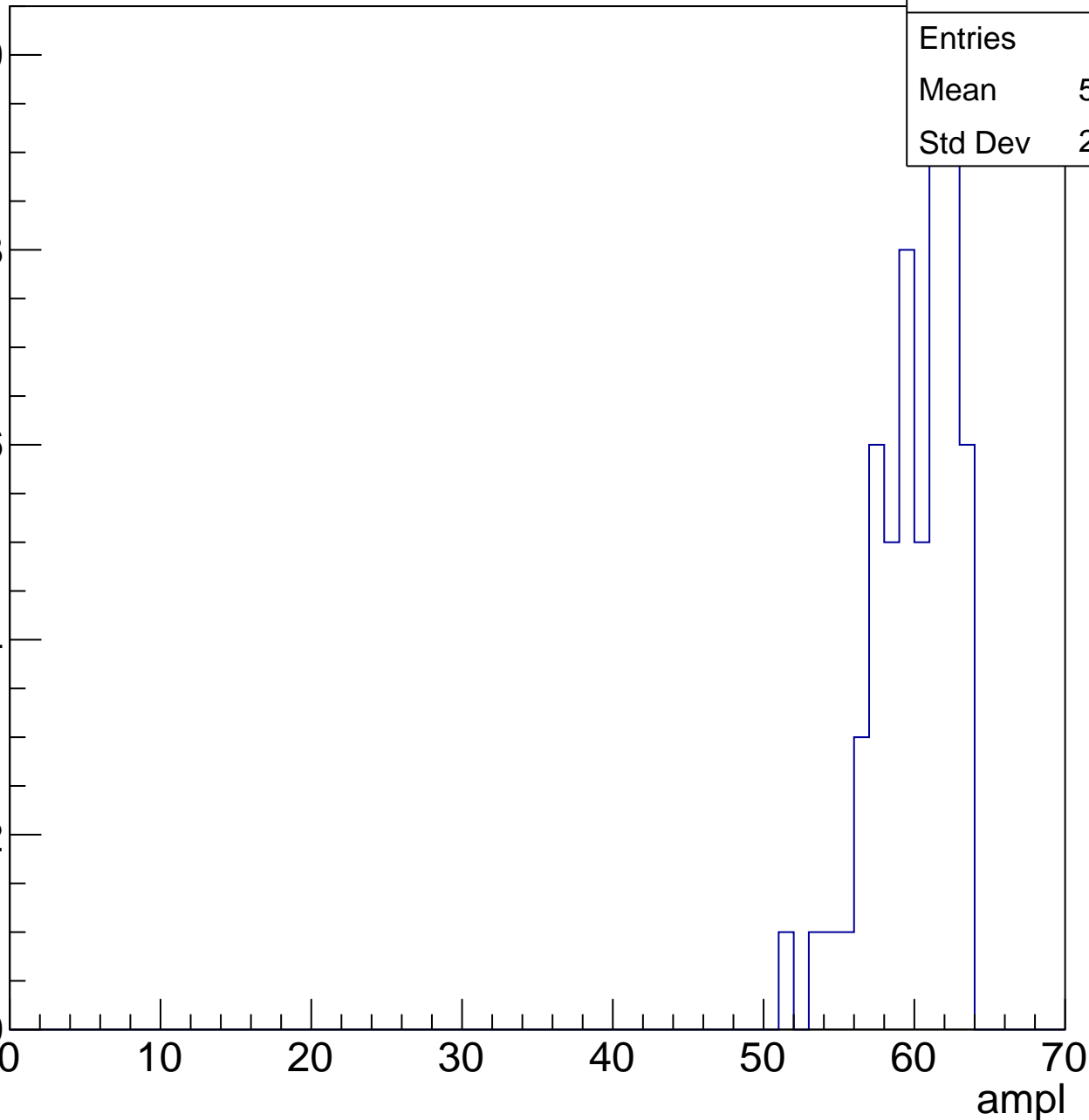
40

50

60

ampl

70



B0L001S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch119, adc0

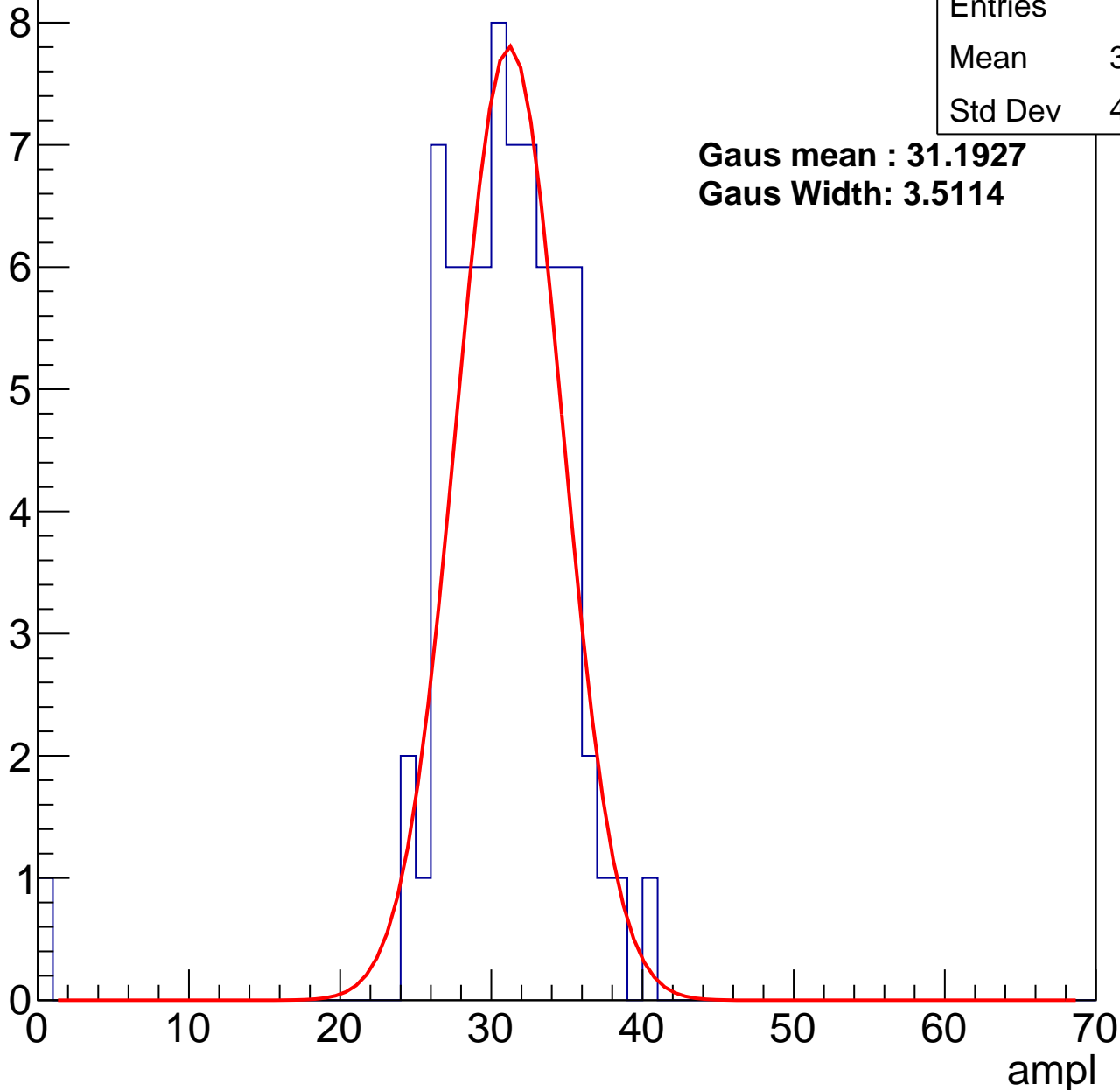
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.26
Std Dev	4.943

Gaus mean : 31.1927

Gaus Width: 3.5114



B0L001S, U15-ch119, adc1

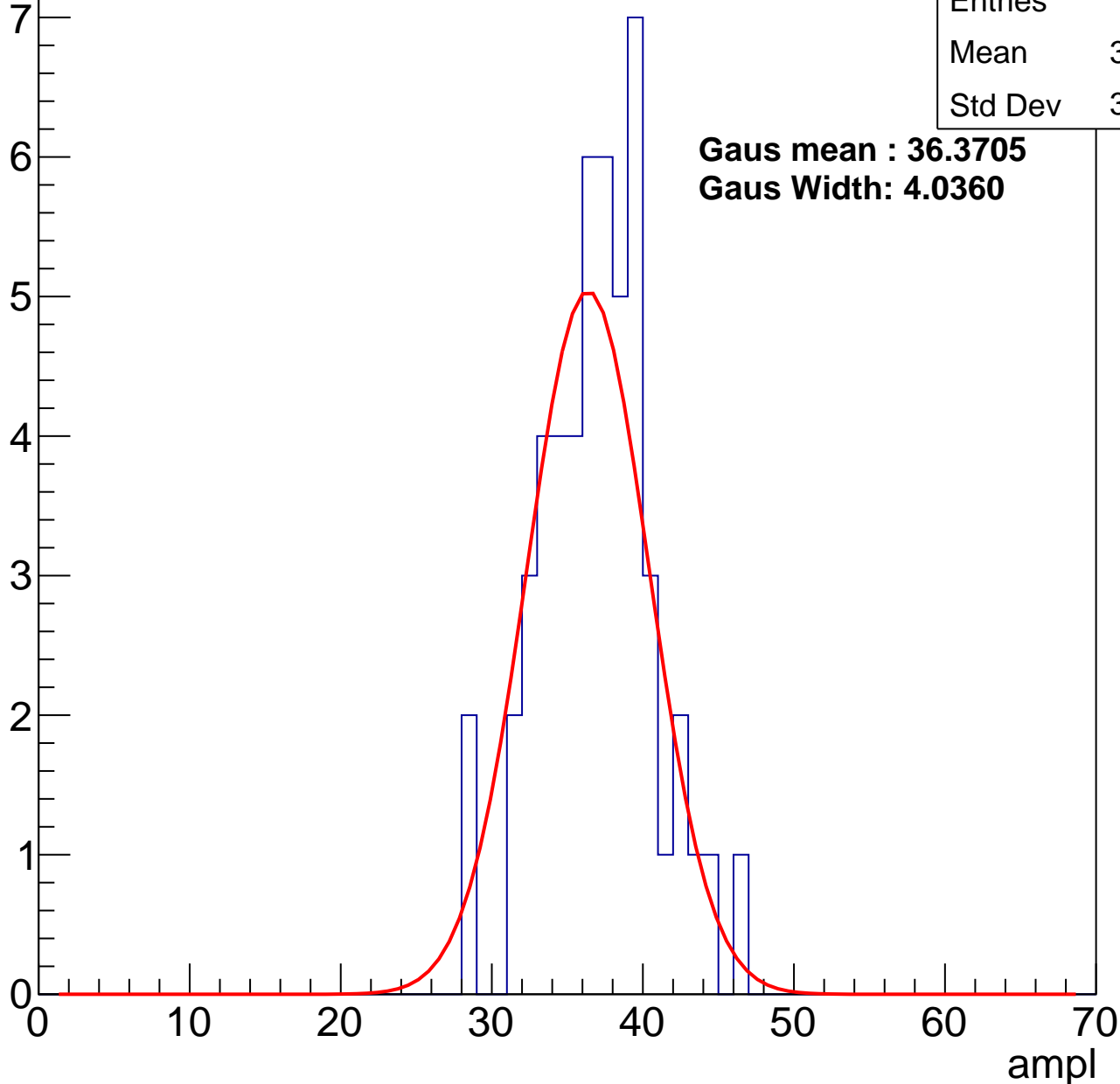
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	36.56
Std Dev	3.702

Gaus mean : 36.3705

Gaus Width: 4.0360



B0L001S, U15-ch119, adc2

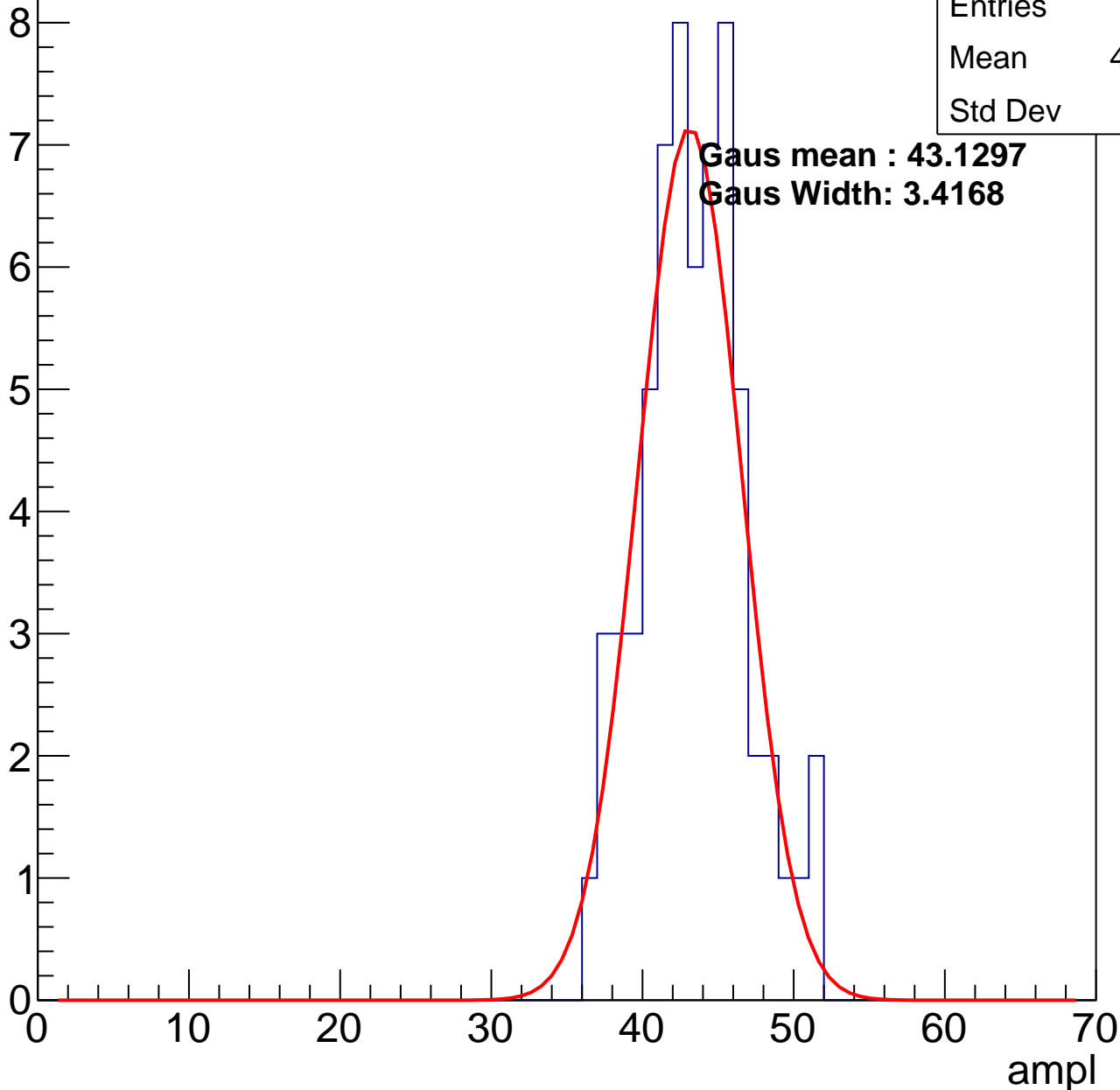
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	42.94
Std Dev	3.4

Gaus mean : 43.1297

Gaus Width: 3.4168

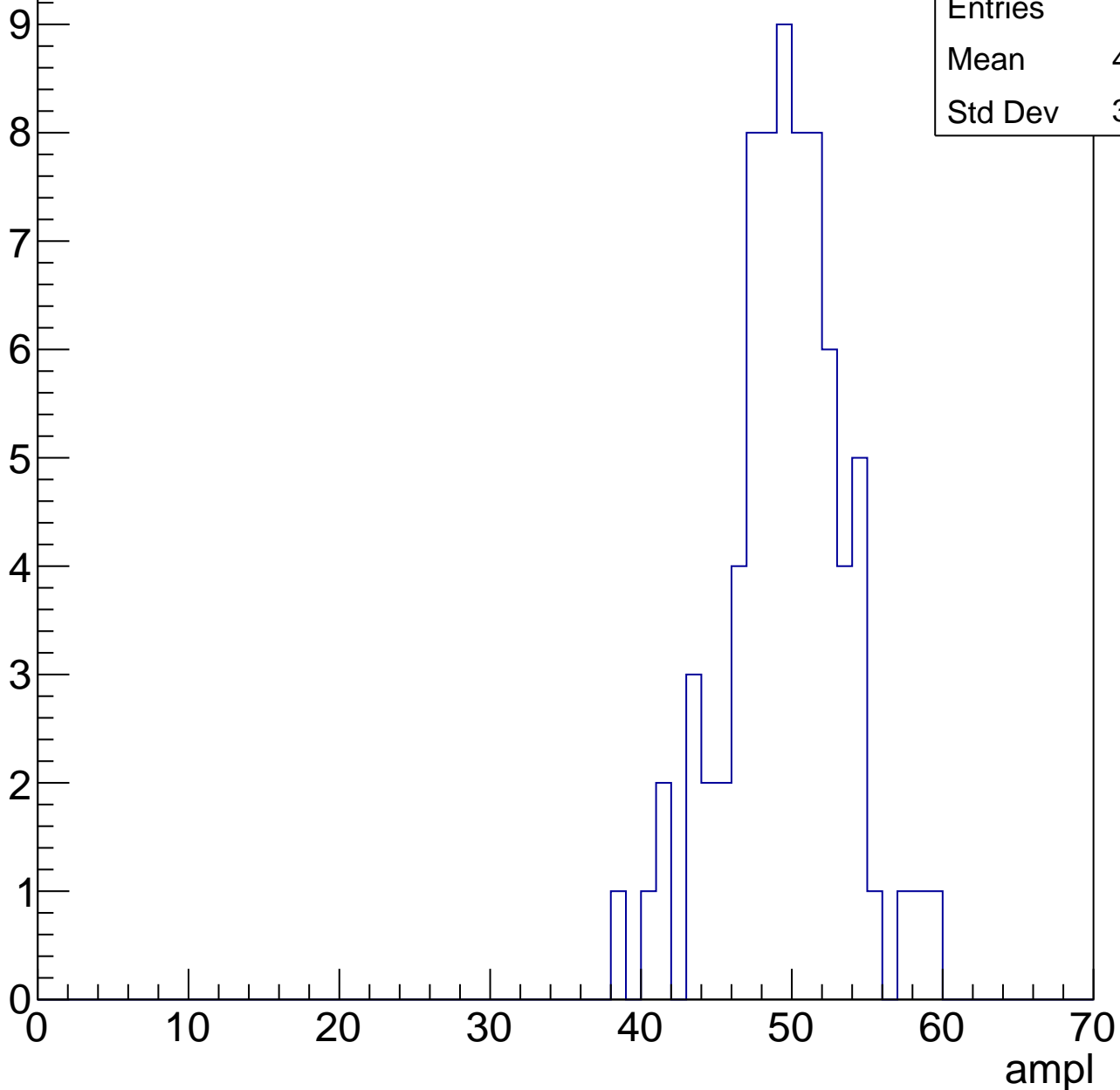


B0L001S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	49.11
Std Dev	3.931

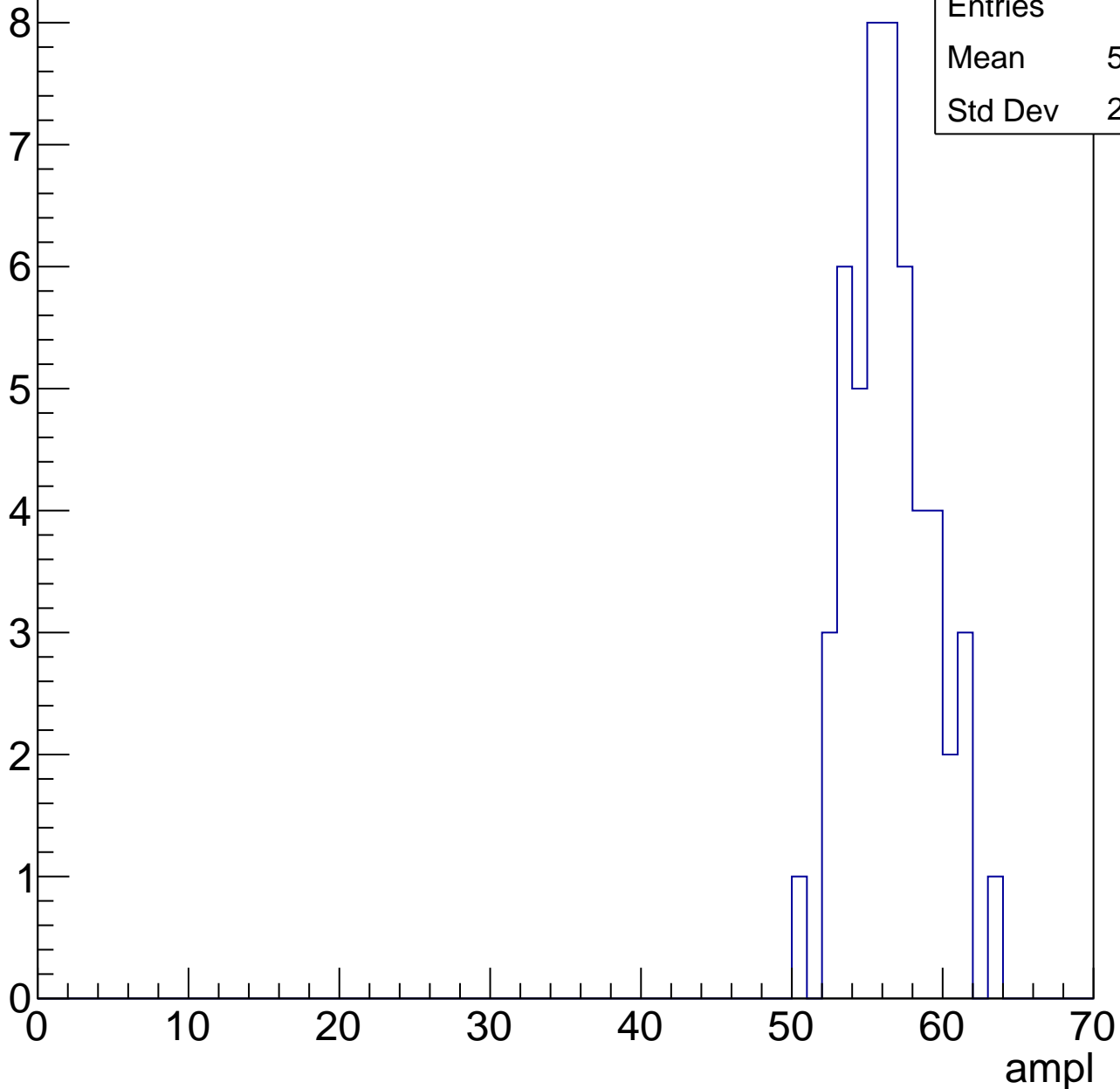


B0L001S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.04
Std Dev	2.729

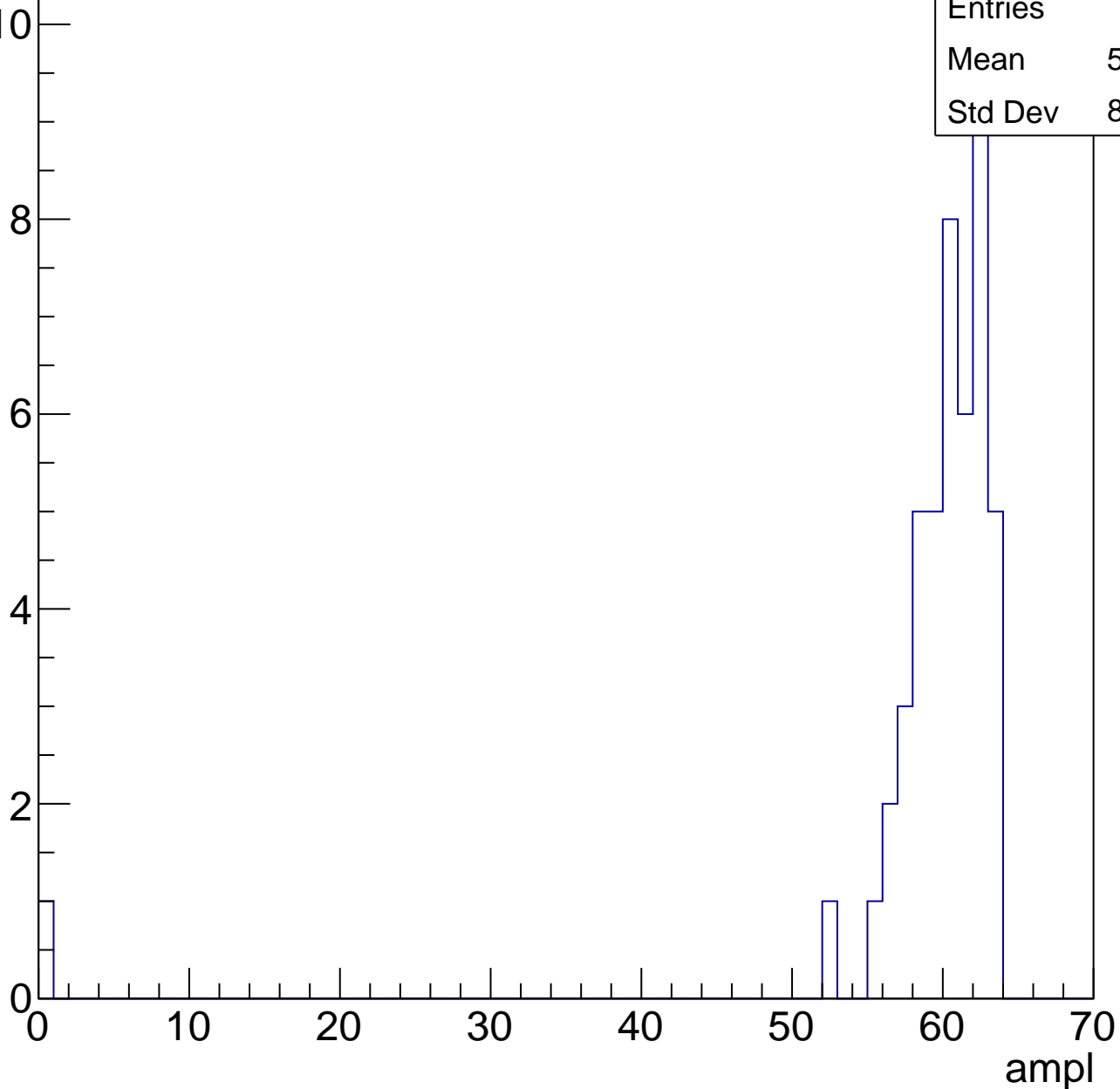


B0L001S, U15-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

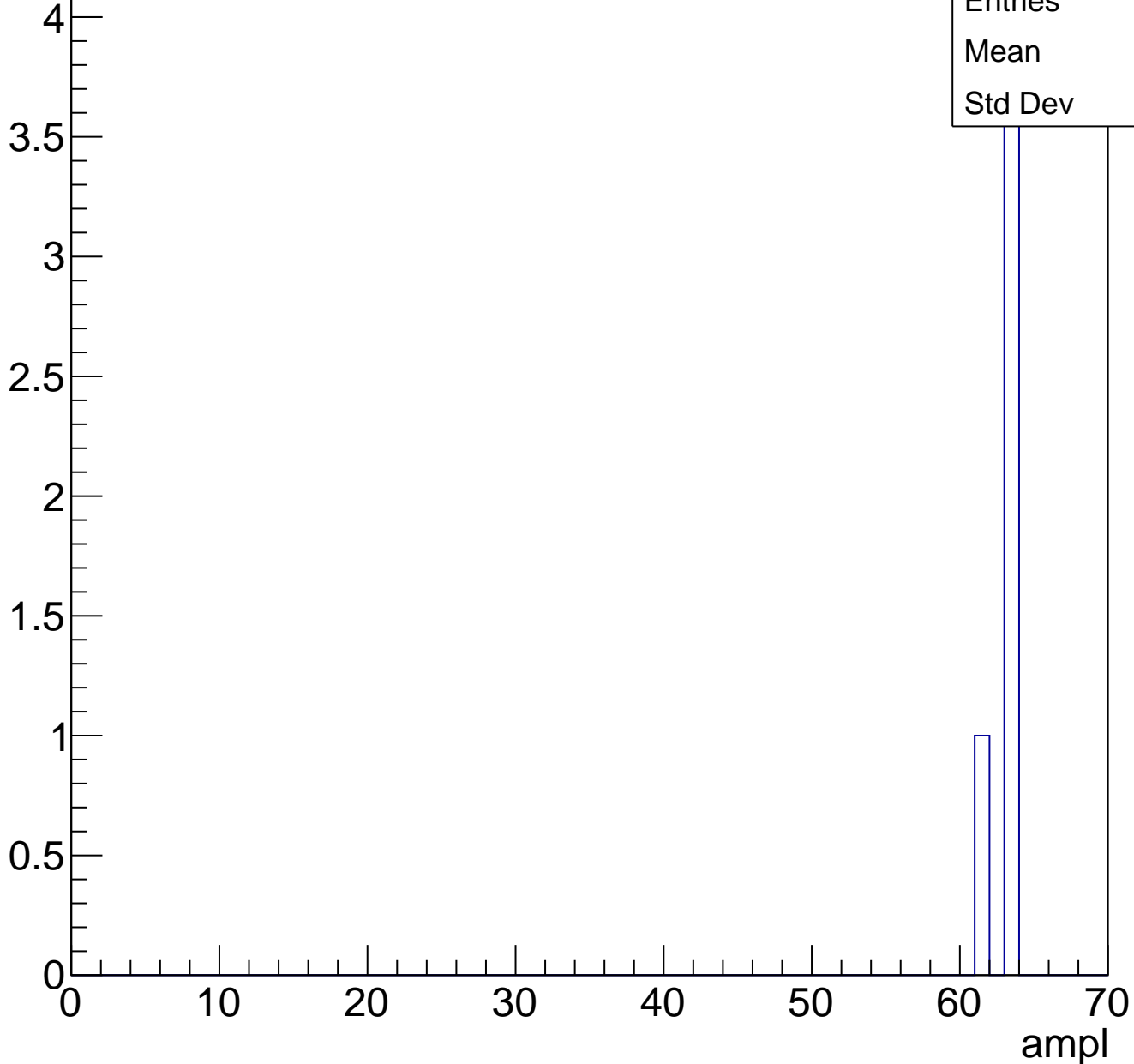
Entries	47
Mean	58.64
Std Dev	8.964



B0L001S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch120, adc0

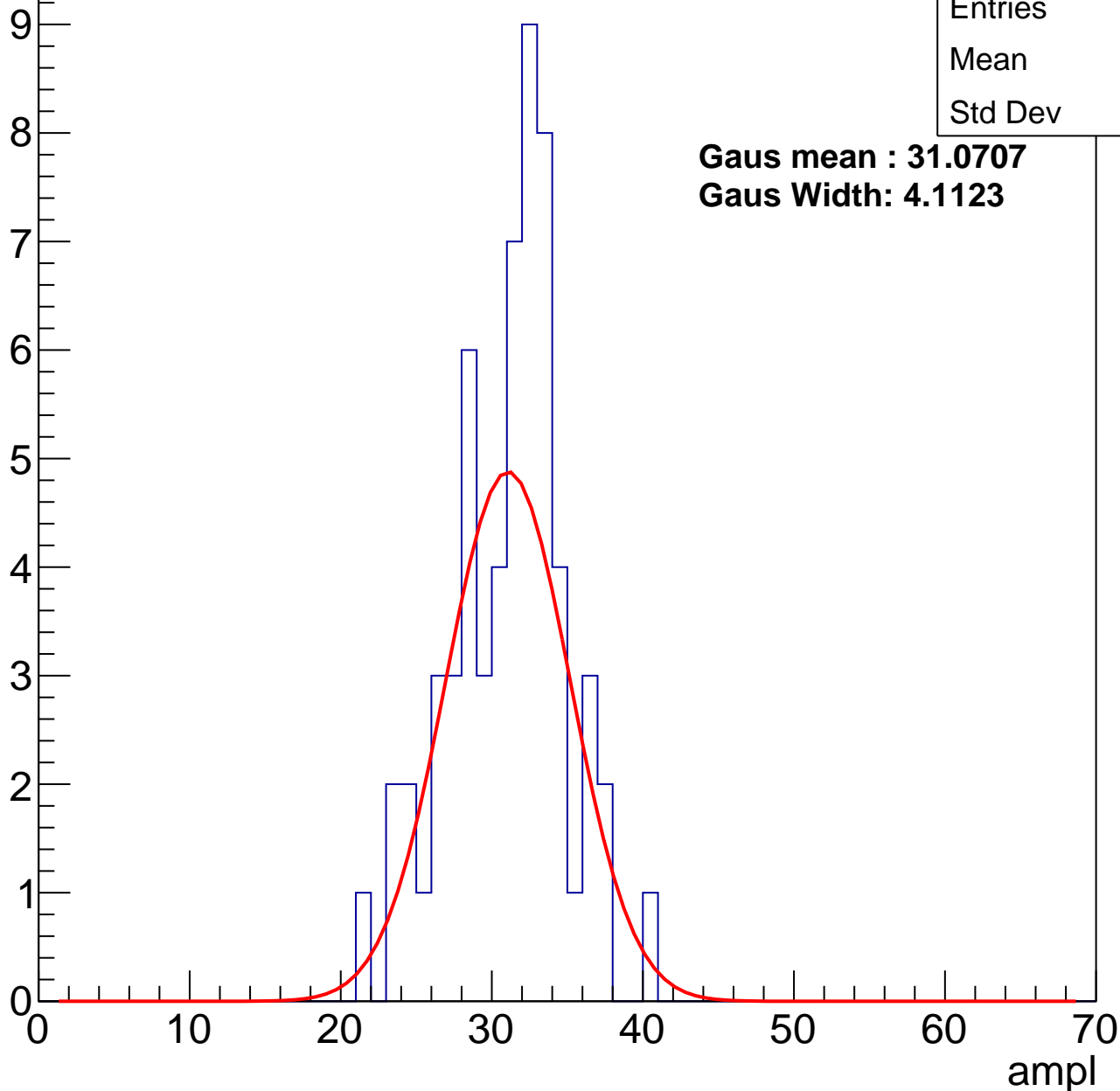
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.6
Std Dev	3.8

Gaus mean : 31.0707

Gaus Width: 4.1123



B0L001S, U15-ch120, adc1

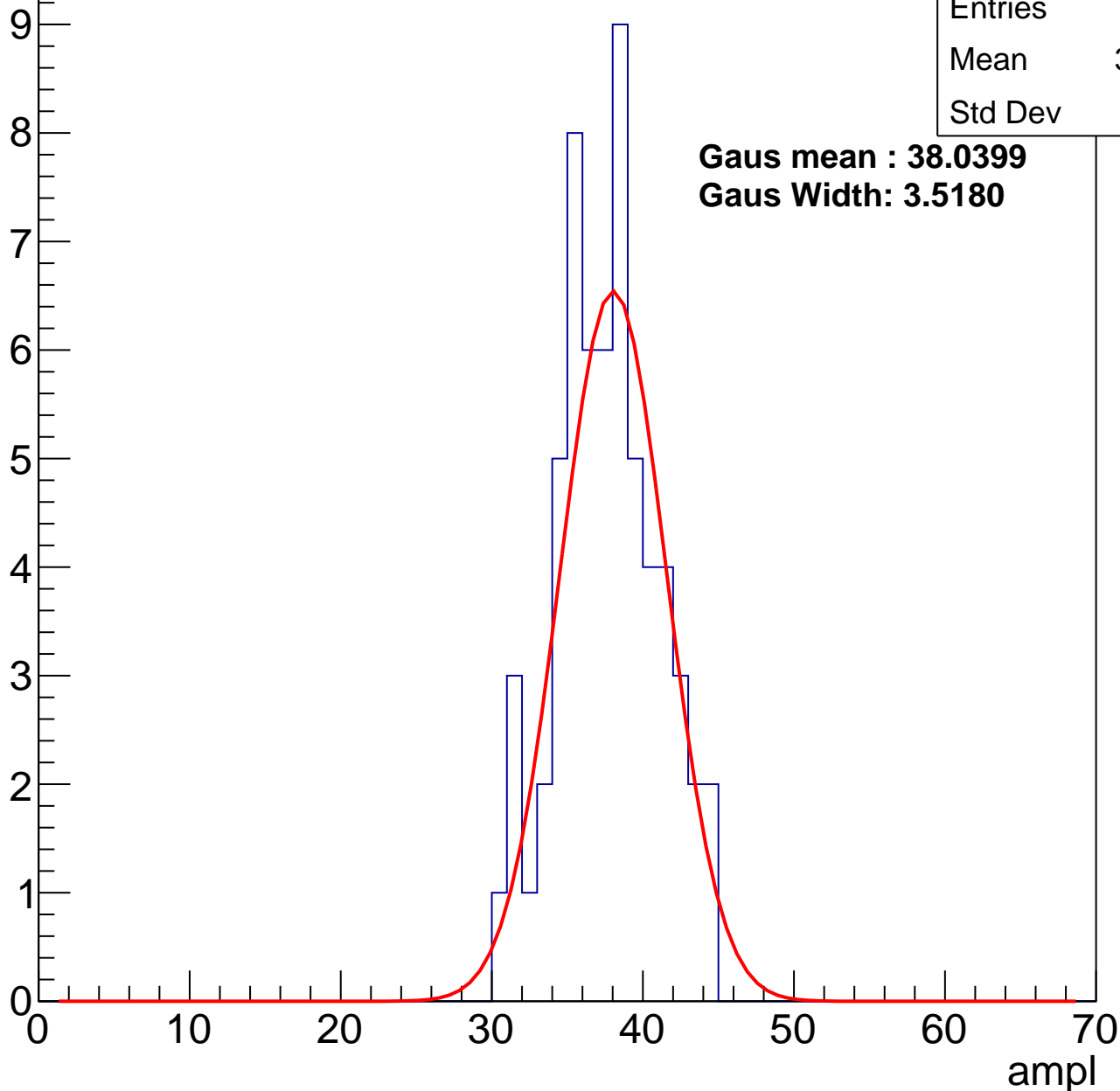
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.21
Std Dev	3.31

Gaus mean : 38.0399

Gaus Width: 3.5180



B0L001S, U15-ch120, adc2

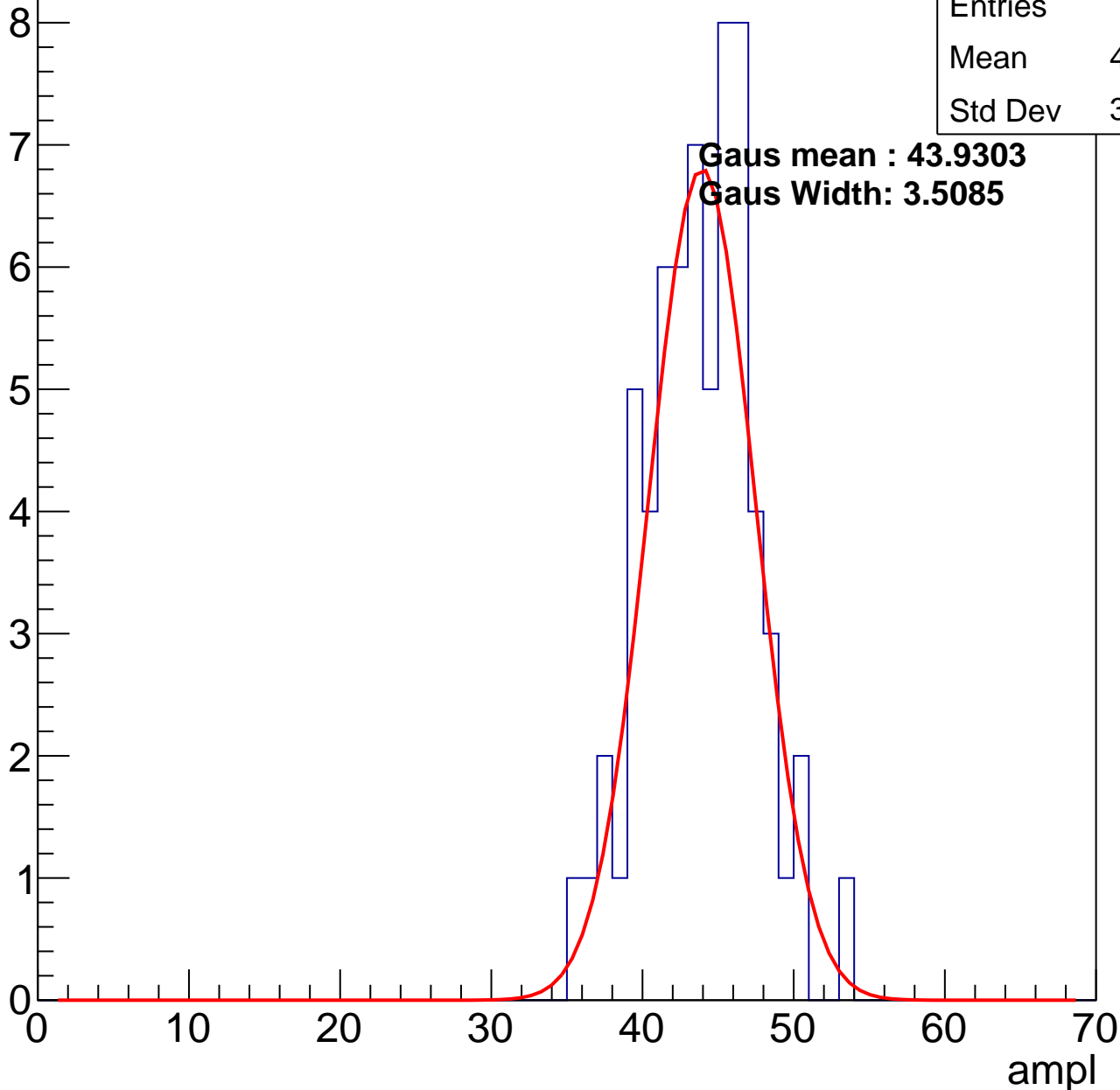
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.37
Std Dev	3.572

Gaus mean : 43.9303

Gaus Width: 3.5085

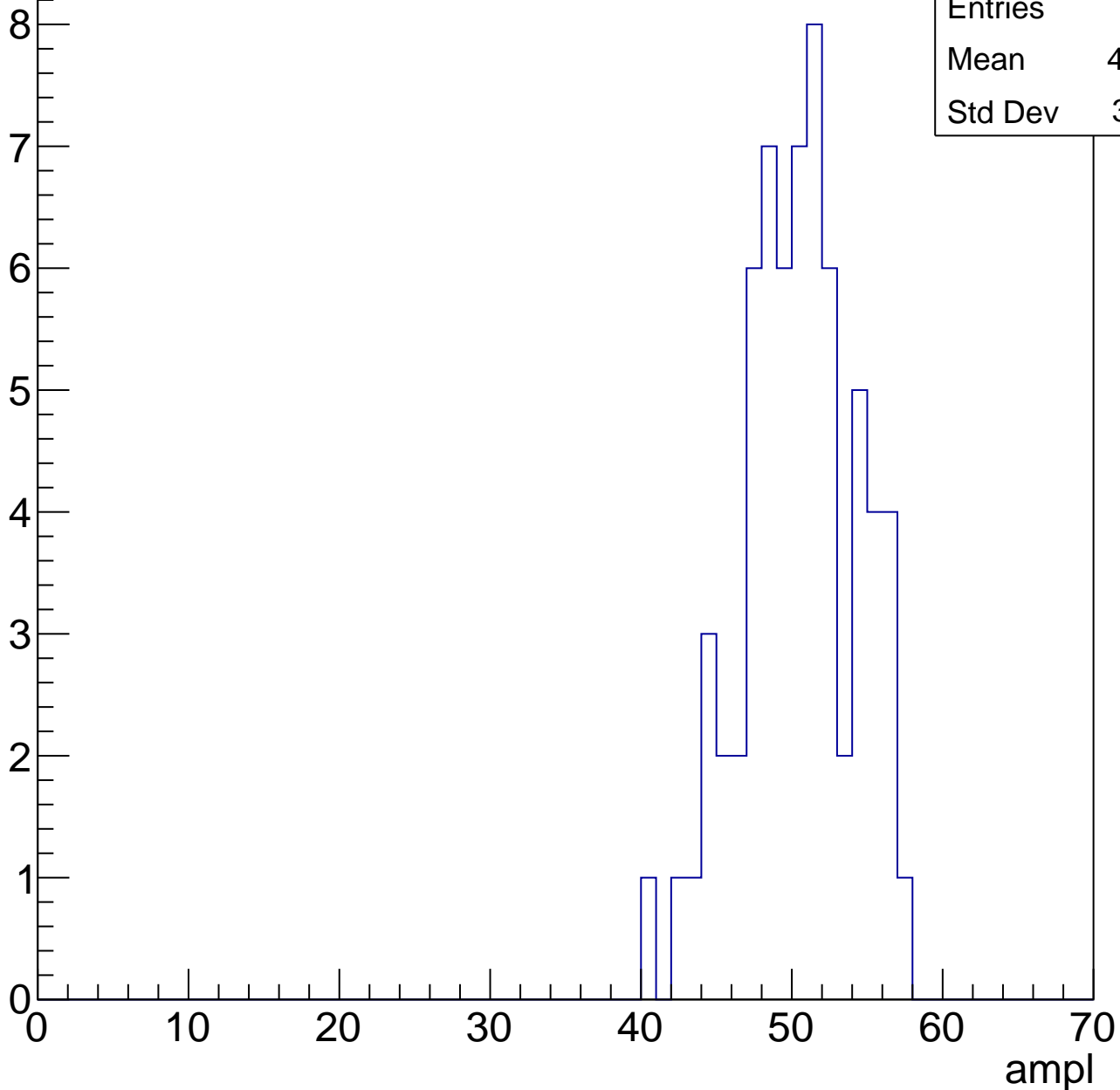


B0L001S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.97
Std Dev	3.721

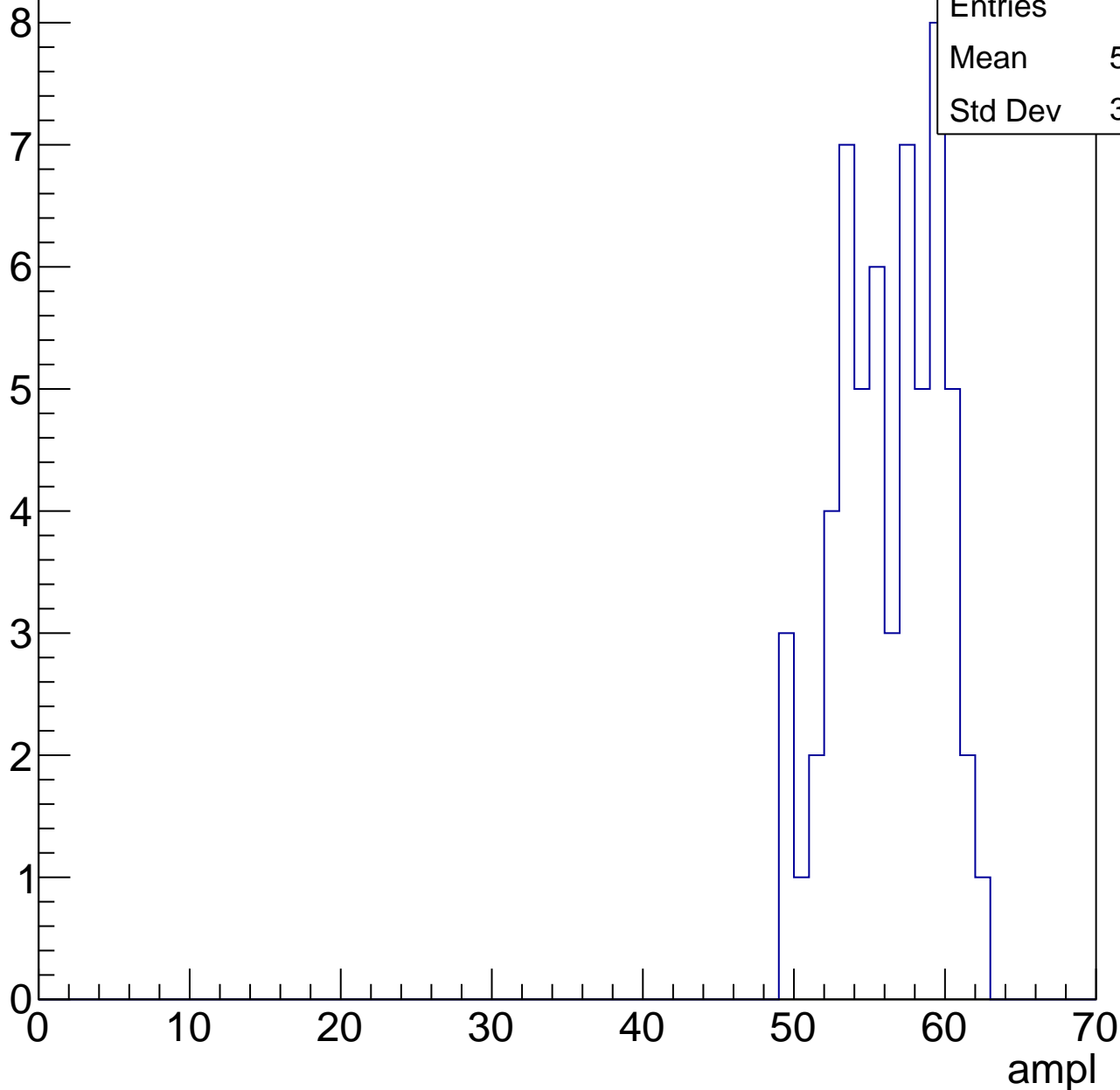


B0L001S, U15-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.78
Std Dev	3.314

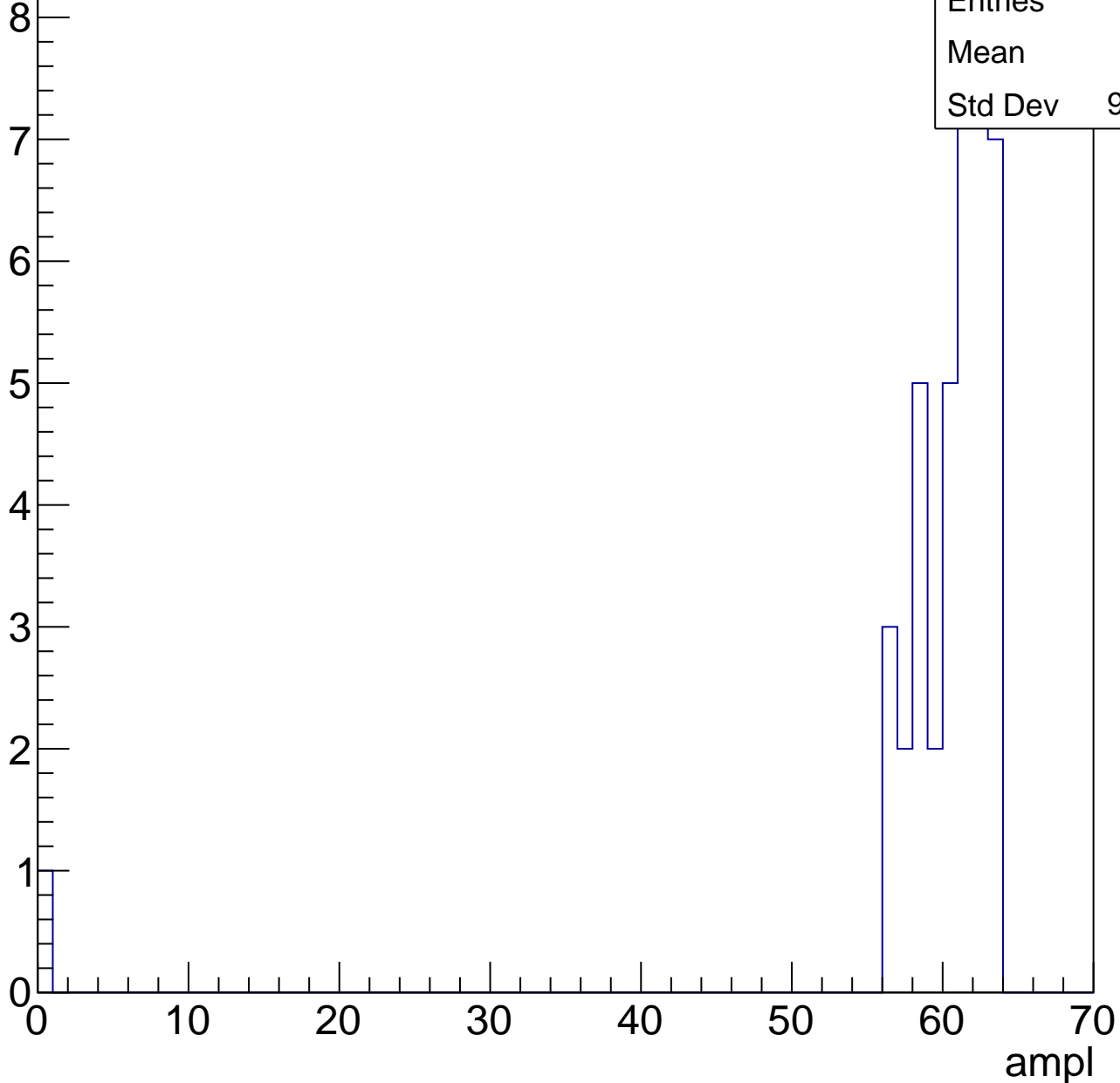


B0L001S, U15-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.9
Std Dev	9.553



B0L001S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch121, adc0

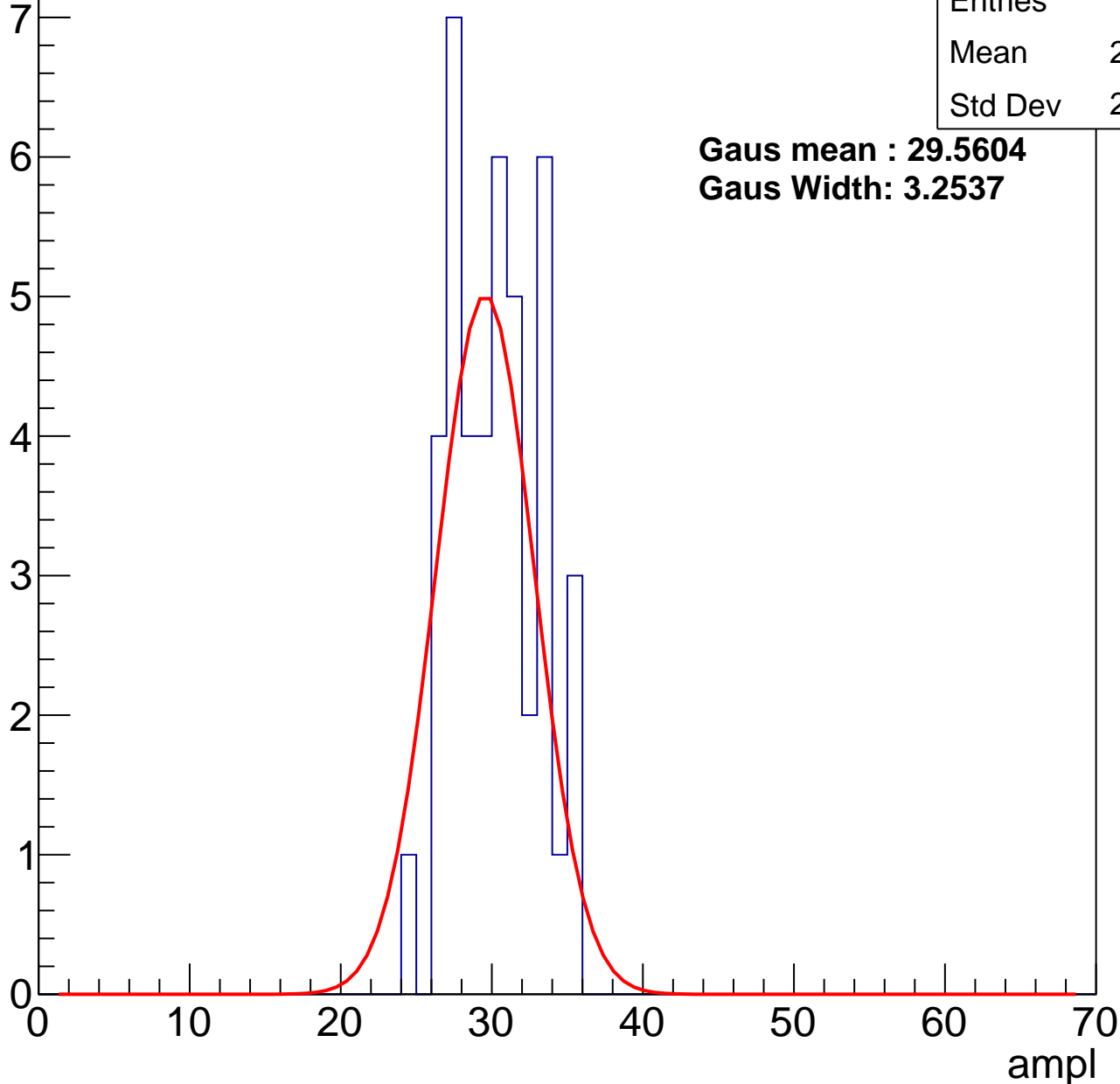
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	29.79
Std Dev	2.808

Gaus mean : 29.5604

Gaus Width: 3.2537



B0L001S, U15-ch121, adc1

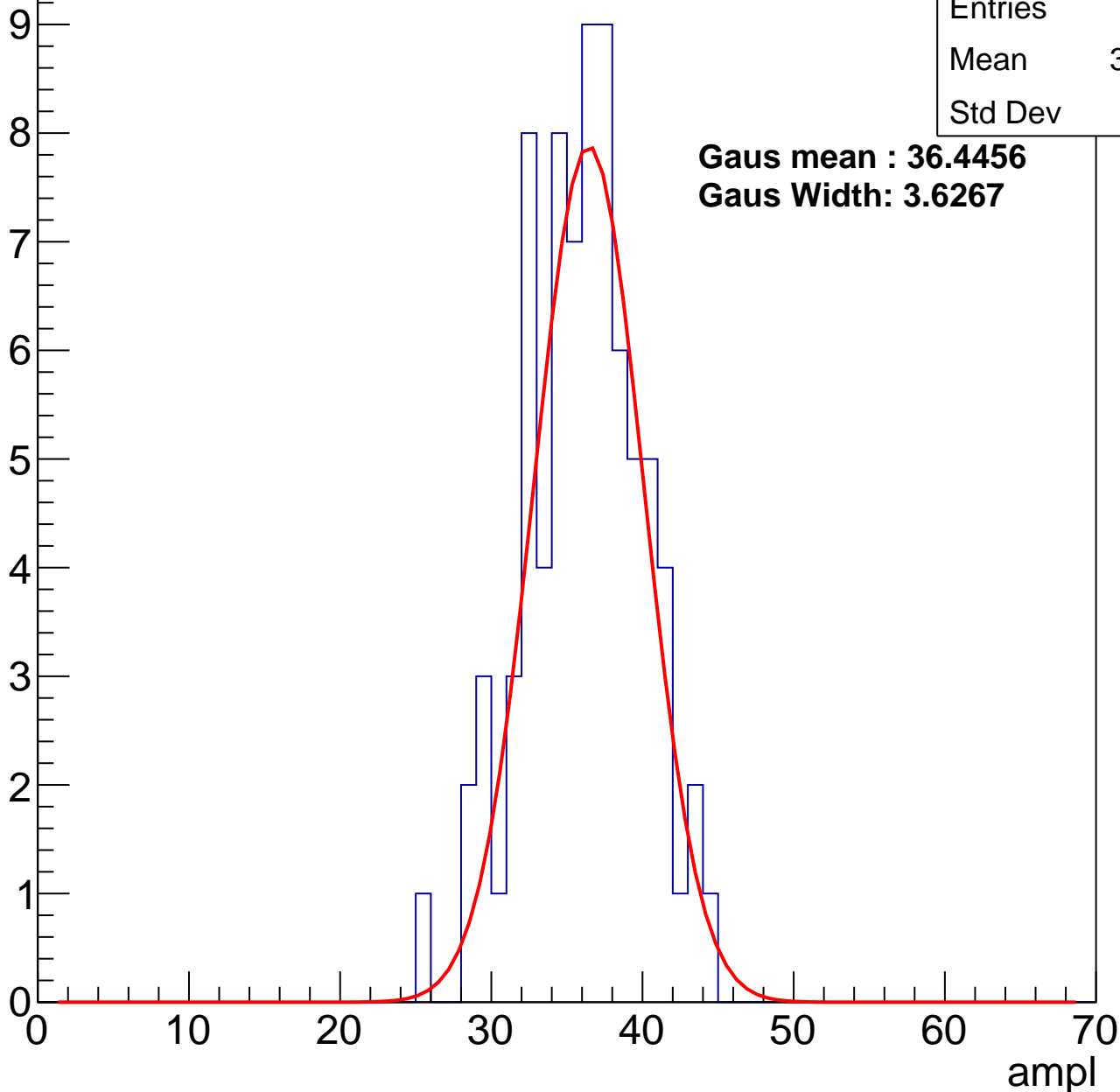
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	35.59
Std Dev	3.82

Gaus mean : 36.4456

Gaus Width: 3.6267



B0L001S, U15-ch121, adc2

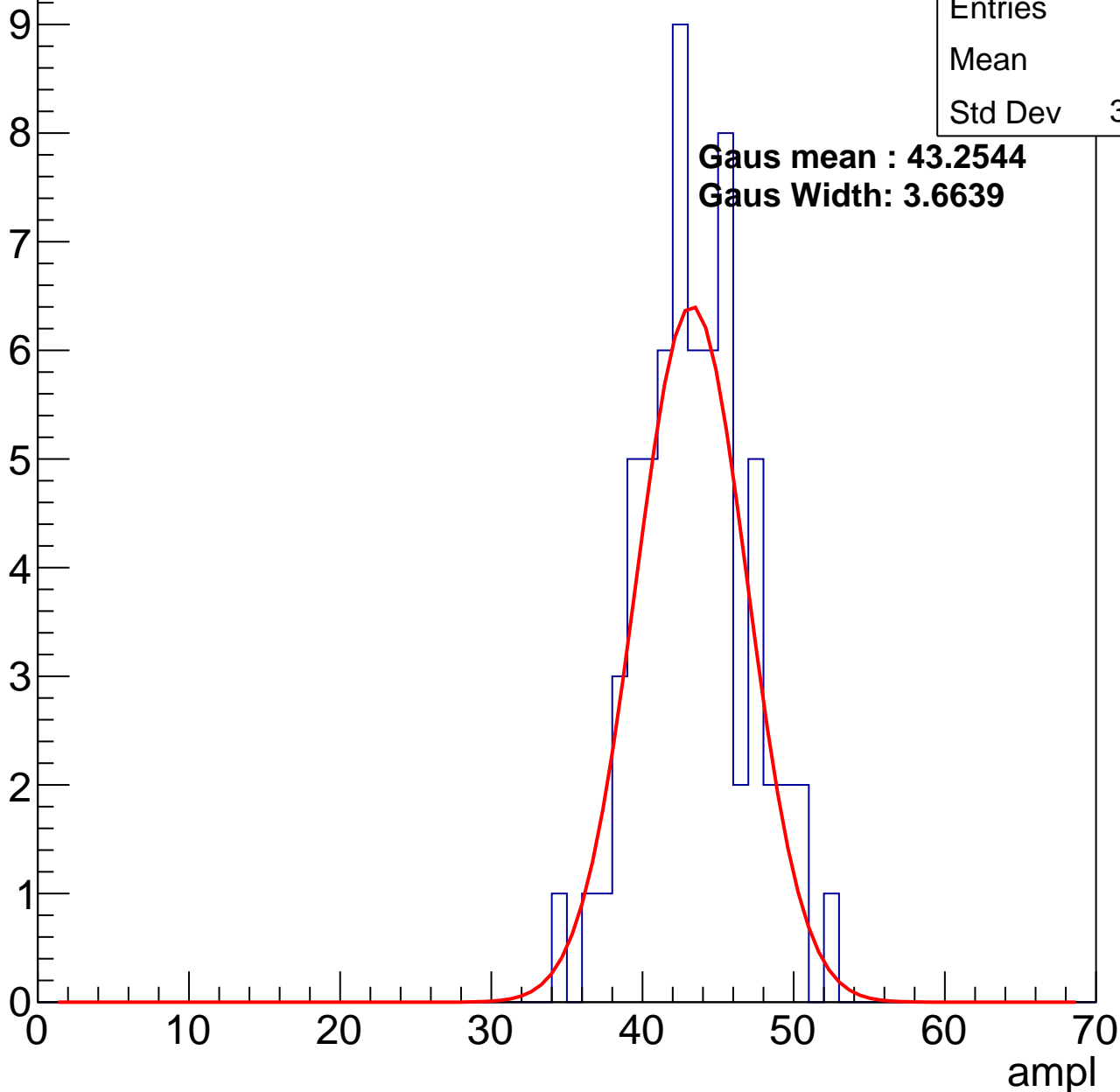
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43
Std Dev	3.586

Gaus mean : 43.2544

Gaus Width: 3.6639

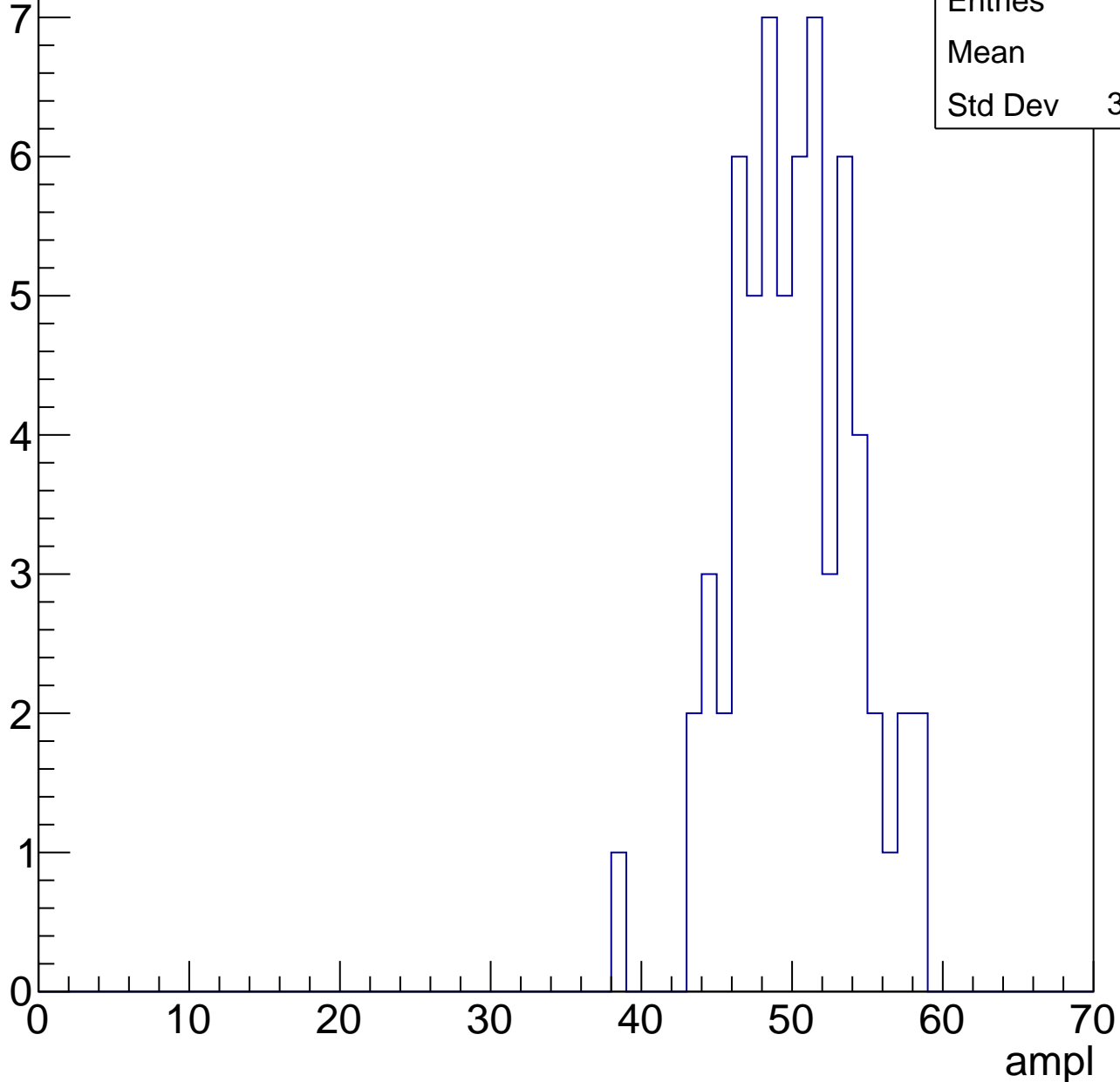


B0L001S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.7
Std Dev	3.987

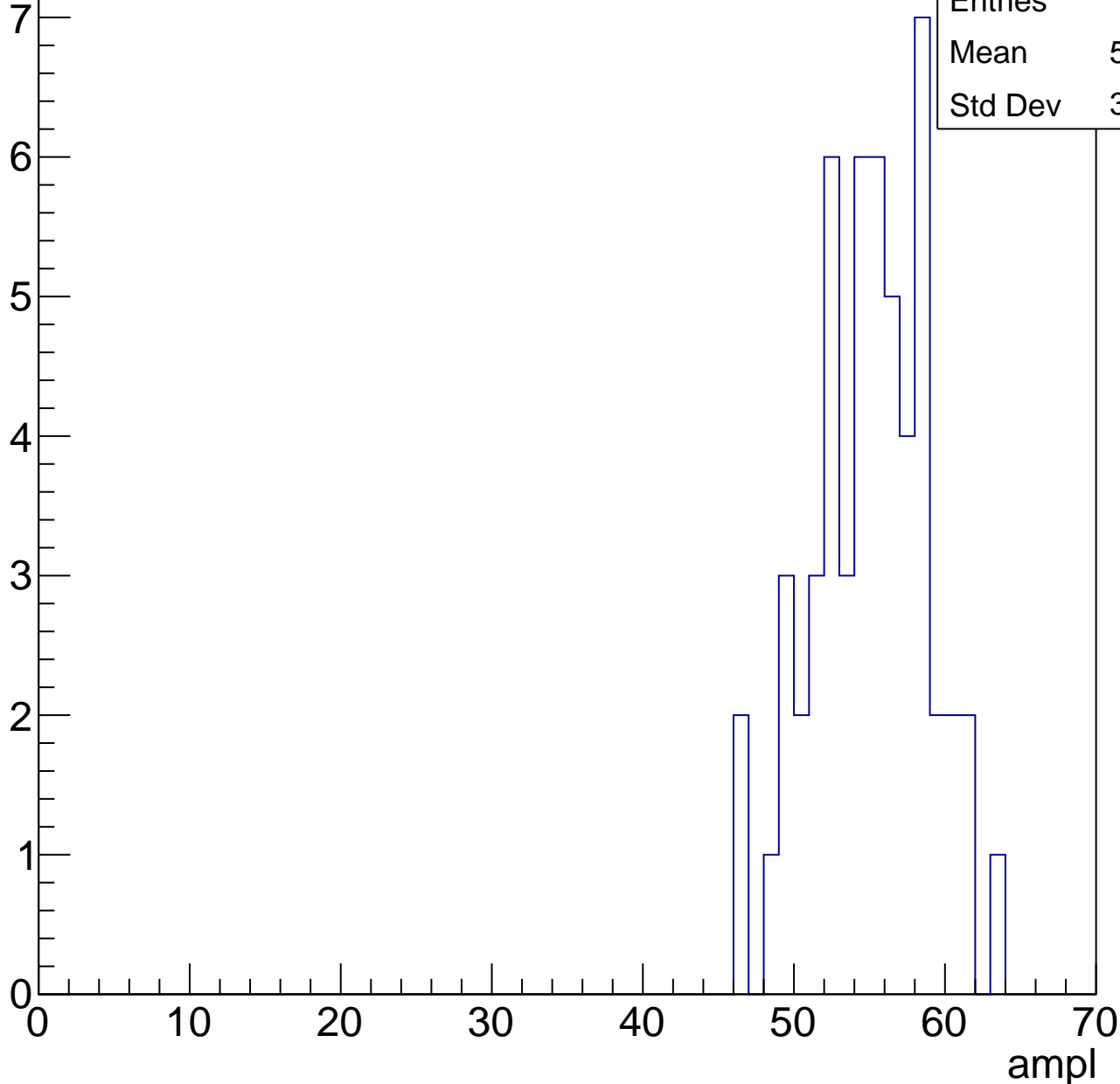


B0L001S, U15-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	54.58
Std Dev	3.769

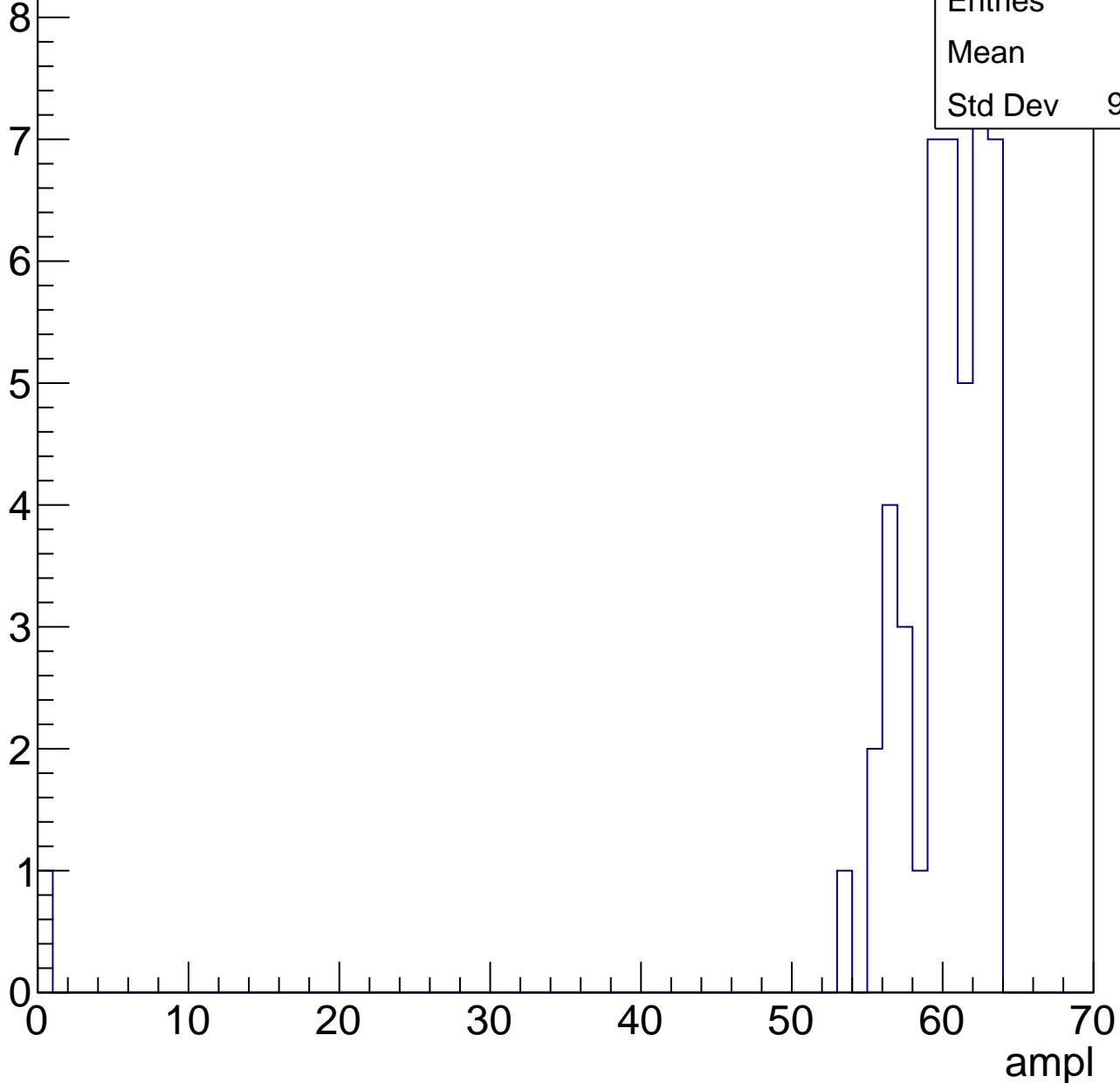


B0L001S, U15-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

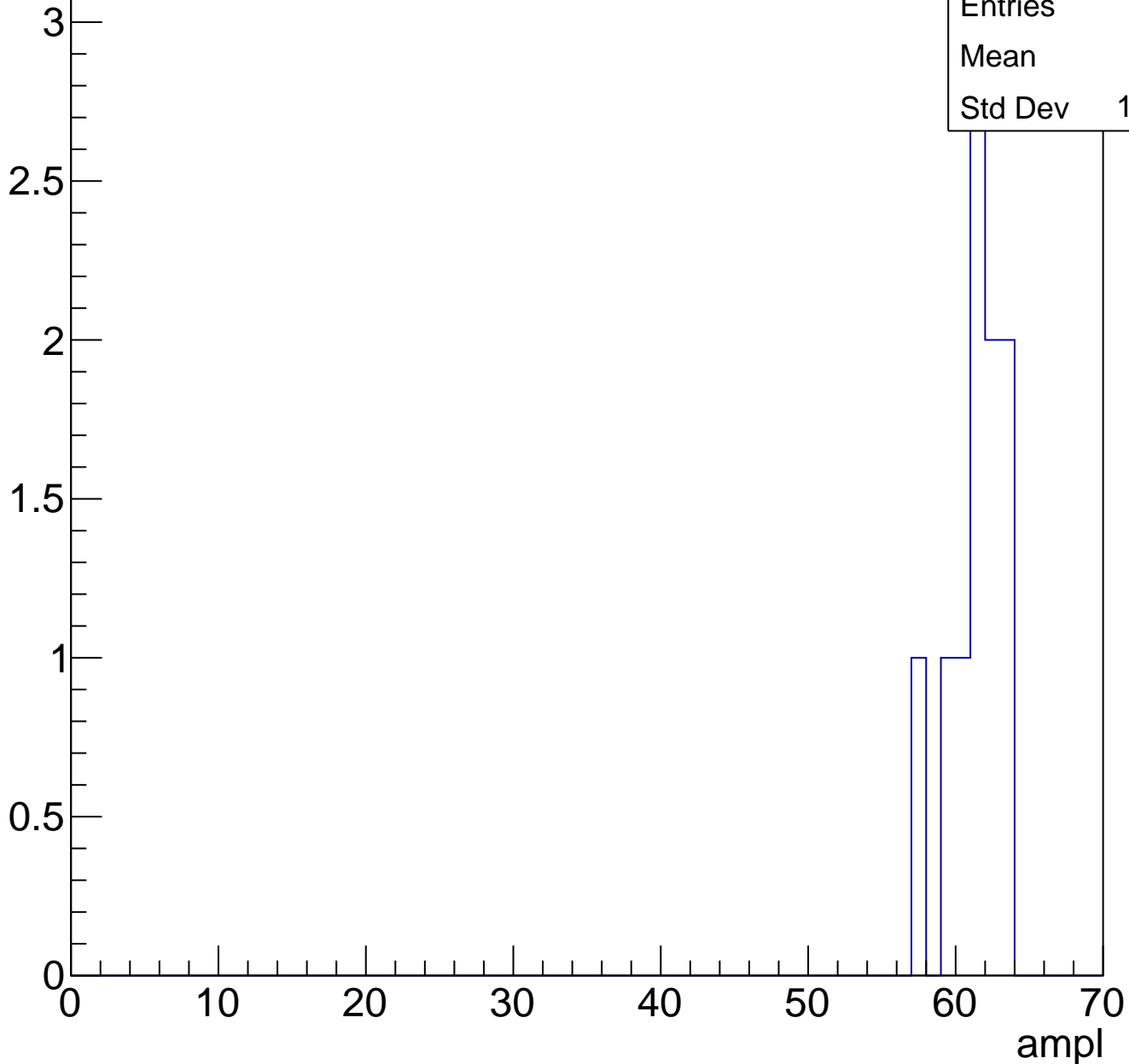
Entries	46
Mean	58.5
Std Dev	9.086



B0L001S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch122, adc0

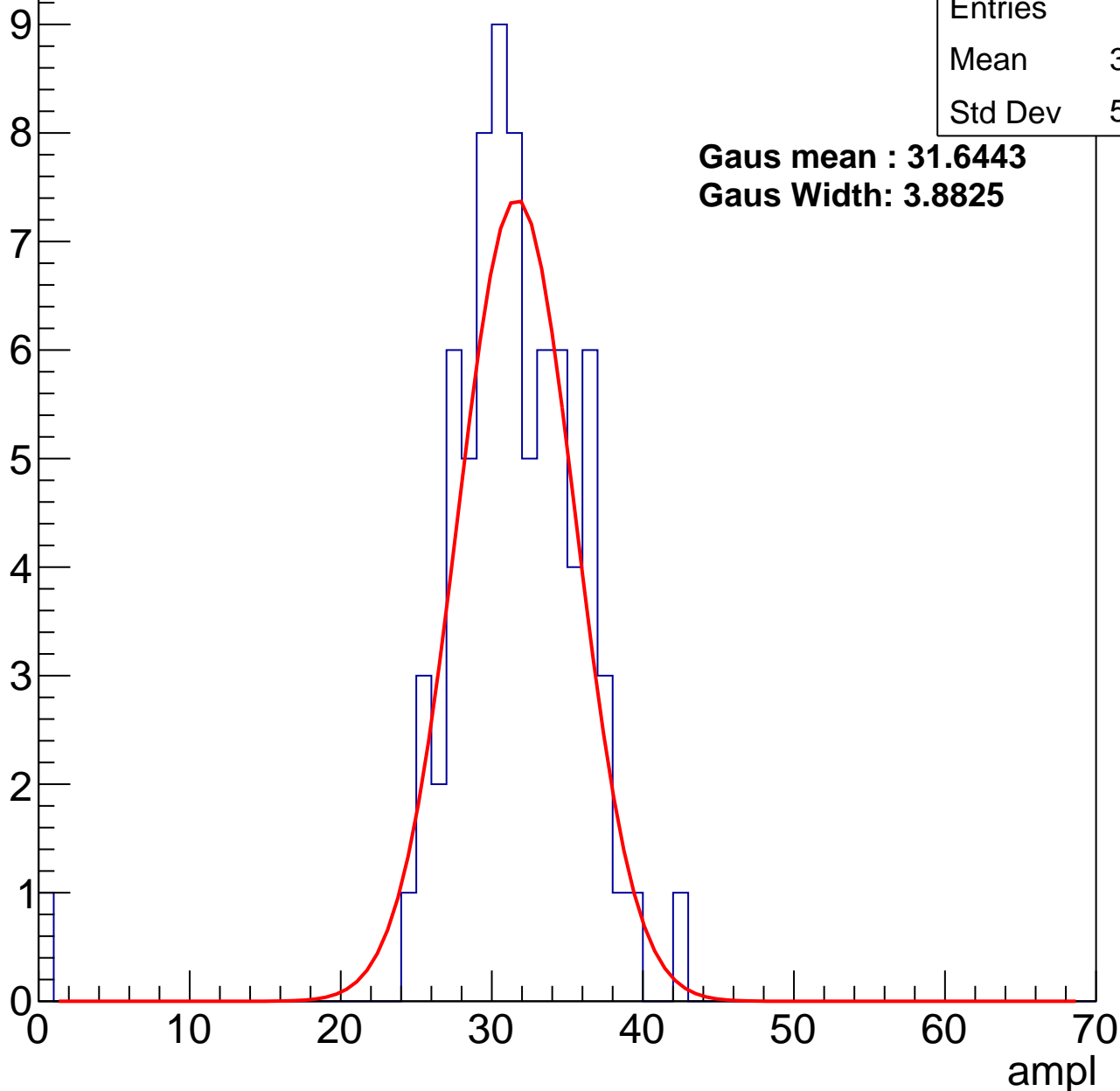
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.93
Std Dev	5.115

Gaus mean : 31.6443

Gaus Width: 3.8825



B0L001S, U15-ch122, adc1

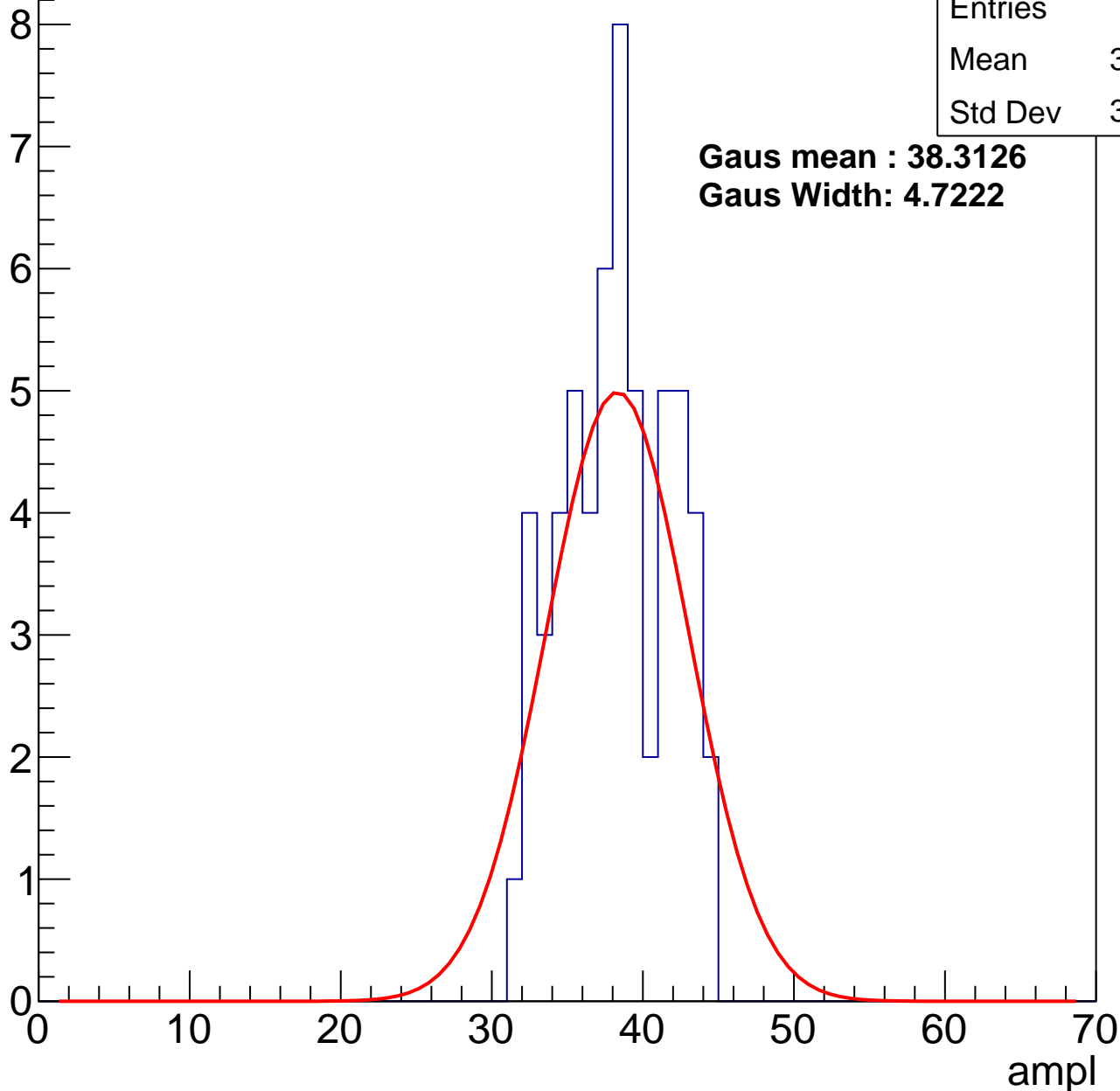
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.74
Std Dev	3.487

Gaus mean : 38.3126

Gaus Width: 4.7222



B0L001S, U15-ch122, adc2

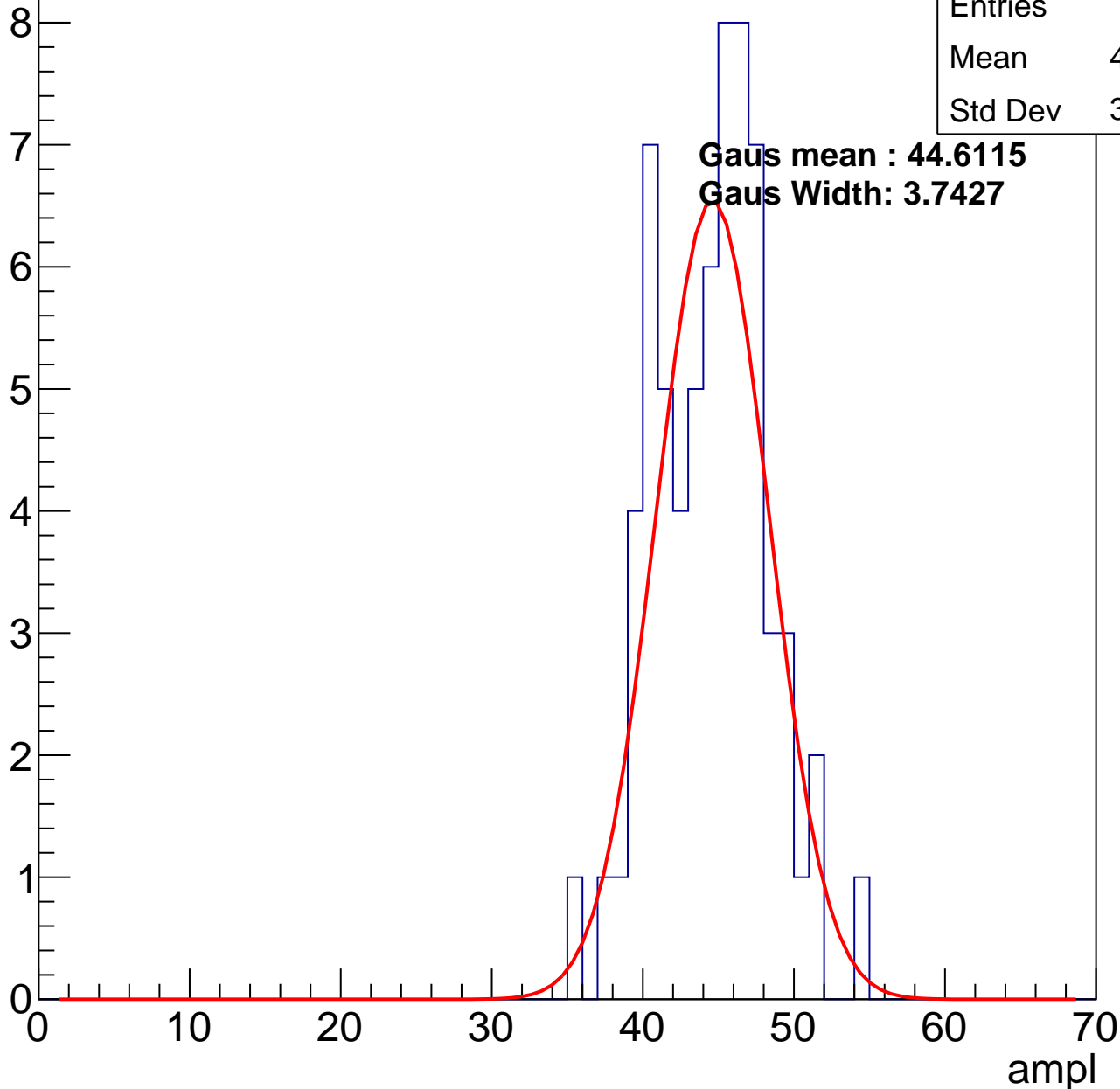
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.06
Std Dev	3.673

Gaus mean : 44.6115

Gaus Width: 3.7427

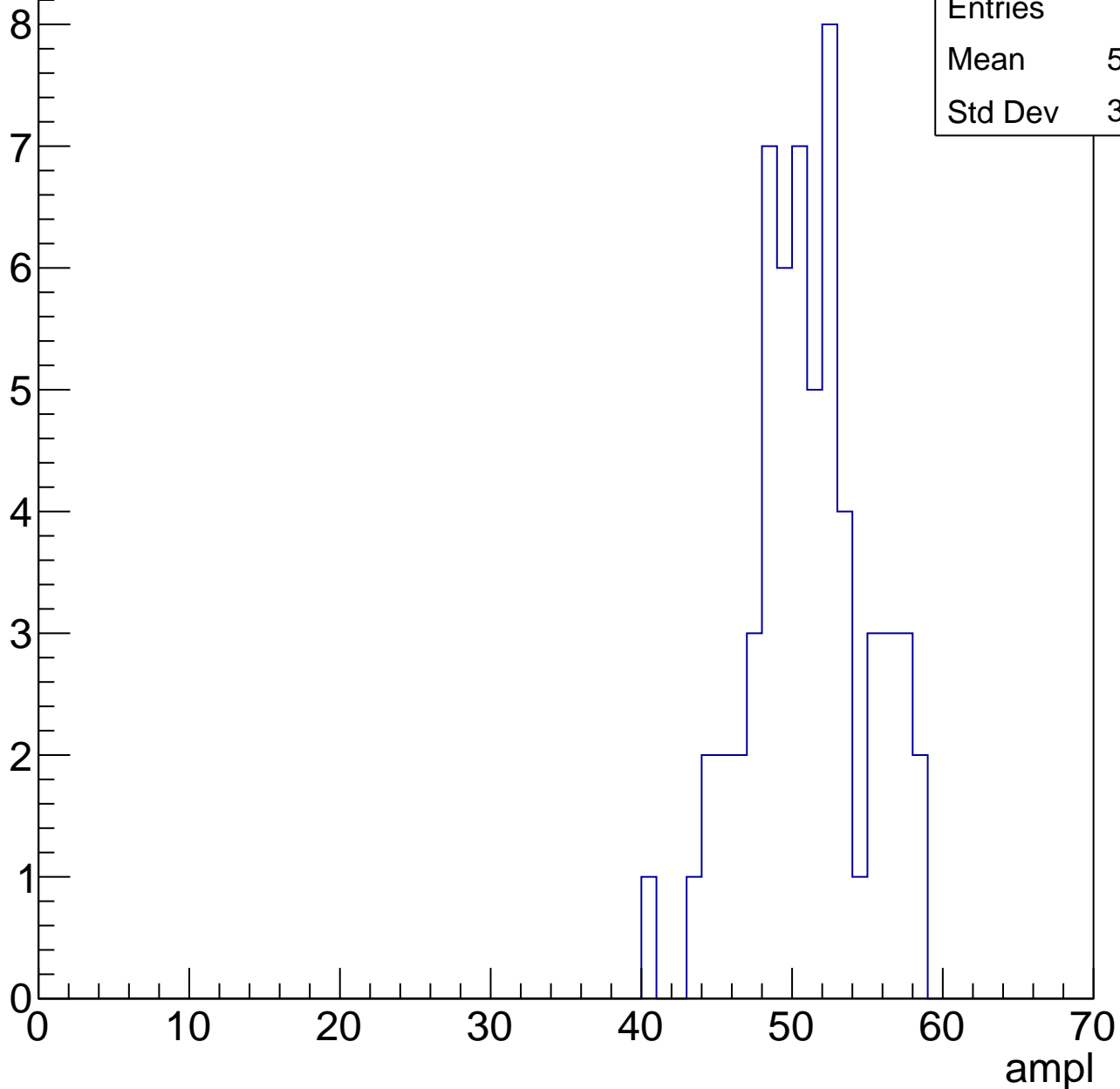


B0L001S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.52
Std Dev	3.862

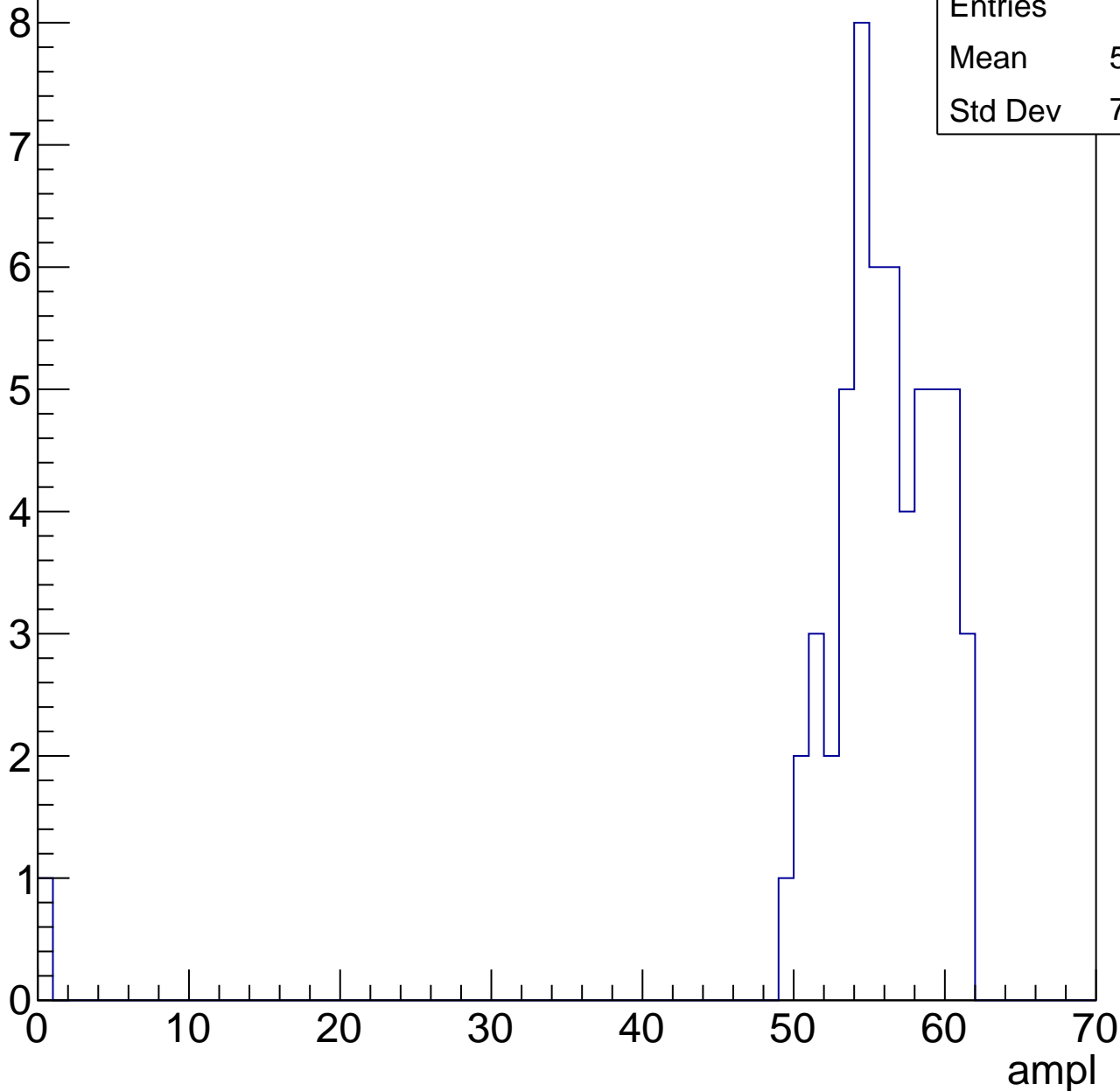


B0L001S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	54.73
Std Dev	7.997

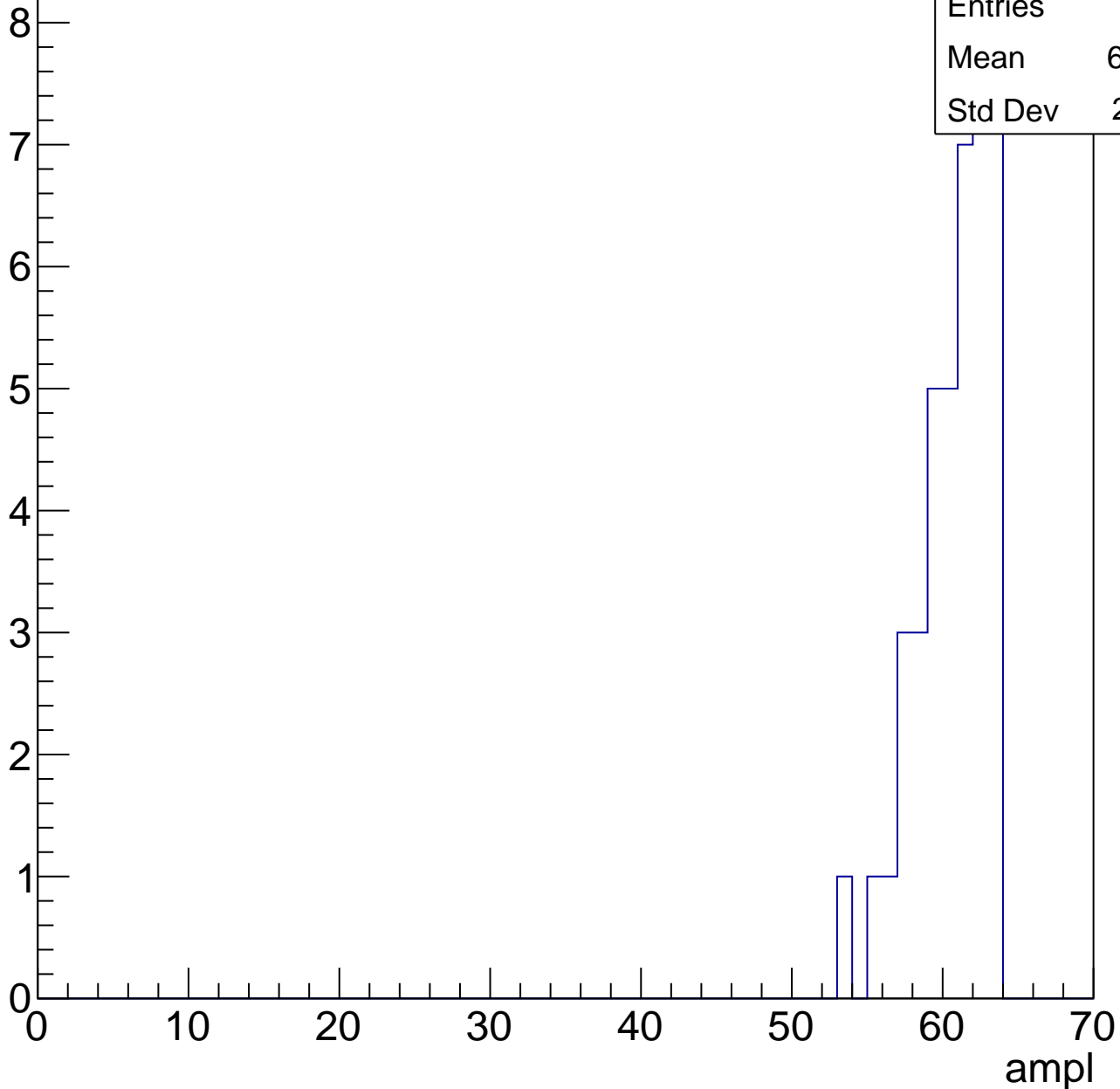


B0L001S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	60.26
Std Dev	2.401



B0L001S, U15-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B0L001S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch123, adc0

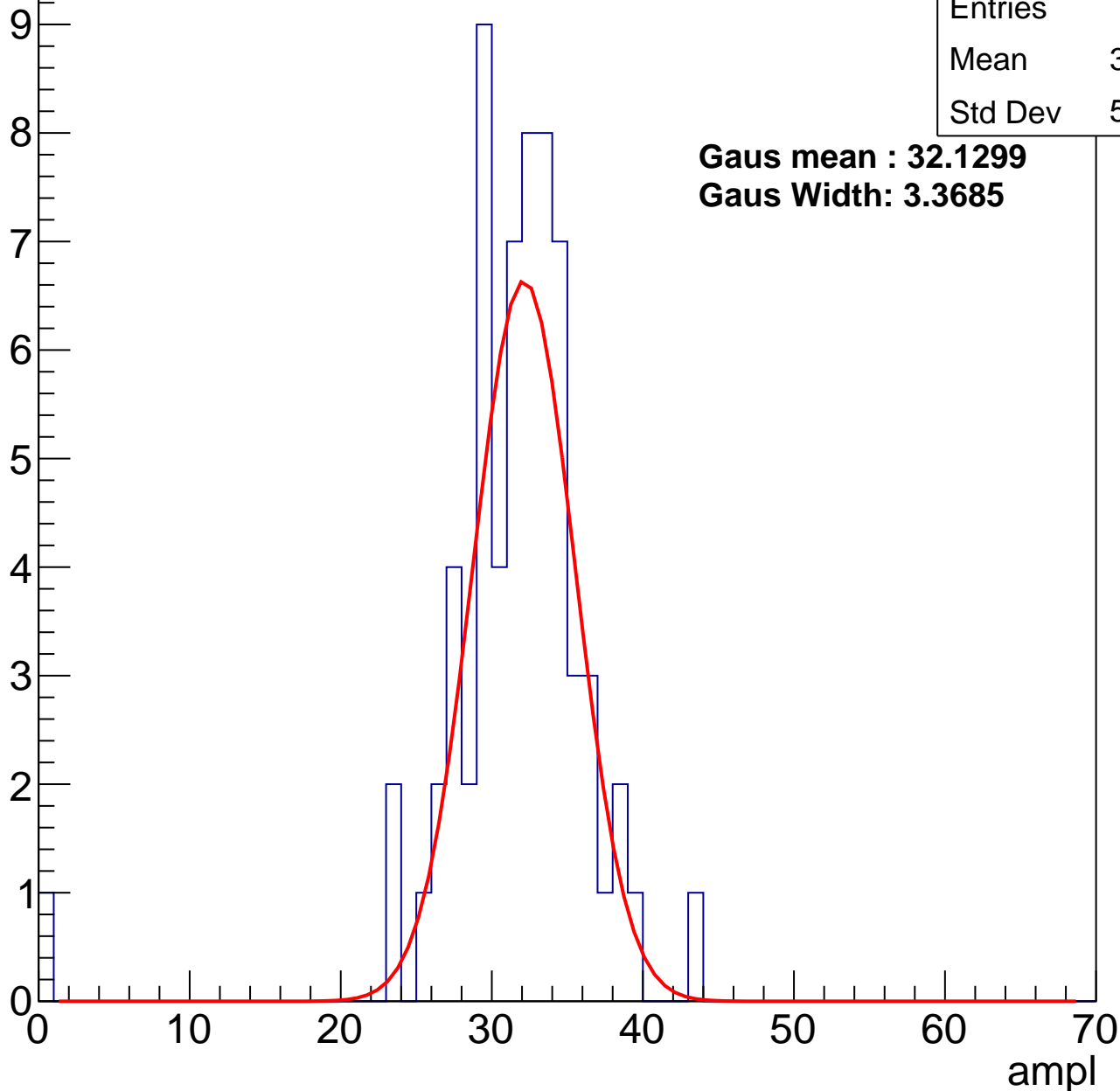
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	31.08
Std Dev	5.315

Gaus mean : 32.1299

Gaus Width: 3.3685



B0L001S, U15-ch123, adc1

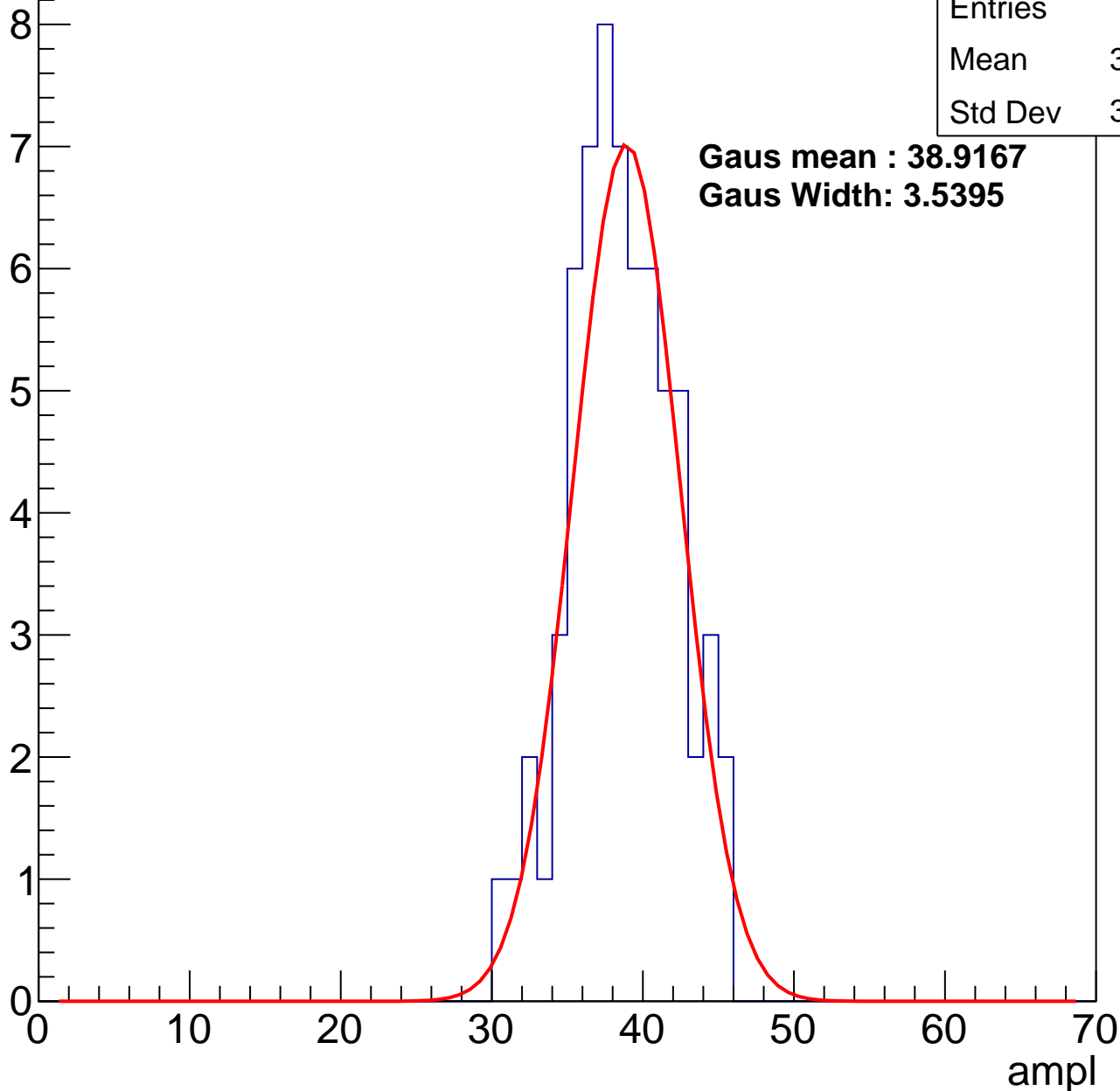
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.17
Std Dev	3.413

Gaus mean : 38.9167

Gaus Width: 3.5395



B0L001S, U15-ch123, adc2

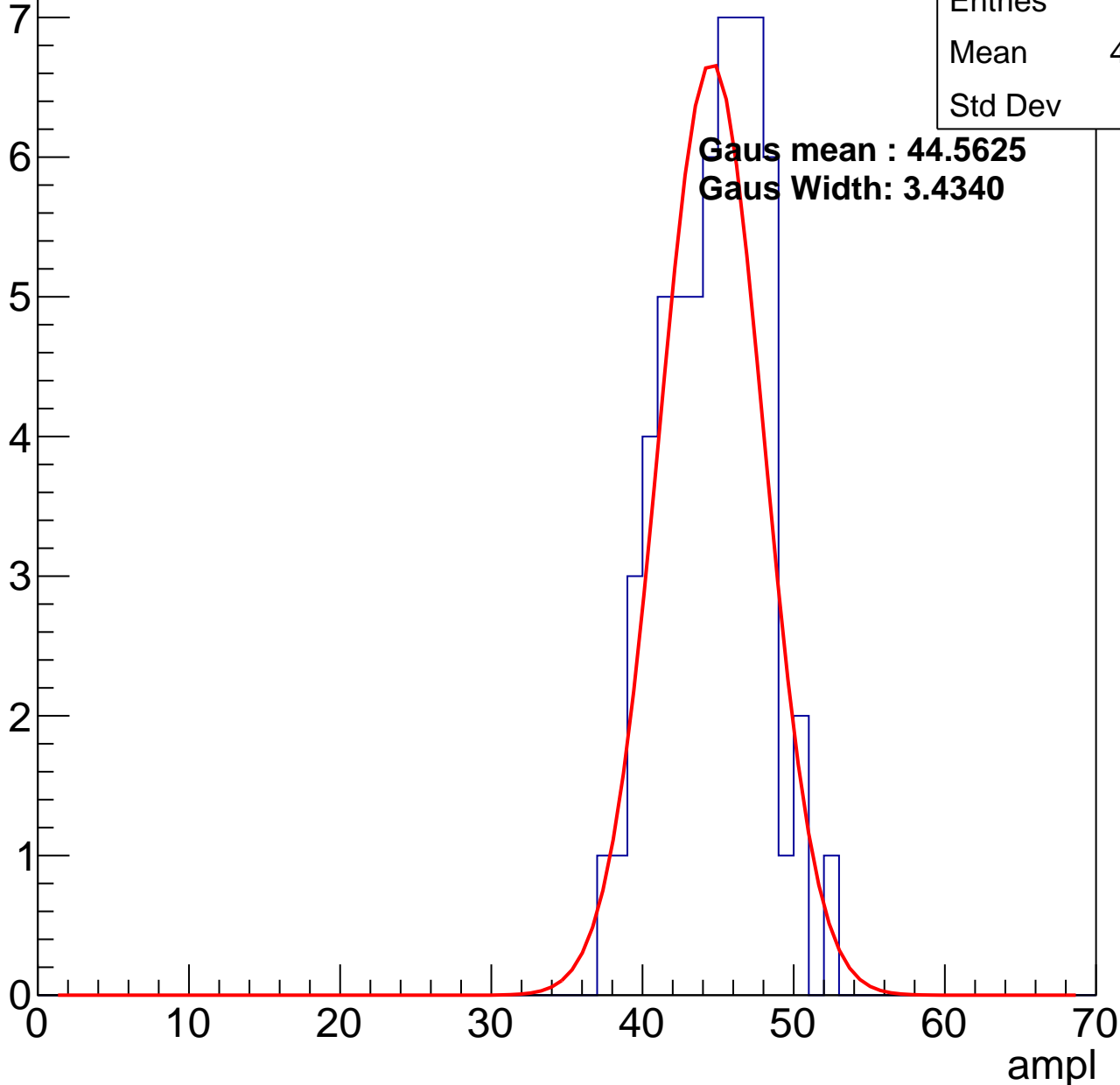
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.28
Std Dev	3.25

Gaus mean : 44.5625

Gaus Width: 3.4340

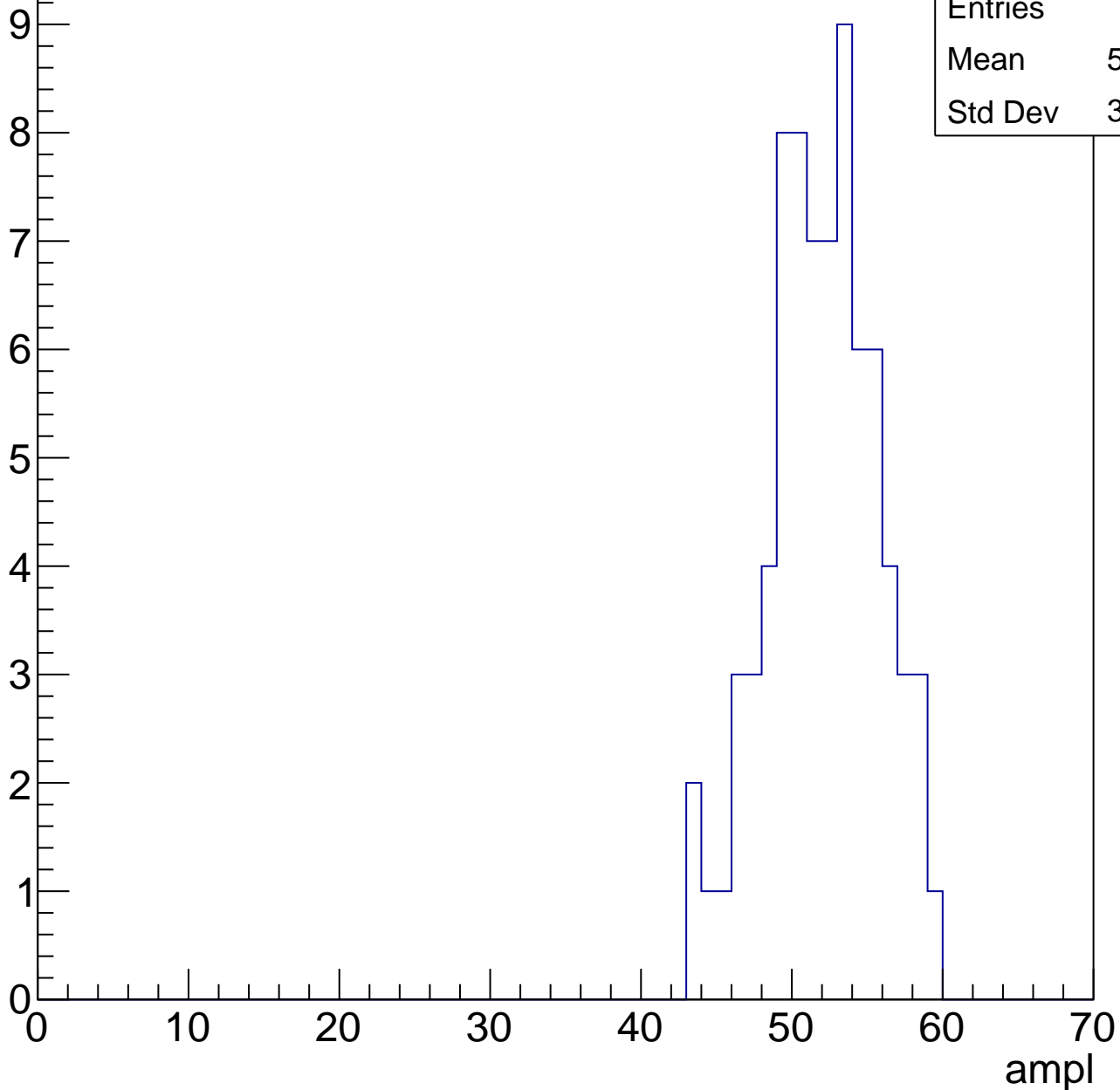


B0L001S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.55
Std Dev	3.629

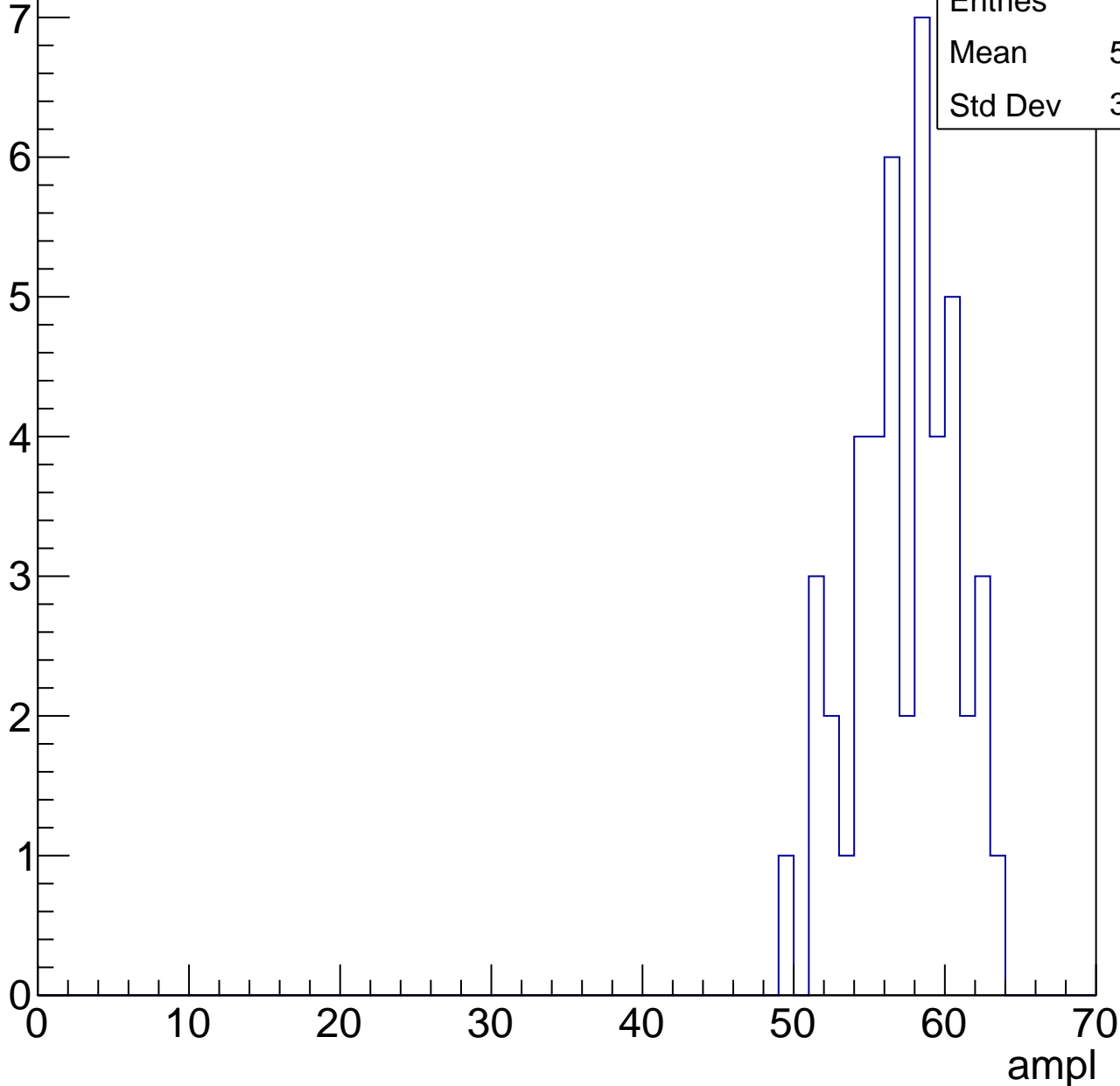


B0L001S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	56.84
Std Dev	3.353

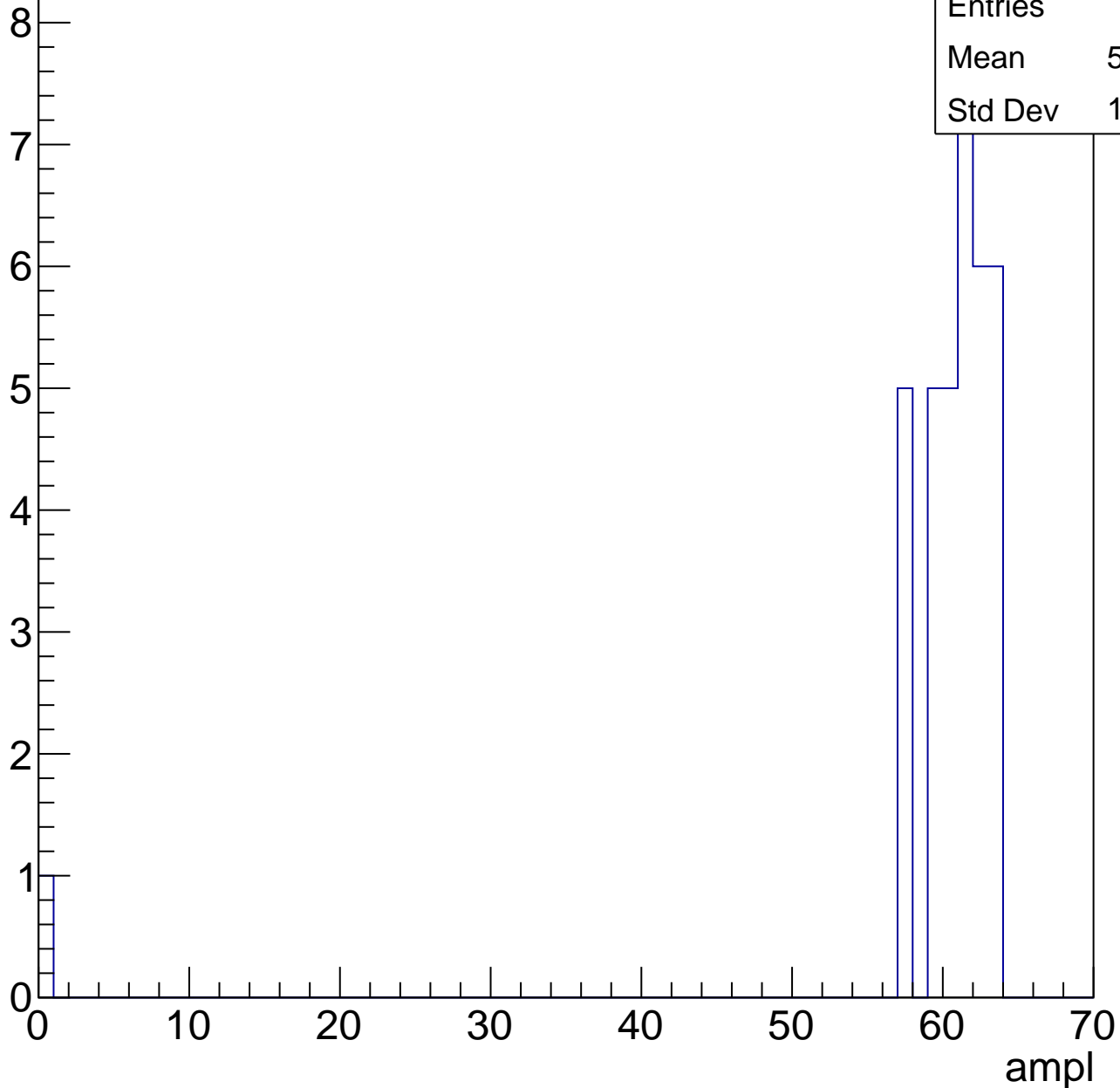


B0L001S, U15-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.83
Std Dev	10.12



B0L001S, U15-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U15-ch124, adc0

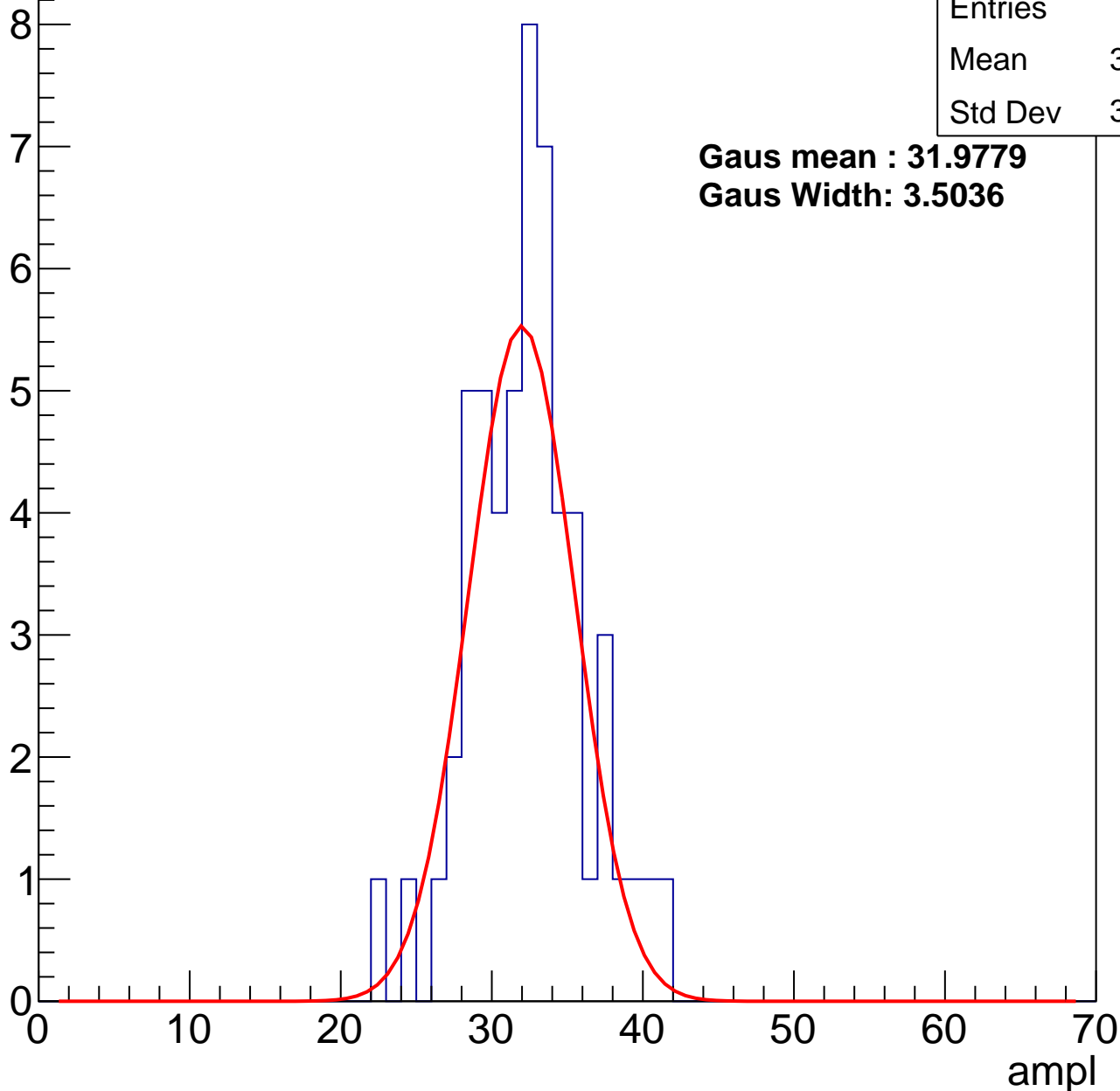
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.89
Std Dev	3.755

Gaus mean : 31.9779

Gaus Width: 3.5036



B0L001S, U15-ch124, adc1

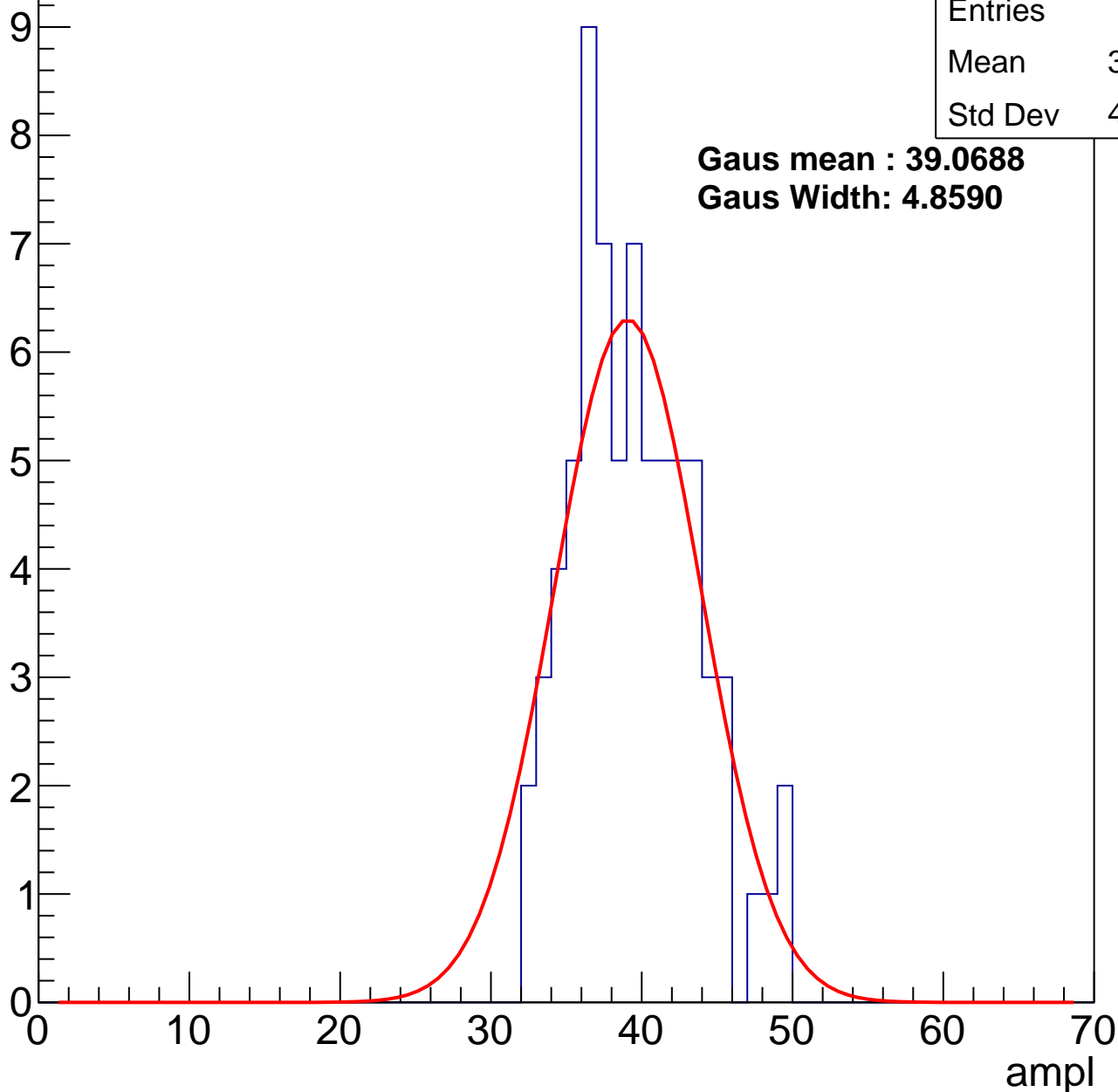
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	39.03
Std Dev	4.045

Gaus mean : 39.0688

Gaus Width: 4.8590



B0L001S, U15-ch124, adc2

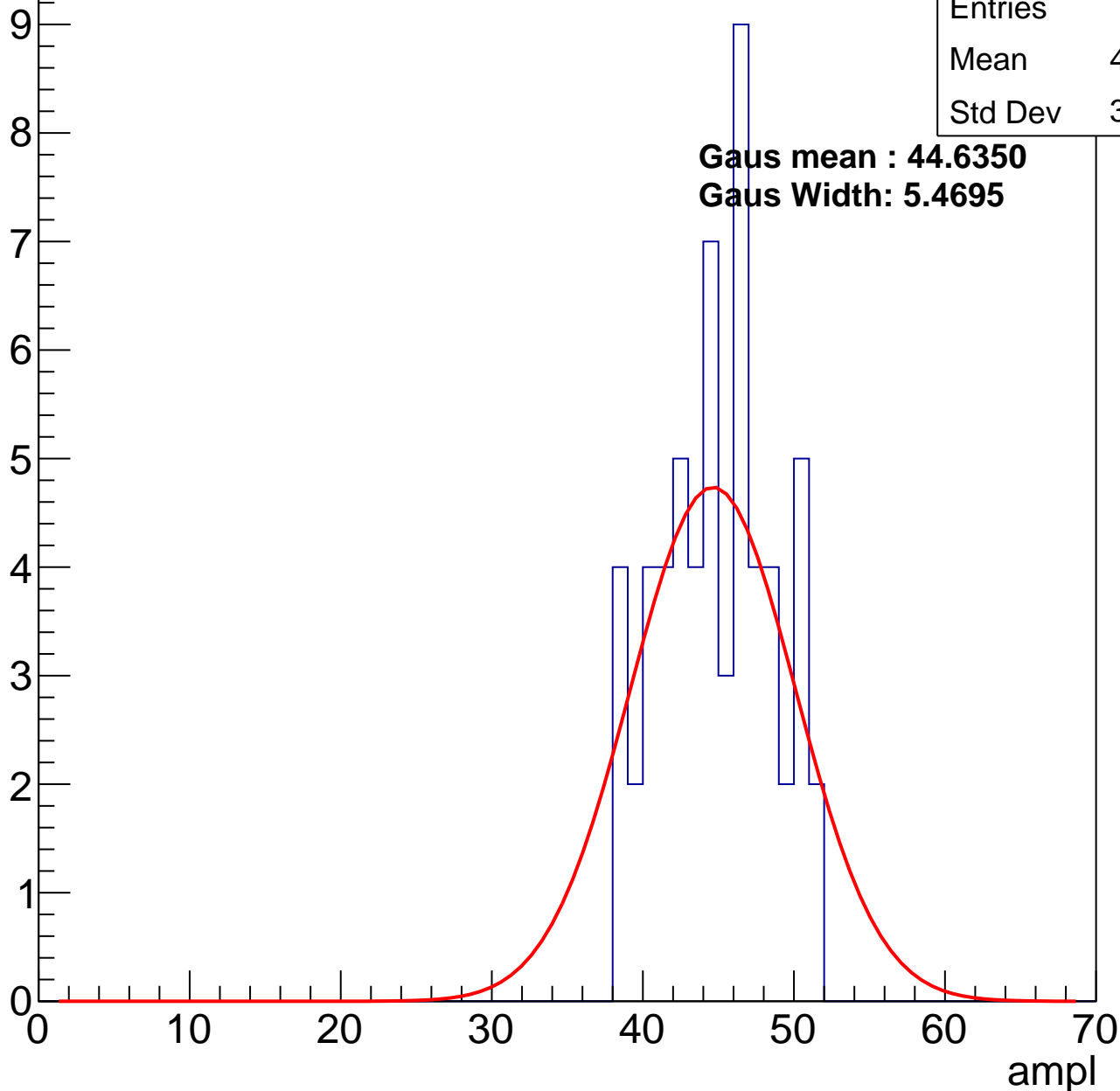
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.46
Std Dev	3.619

Gaus mean : 44.6350

Gaus Width: 5.4695

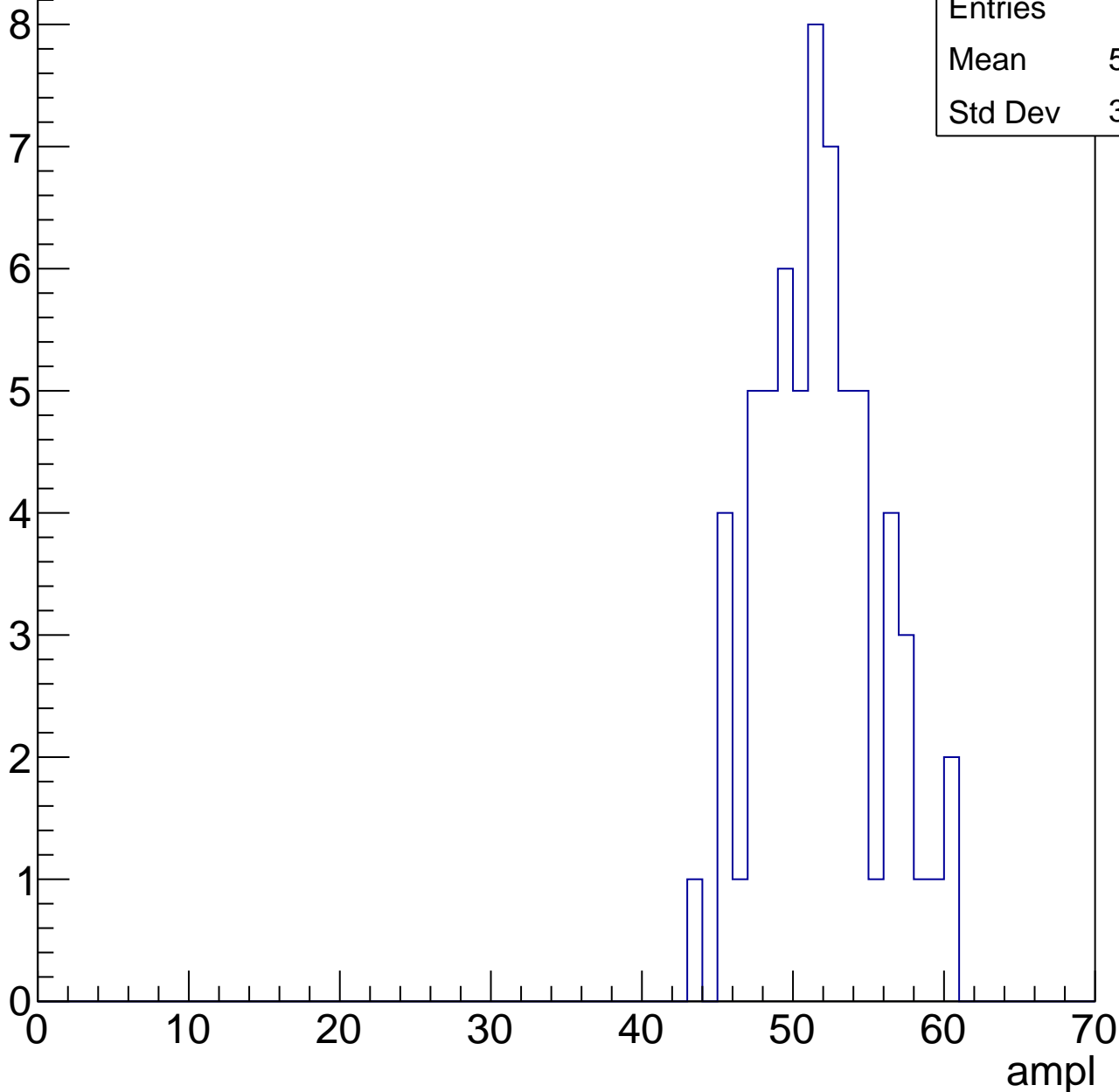


B0L001S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.28
Std Dev	3.859

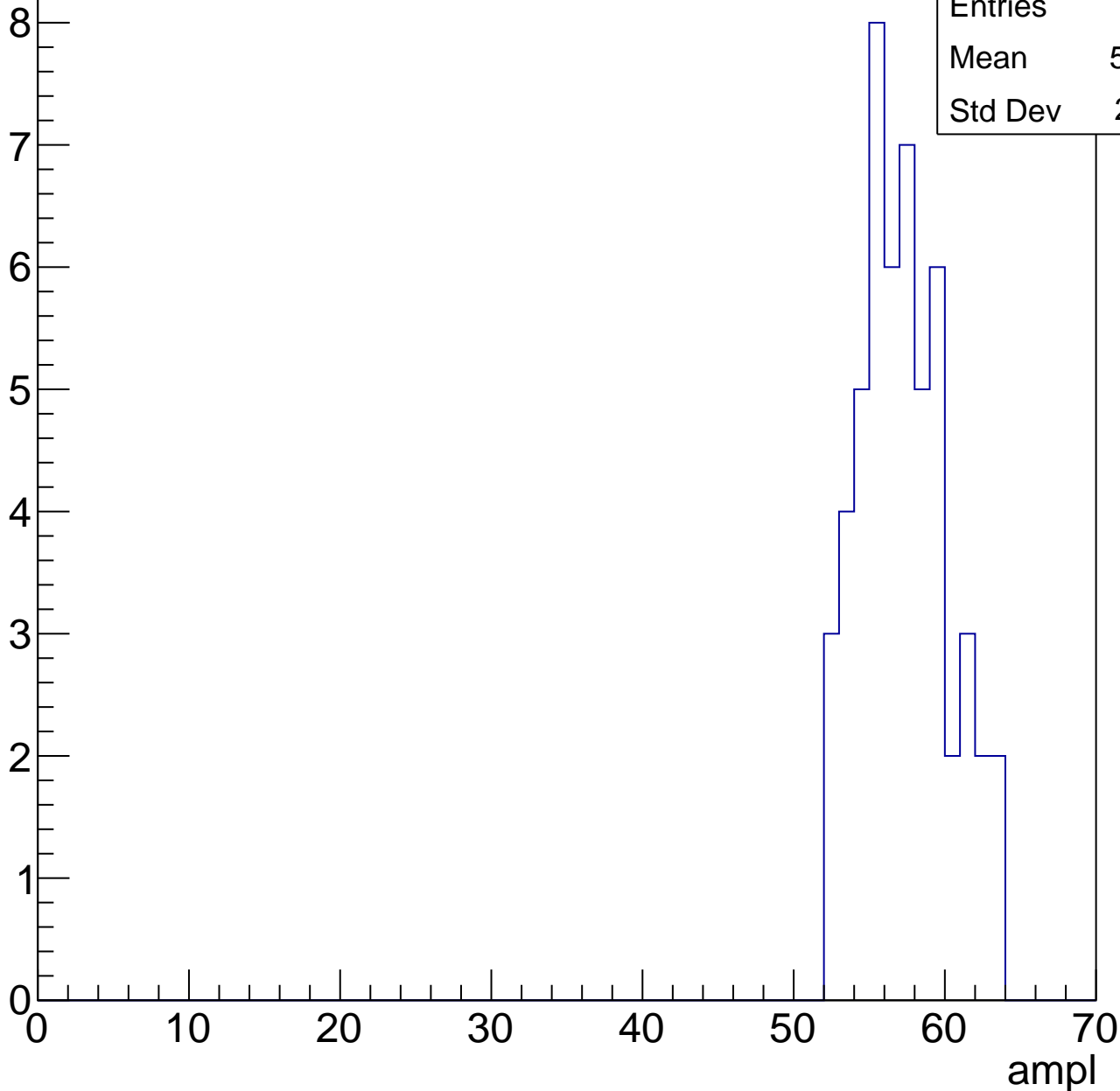


B0L001S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.79
Std Dev	2.871

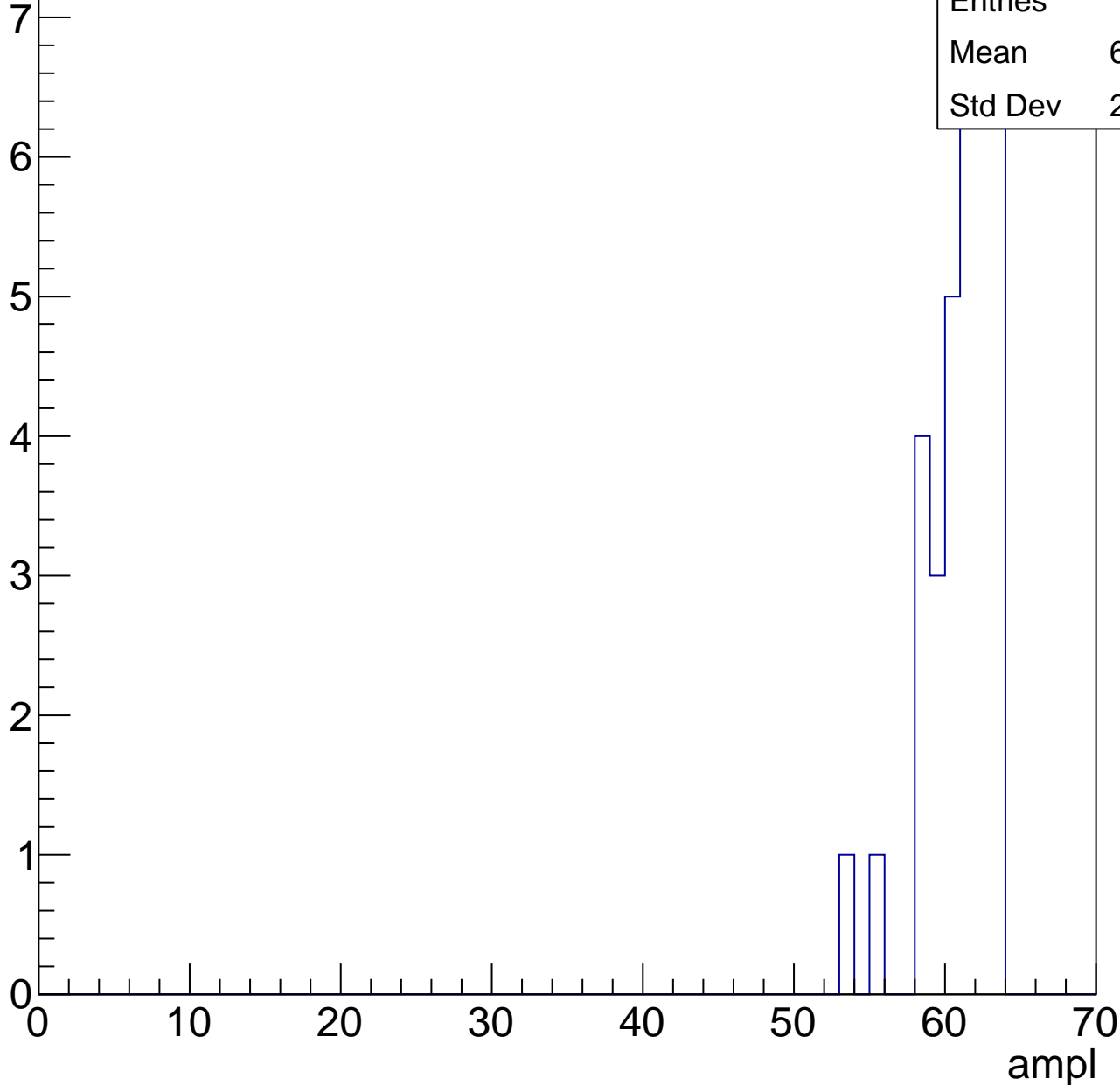


B0L001S, U15-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

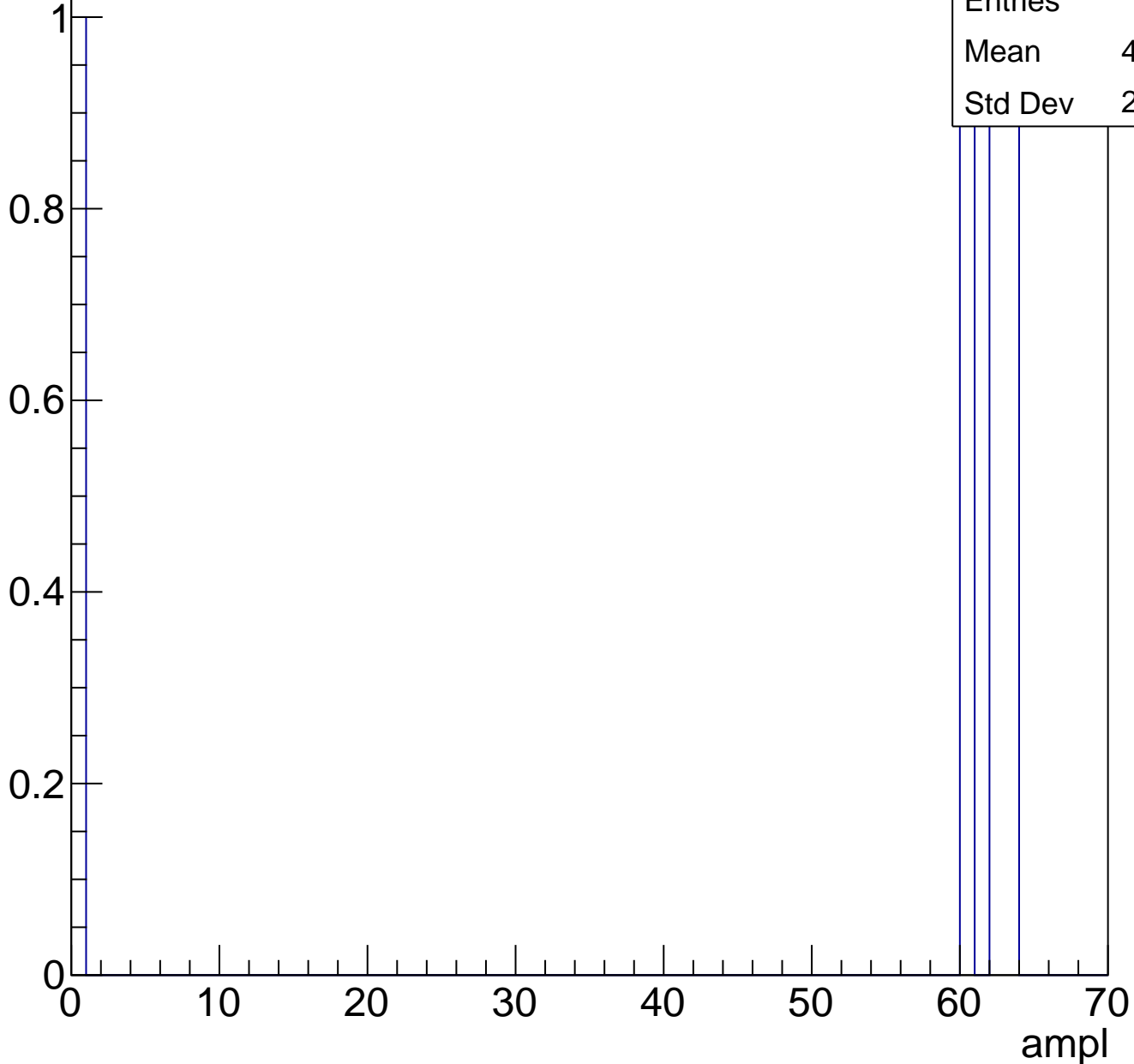
Entries	35
Mean	60.54
Std Dev	2.272



B0L001S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch125, adc0

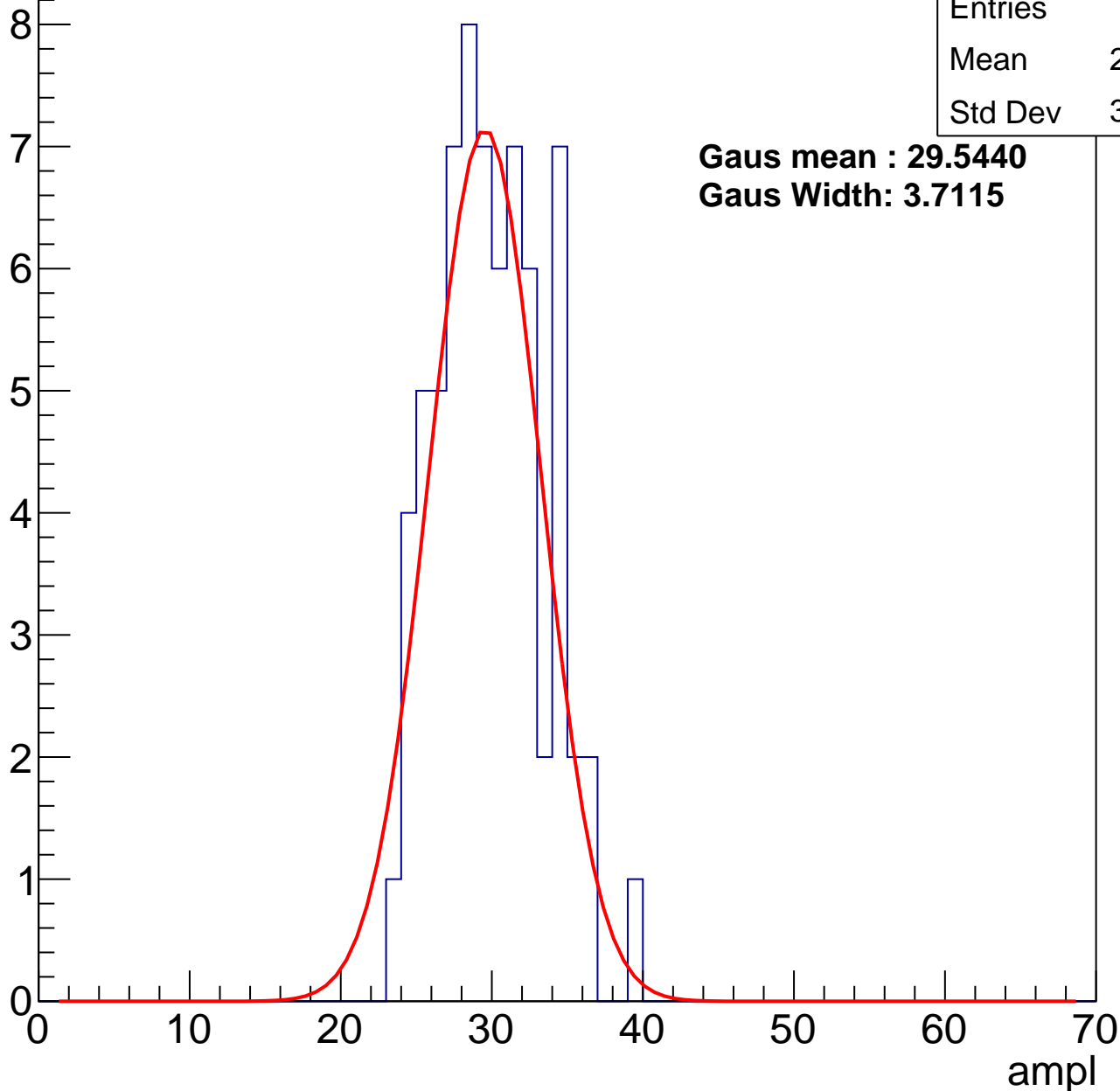
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.49
Std Dev	3.475

Gaus mean : 29.5440

Gaus Width: 3.7115



B0L001S, U15-ch125, adc1

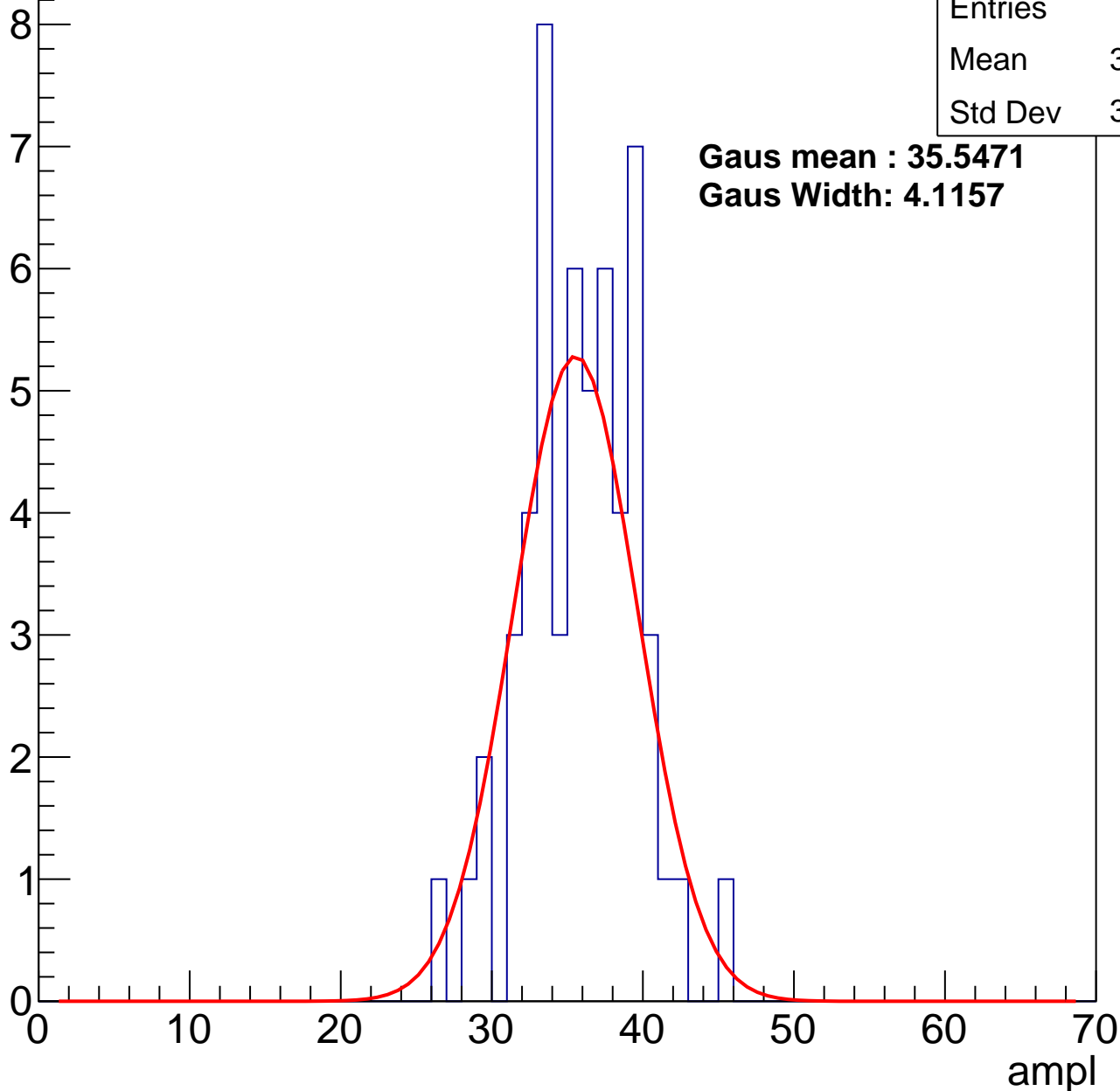
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	35.43
Std Dev	3.654

Gaus mean : 35.5471

Gaus Width: 4.1157



B0L001S, U15-ch125, adc2

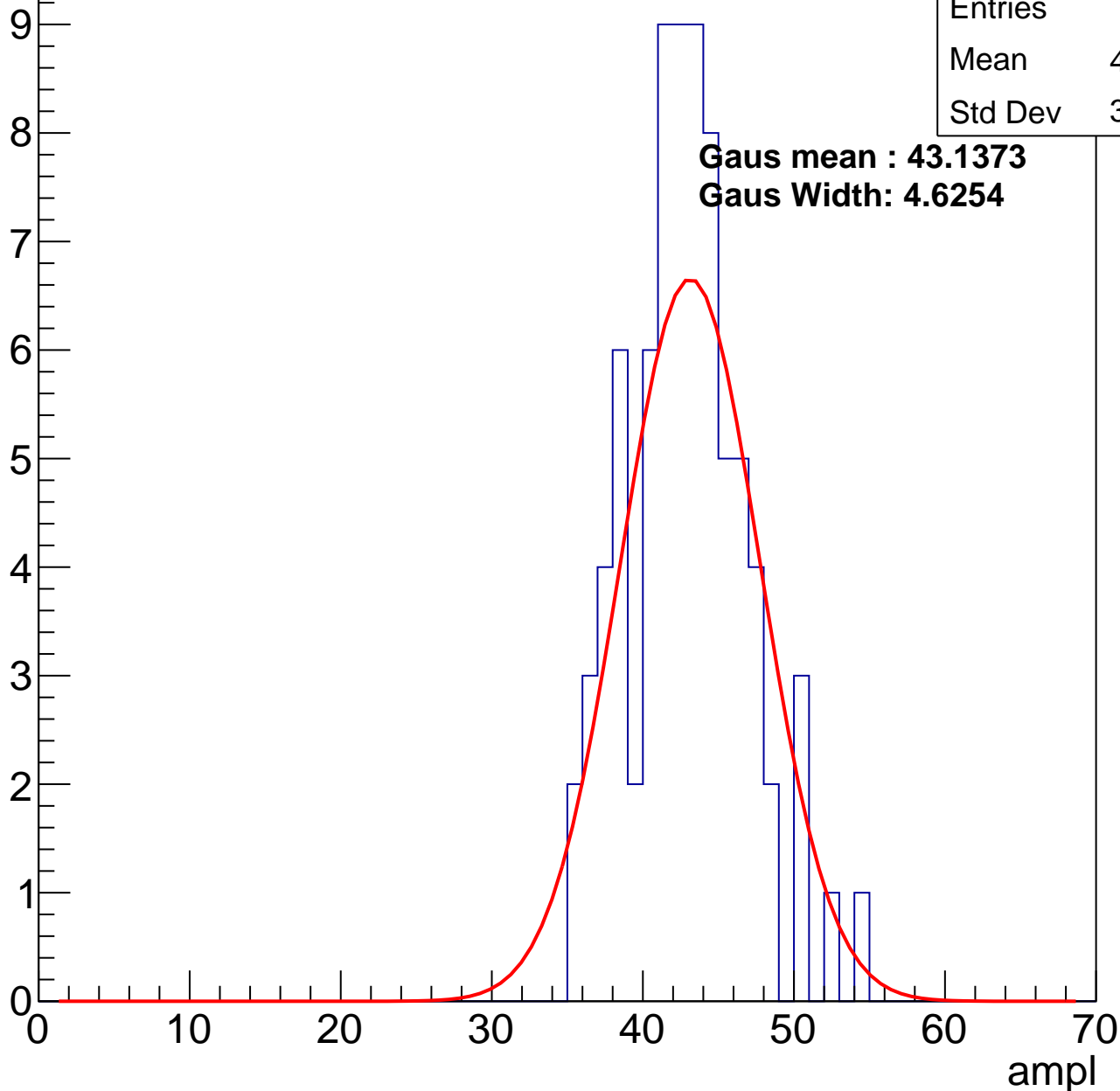
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	42.44
Std Dev	3.916

Gaus mean : 43.1373

Gaus Width: 4.6254



B0L001S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

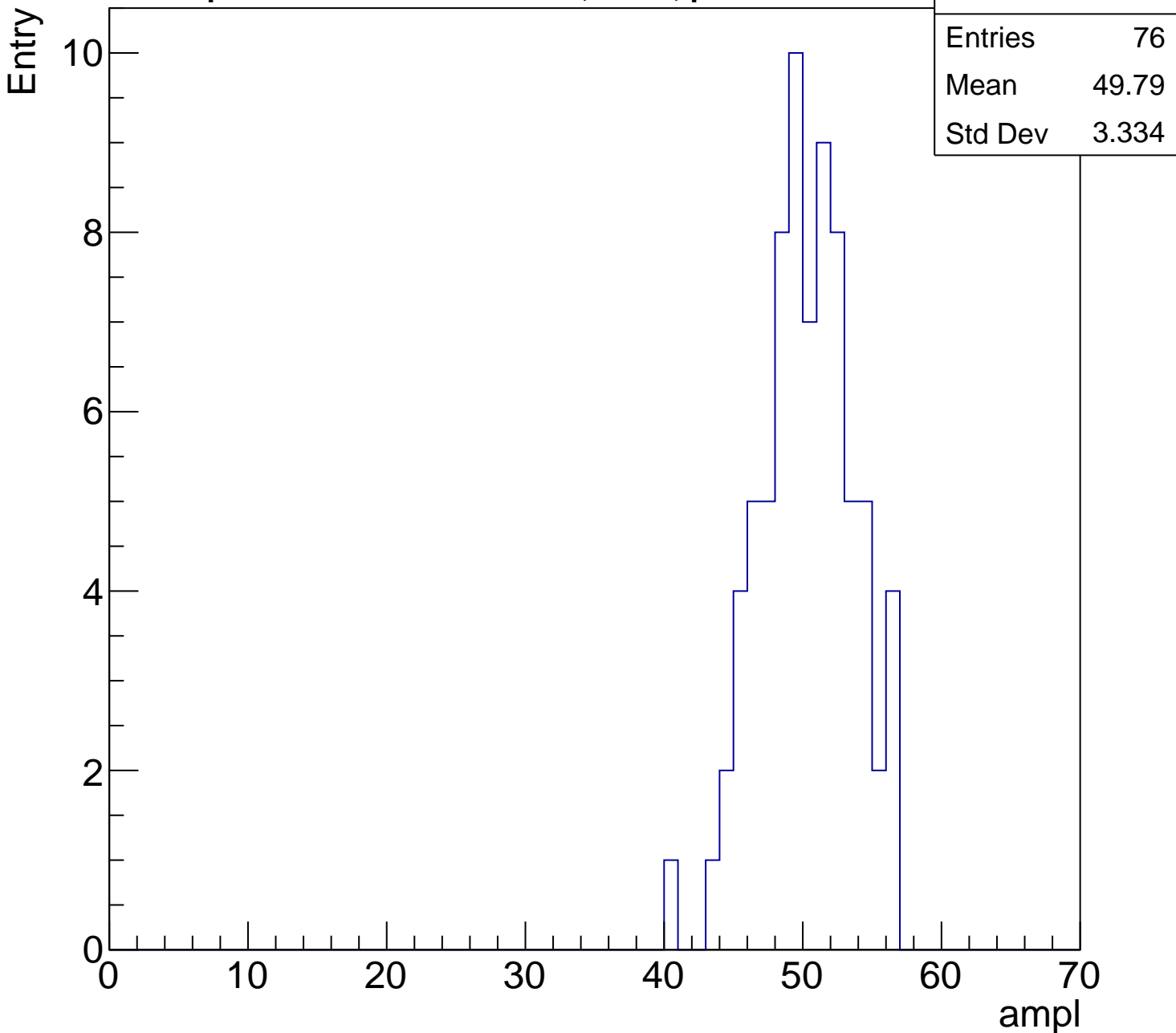
Entries	76
Mean	49.79
Std Dev	3.334

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

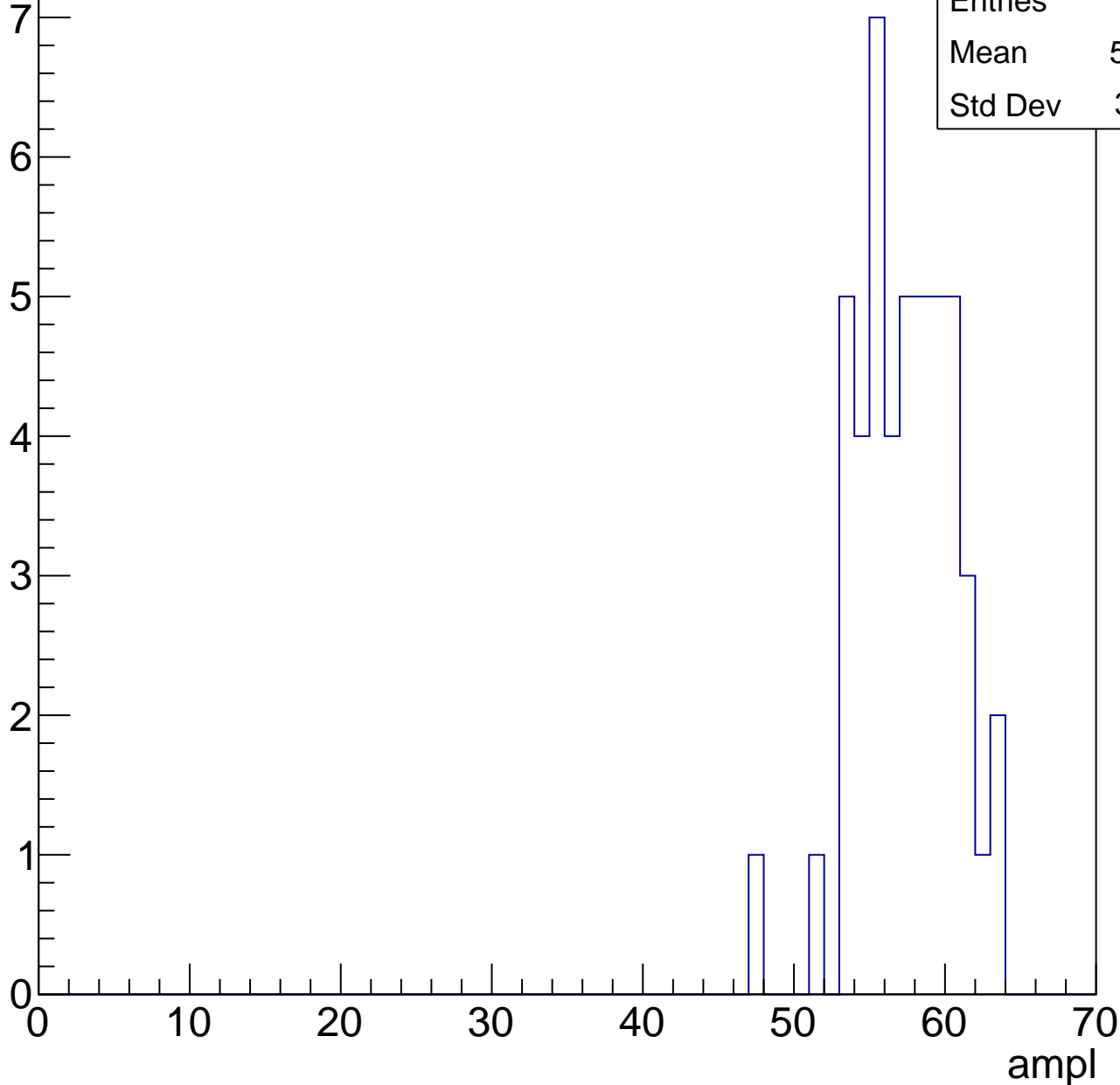


B0L001S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.85
Std Dev	3.221

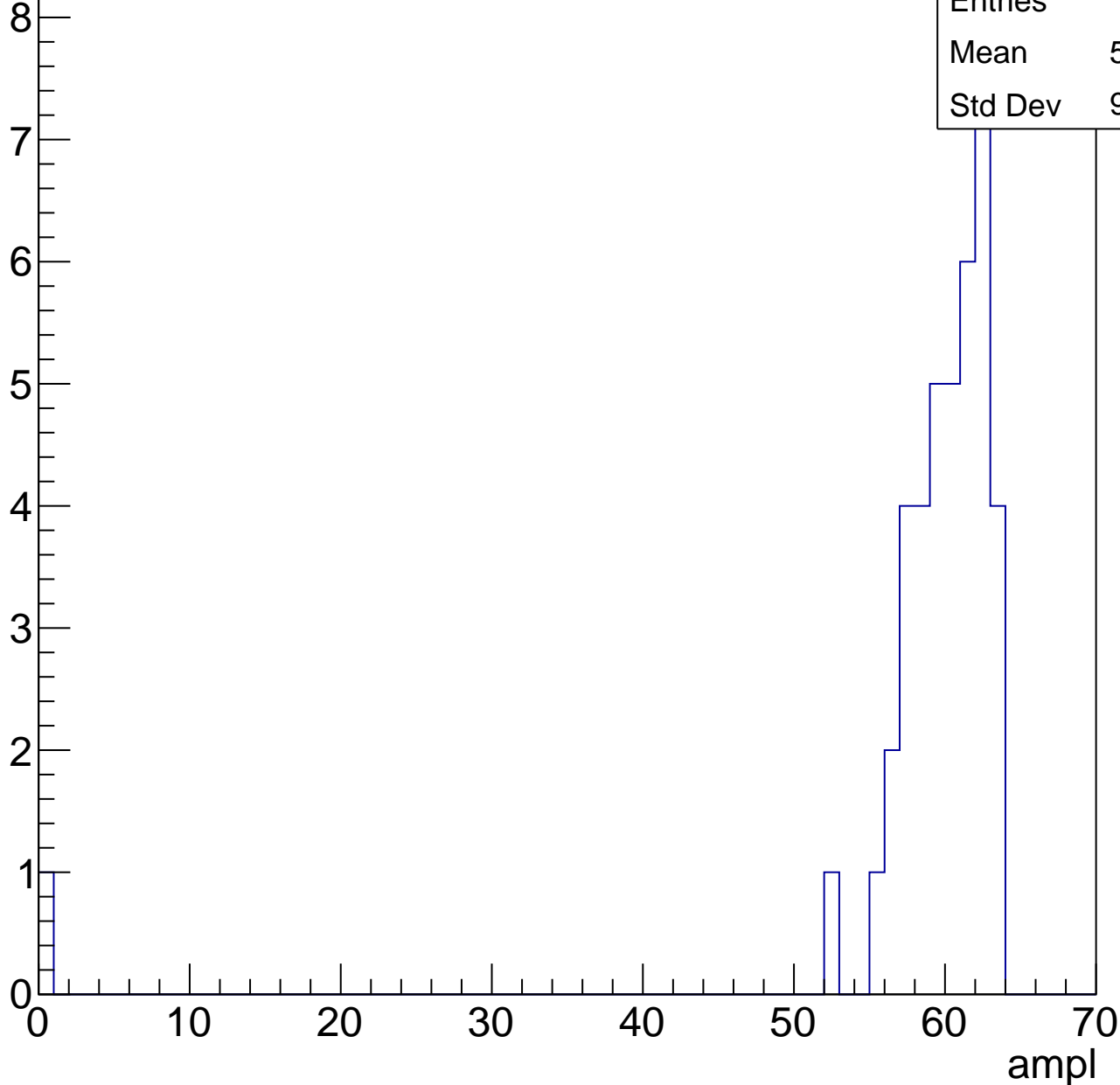


B0L001S, U15-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

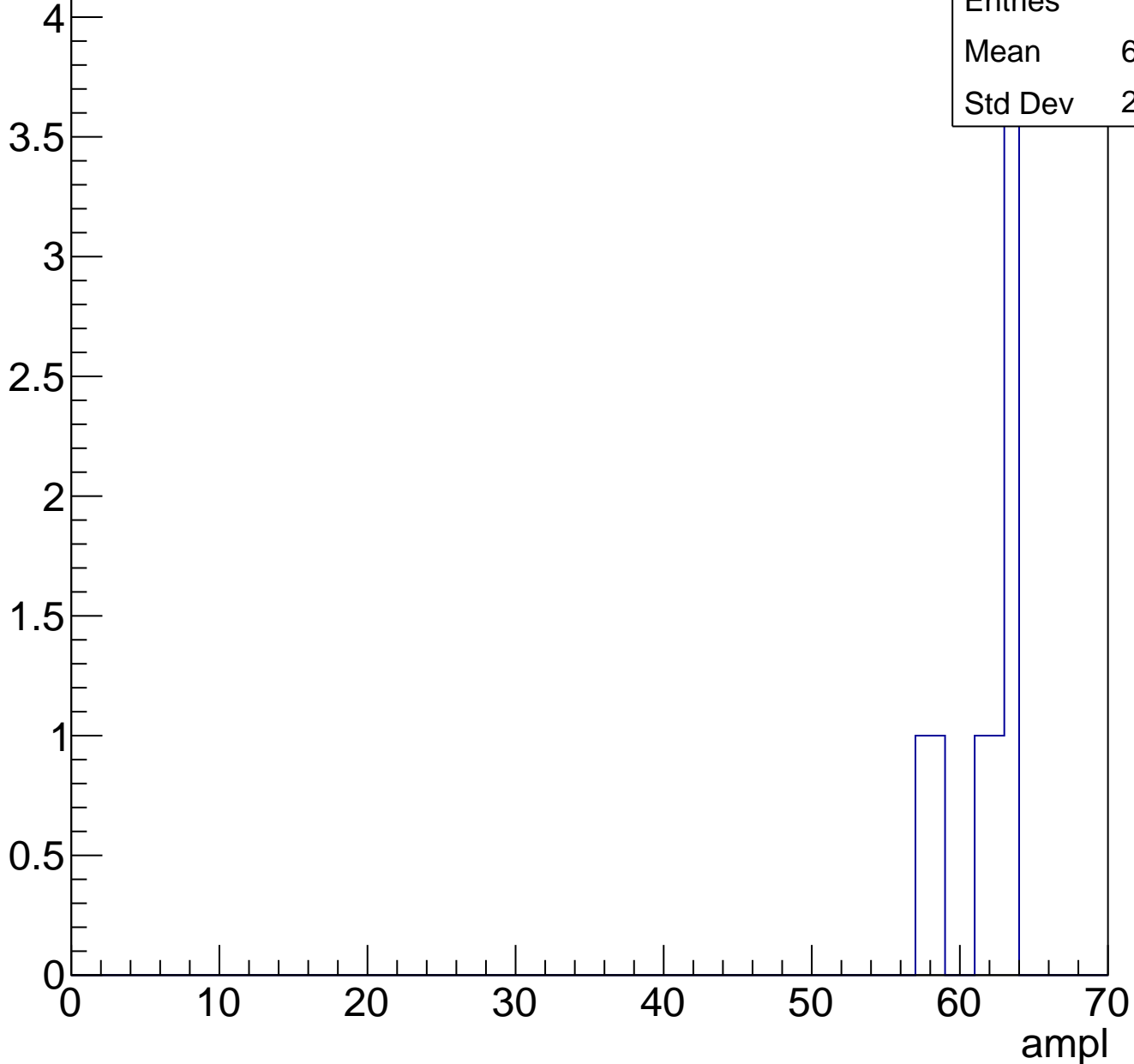
Entries	41
Mean	58.24
Std Dev	9.532



B0L001S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U15-ch126, adc0

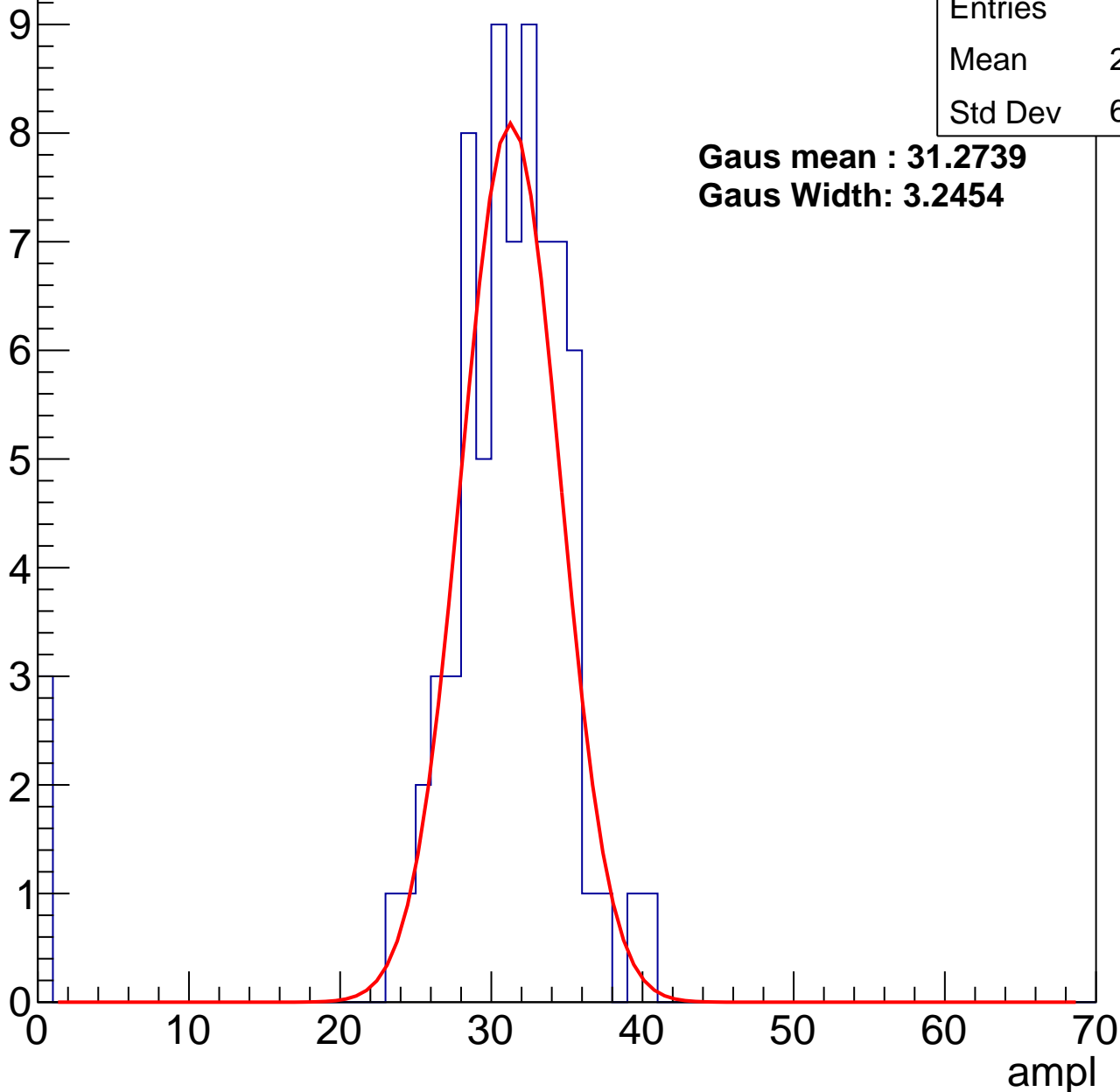
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.75
Std Dev	6.905

Gaus mean : 31.2739

Gaus Width: 3.2454



B0L001S, U15-ch126, adc1

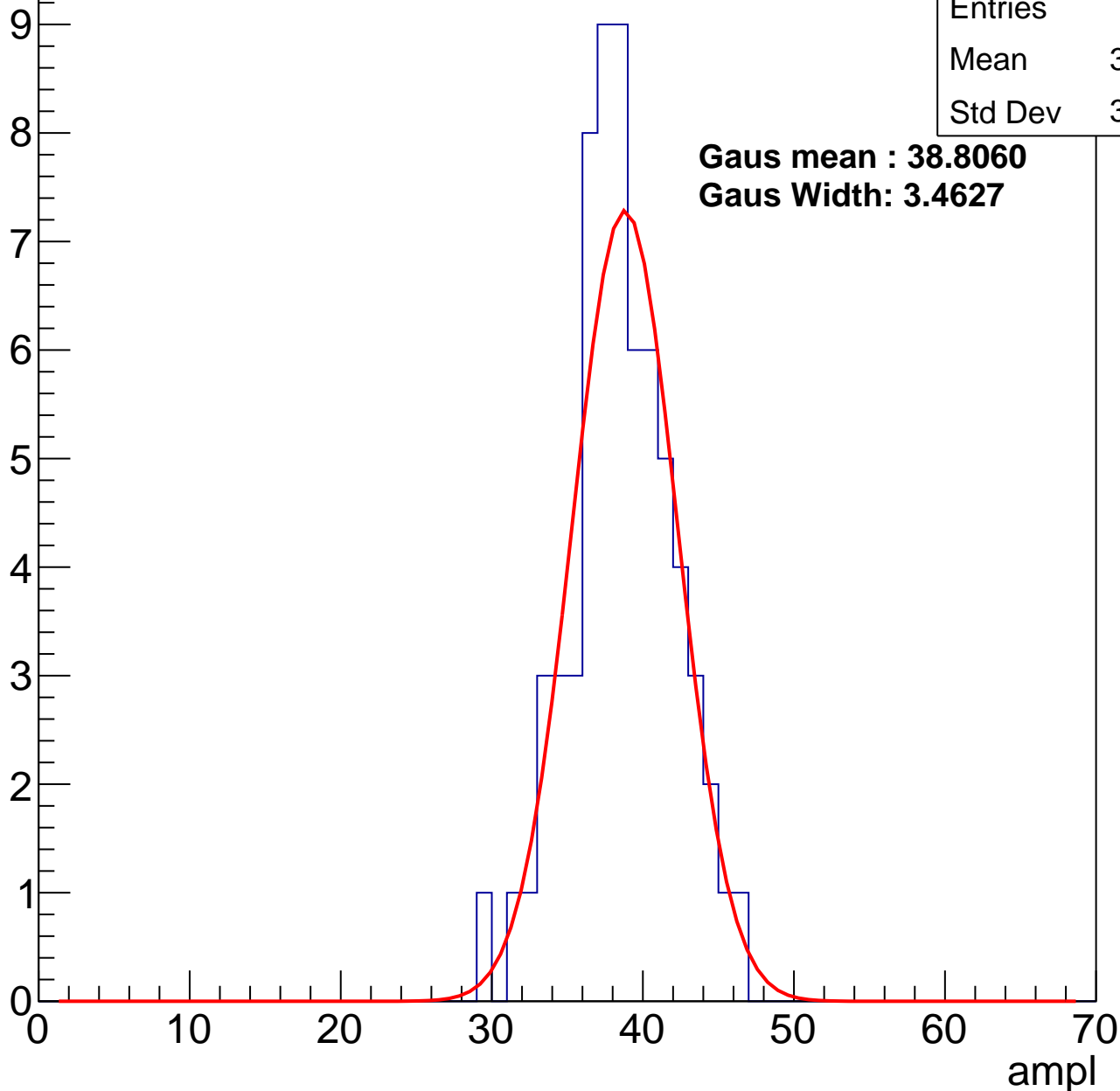
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.12
Std Dev	3.382

Gaus mean : 38.8060

Gaus Width: 3.4627



B0L001S, U15-ch126, adc2

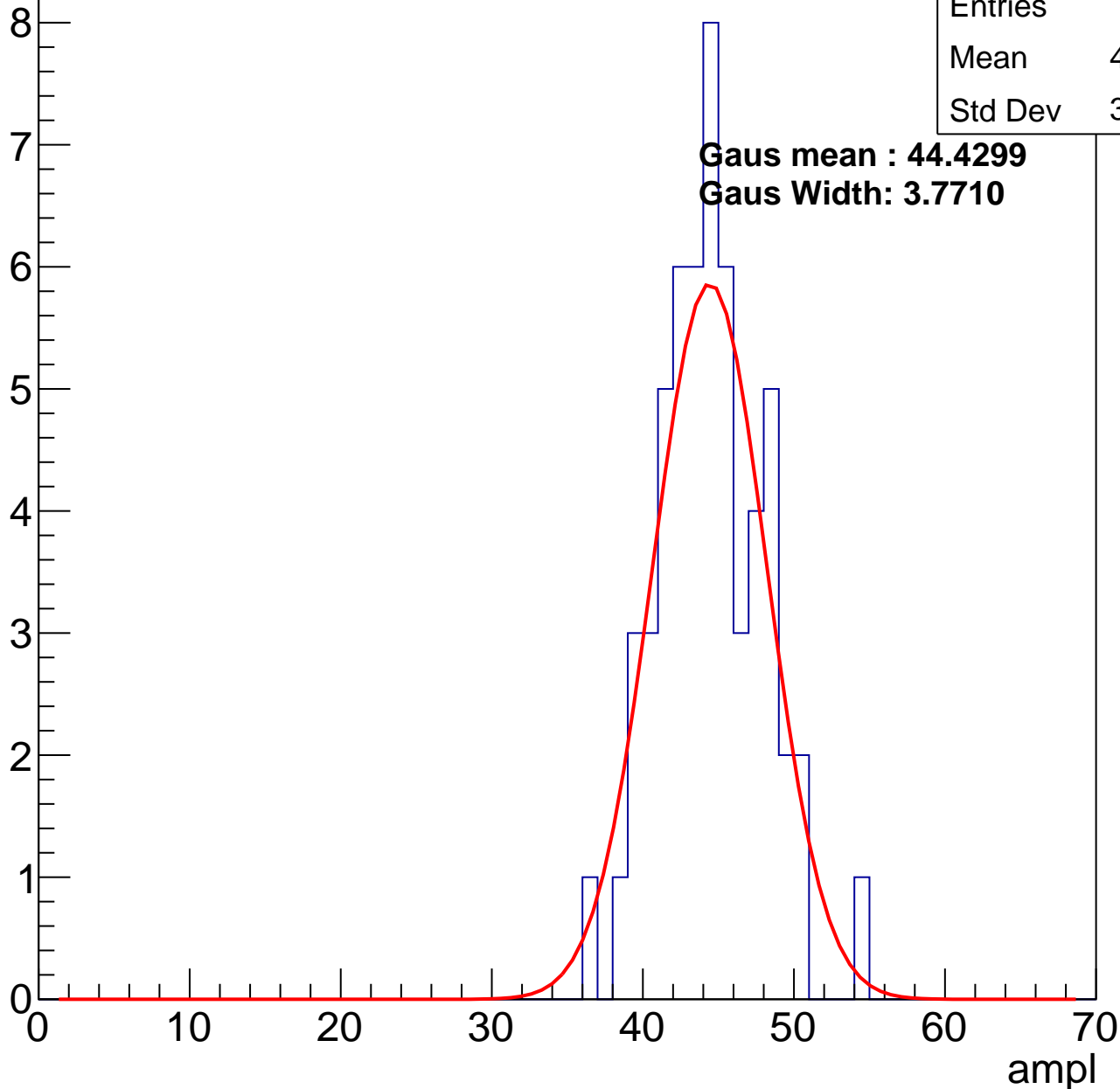
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.04
Std Dev	3.417

Gaus mean : 44.4299

Gaus Width: 3.7710

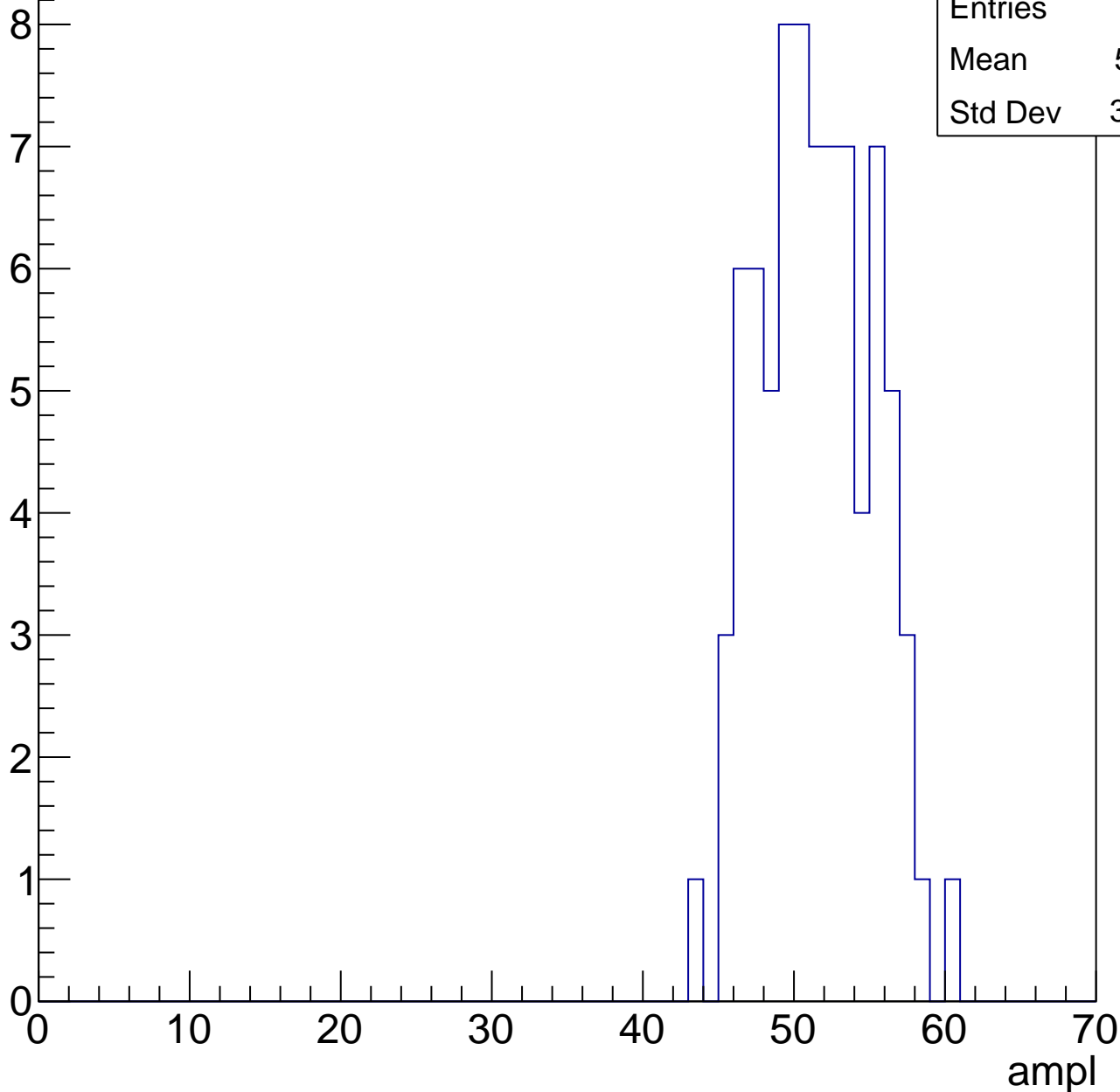


B0L001S, U15-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	51.01
Std Dev	3.644

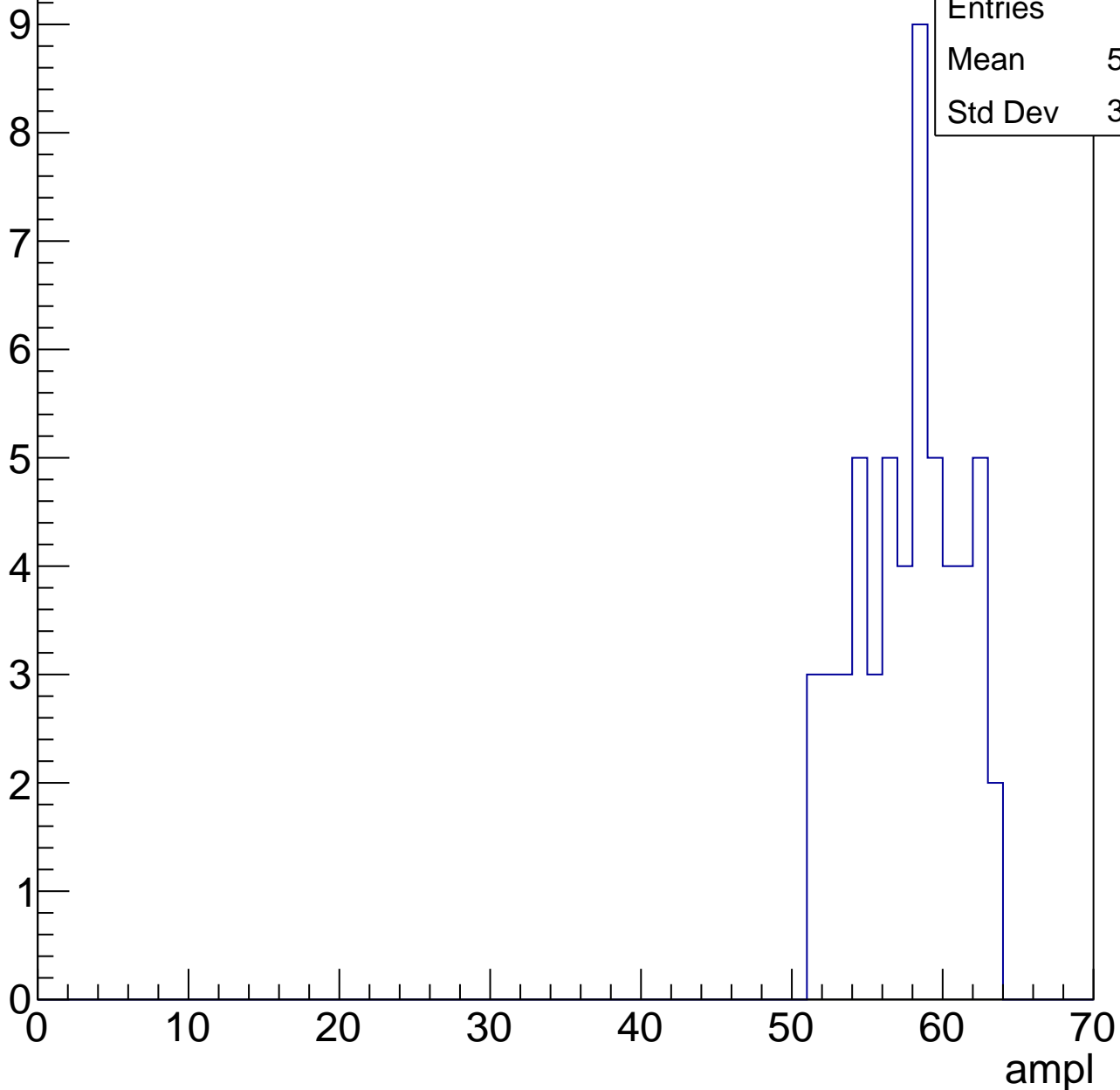


B0L001S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.24
Std Dev	3.346

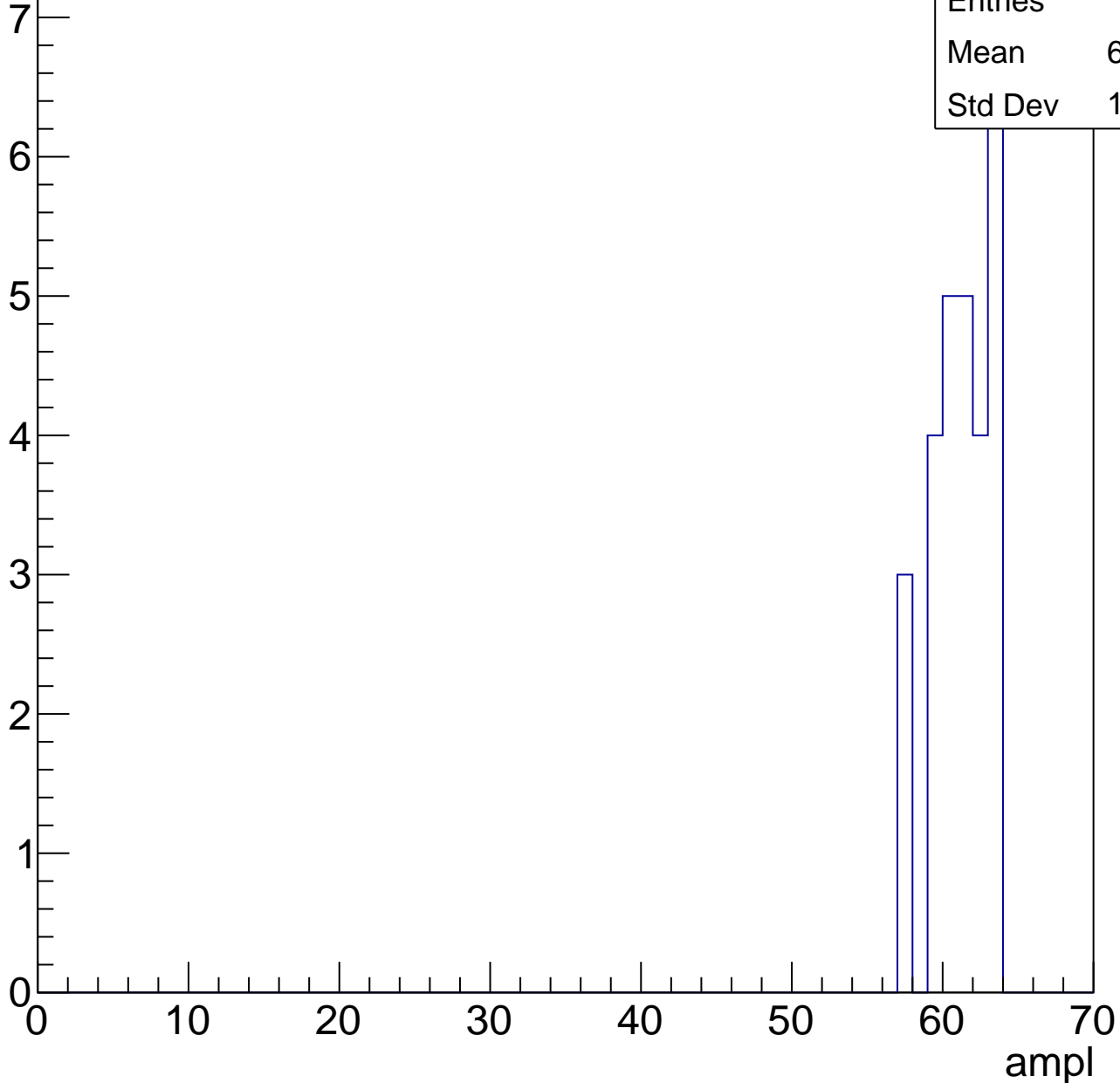


B0L001S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

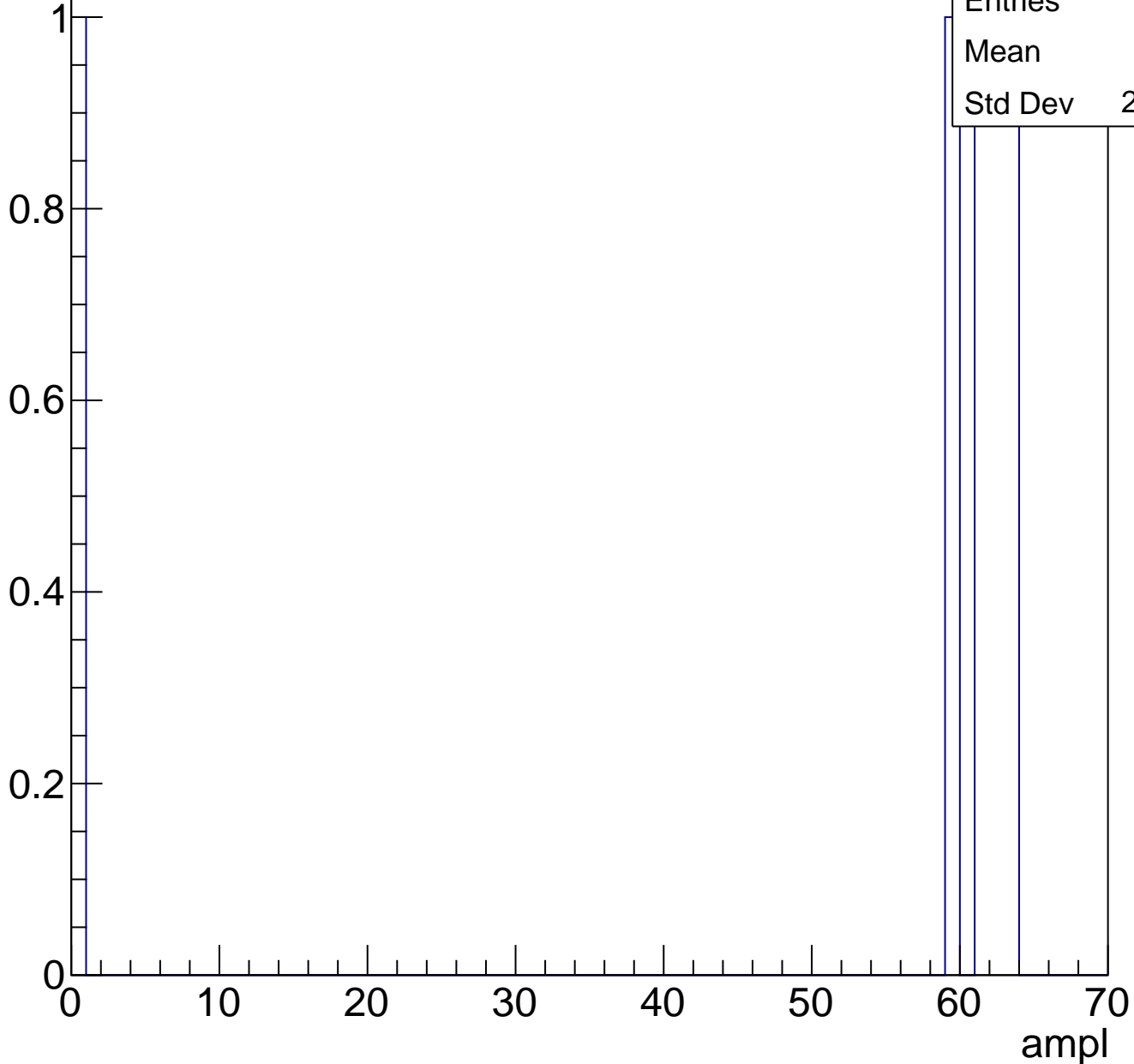
Entries	28
Mean	60.75
Std Dev	1.883



B0L001S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch127, adc0

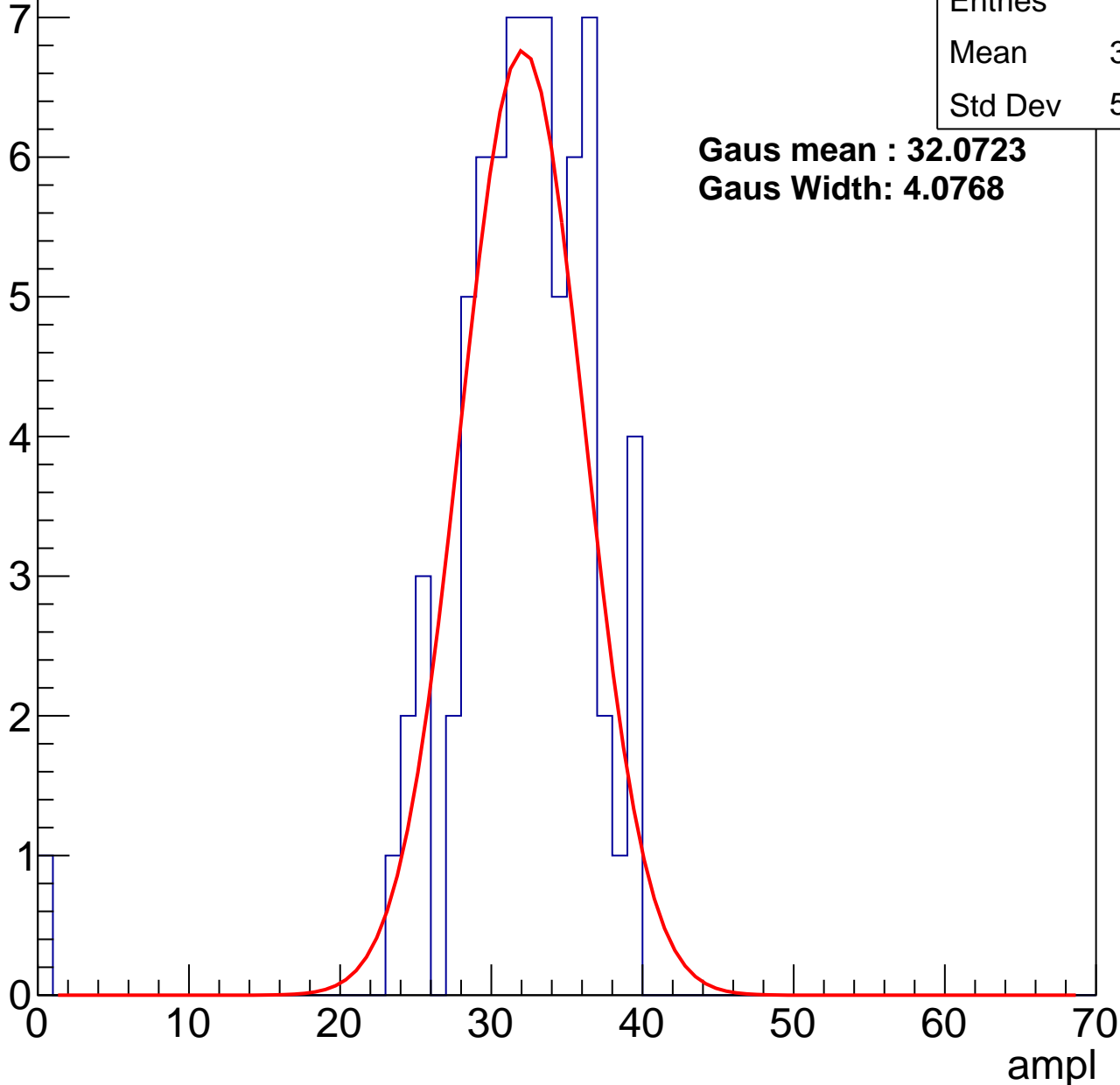
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	31.47
Std Dev	5.336

Gaus mean : 32.0723

Gaus Width: 4.0768



B0L001S, U15-ch127, adc1

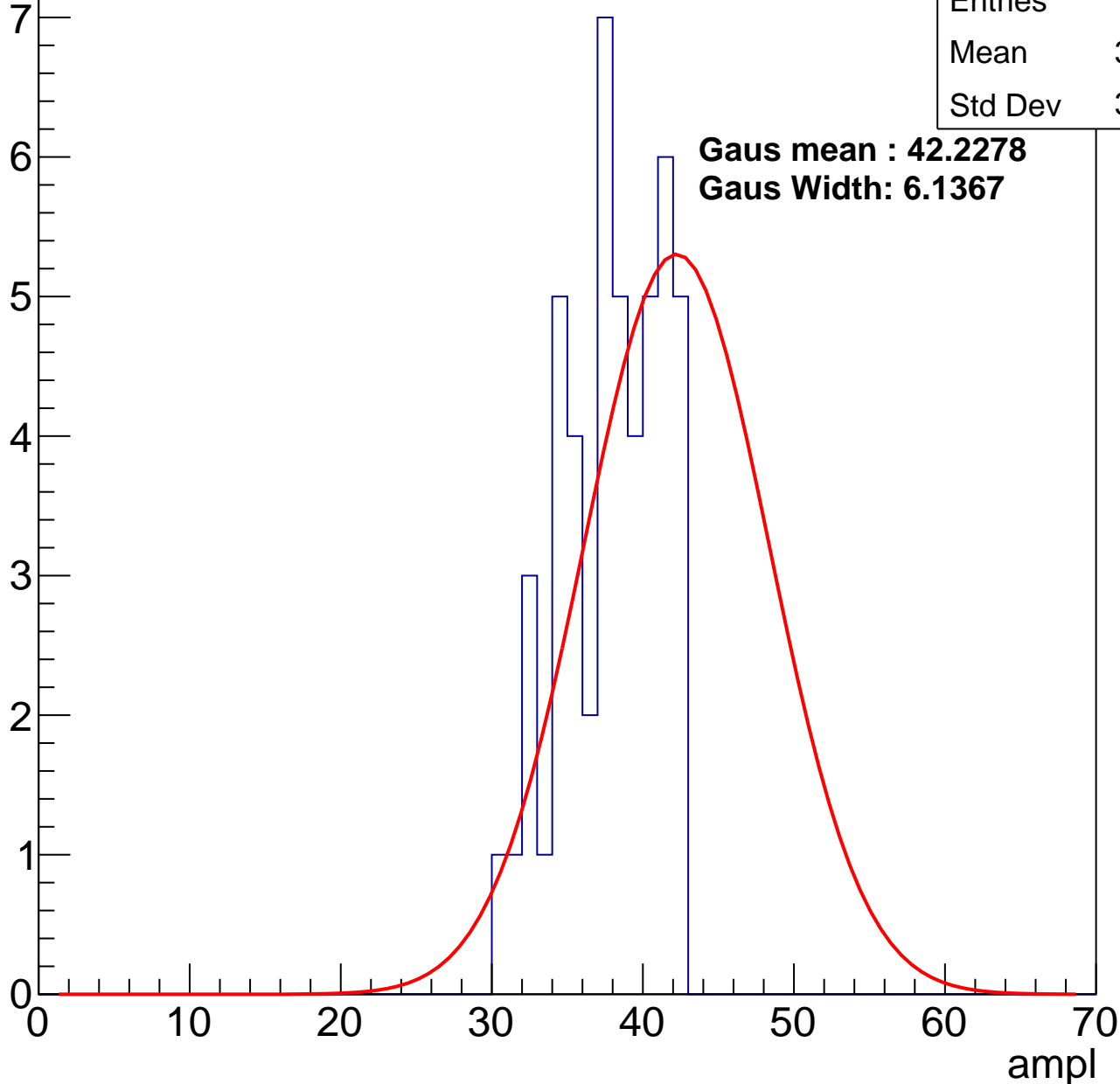
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	37.41
Std Dev	3.251

Gaus mean : 42.2278

Gaus Width: 6.1367



B0L001S, U15-ch127, adc2

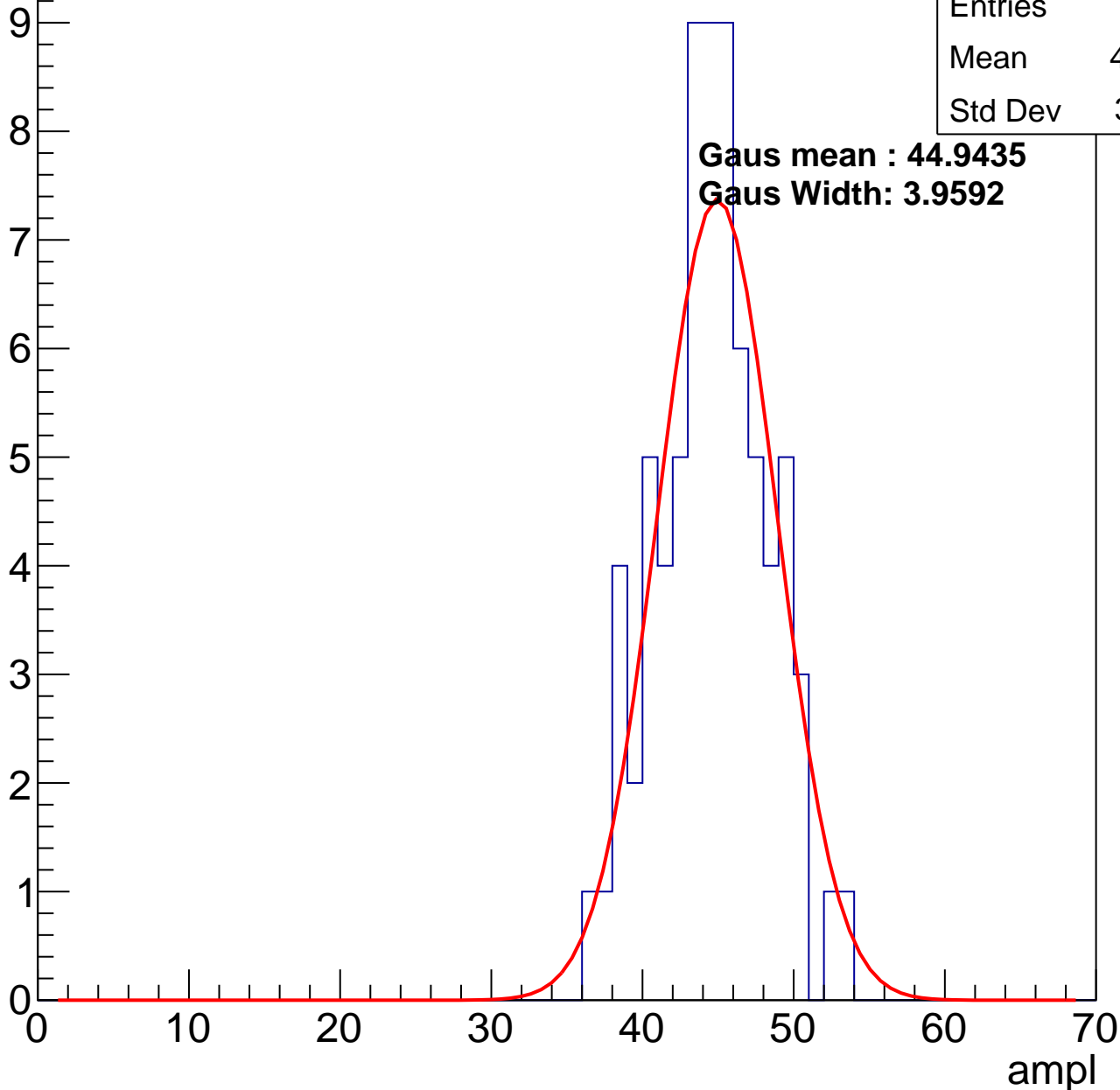
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	44.16
Std Dev	3.621

Gaus mean : 44.9435

Gaus Width: 3.9592

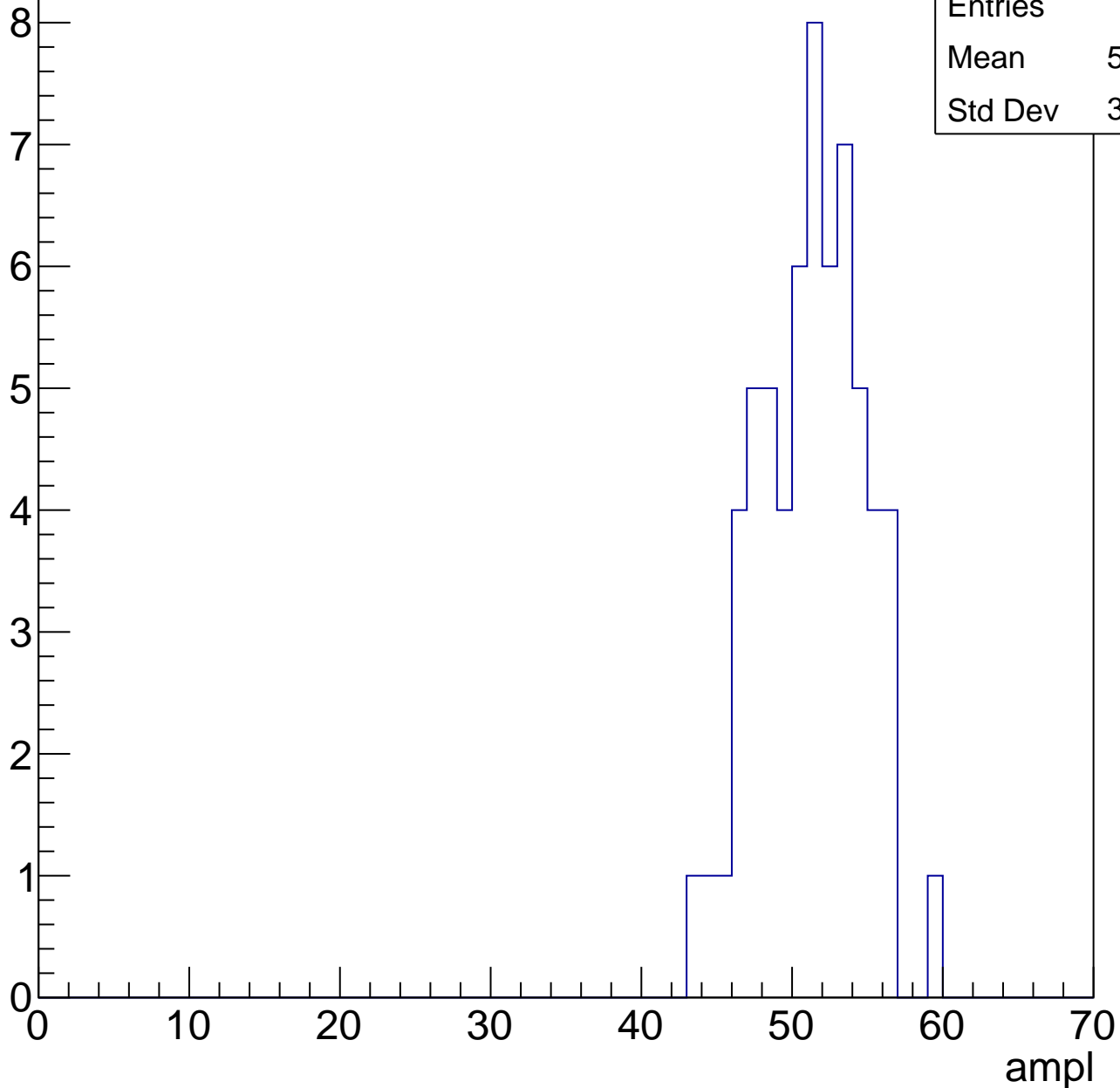


B0L001S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.82
Std Dev	3.363

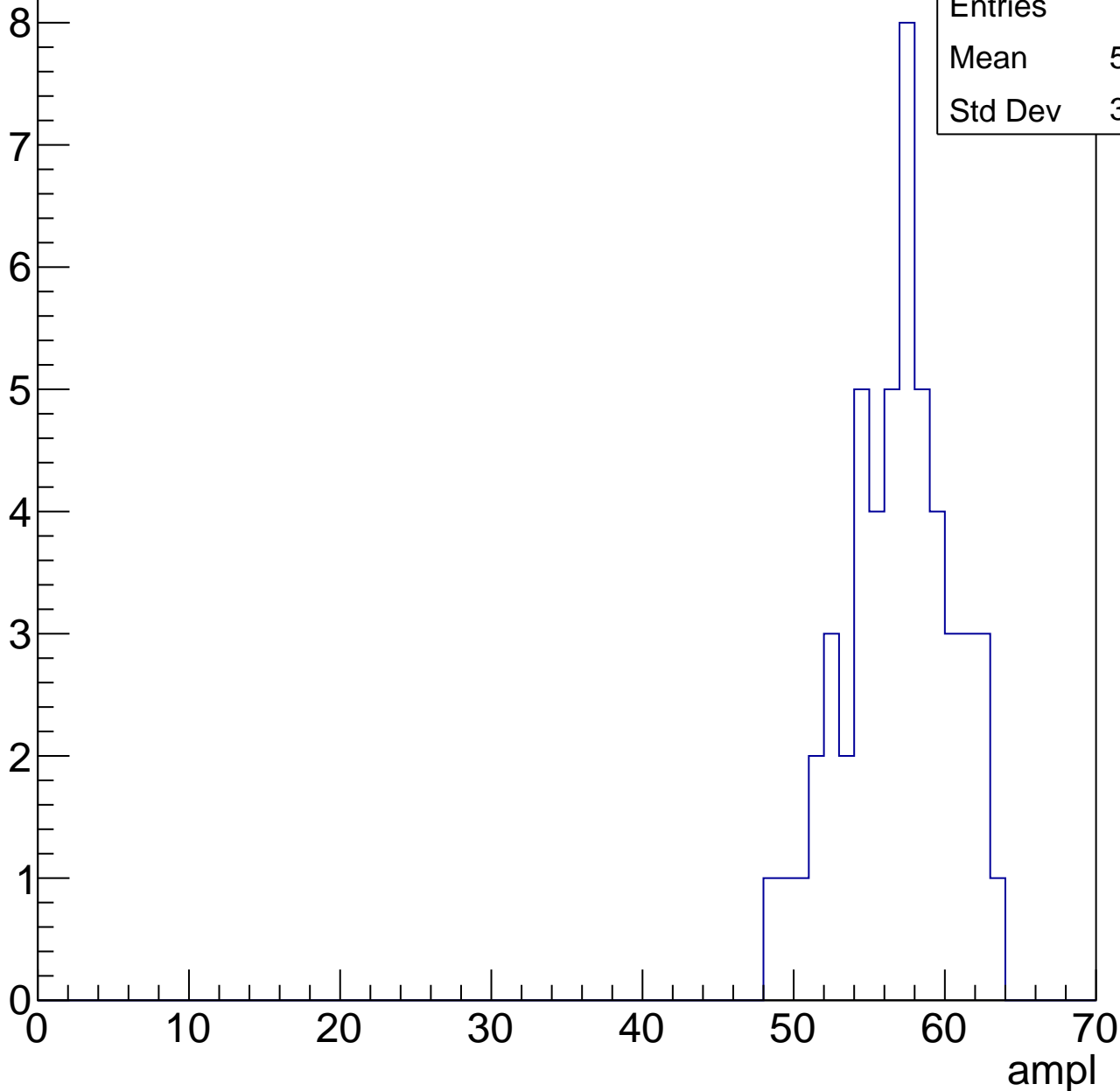


B0L001S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

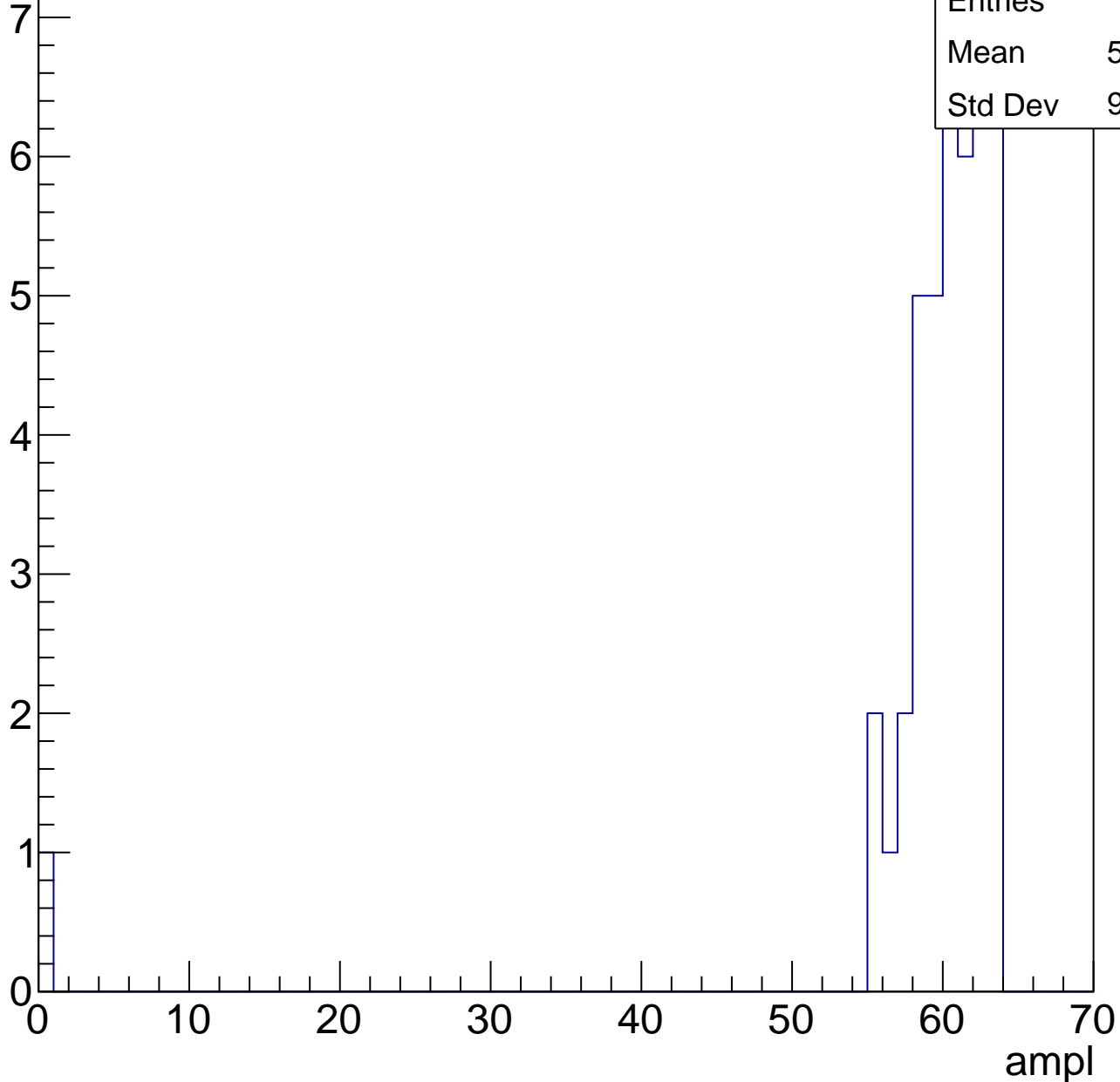
Entries	51
Mean	56.37
Std Dev	3.503



B0L001S, U15-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0