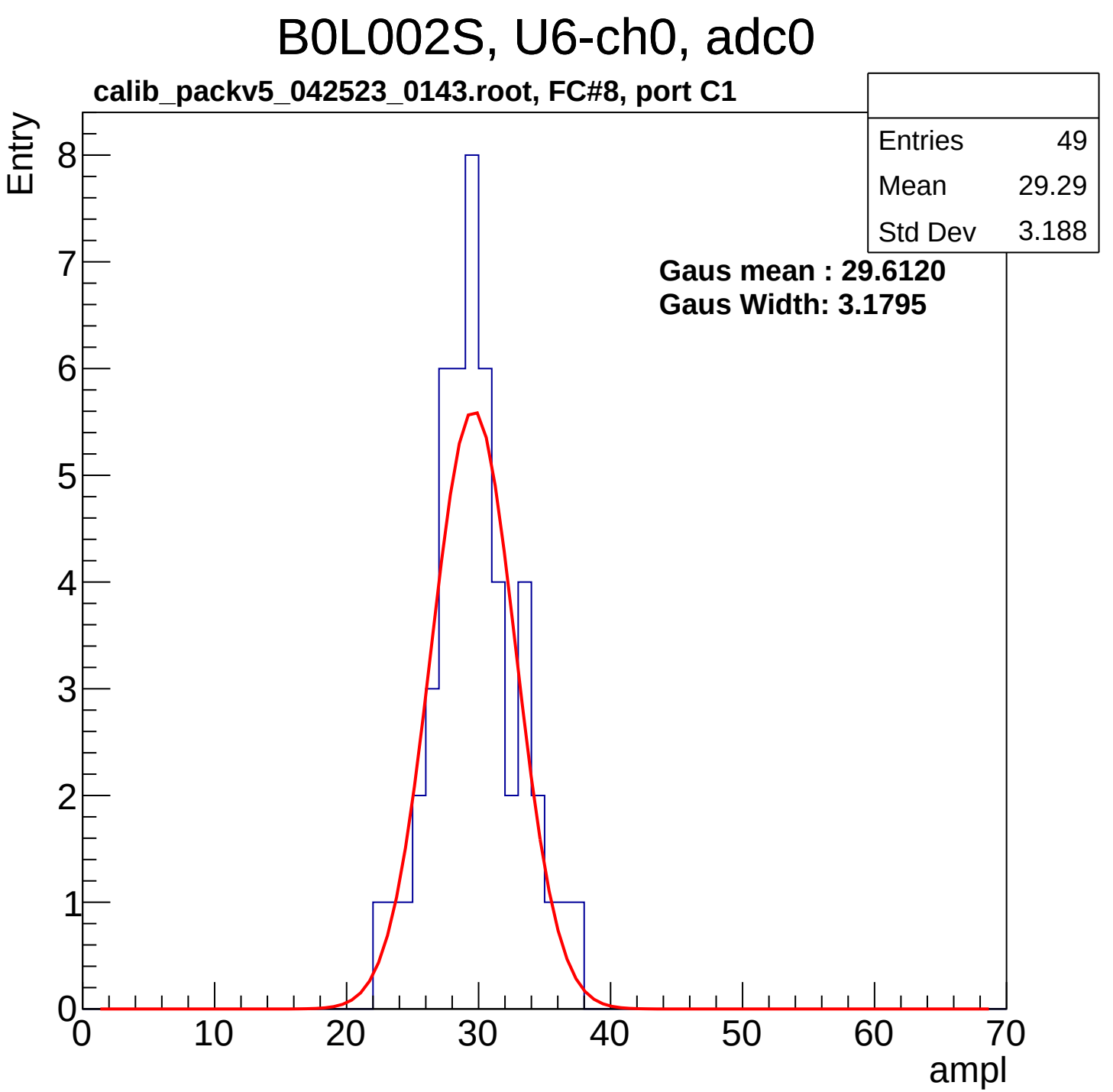
B0L002S, U6-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 29.29
Std Dev 3.188
Gaus mean : 29.6120
Gaus Width: 3.1795
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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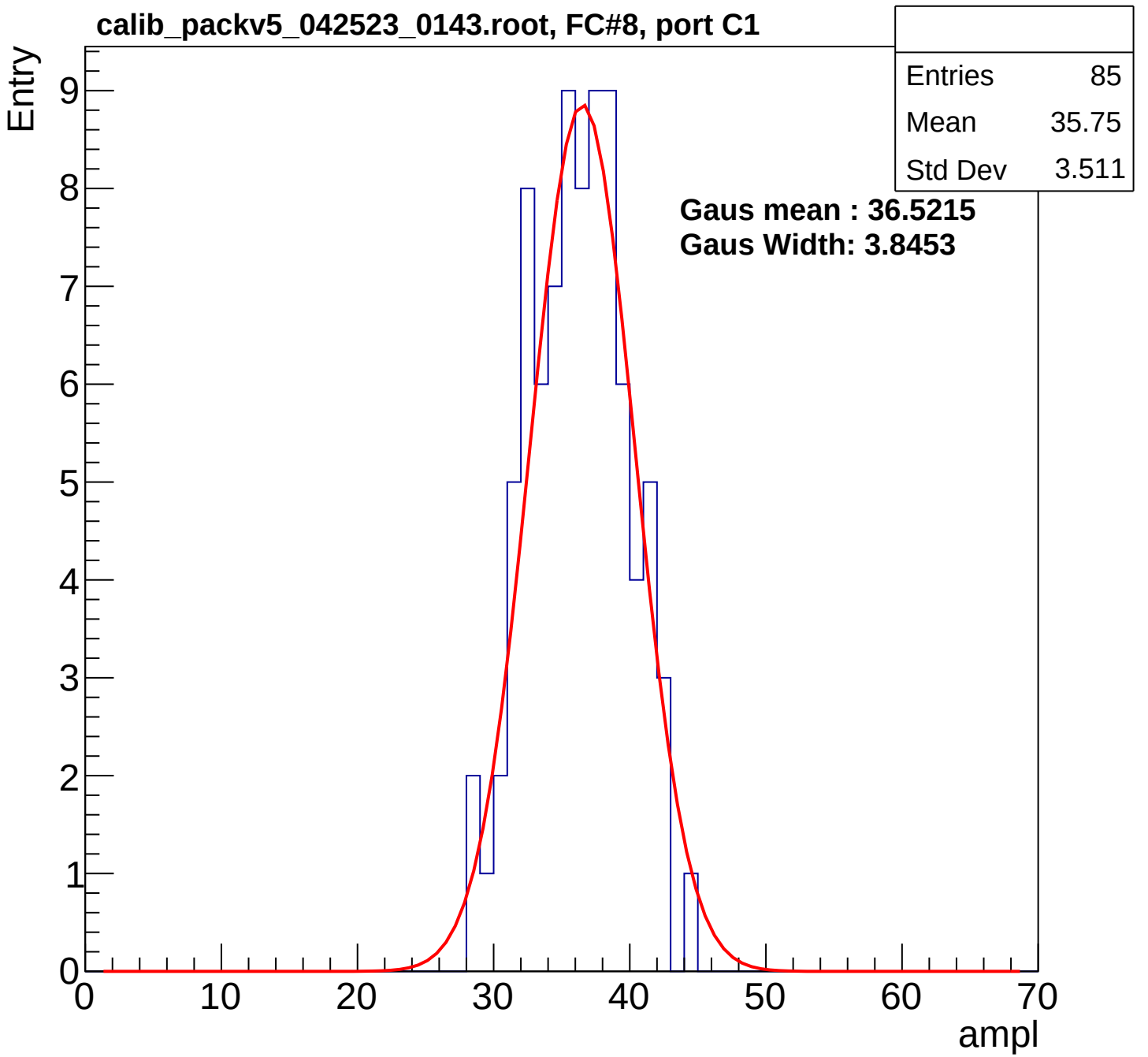
B0L002S, U6-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 35.75
Std Dev 3.511
Gaus mean : 36.5215
Gaus Width: 3.8453
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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40
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60
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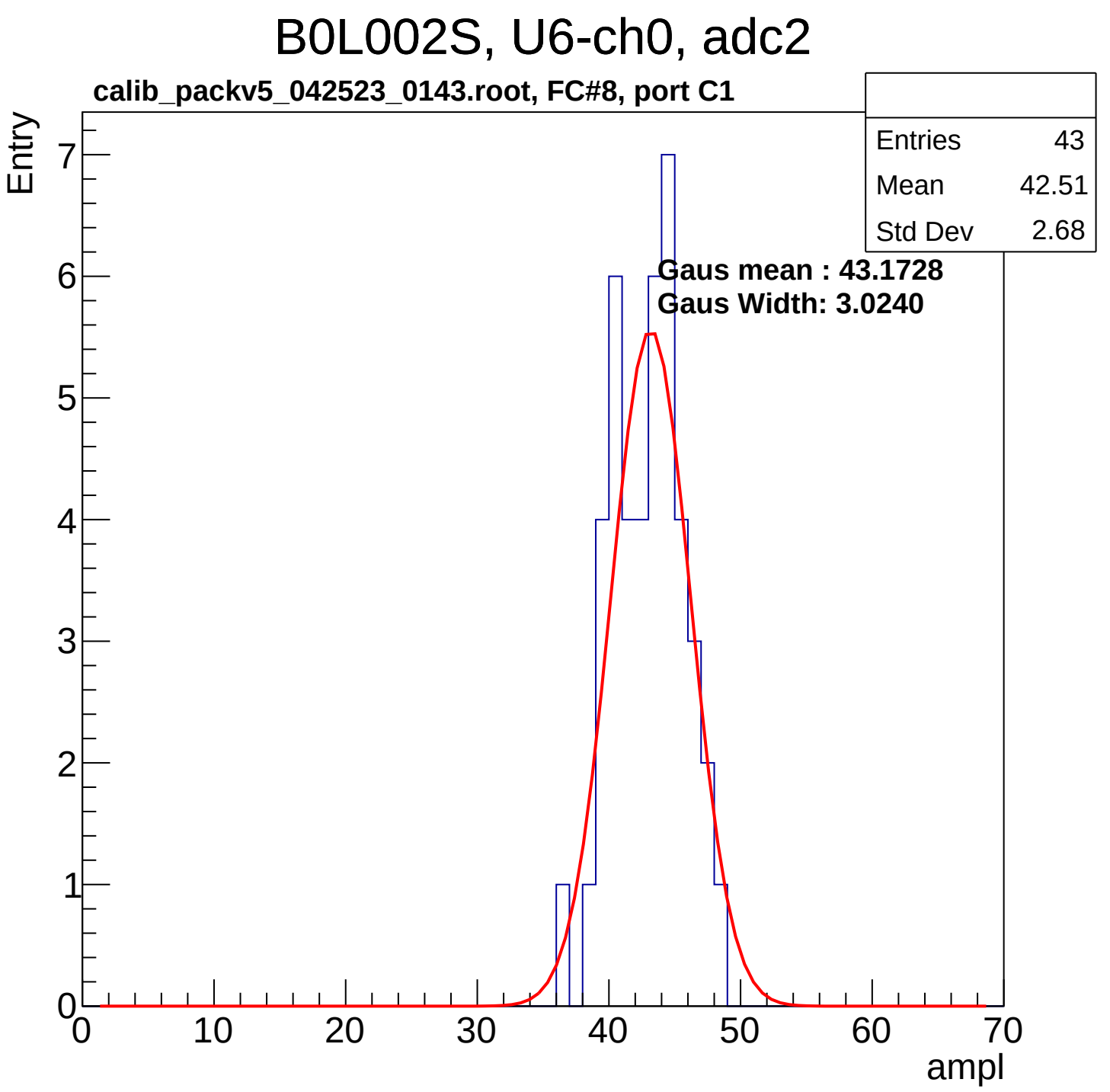
B0L002S, U6-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 42.51
Std Dev 2.68
Gaus mean : 43.1728
Gaus Width: 3.0240
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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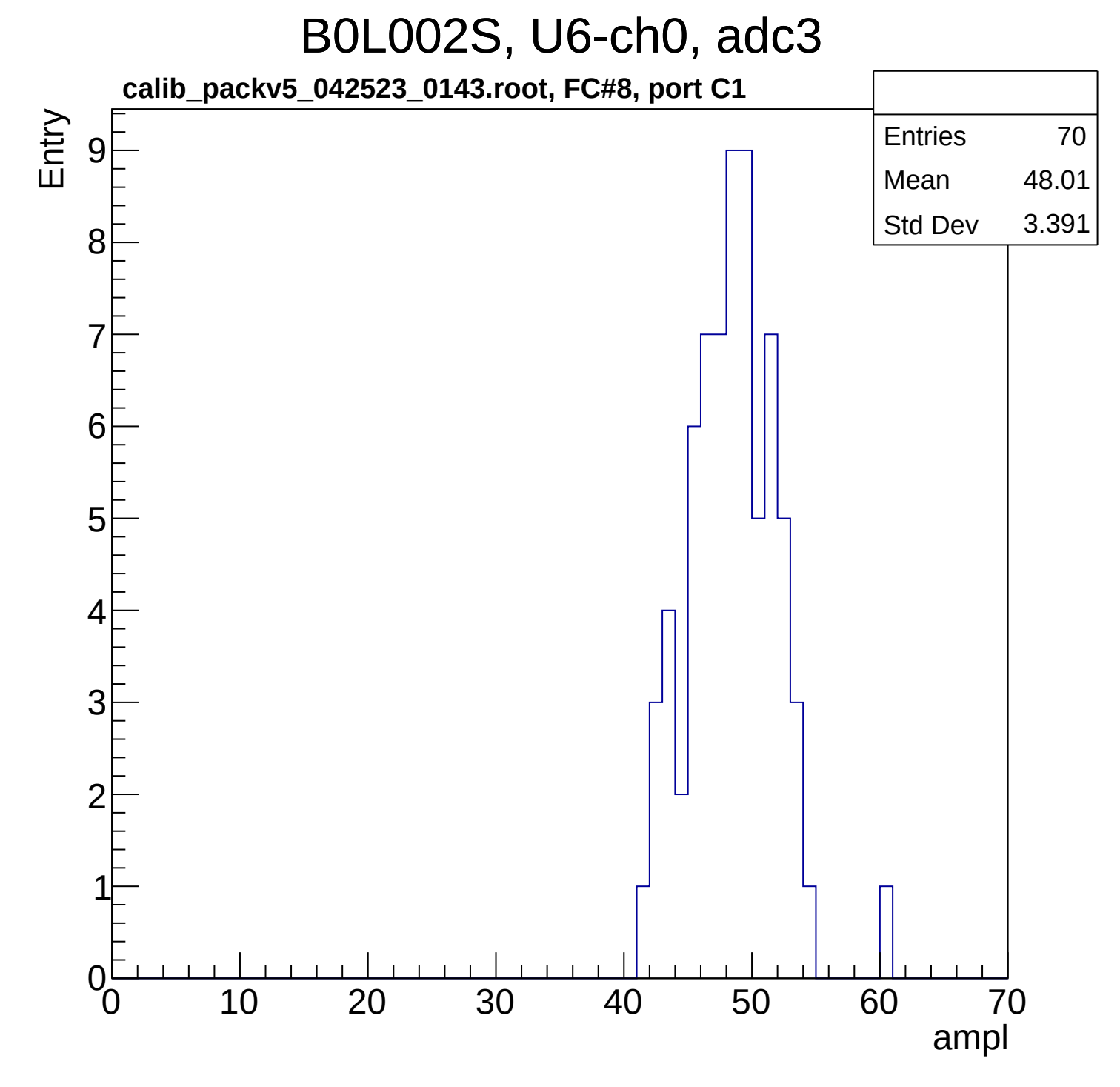
B0L002S, U6-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 48.01
Std Dev 3.391
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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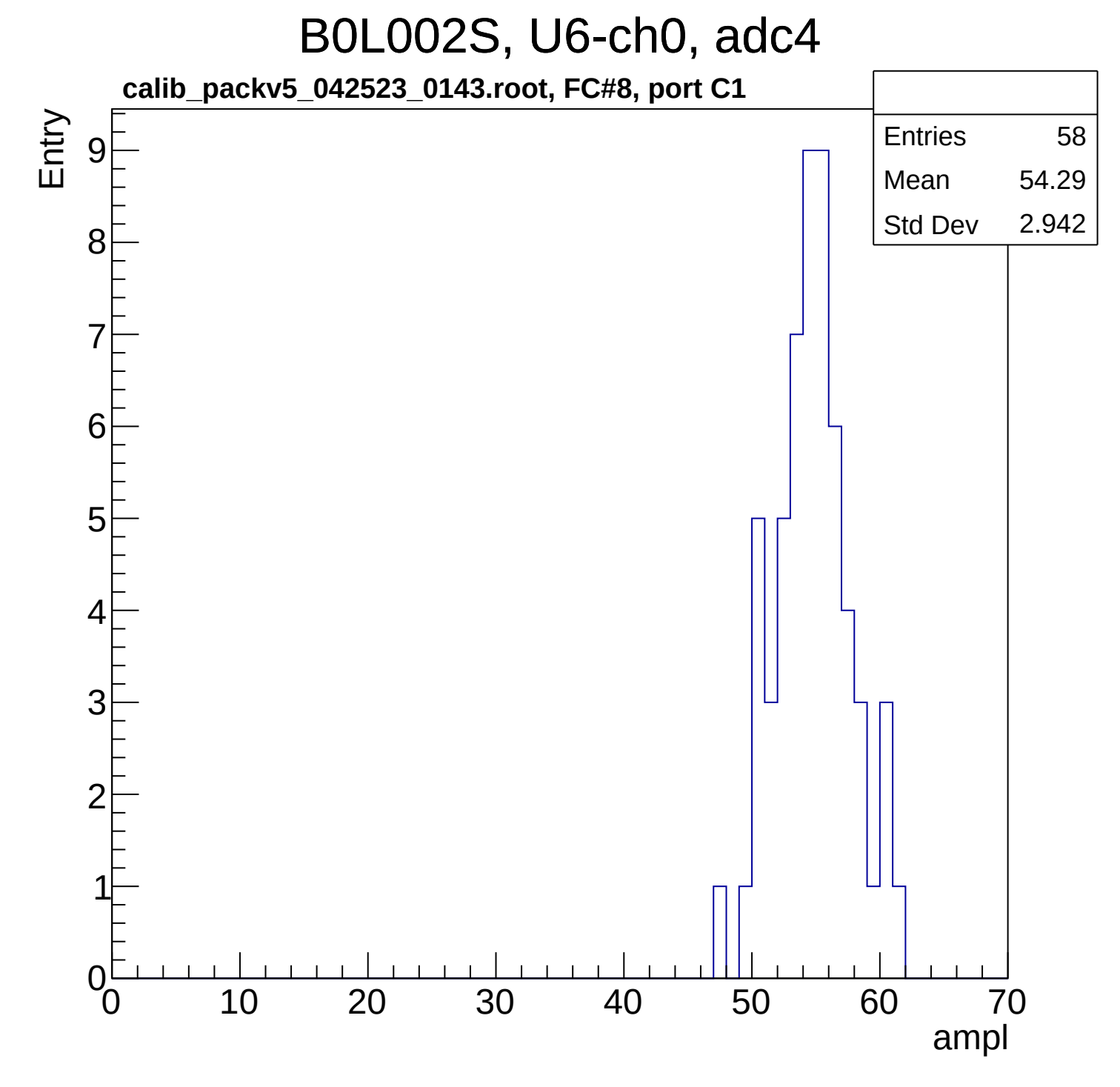
B0L002S, U6-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 54.29
Std Dev 2.942
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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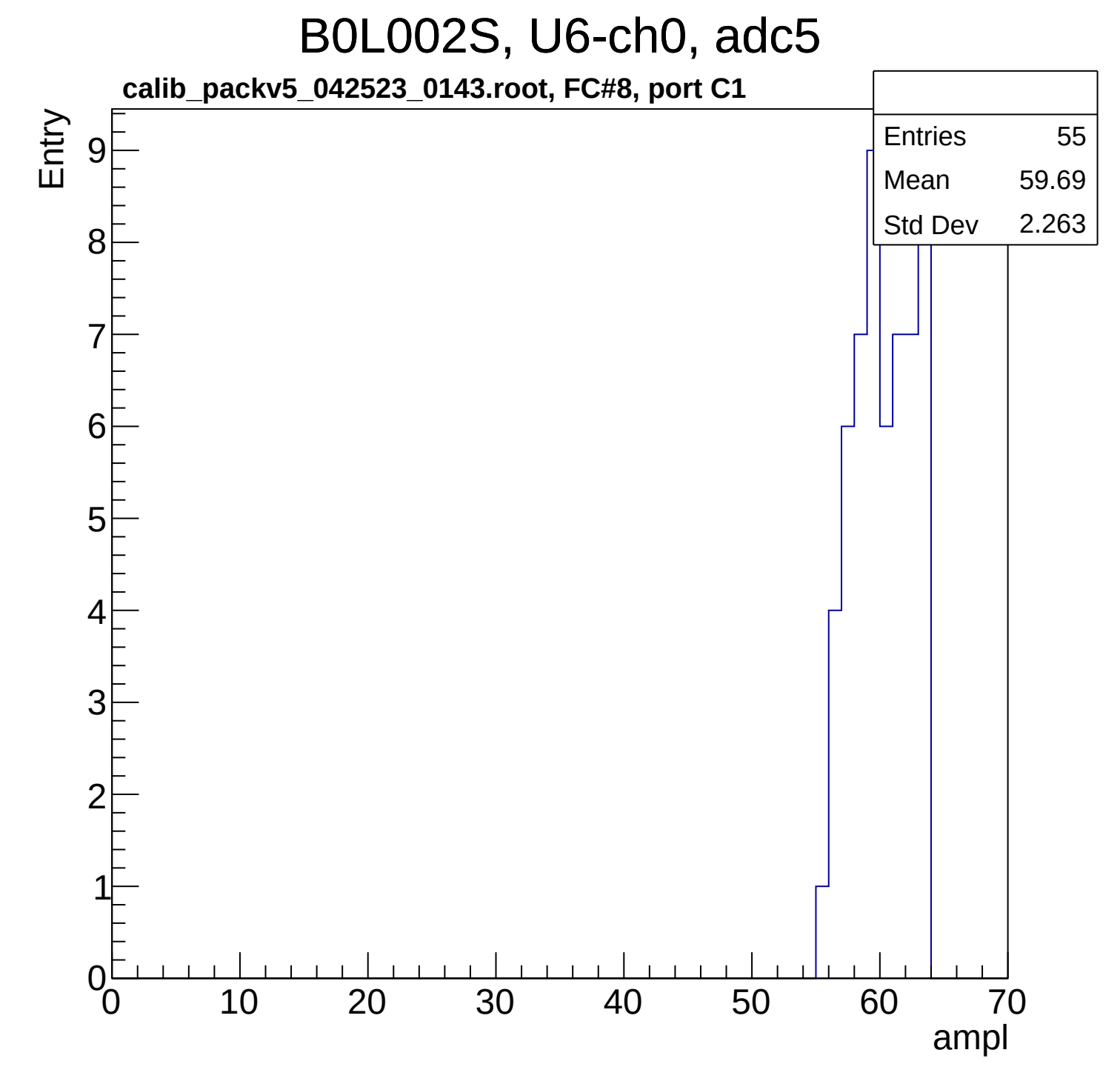
B0L002S, U6-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 59.69
Std Dev 2.263
Entry
ampl
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4
5
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0
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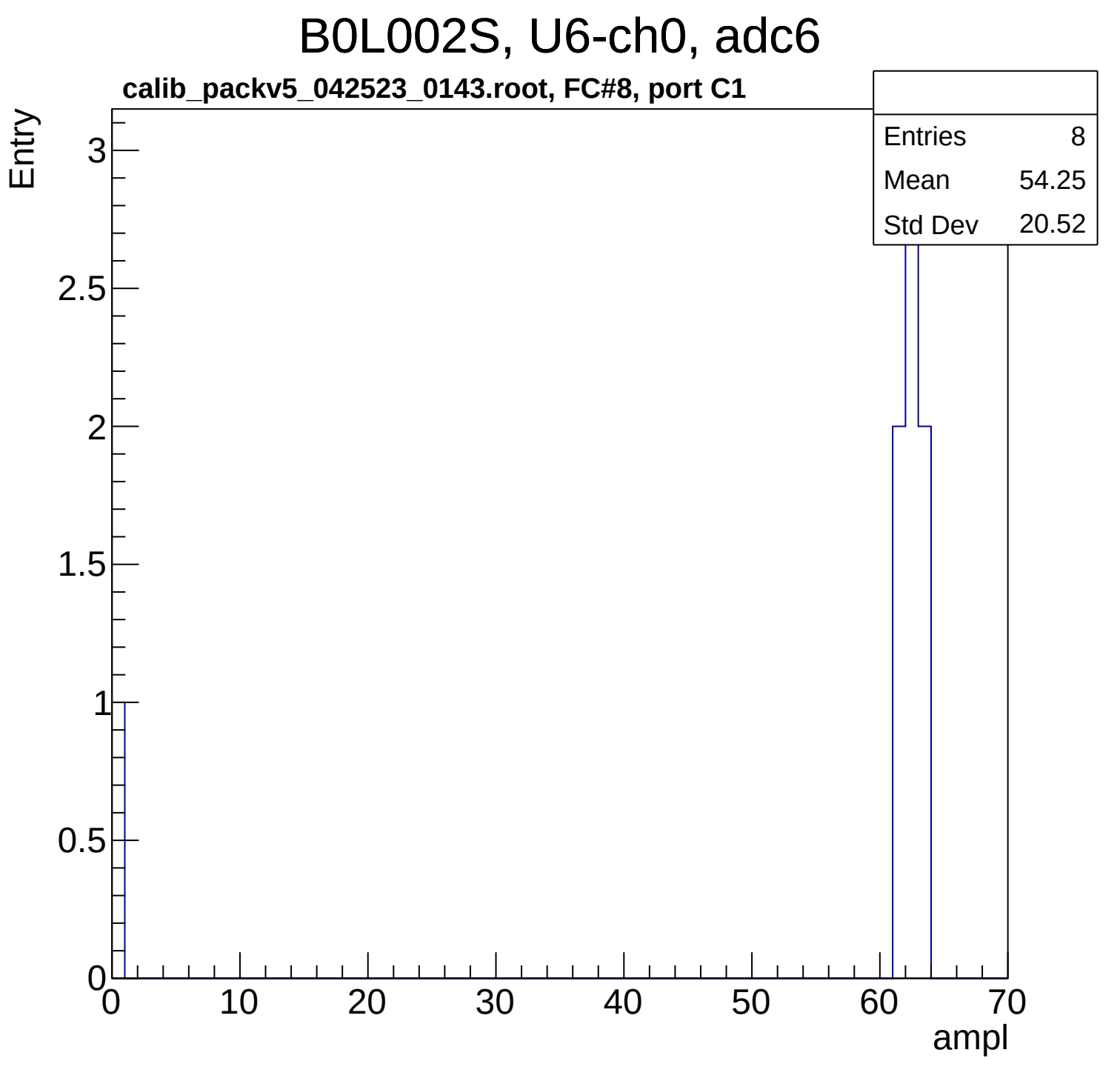
B0L002S, U6-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.25
Std Dev 20.52
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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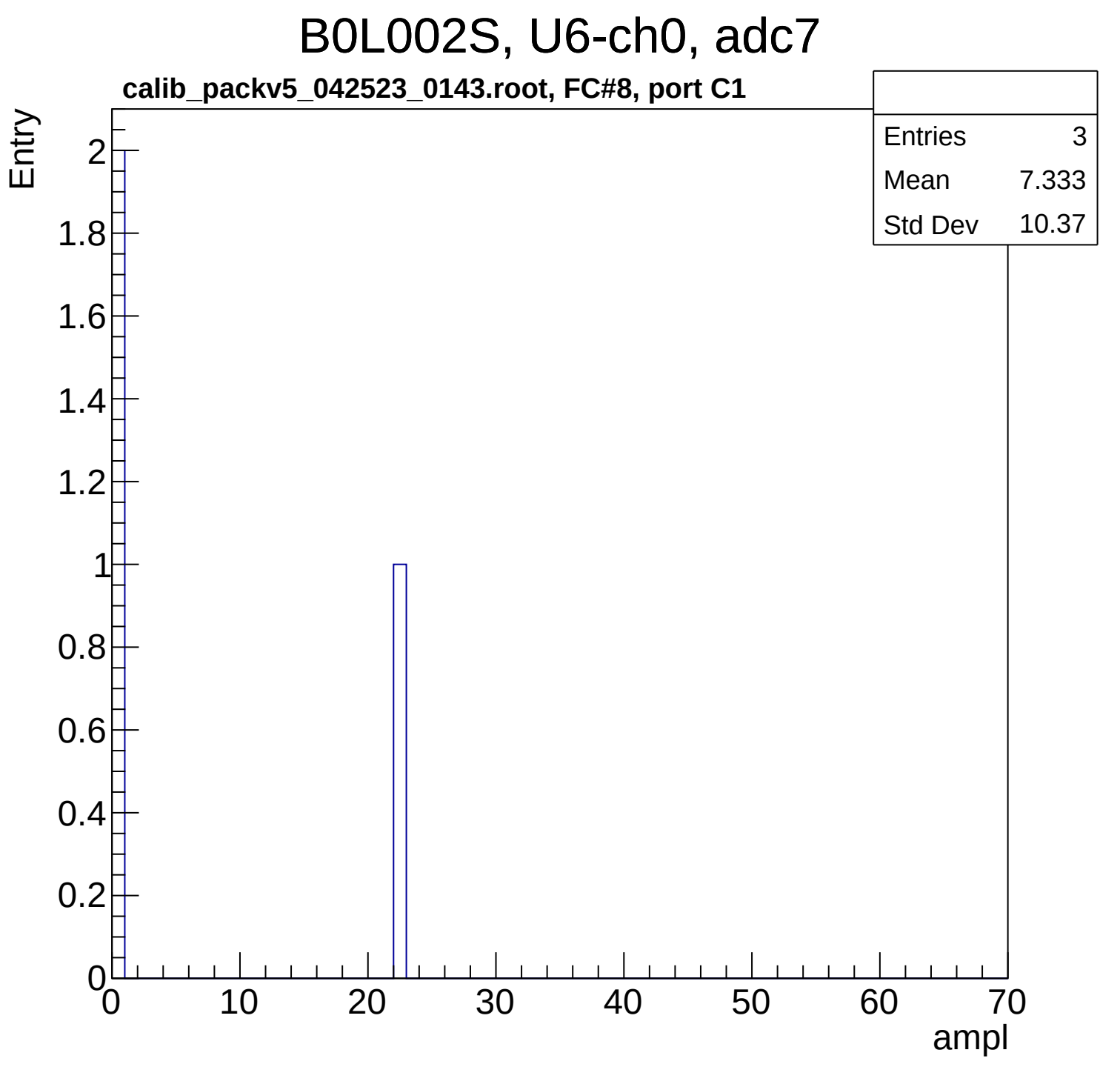
B0L002S, U6-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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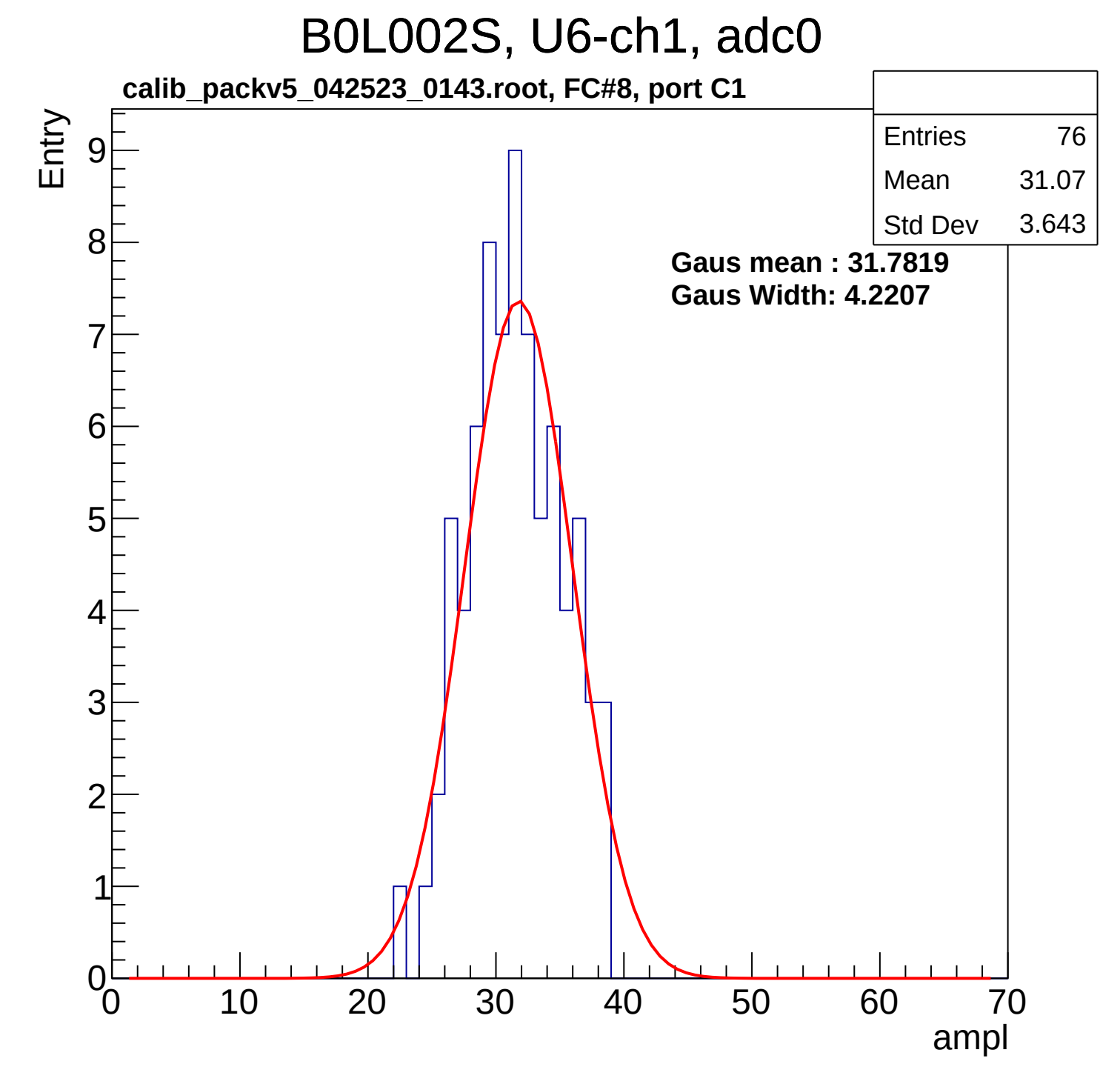
B0L002S, U6-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 31.07
Std Dev 3.643
Gaus mean : 31.7819
Gaus Width: 4.2207
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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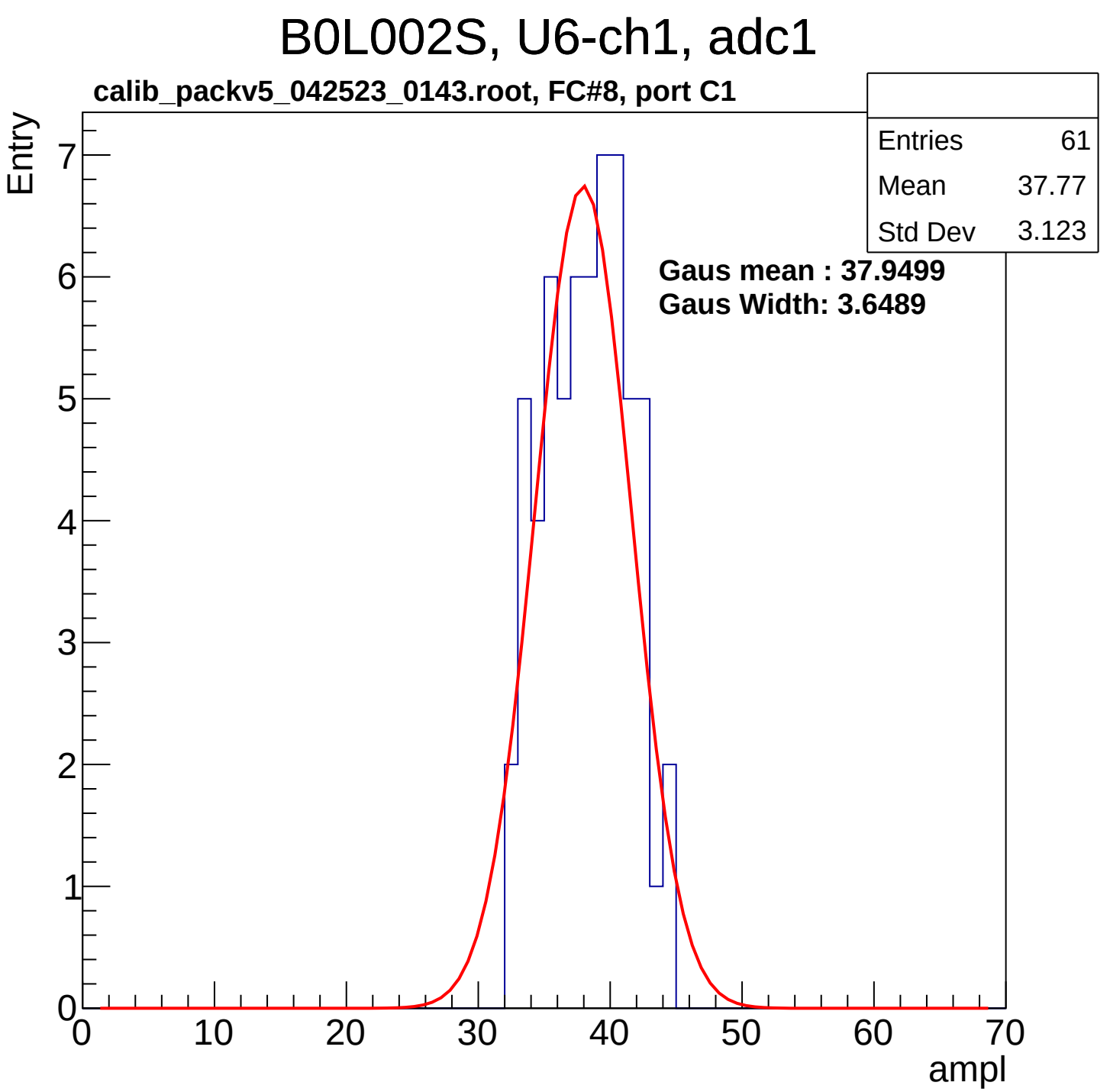
B0L002S, U6-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.77
Std Dev 3.123
Gaus mean : 37.9499
Gaus Width: 3.6489
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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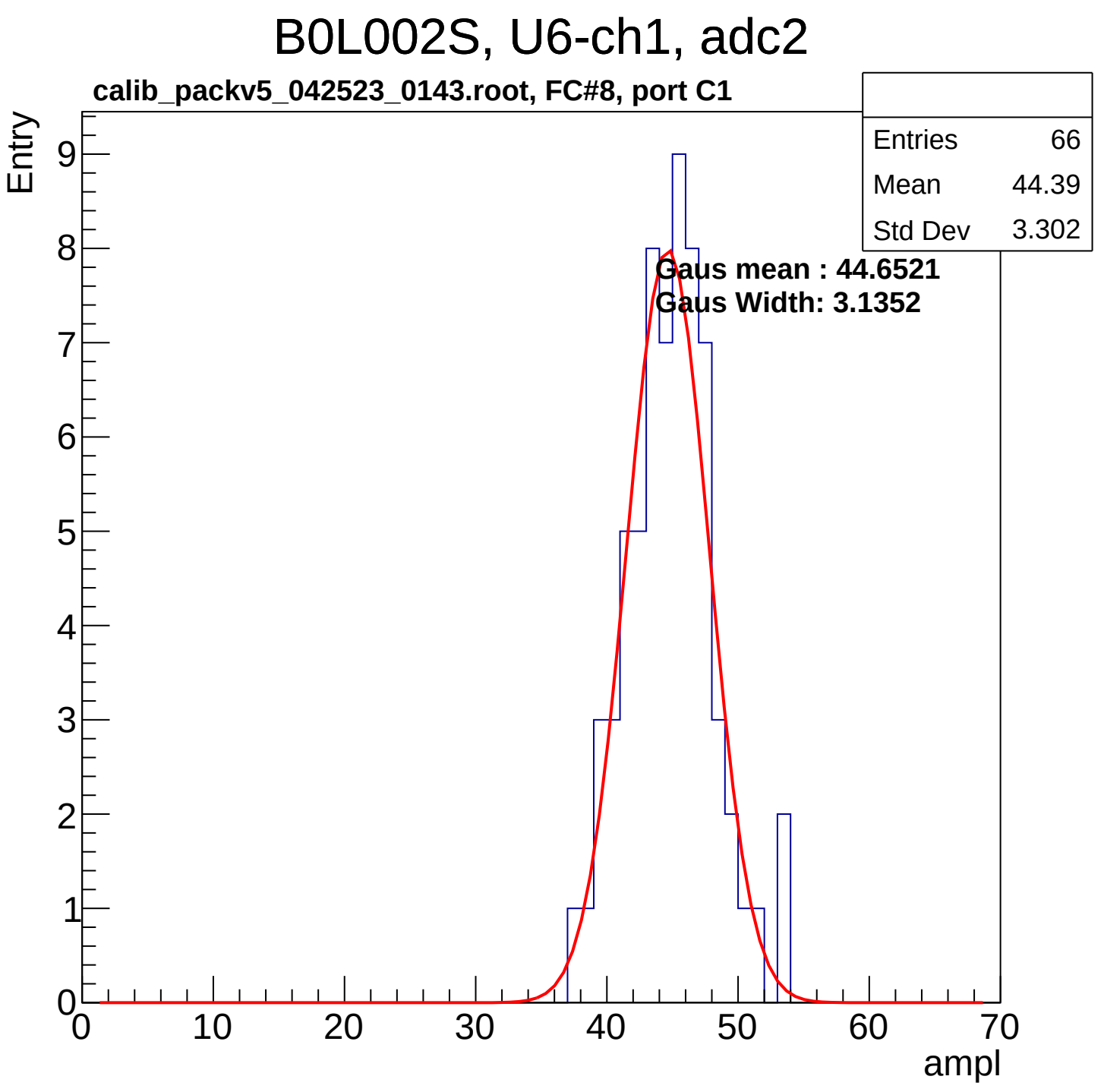
B0L002S, U6-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.39
Std Dev 3.302
Gaus mean : 44.6521
Gaus Width: 3.1352
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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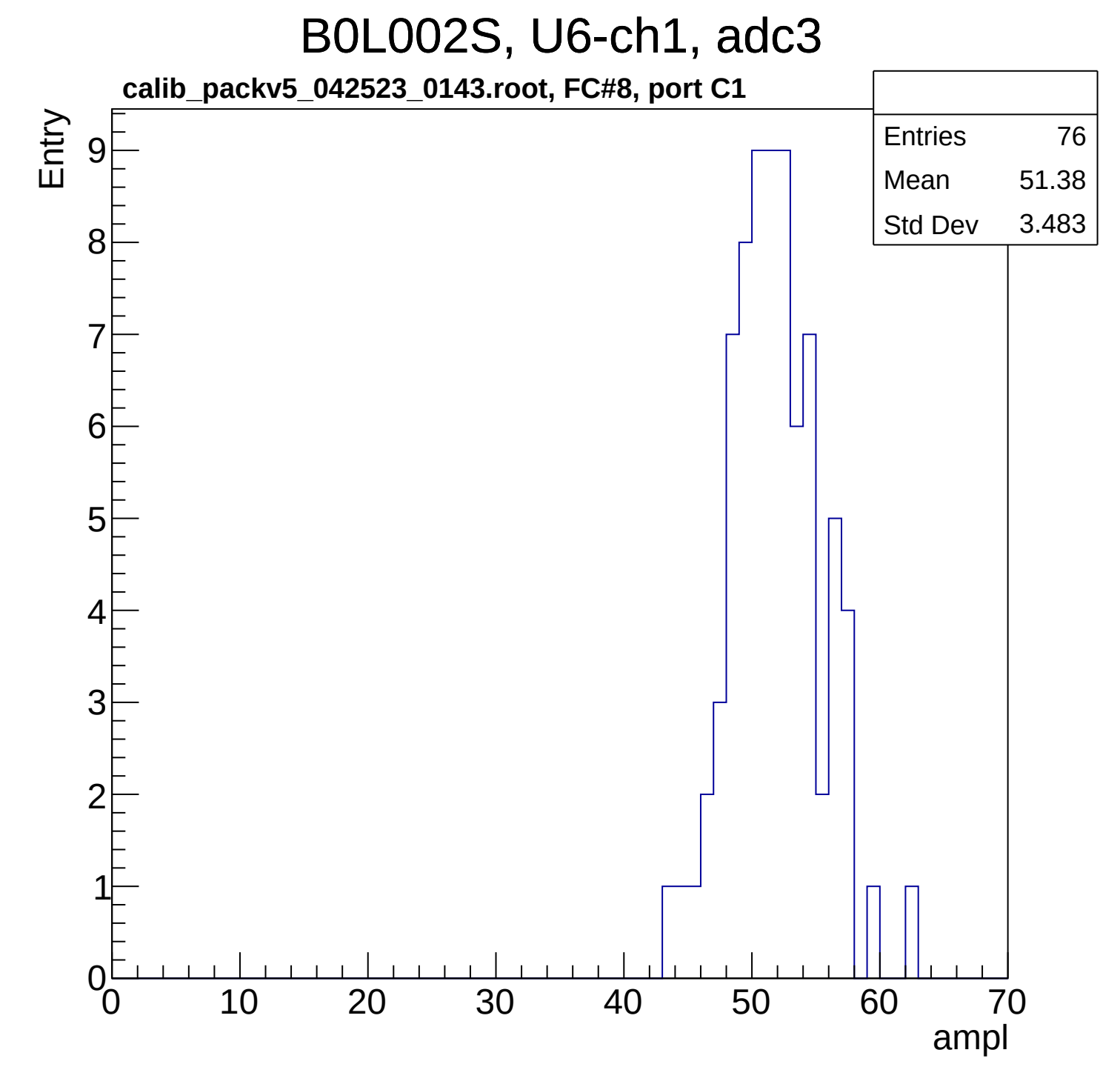
B0L002S, U6-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.38
Std Dev 3.483
Entry
ampl
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3
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5
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0
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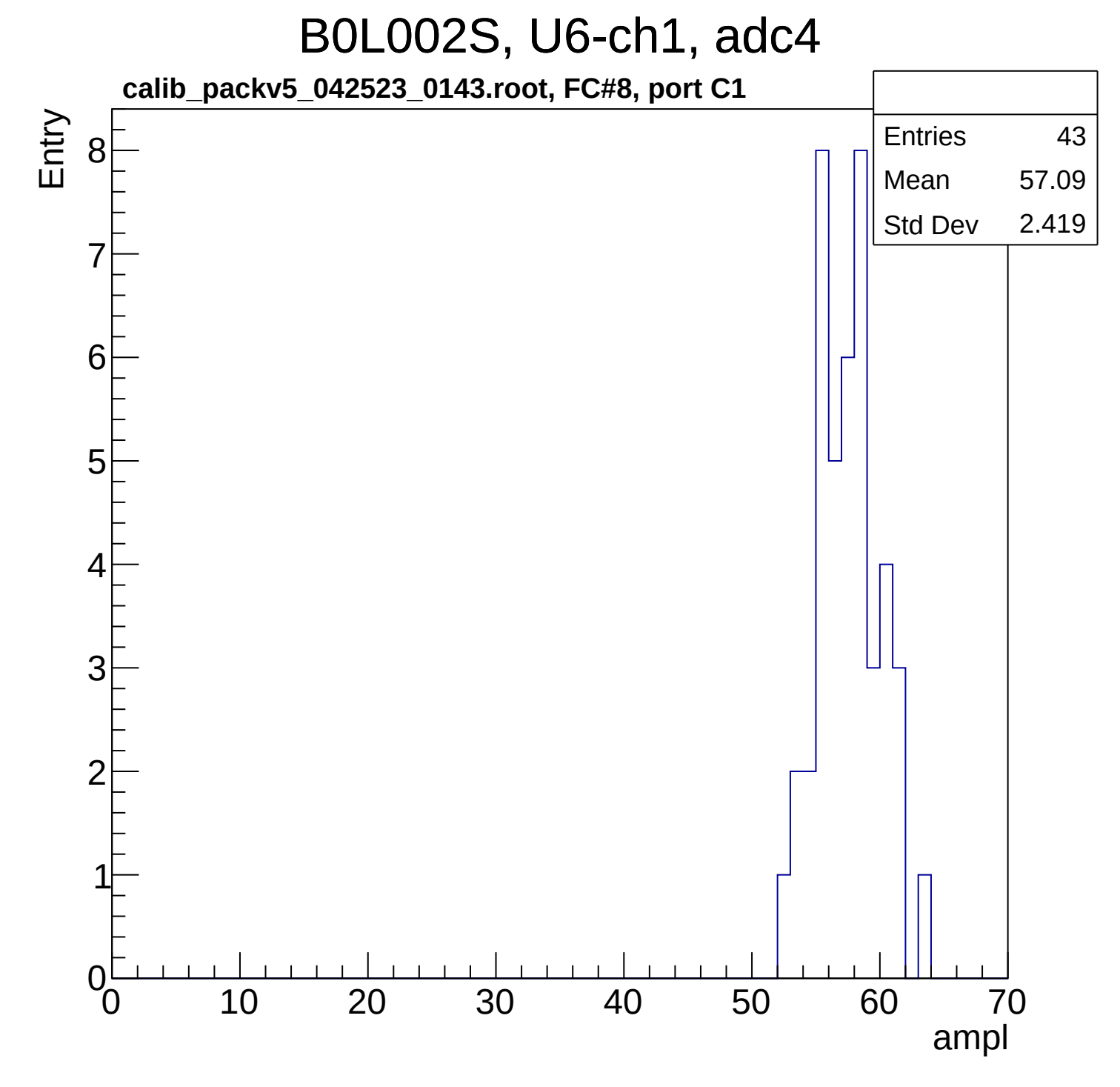
B0L002S, U6-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 57.09
Std Dev 2.419
Entry
ampl
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2
3
4
5
6
7
8
0
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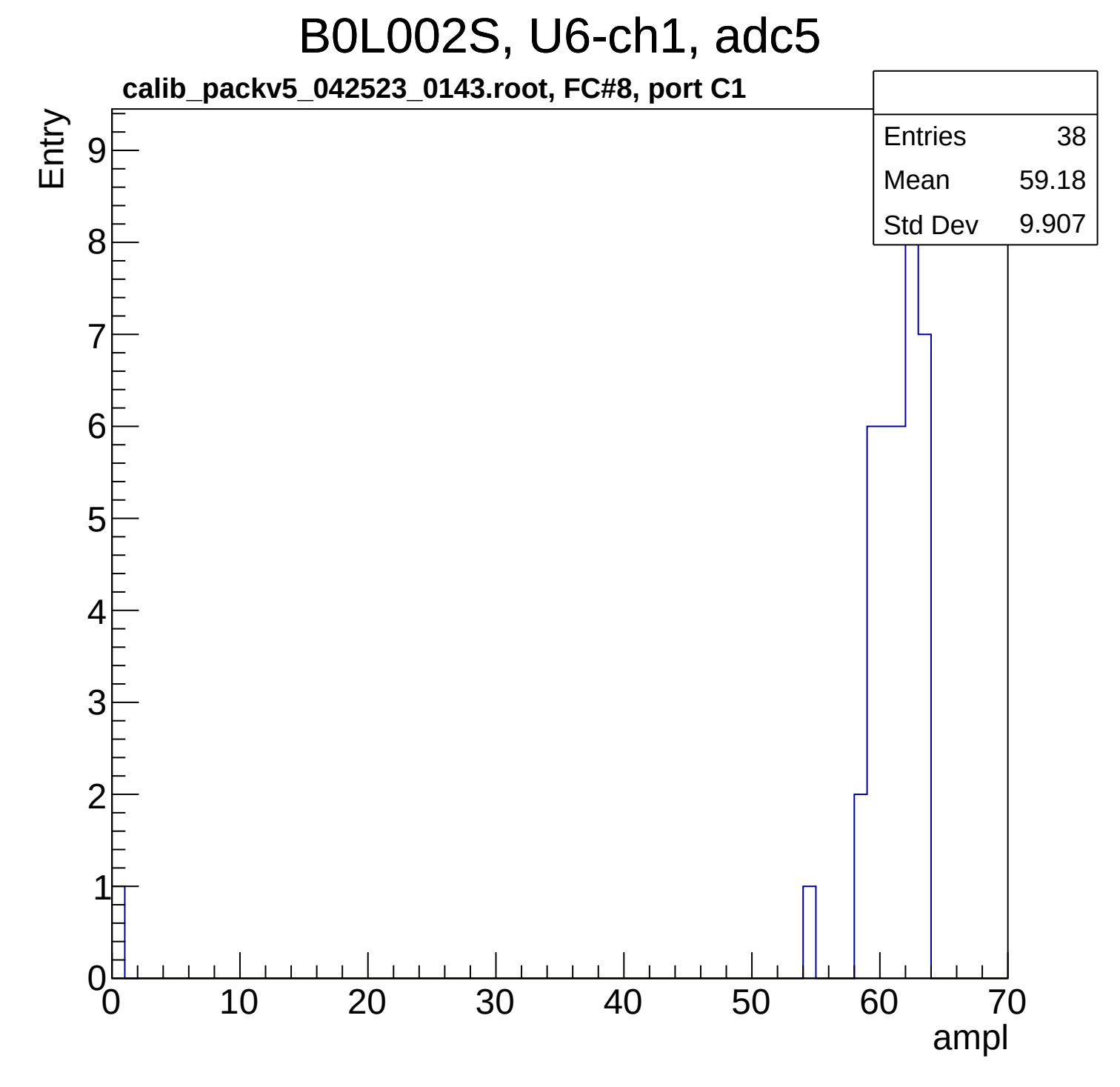
B0L002S, U6-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.18
Std Dev 9.907
Entry
ampl
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2
3
4
5
6
7
8
9
0
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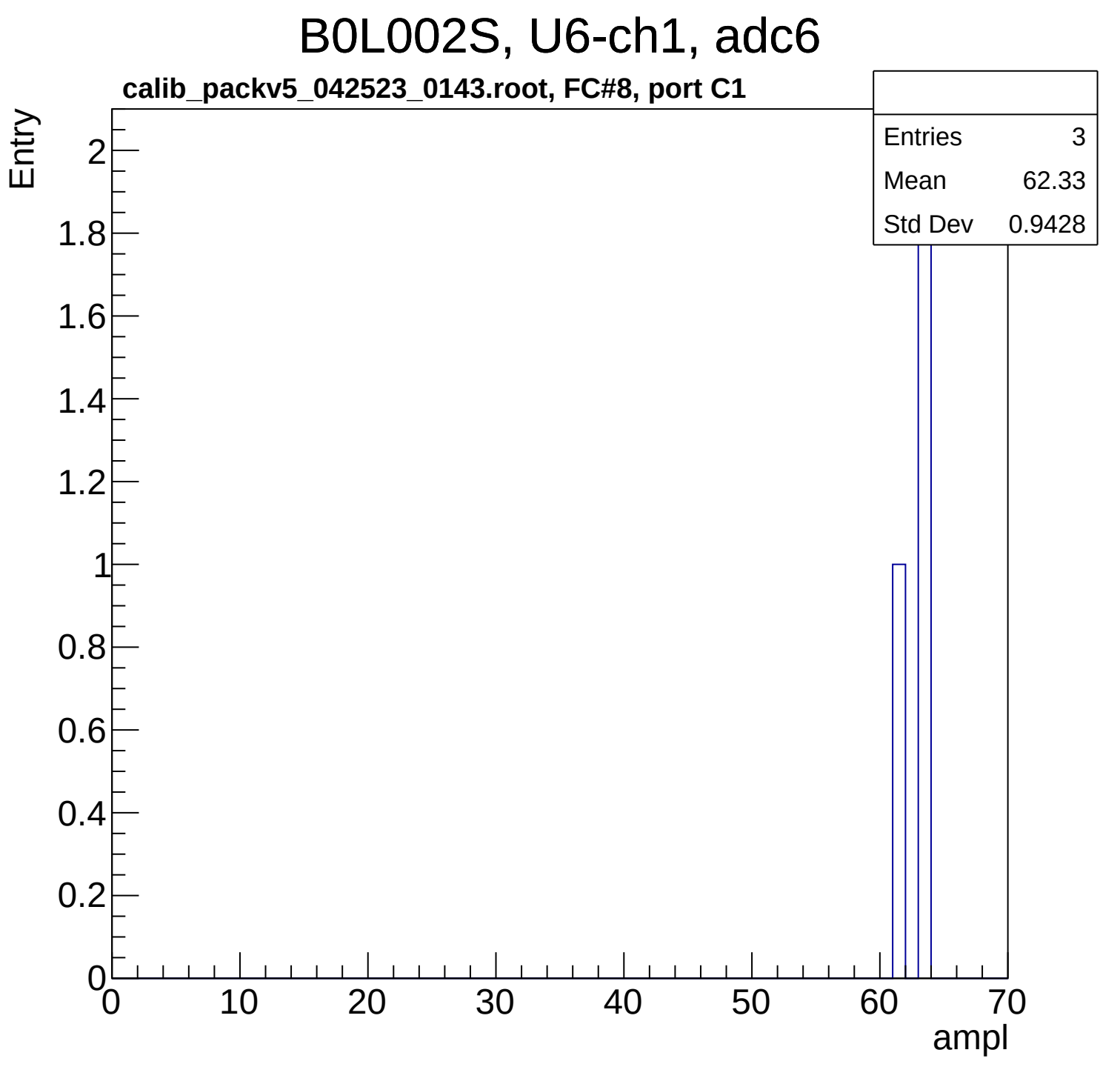
B0L002S, U6-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
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60
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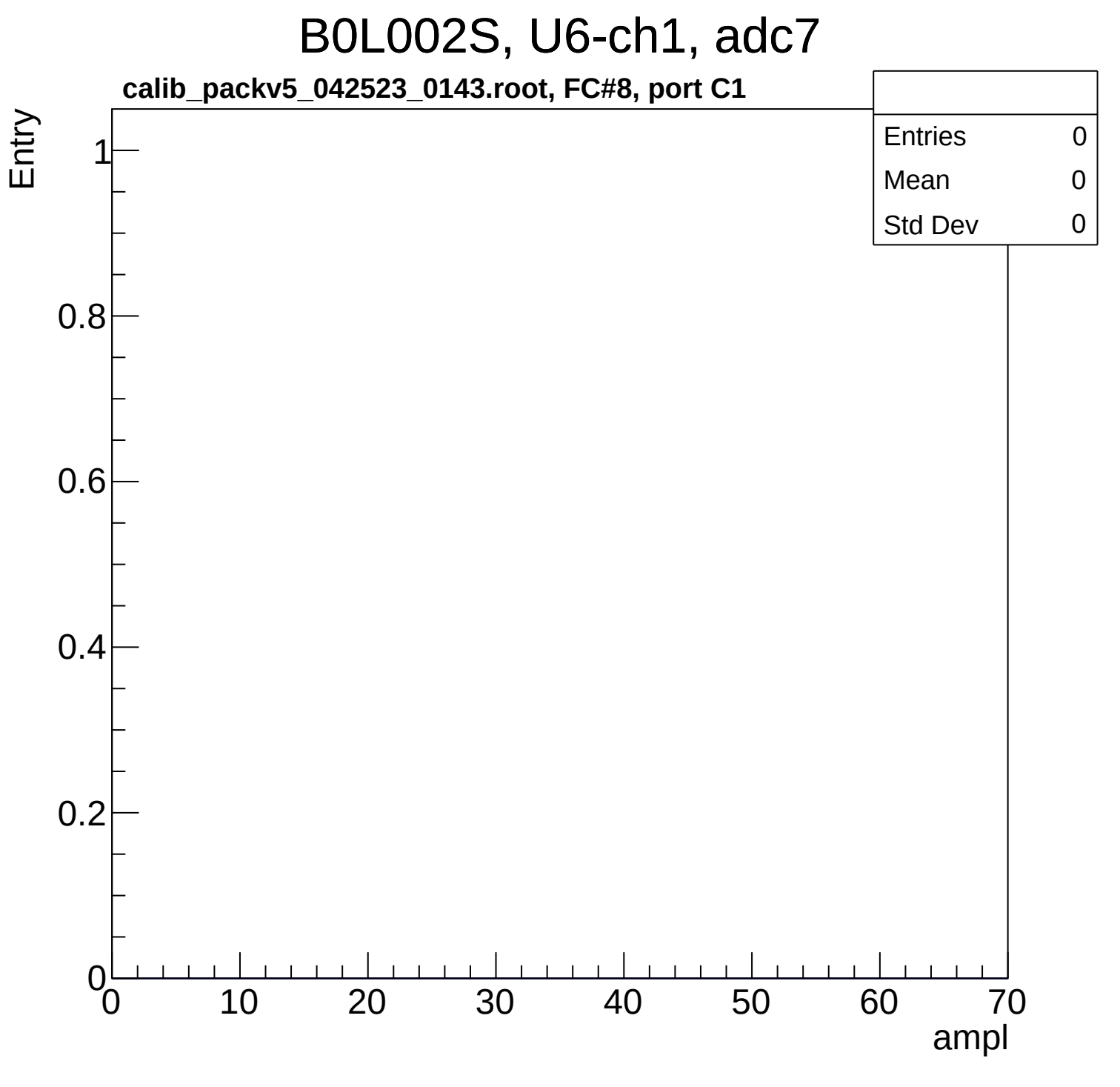
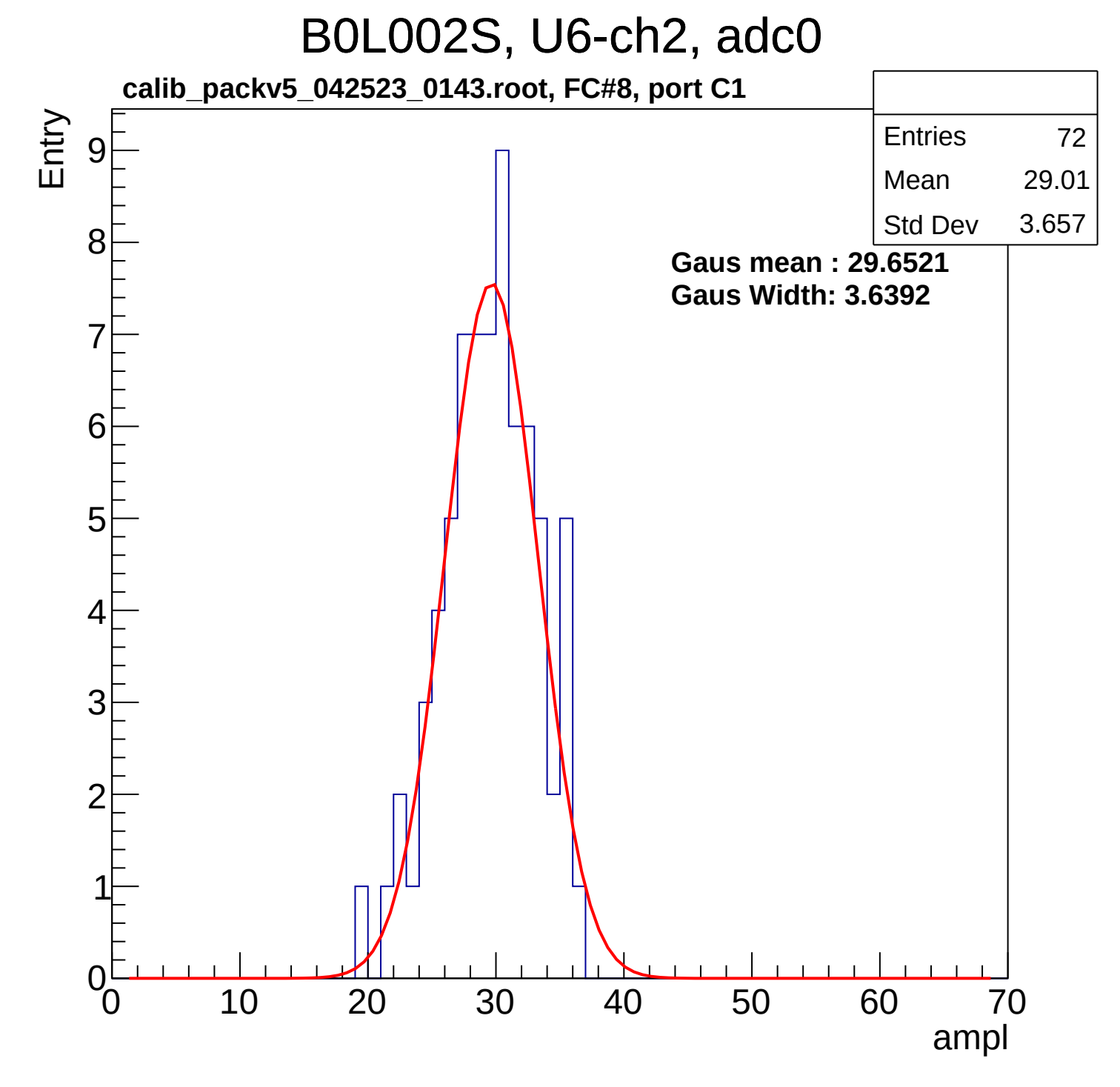

B0L002S, U6-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.01
Std Dev 3.657
Gaus mean : 29.6521
Gaus Width: 3.6392
Entry
ampl
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2
3
4
5
6
7
8
9
0
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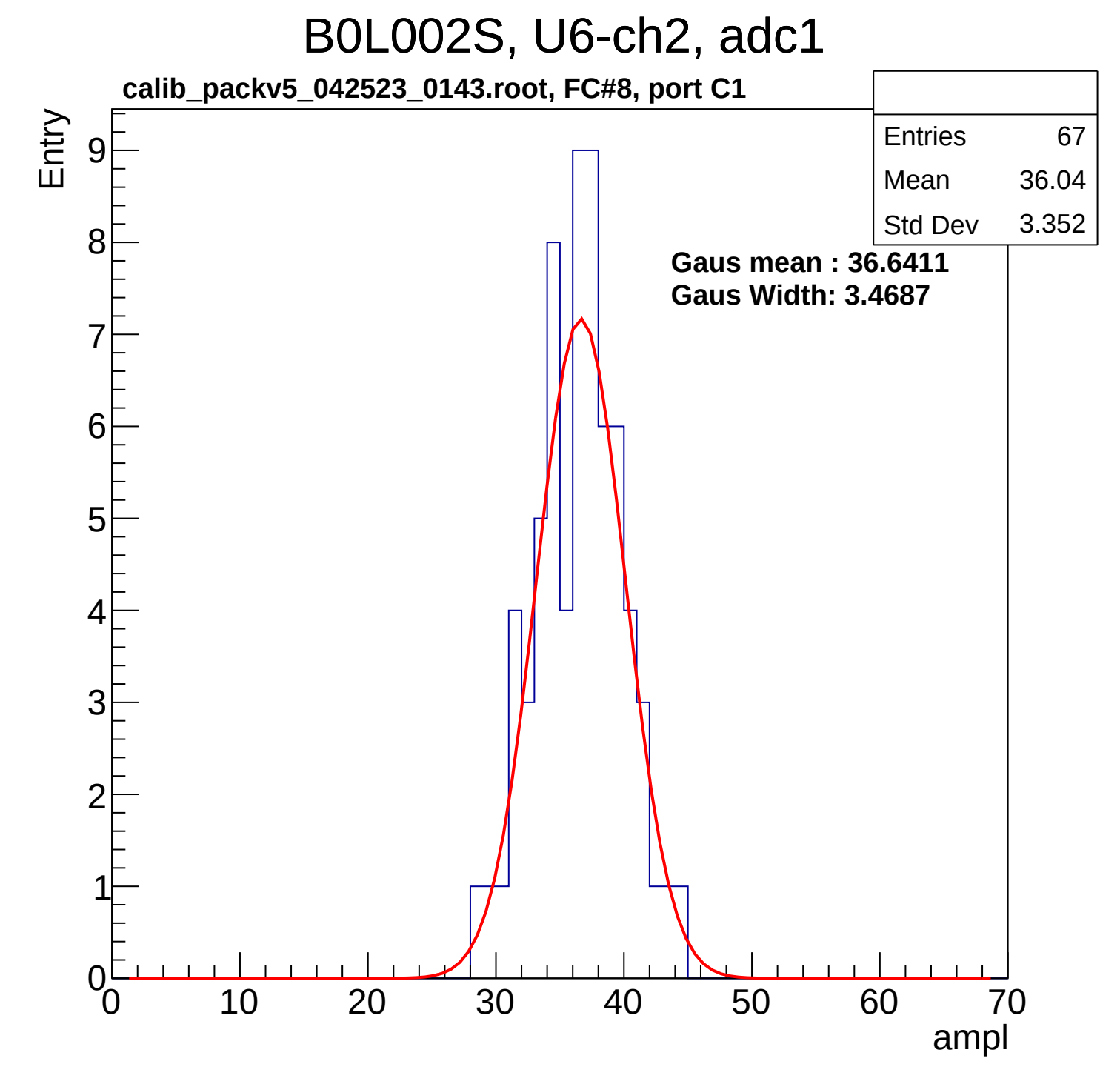
20
30
40
50
60
70


B0L002S, U6-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.04
Std Dev 3.352
Gaus mean : 36.6411
Gaus Width: 3.4687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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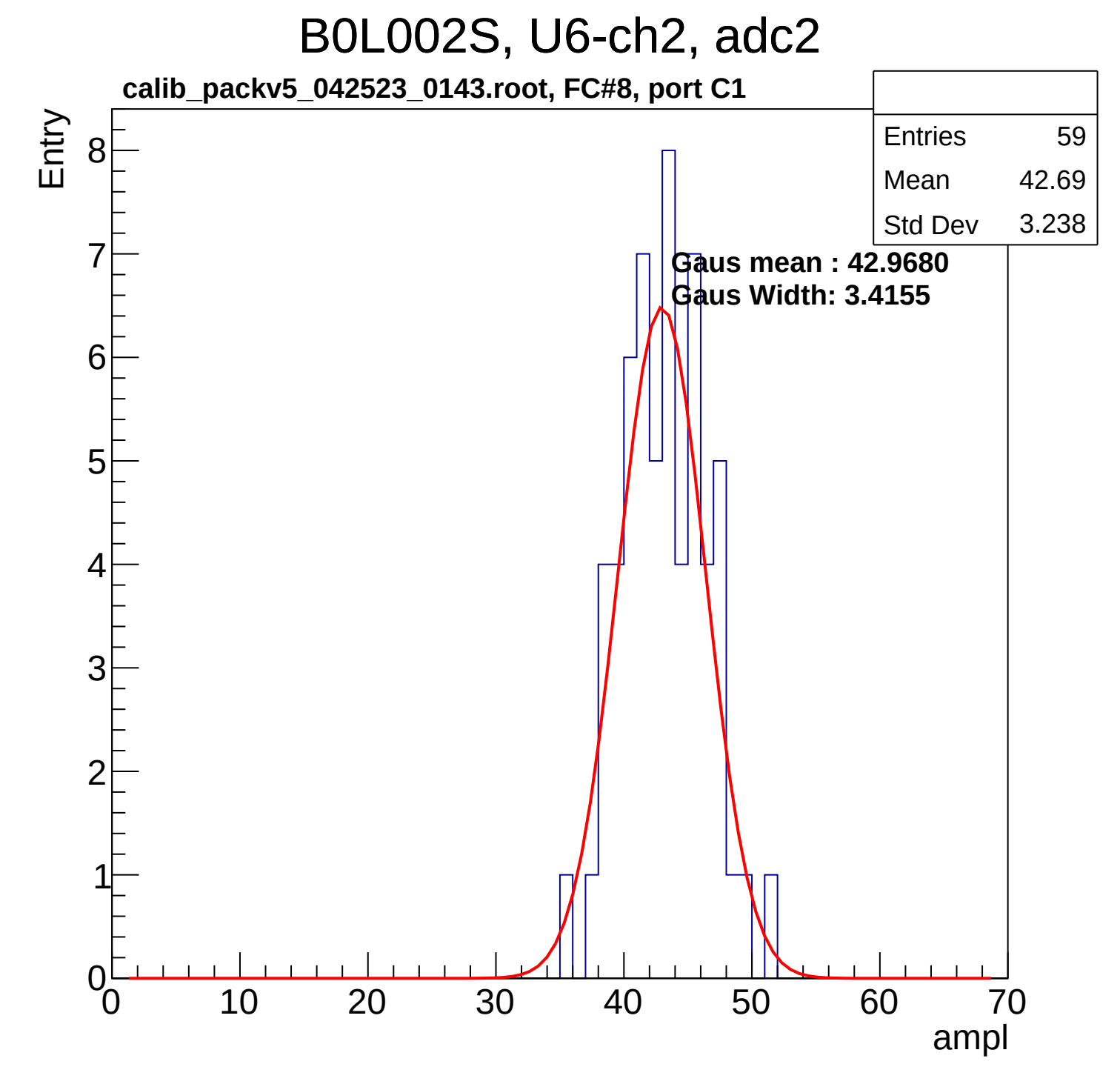
B0L002S, U6-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 42.69
Std Dev 3.238
Gaus mean : 42.9680
Gaus Width: 3.4155
Entry
ampl
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2
3
4
5
6
7
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0
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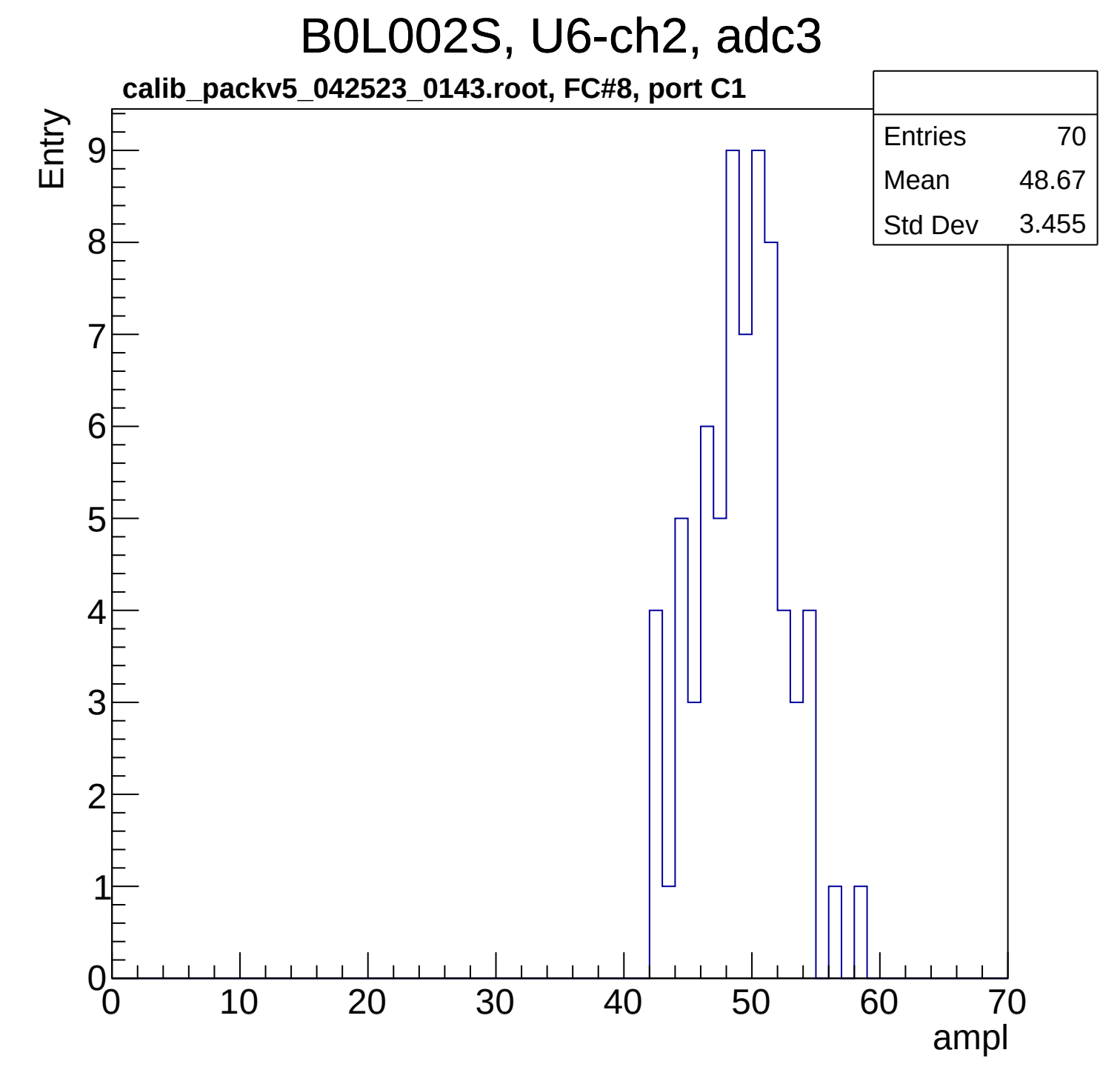
B0L002S, U6-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 48.67
Std Dev 3.455
Entry
ampl
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3
4
5
6
7
8
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0
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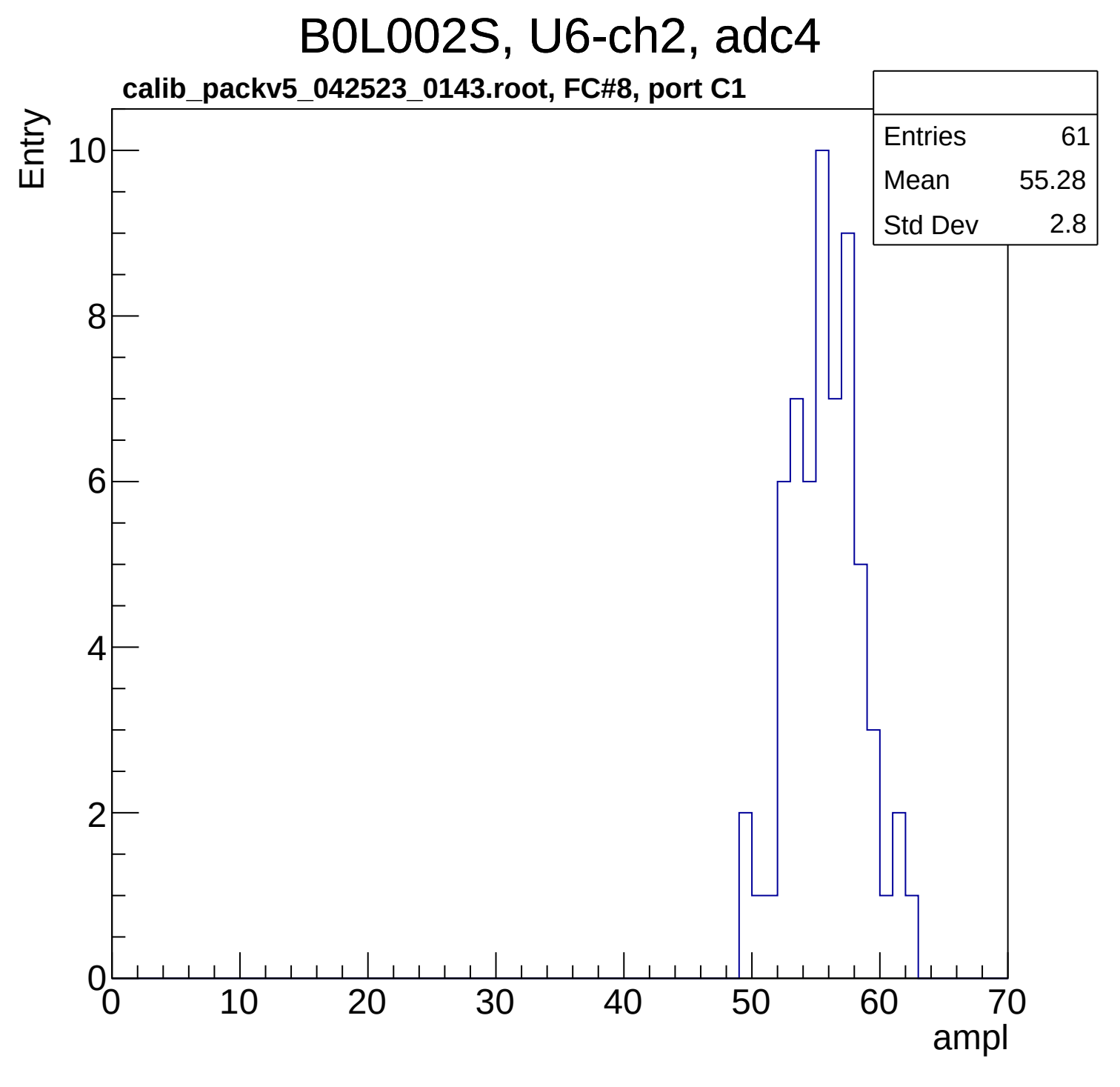
B0L002S, U6-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.28
Std Dev 2.8
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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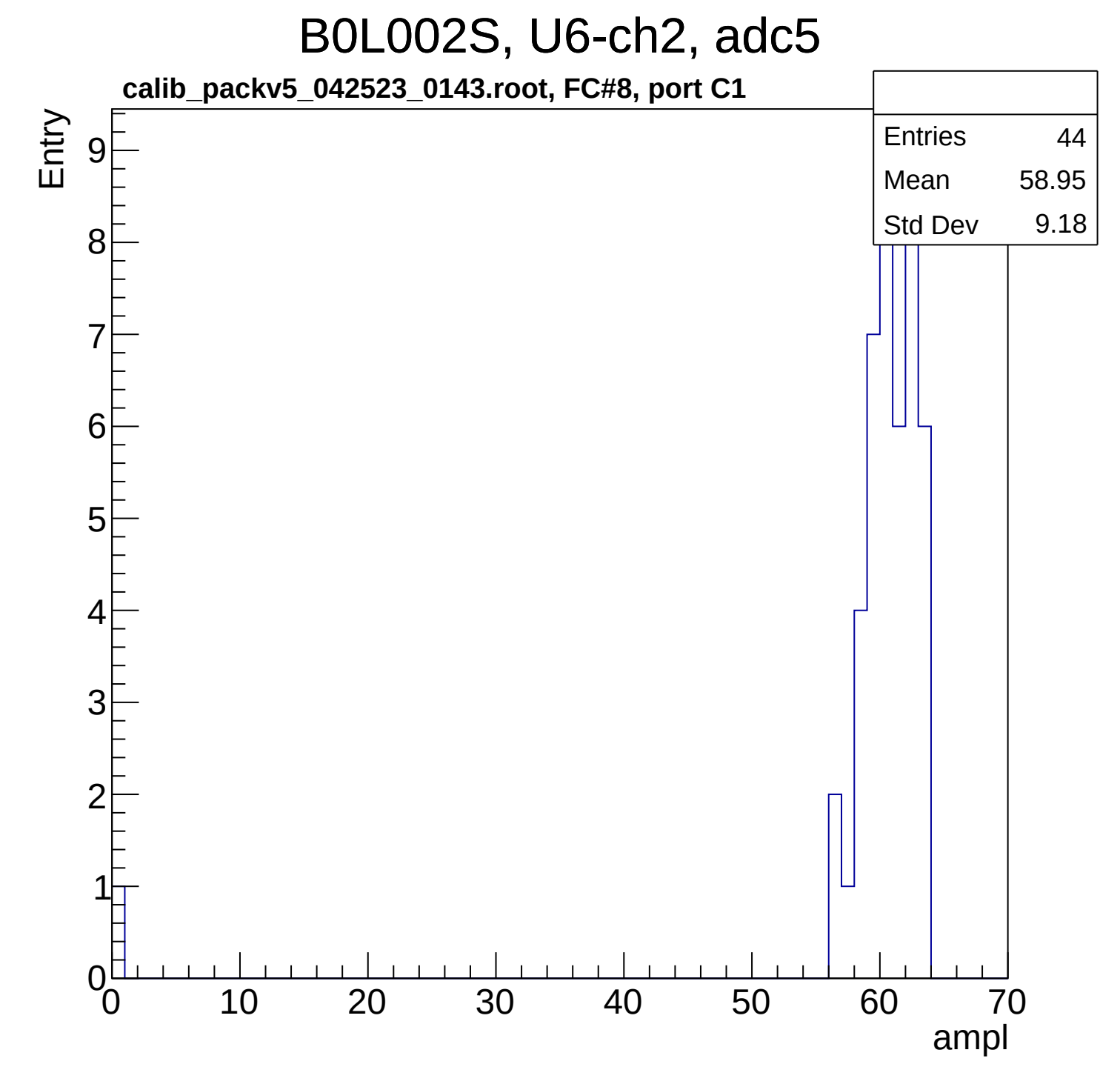
B0L002S, U6-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.95
Std Dev 9.18
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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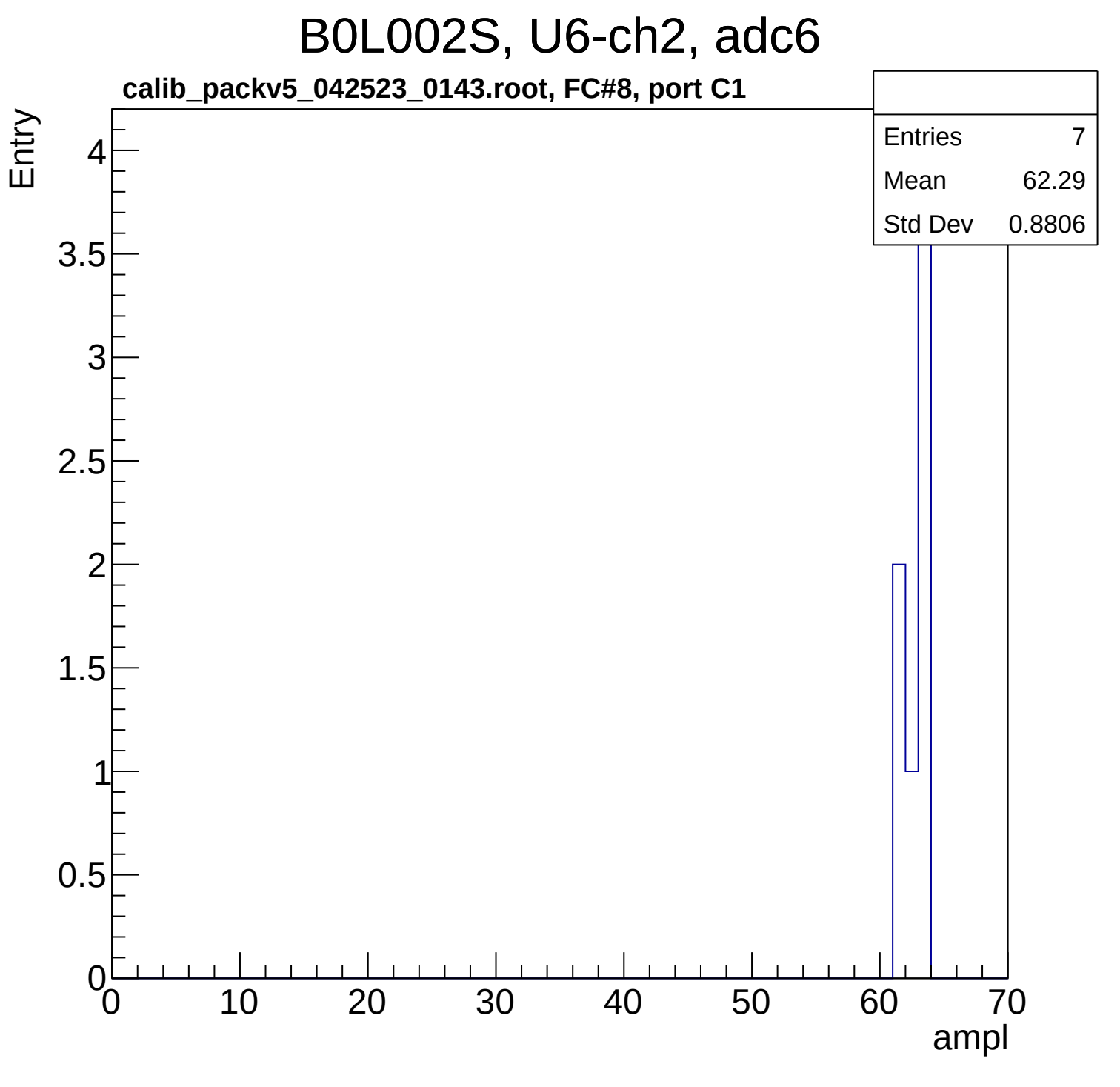
B0L002S, U6-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.29
Std Dev 0.8806
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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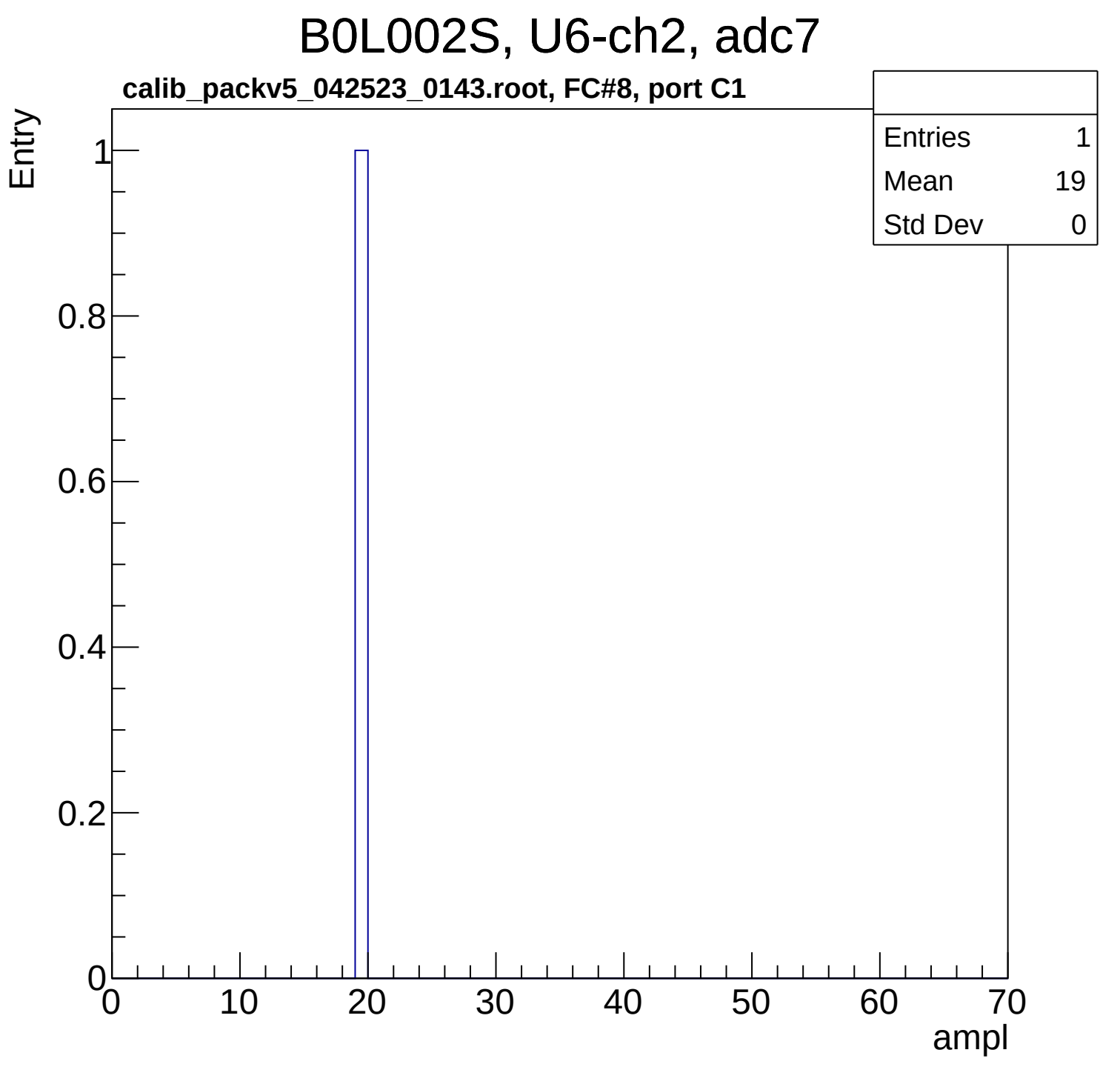
B0L002S, U6-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
0.8
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0
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40
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60
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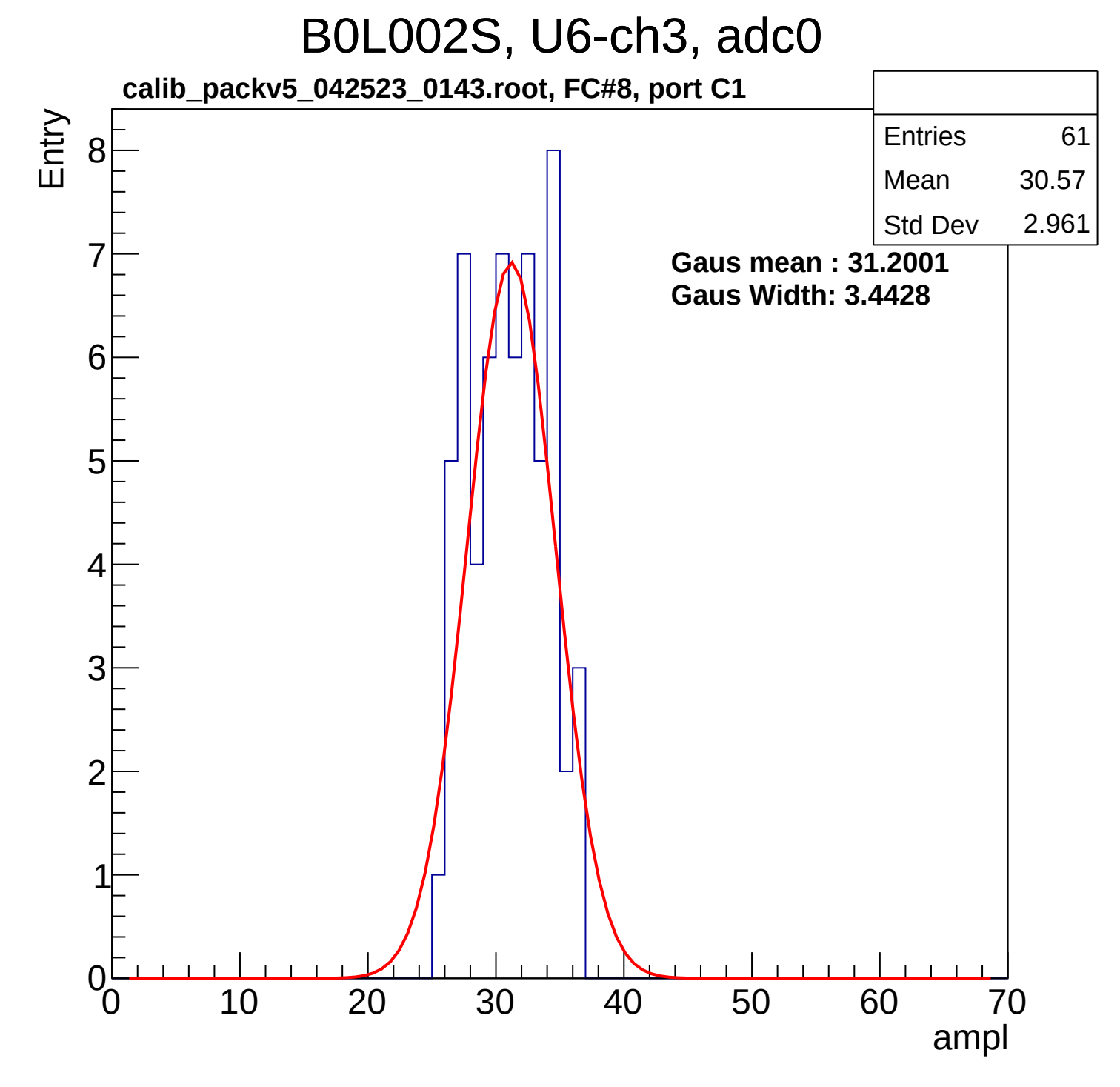
B0L002S, U6-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.57
Std Dev 2.961
Gaus mean : 31.2001
Gaus Width: 3.4428
Entry
ampl
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2
3
4
5
6
7
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0
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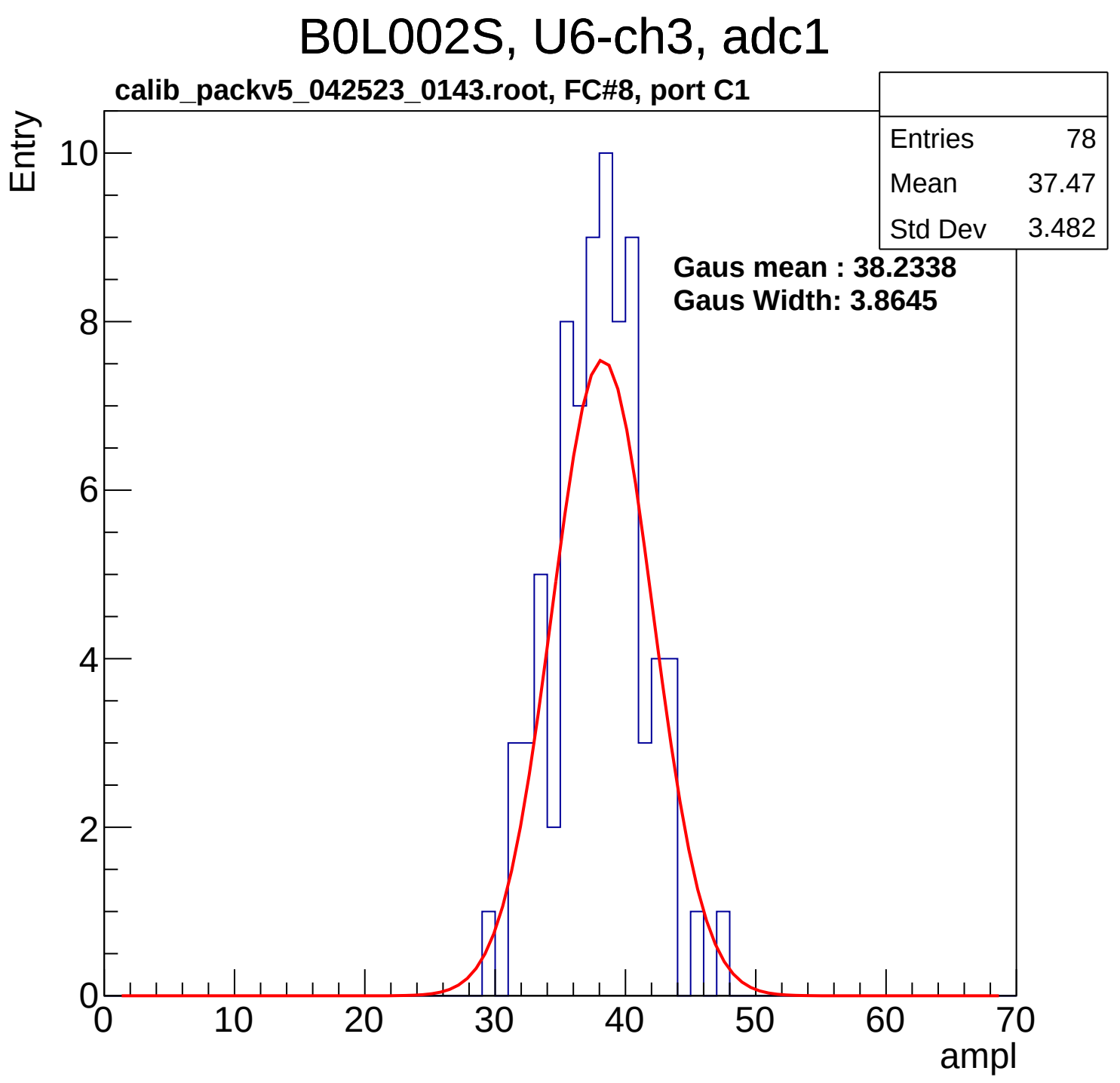
B0L002S, U6-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.47
Std Dev 3.482
Gaus mean : 38.2338
Gaus Width: 3.8645
Entry
ampl
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4
6
8
10
0
10
20
30
40
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60
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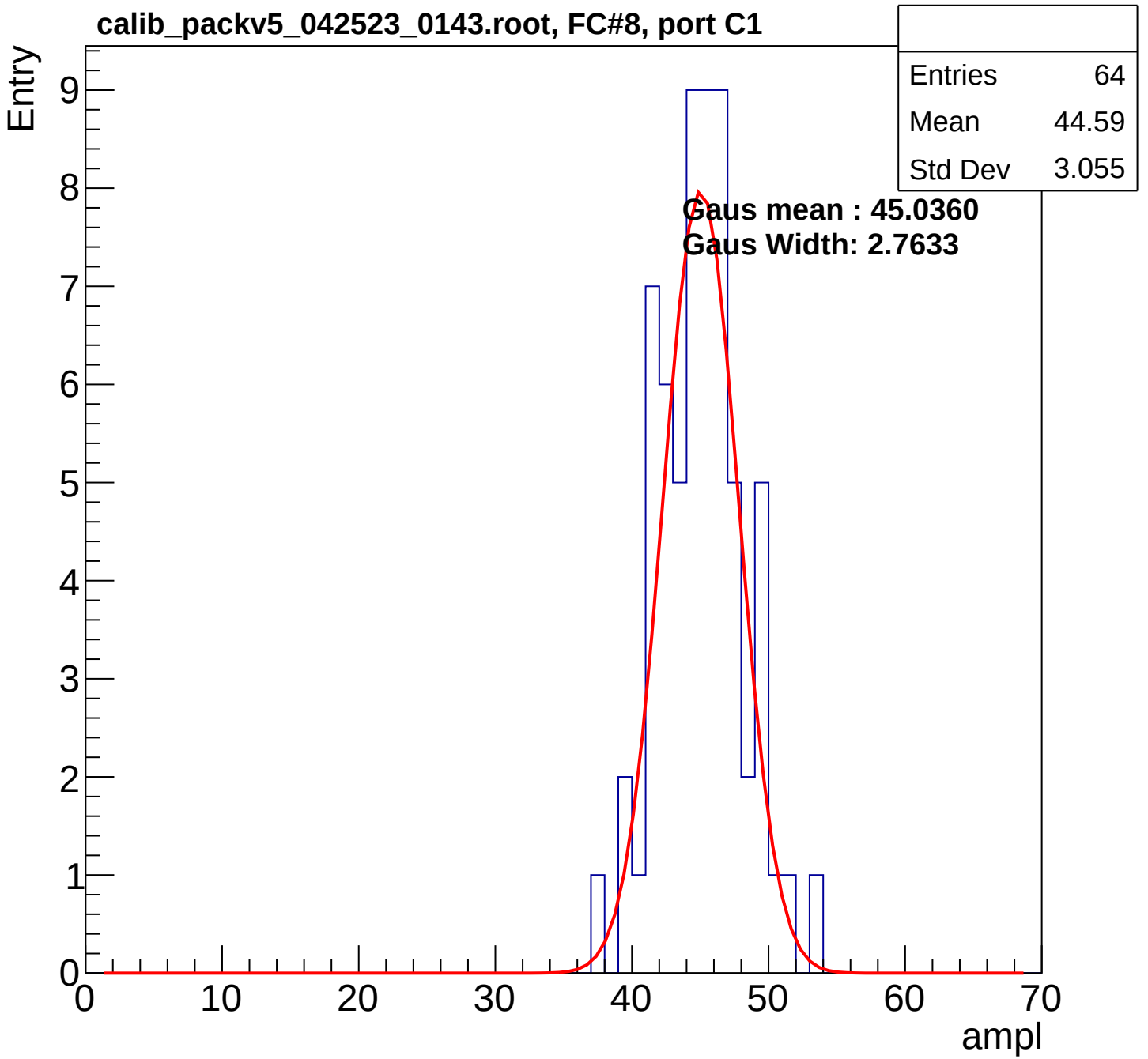
B0L002S, U6-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.59
Std Dev 3.055
Gaus mean : 45.0360
Gaus Width: 2.7633
Entry
ampl
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2
3
4
5
6
7
8
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0
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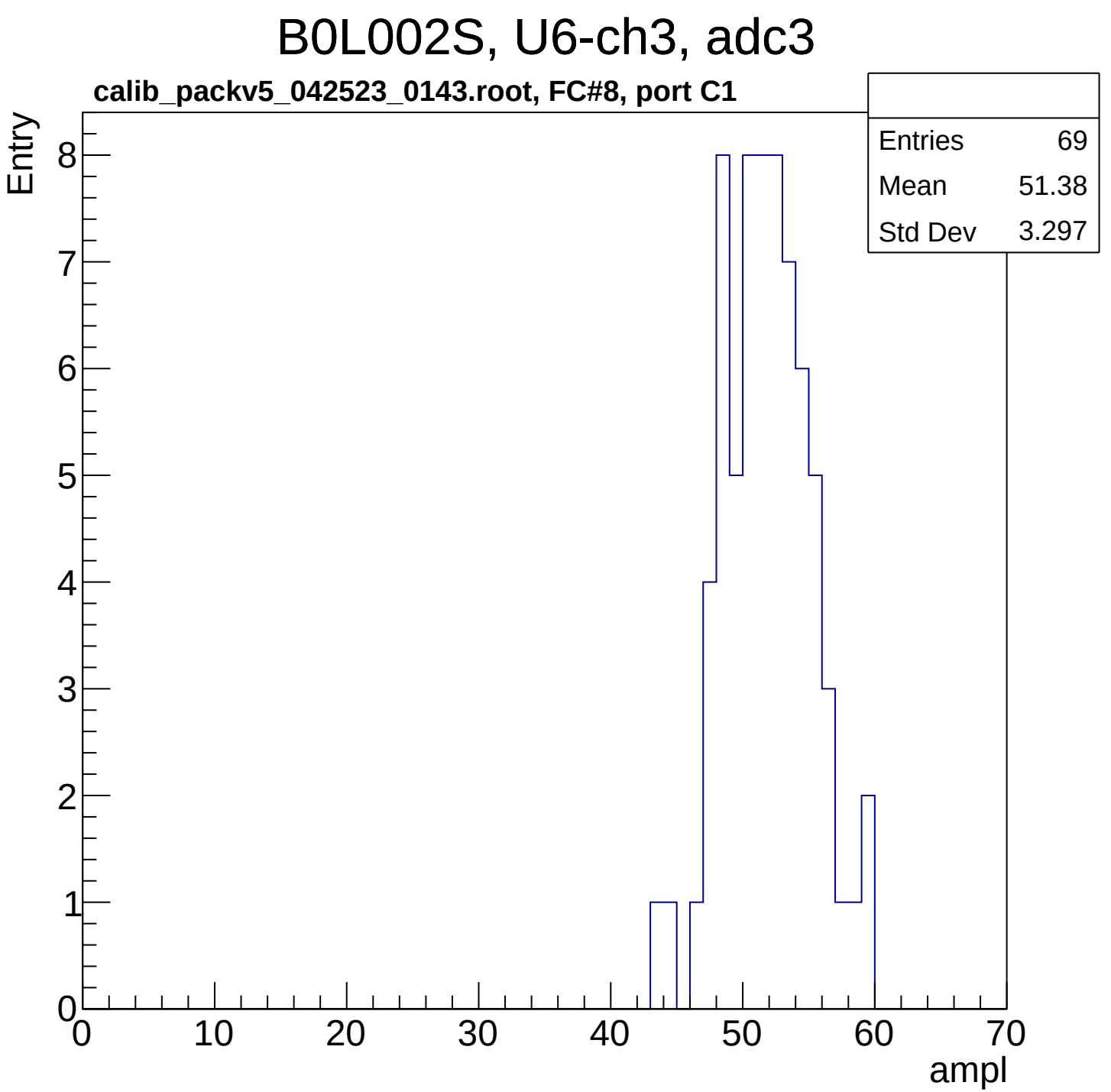
B0L002S, U6-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.38
Std Dev 3.297
Entry
ampl
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4
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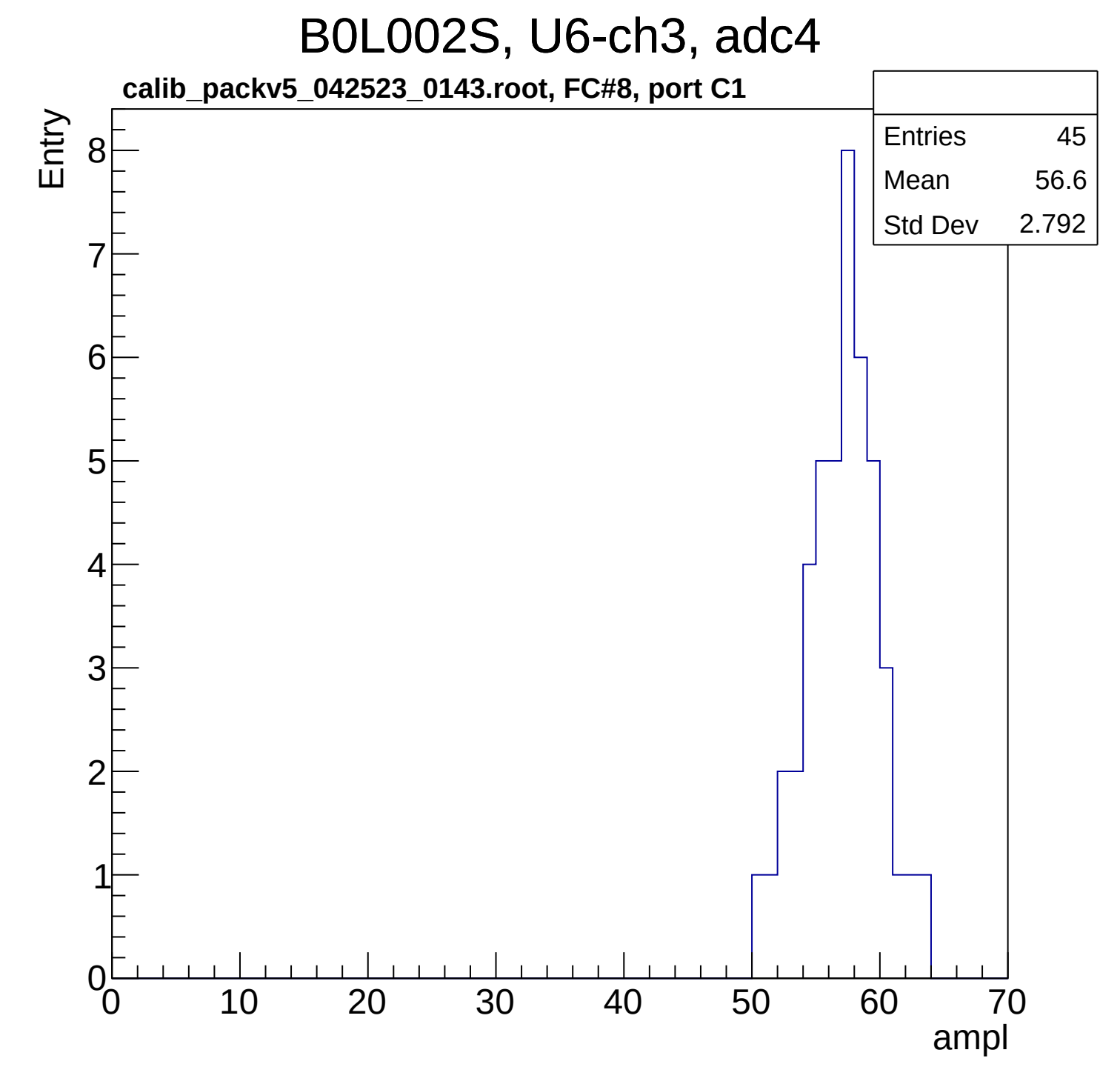
B0L002S, U6-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.6
Std Dev 2.792
Entry
ampl
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3
4
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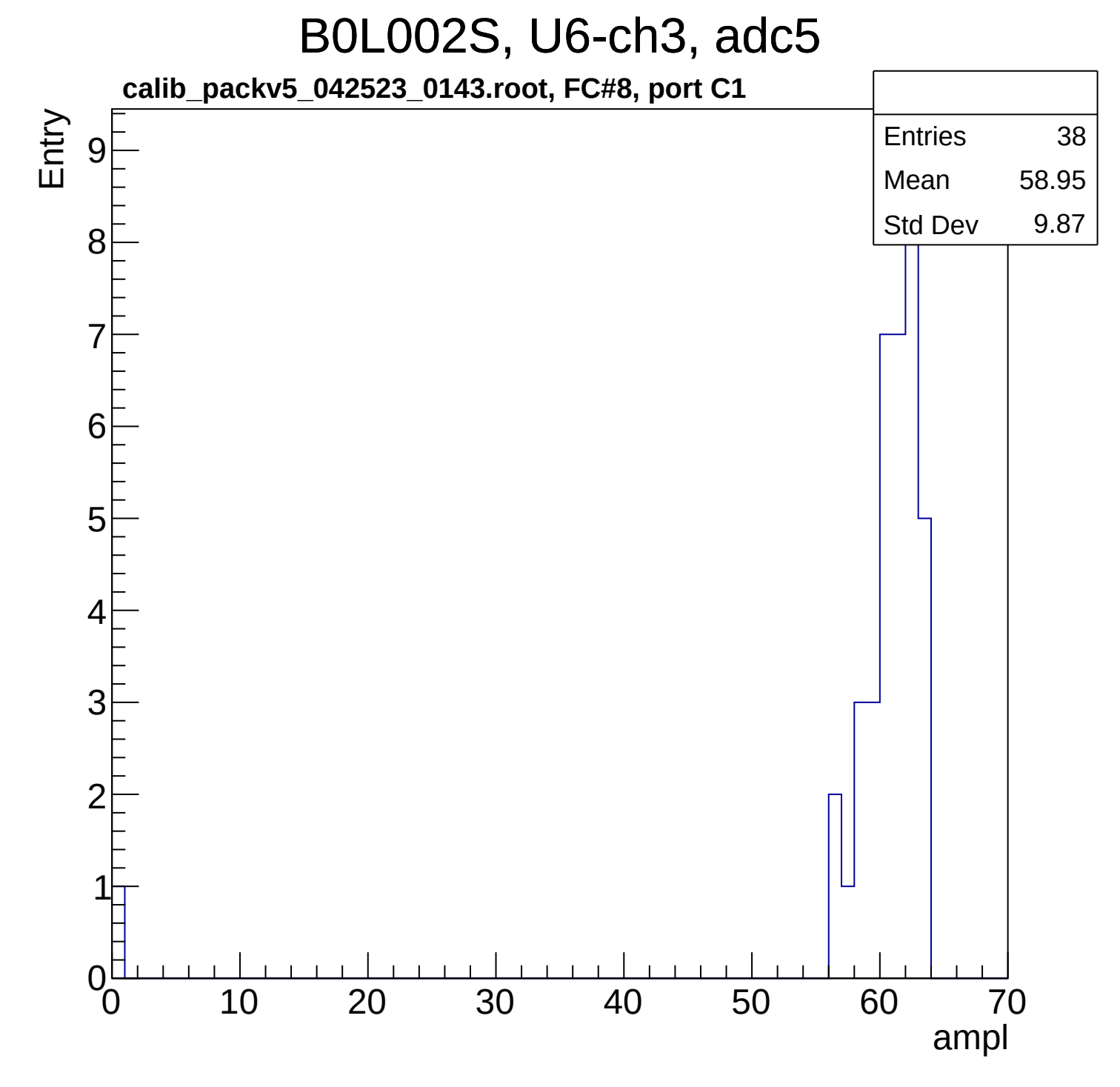
B0L002S, U6-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 58.95
Std Dev 9.87
Entry
ampl
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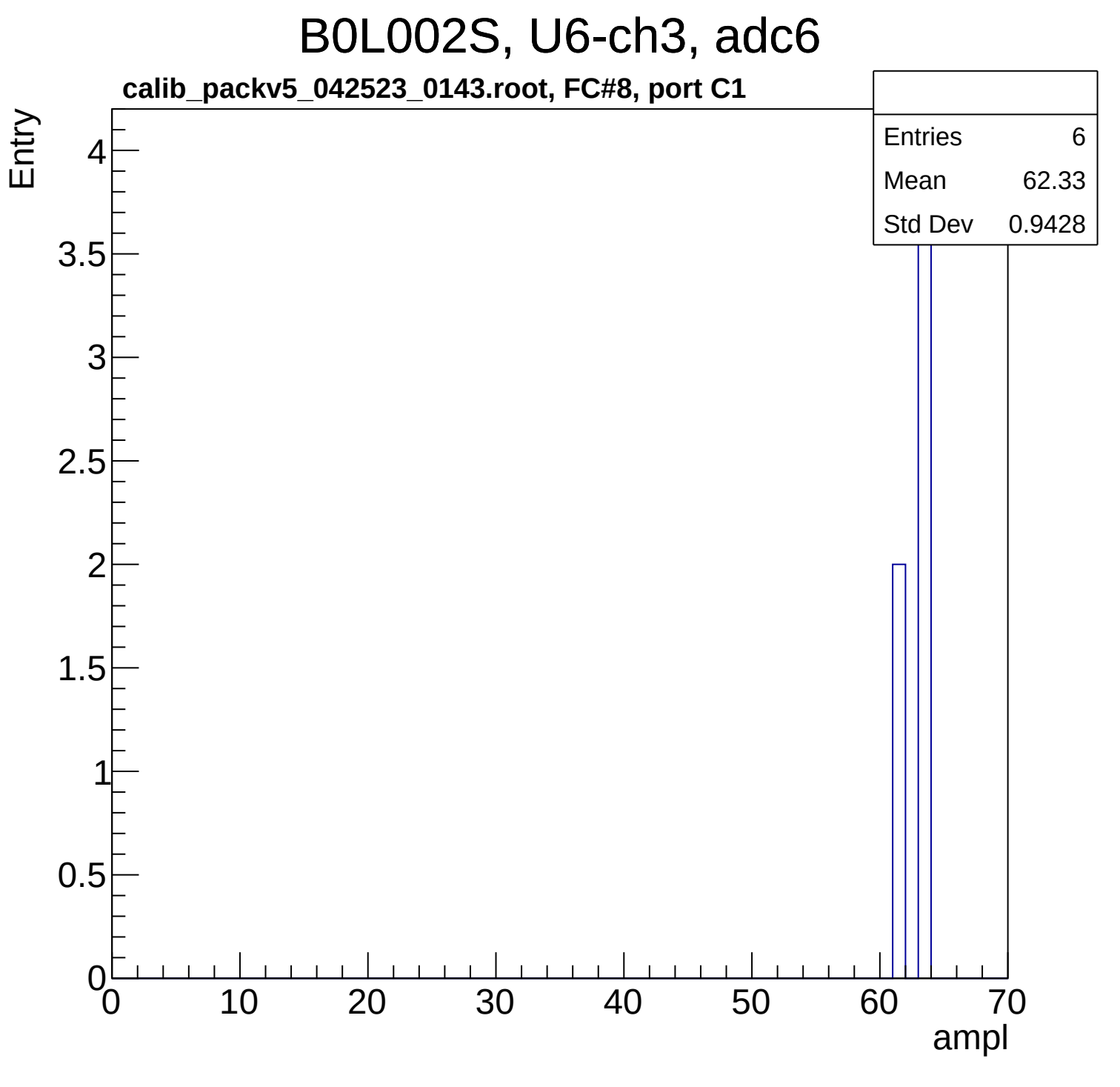
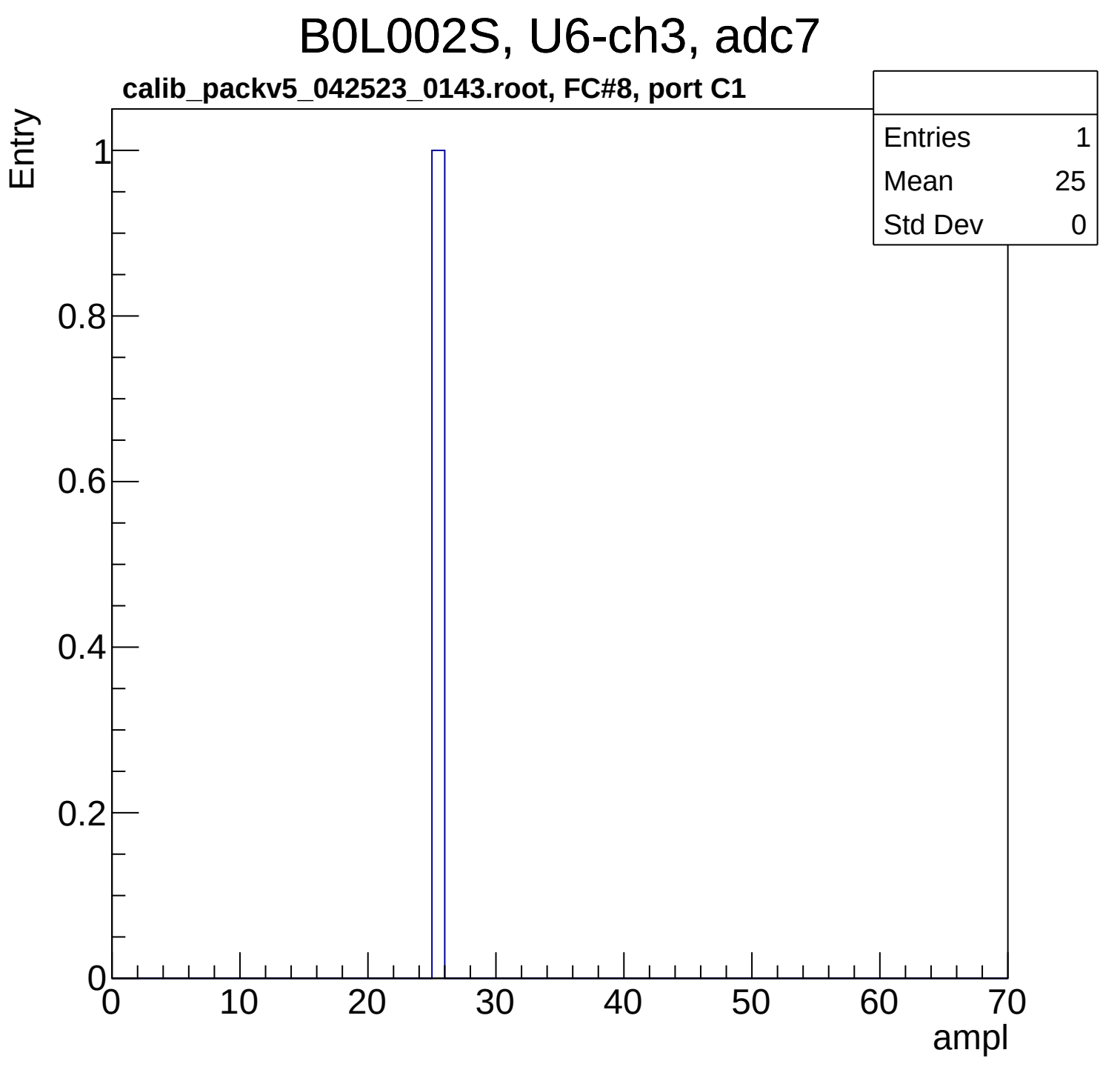
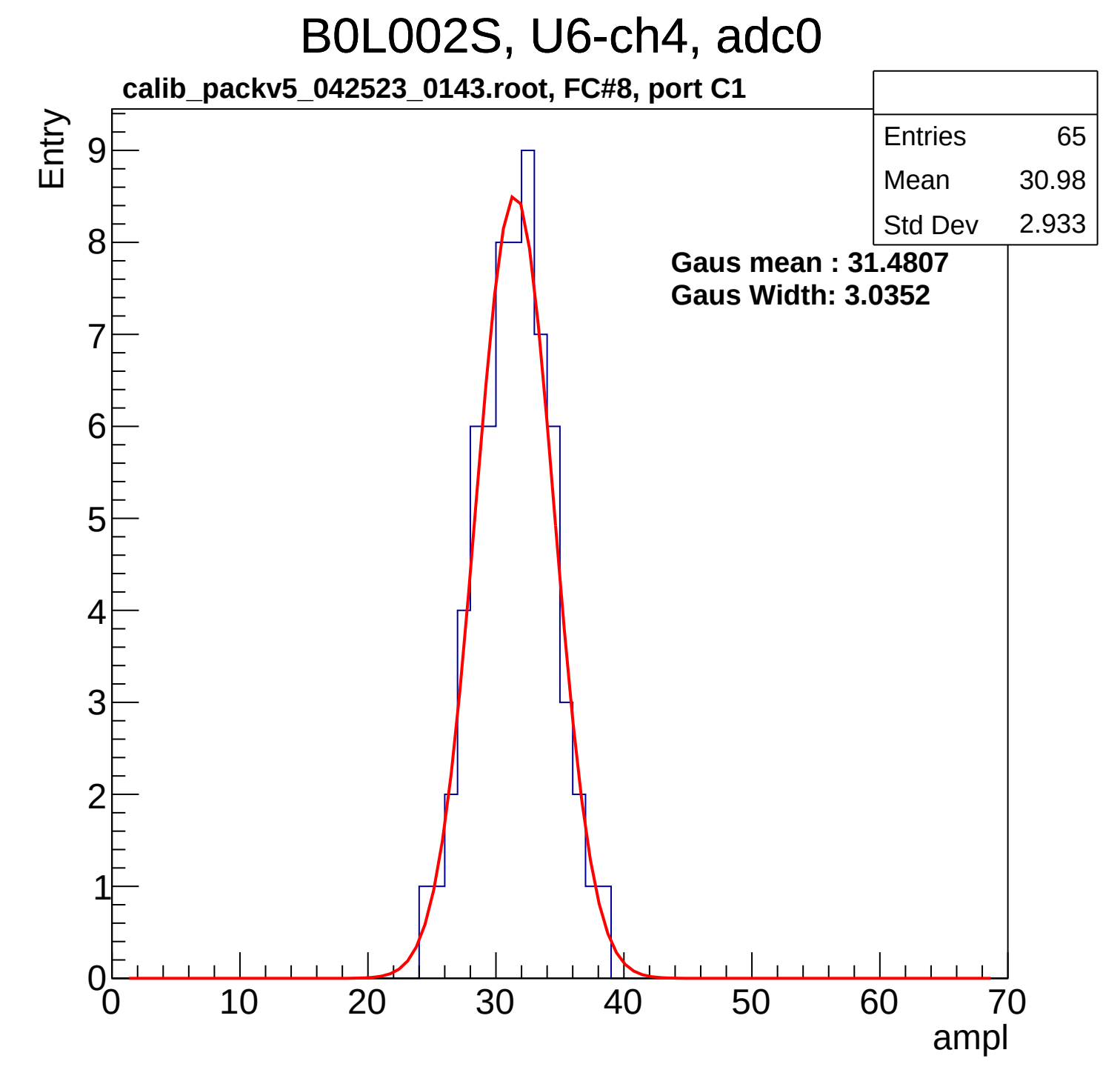

B0L002S, U6-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U6-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.98
Std Dev 2.933
Gaus mean : 31.4807
Gaus Width: 3.0352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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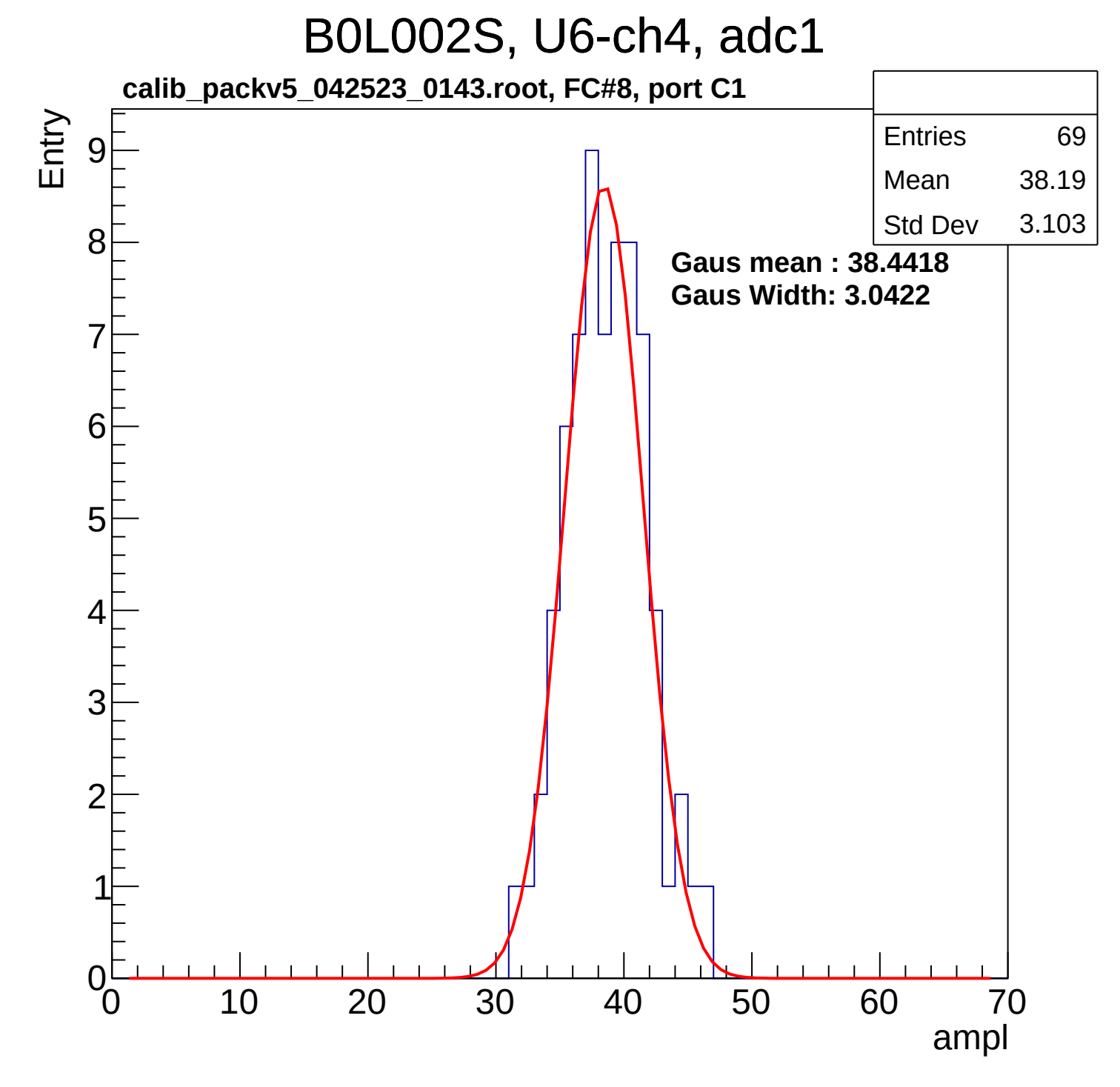
B0L002S, U6-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.19
Std Dev 3.103
Gaus mean : 38.4418
Gaus Width: 3.0422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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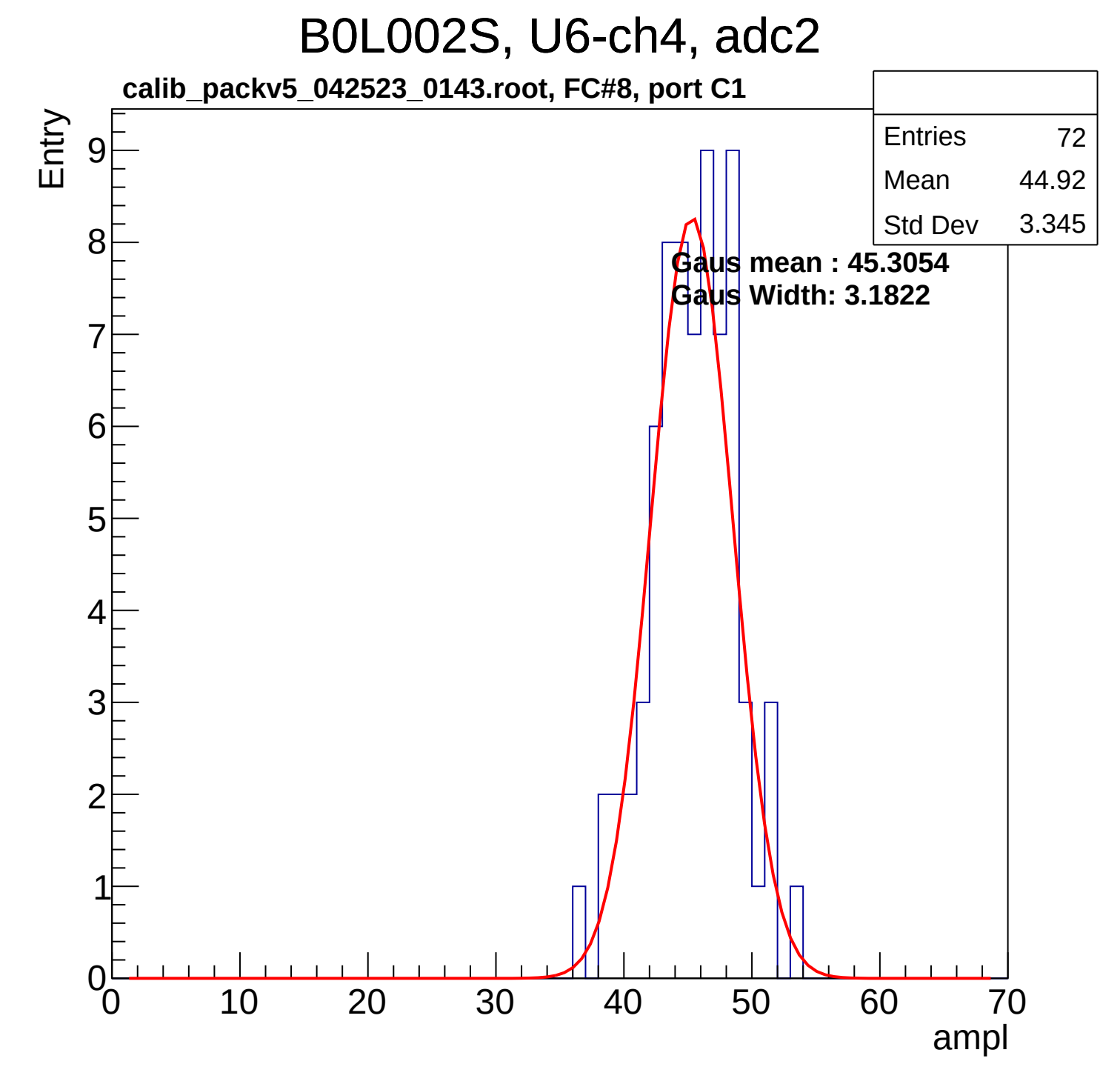
B0L002S, U6-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.92
Std Dev 3.345
Gaus mean : 45.3054
Gaus Width: 3.1822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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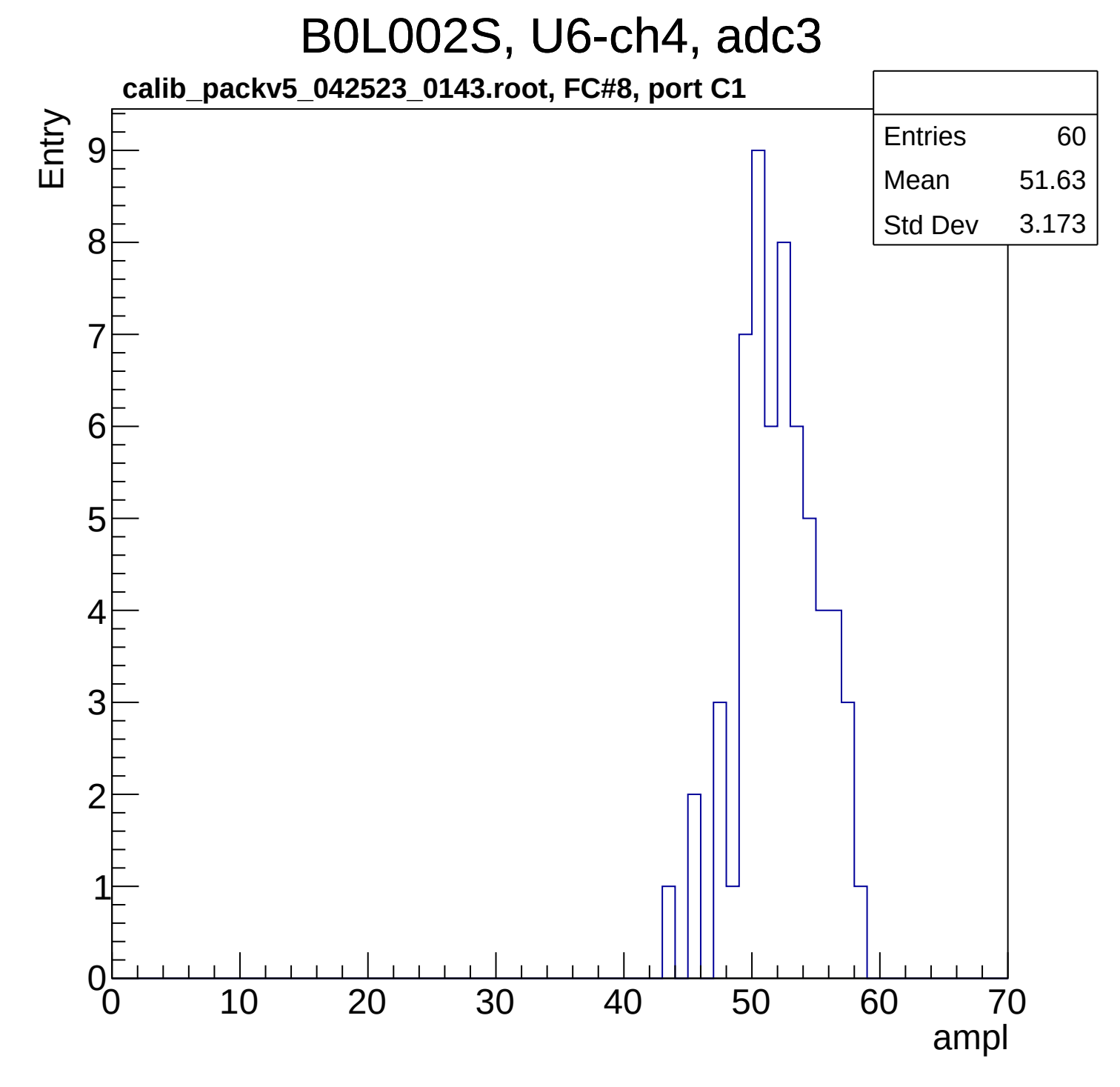
B0L002S, U6-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.63
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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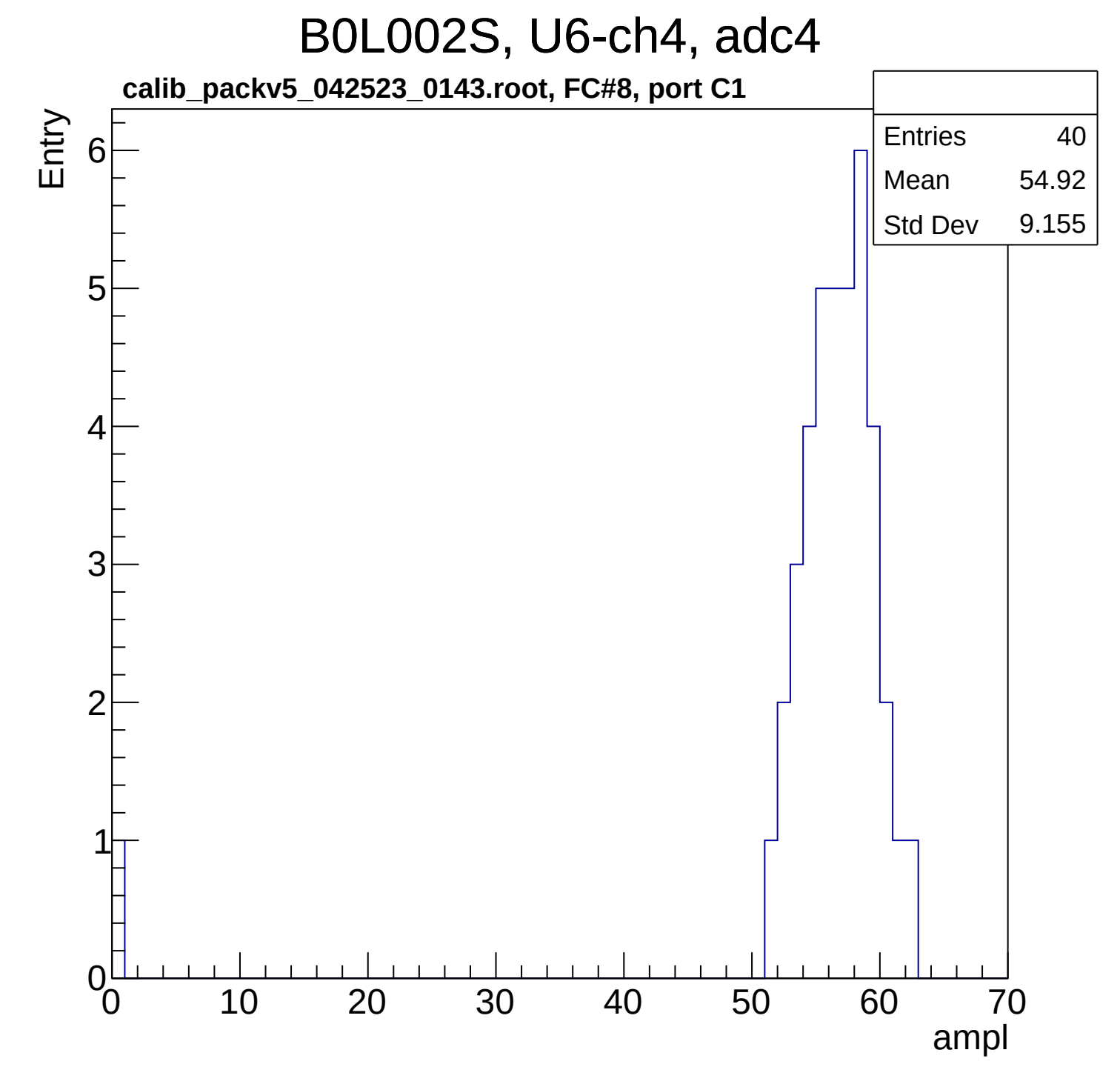
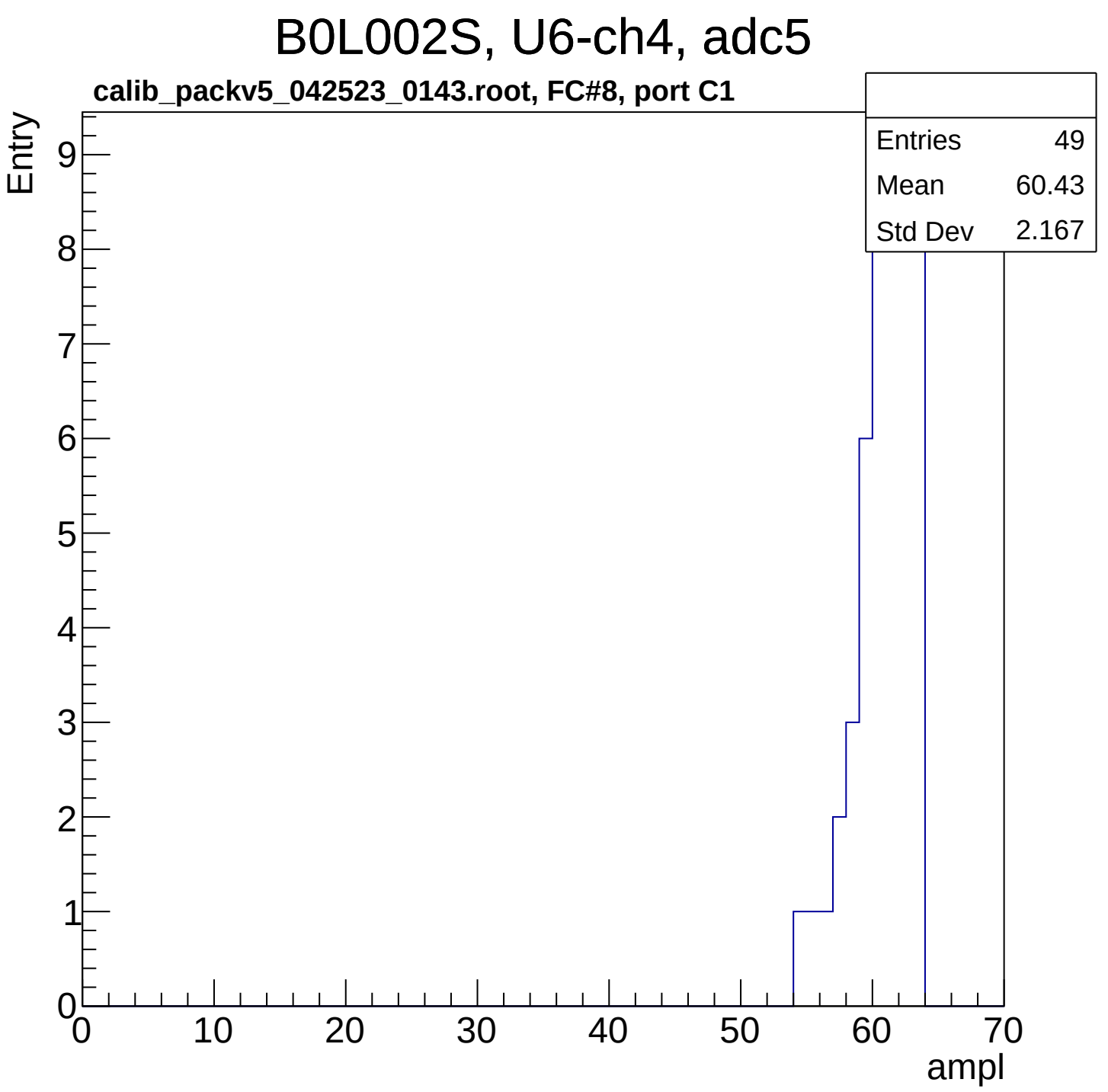

B0L002S, U6-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 54.92
Std Dev 9.155
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U6-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 60.43
Std Dev 2.167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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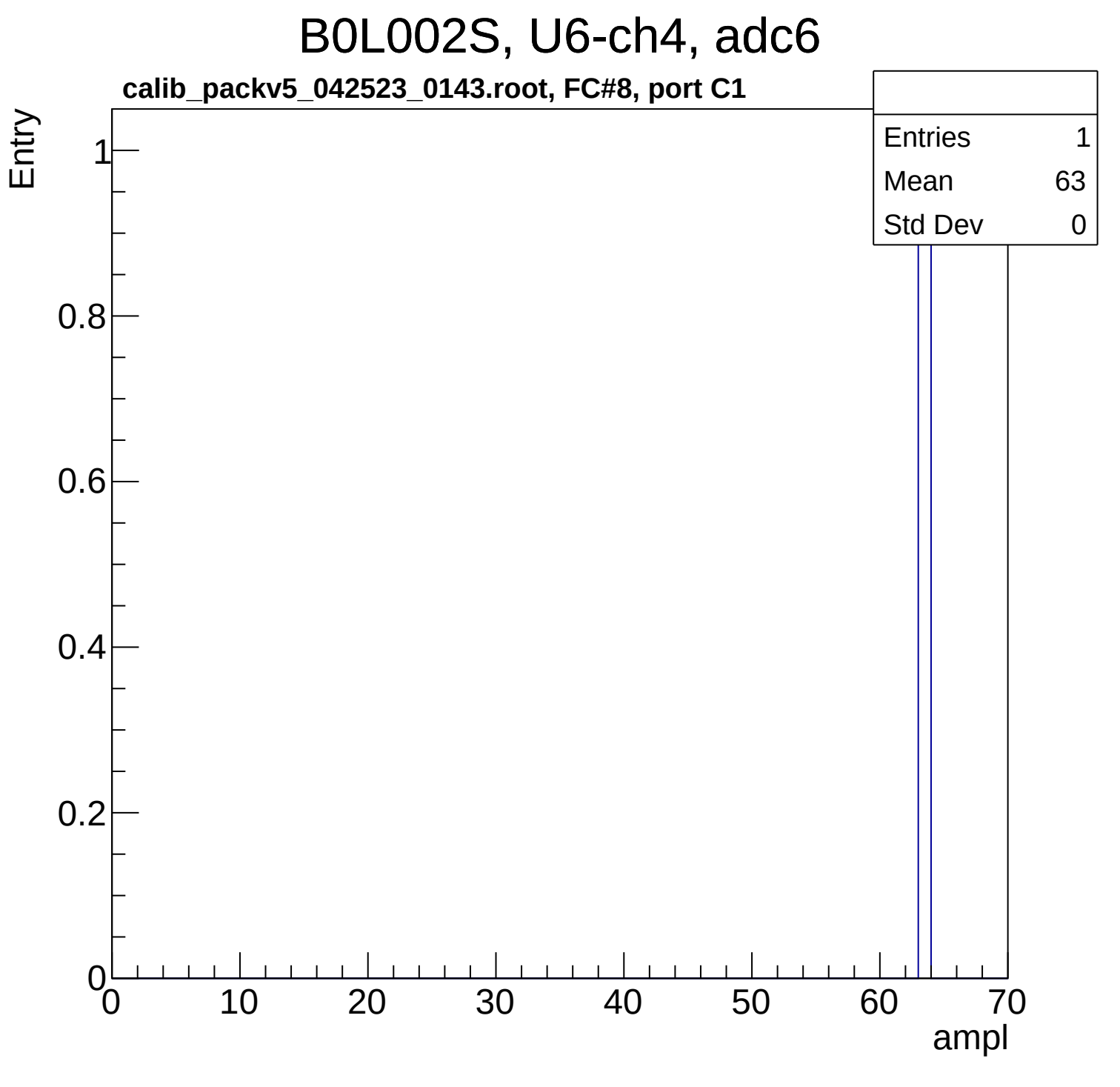
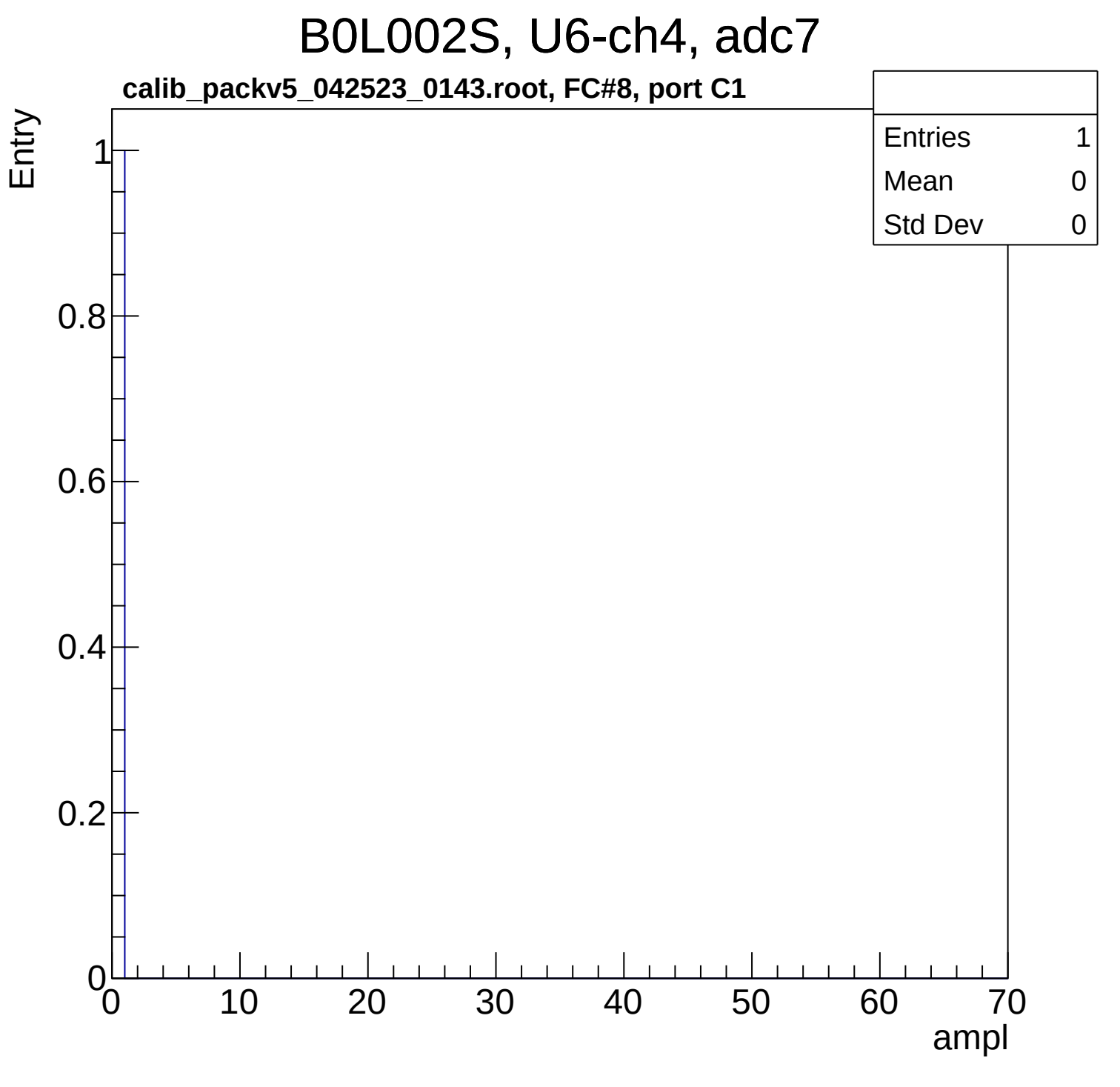
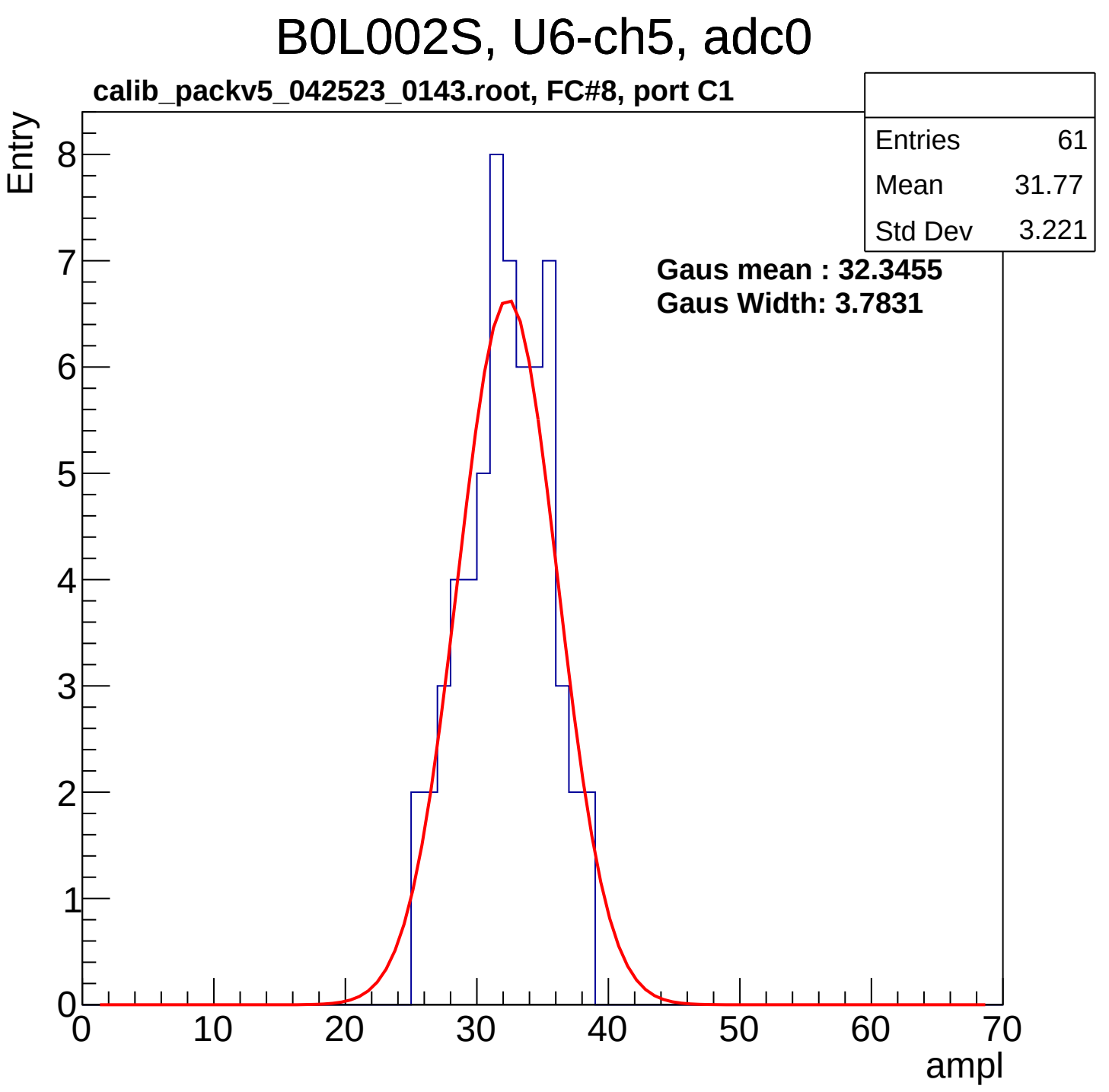
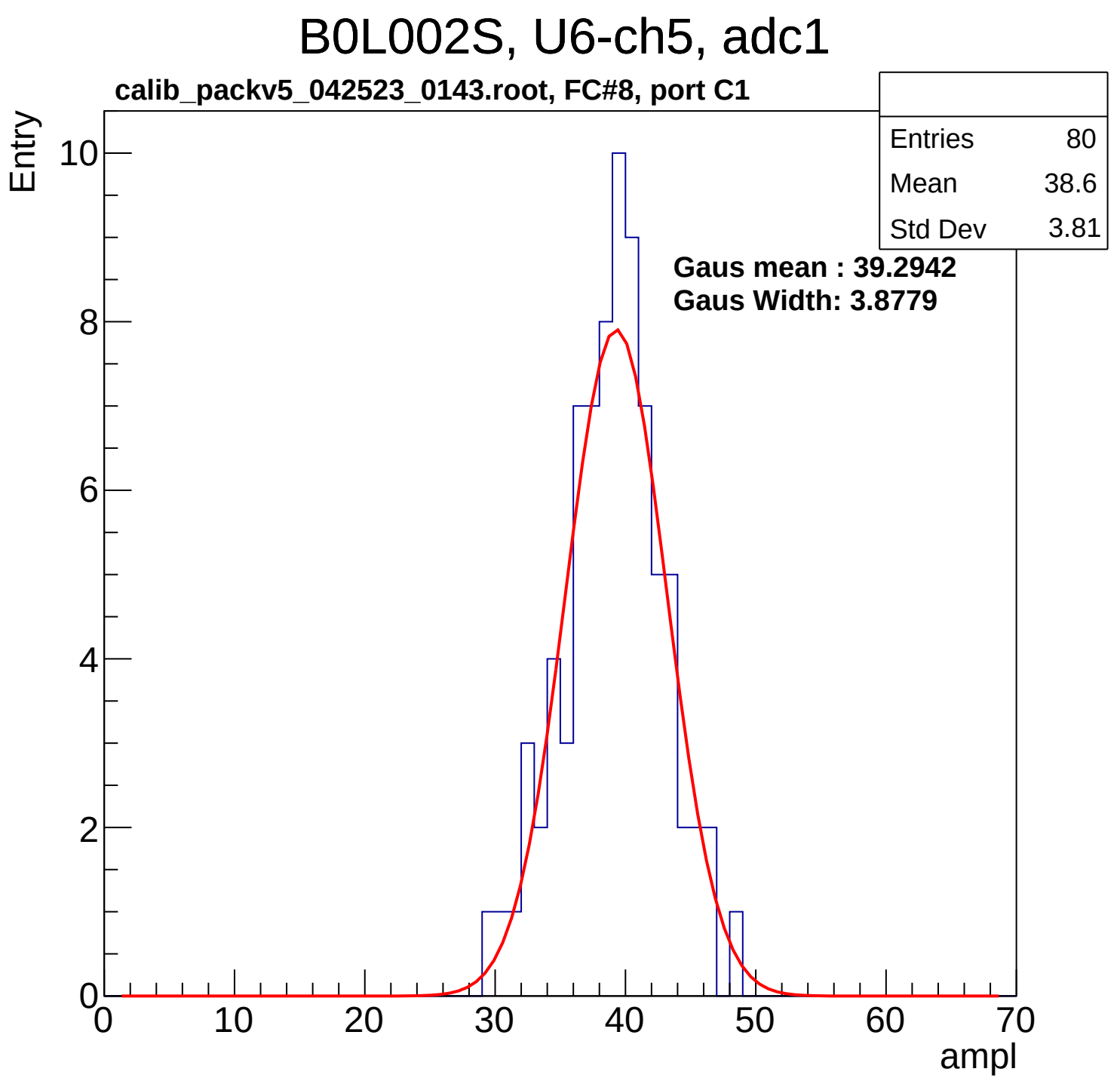

B0L002S, U6-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 31.77
Std Dev 3.221
Gaus mean : 32.3455
Gaus Width: 3.7831
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.6
Std Dev 3.81
Gaus mean : 39.2942
Gaus Width: 3.8779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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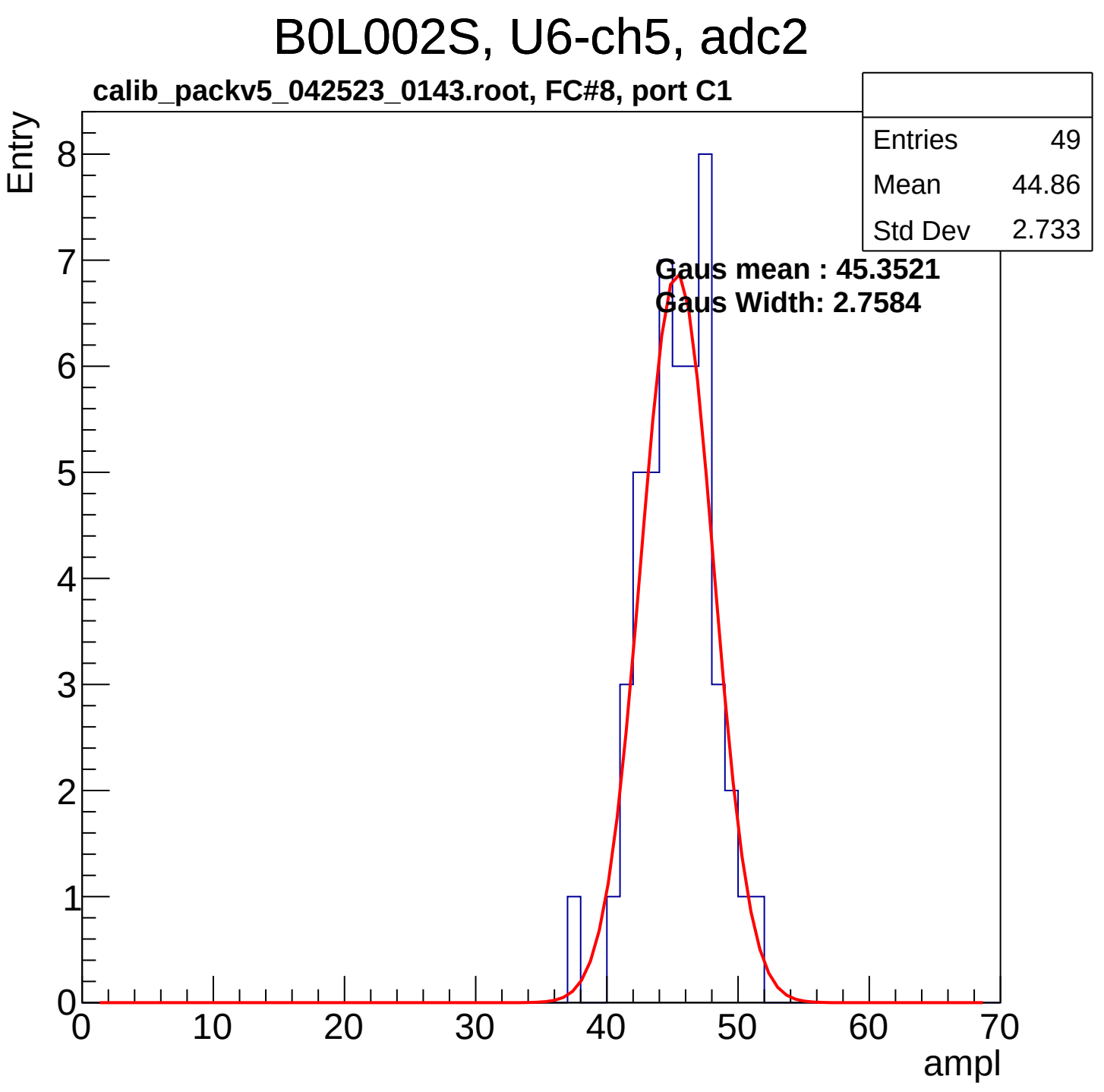
B0L002S, U6-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 44.86
Std Dev 2.733
Gaus mean : 45.3521
Gaus Width: 2.7584
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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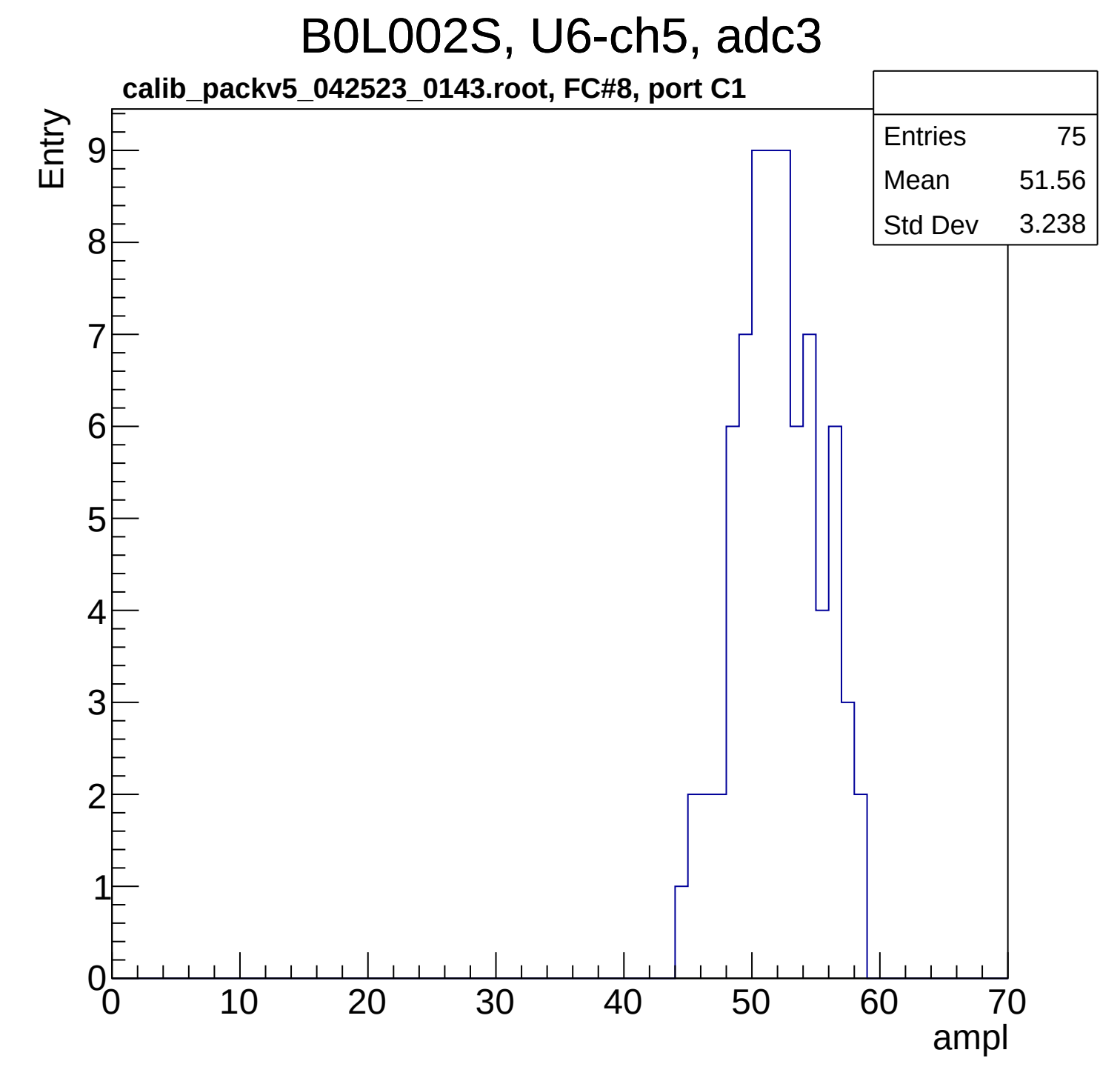
B0L002S, U6-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.56
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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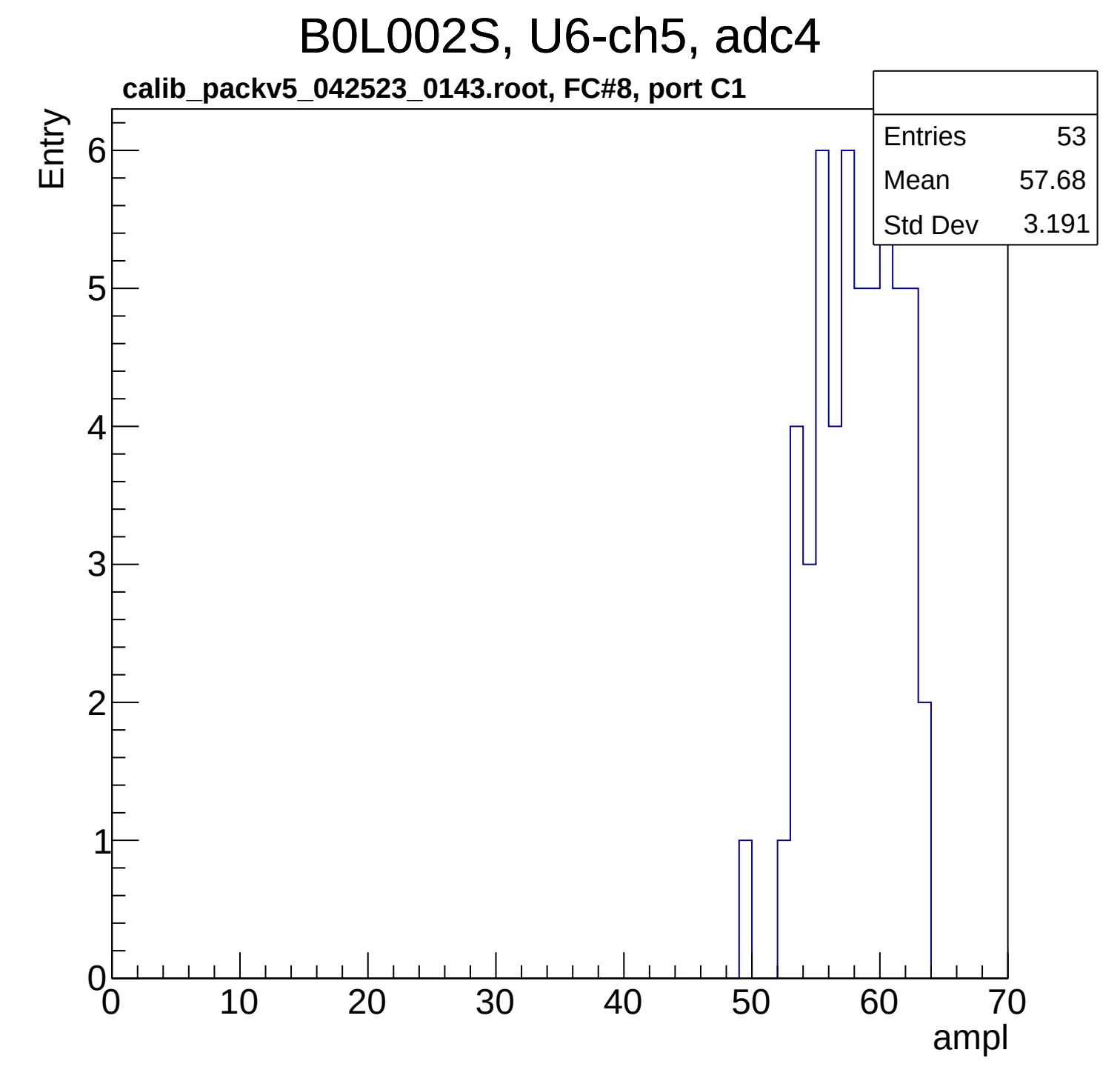
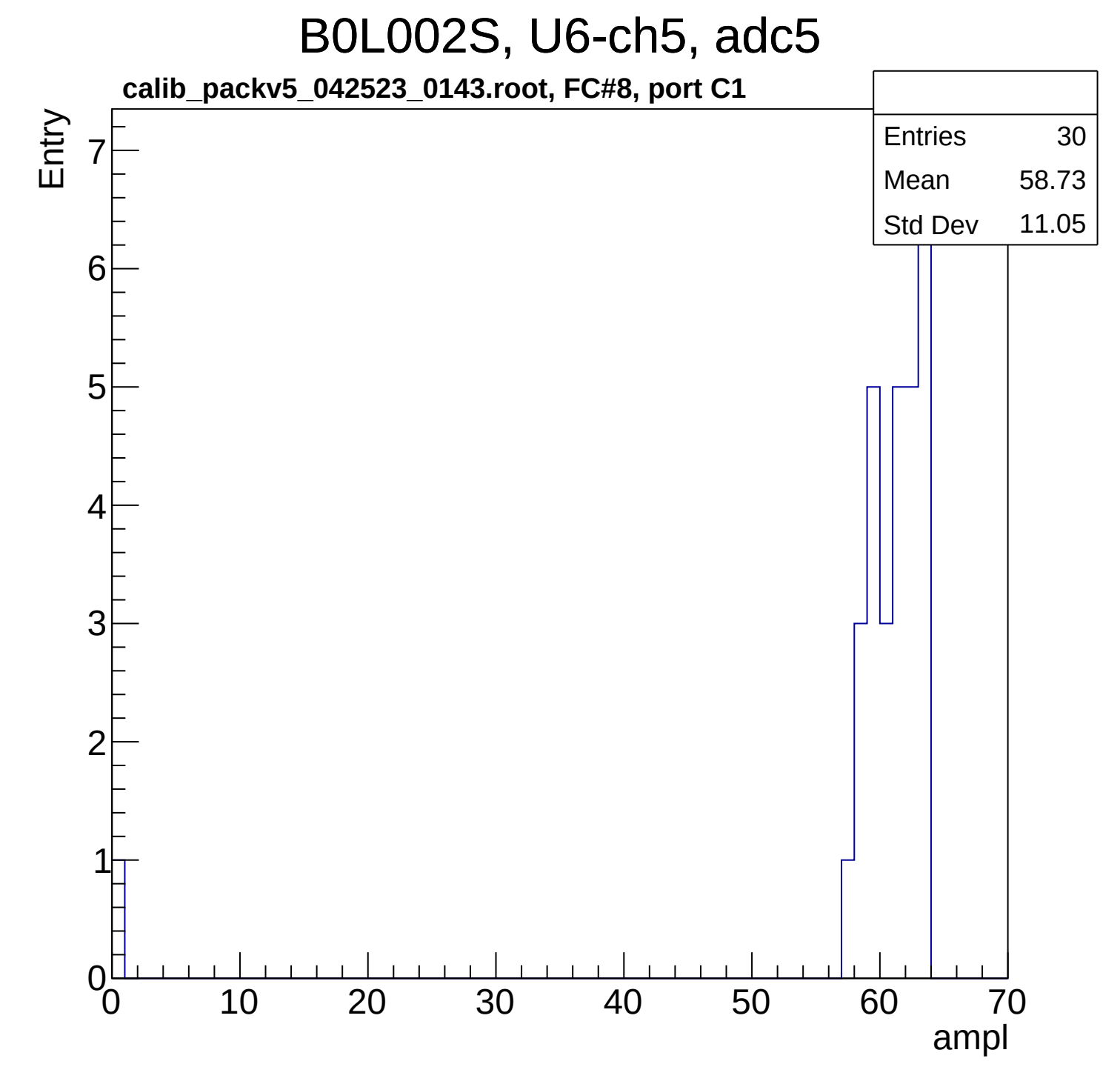
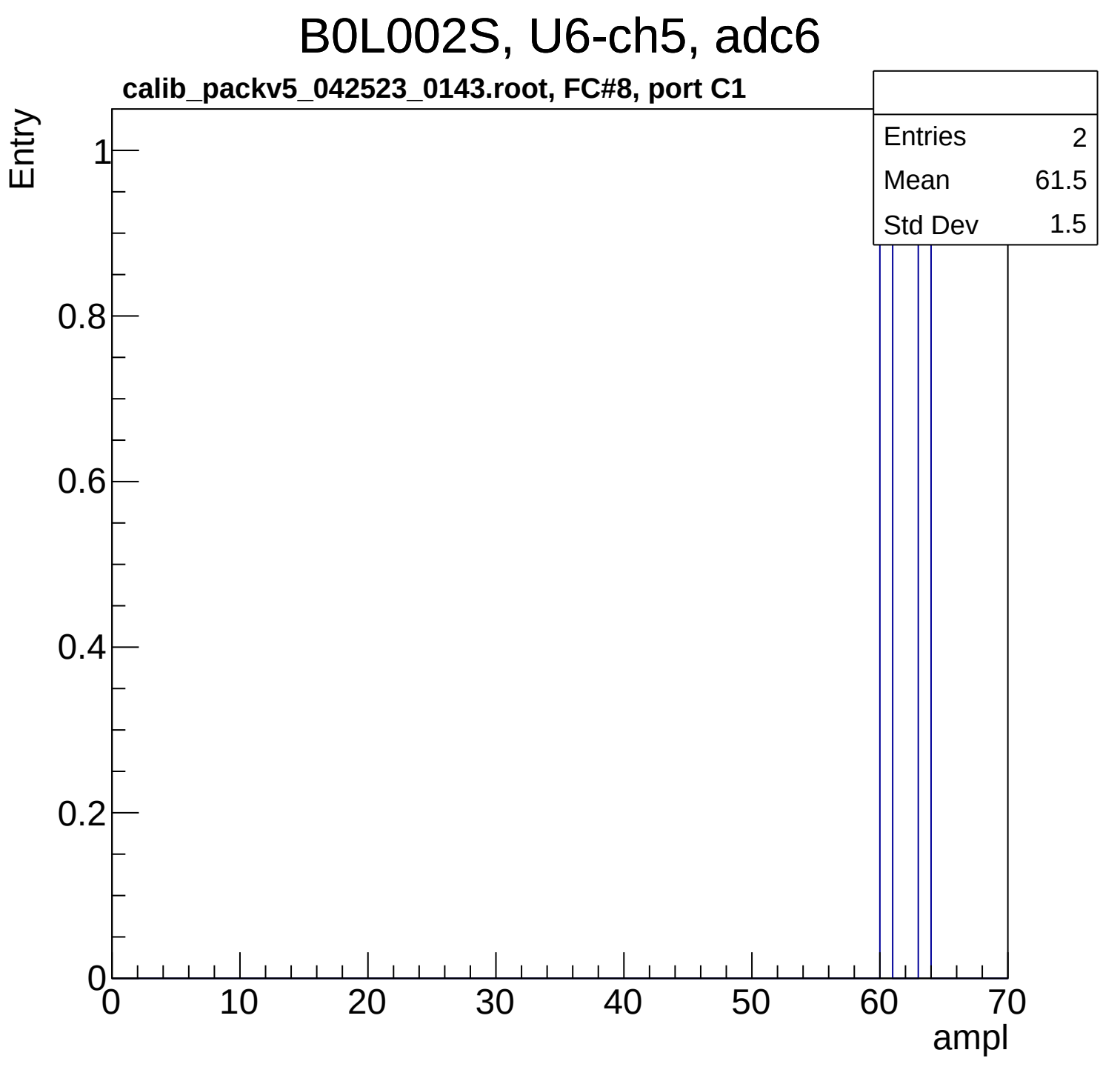
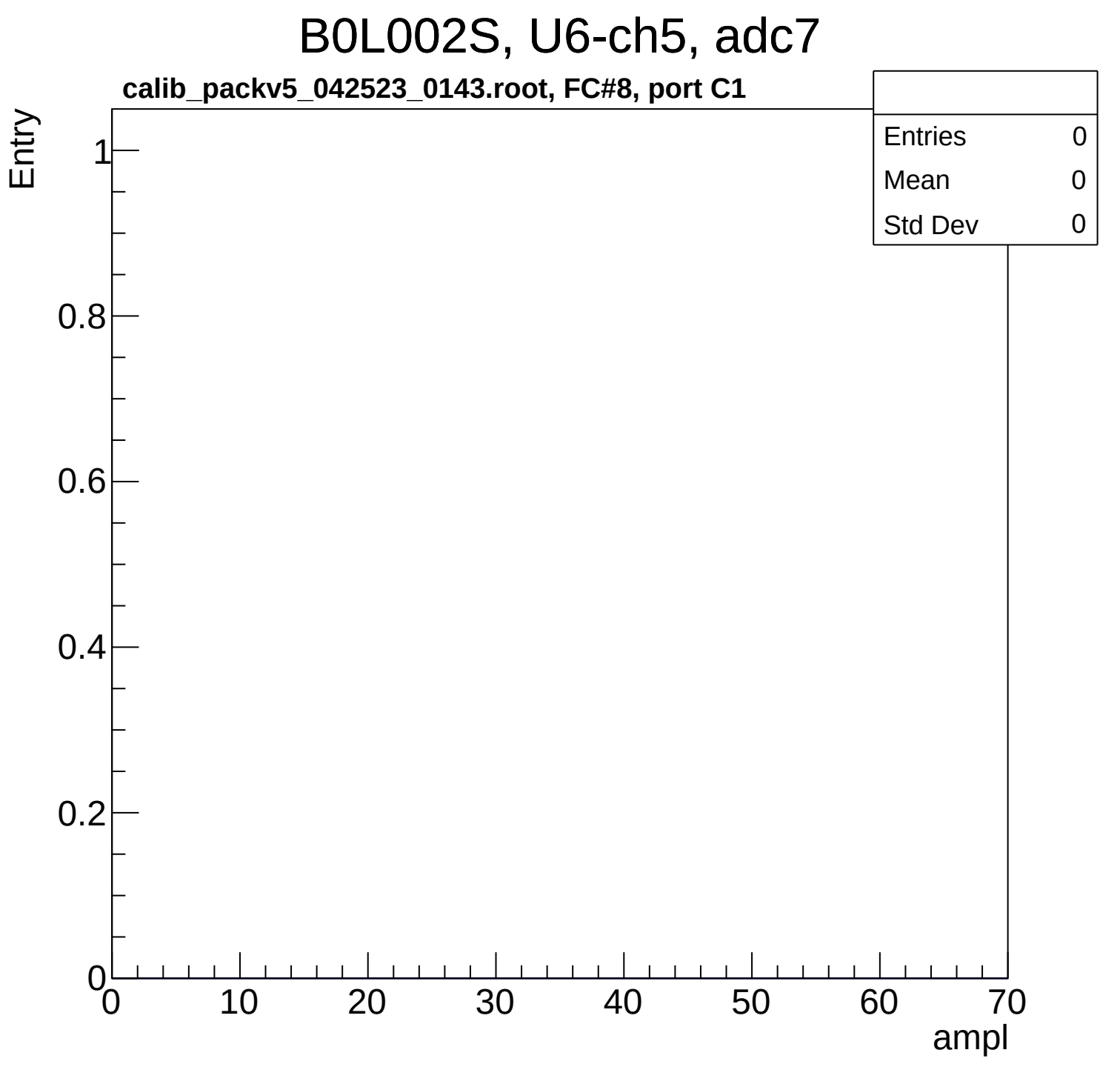
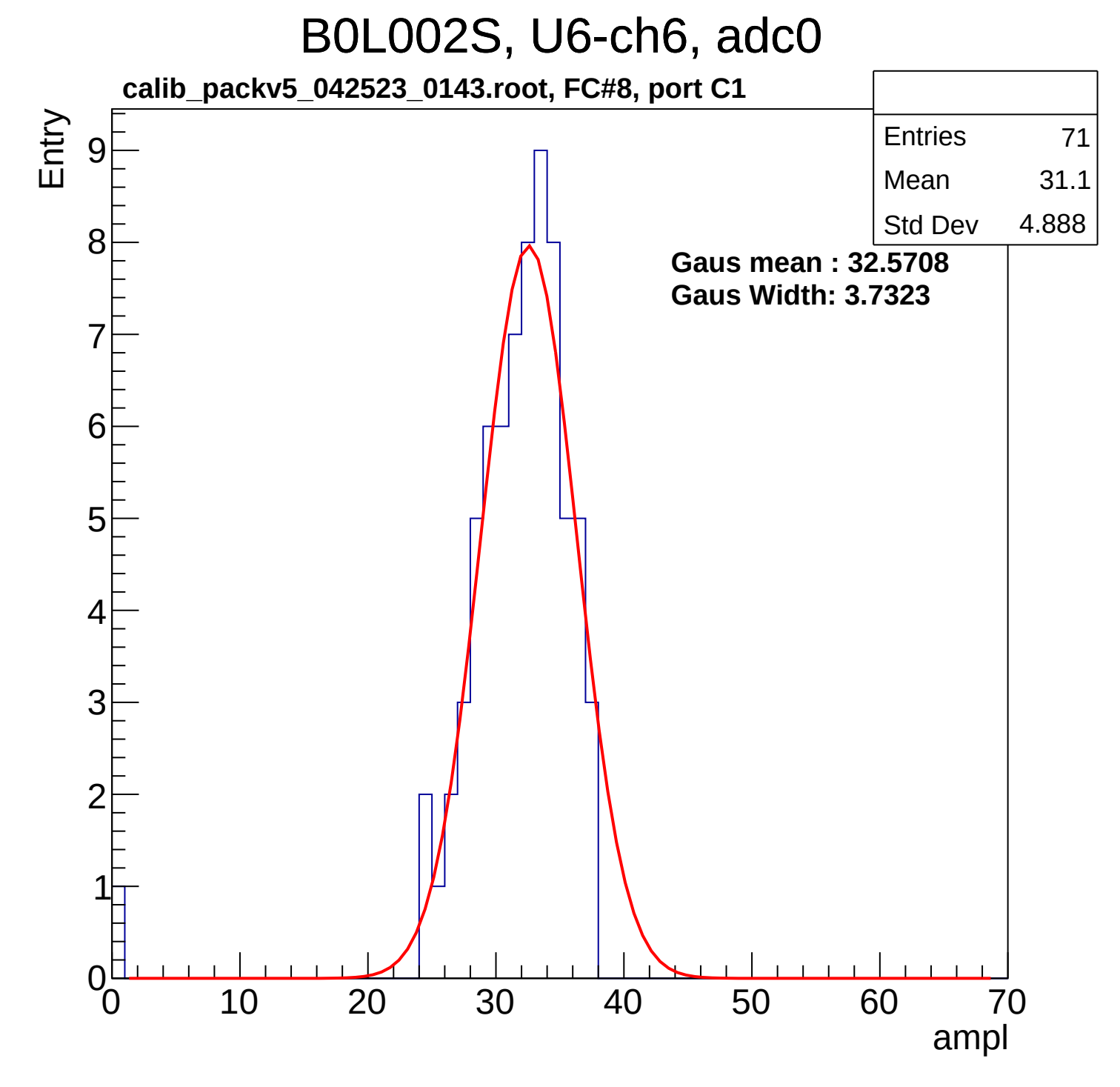

B0L002S, U6-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.68
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U6-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.73
Std Dev 11.05
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.1
Std Dev 4.888
Gaus mean : 32.5708
Gaus Width: 3.7323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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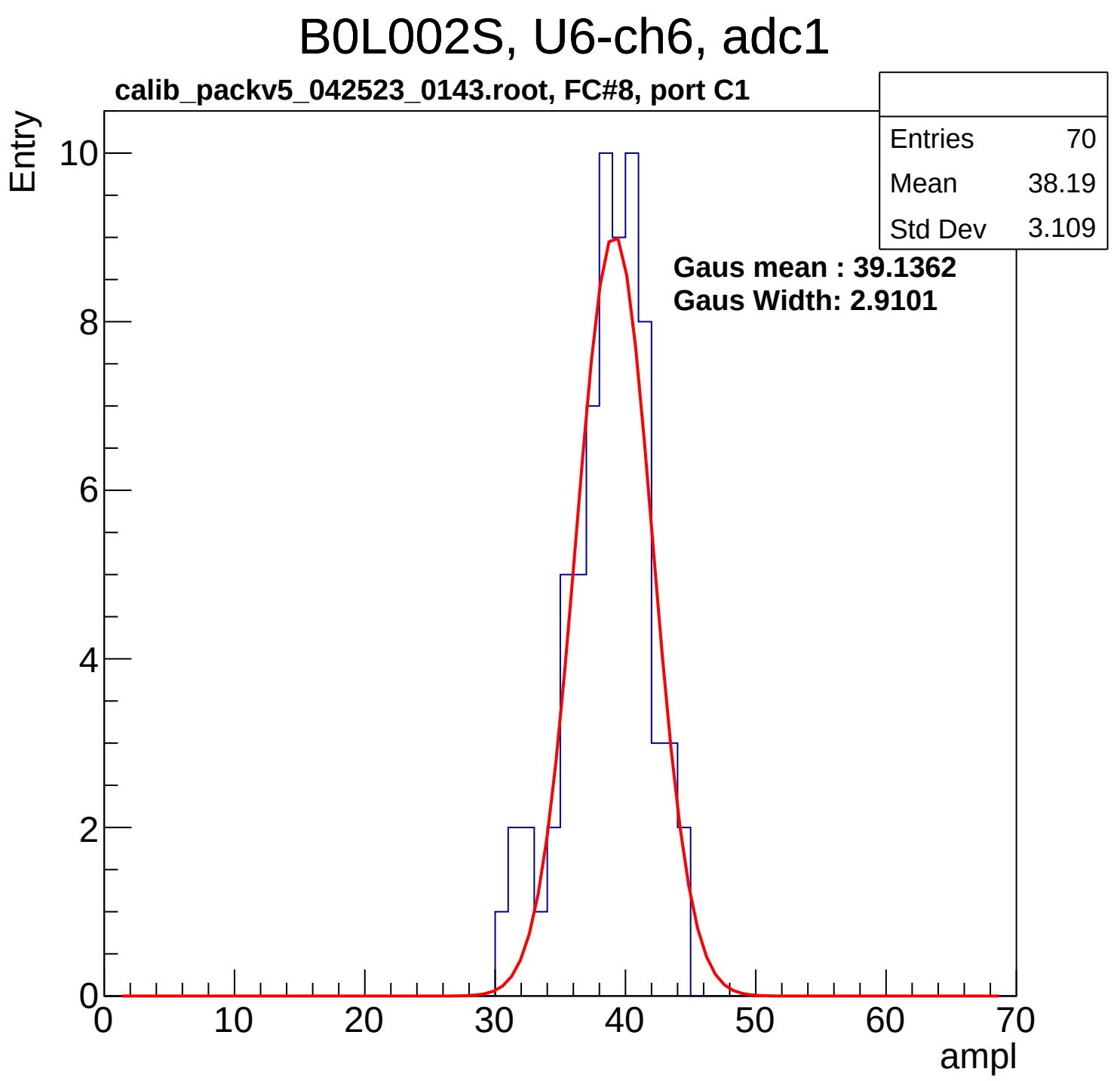
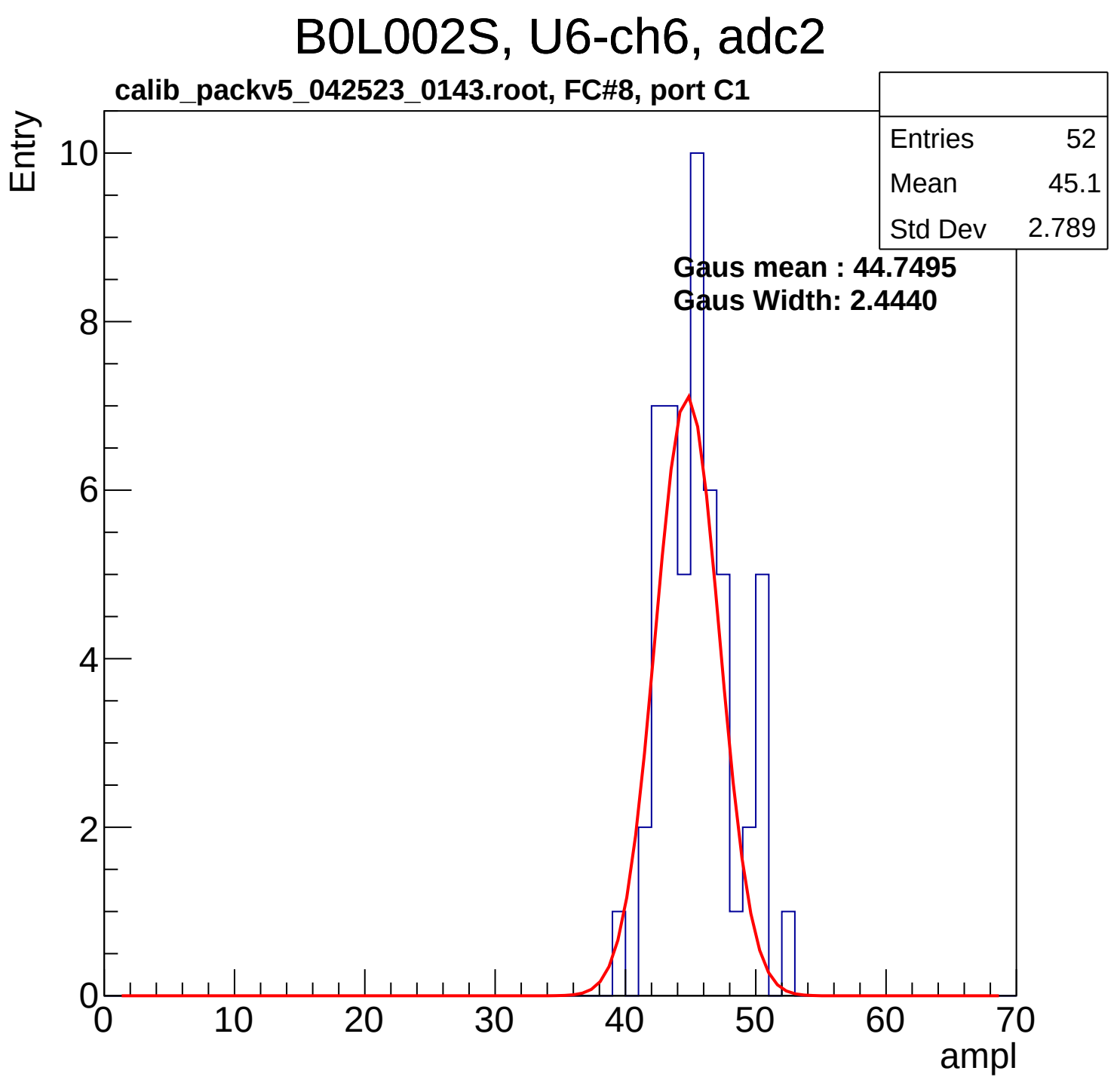

B0L002S, U6-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.19
Std Dev 3.109
Gaus mean : 39.1362
Gaus Width: 2.9101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 45.1
Std Dev 2.789
Gaus mean : 44.7495
Gaus Width: 2.4440
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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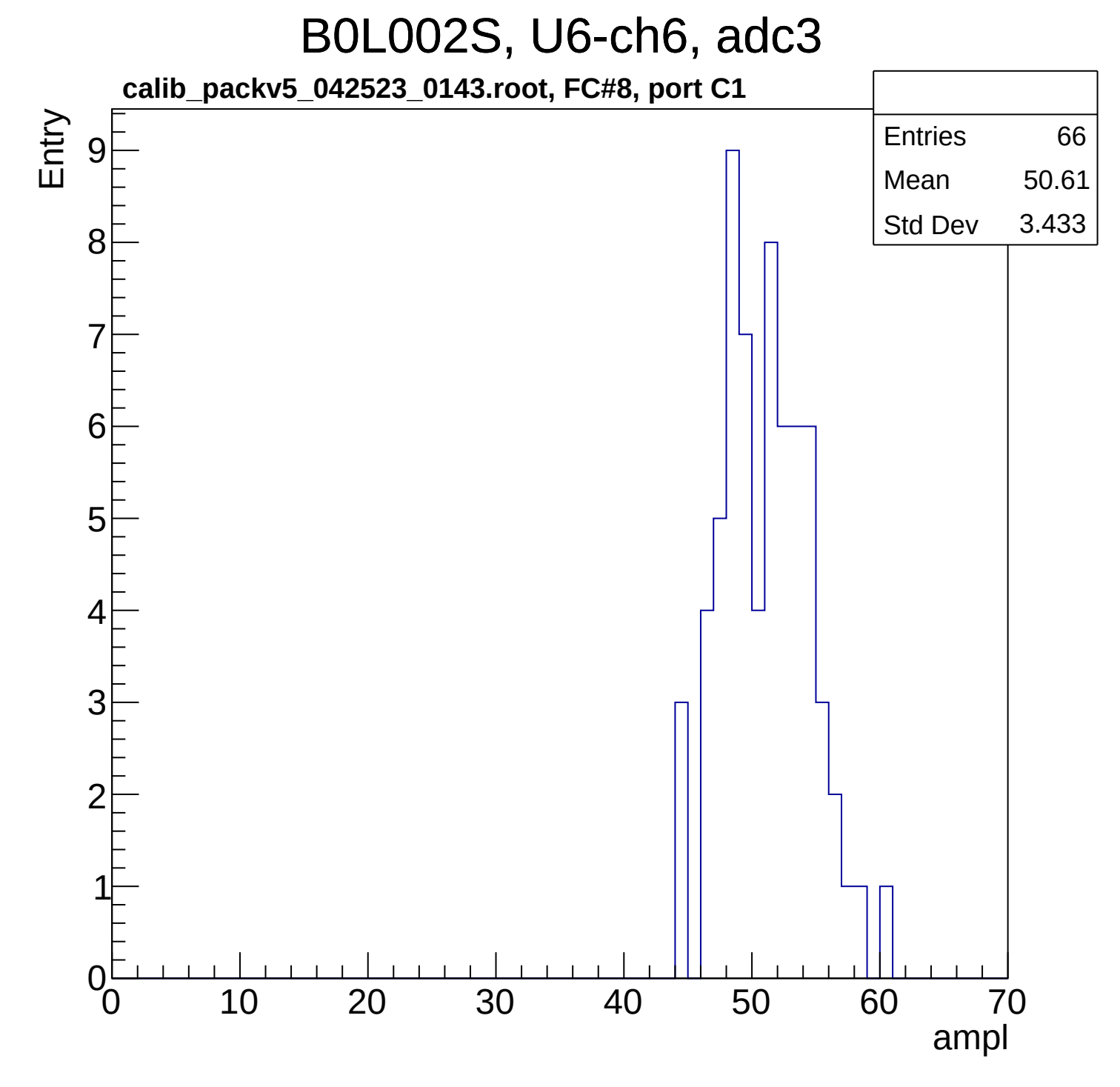
B0L002S, U6-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.61
Std Dev 3.433
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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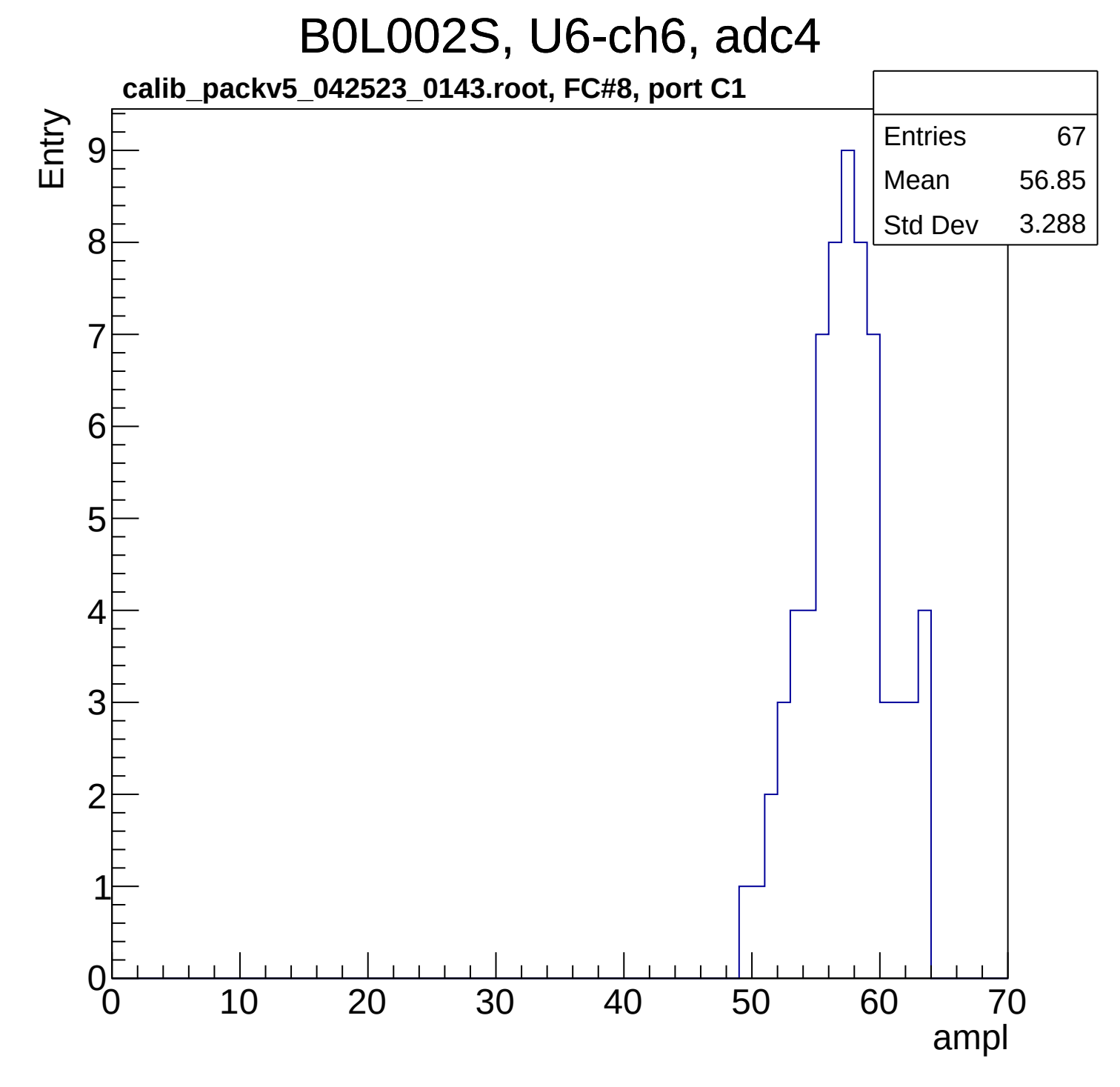
B0L002S, U6-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.85
Std Dev 3.288
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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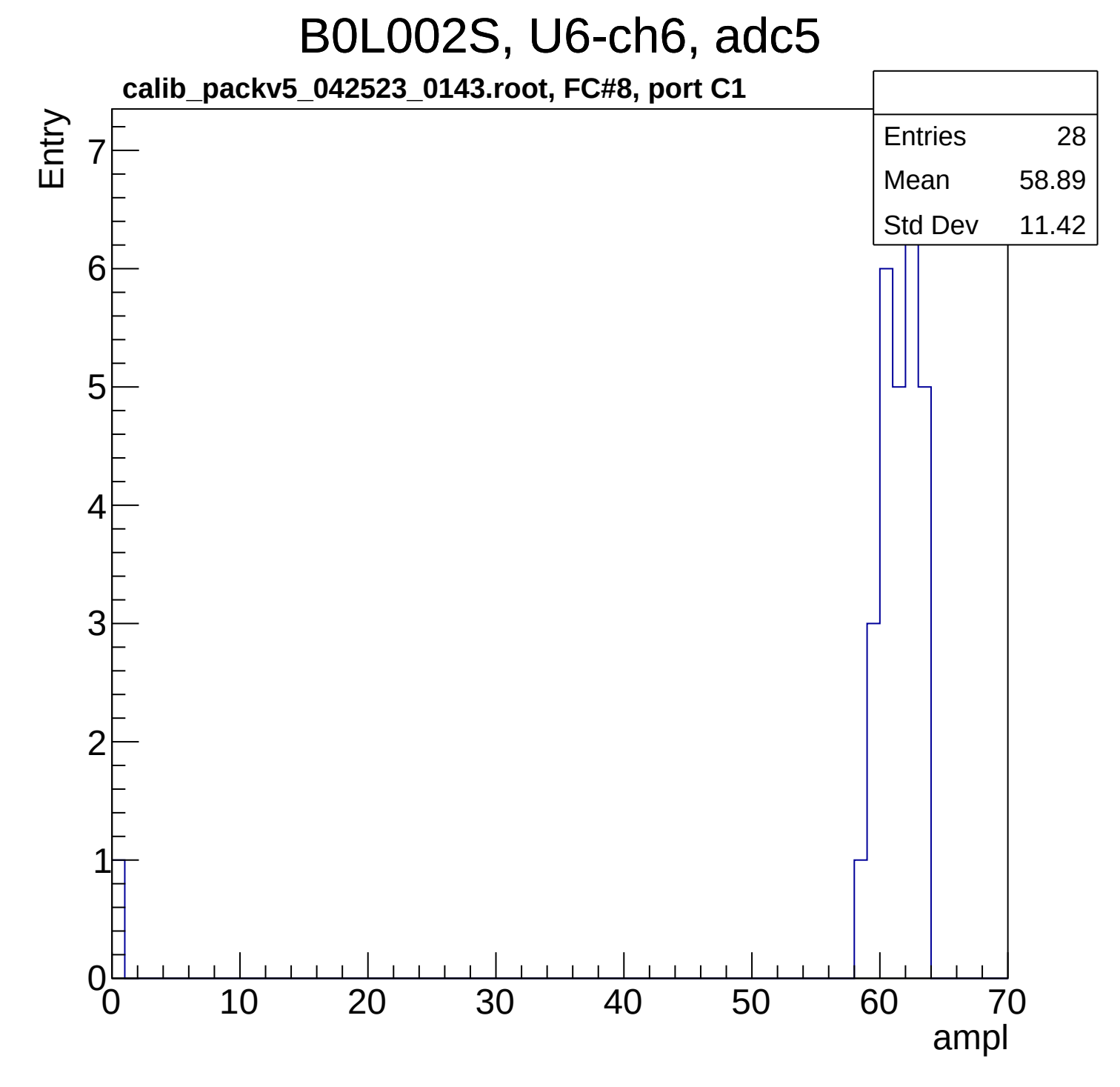
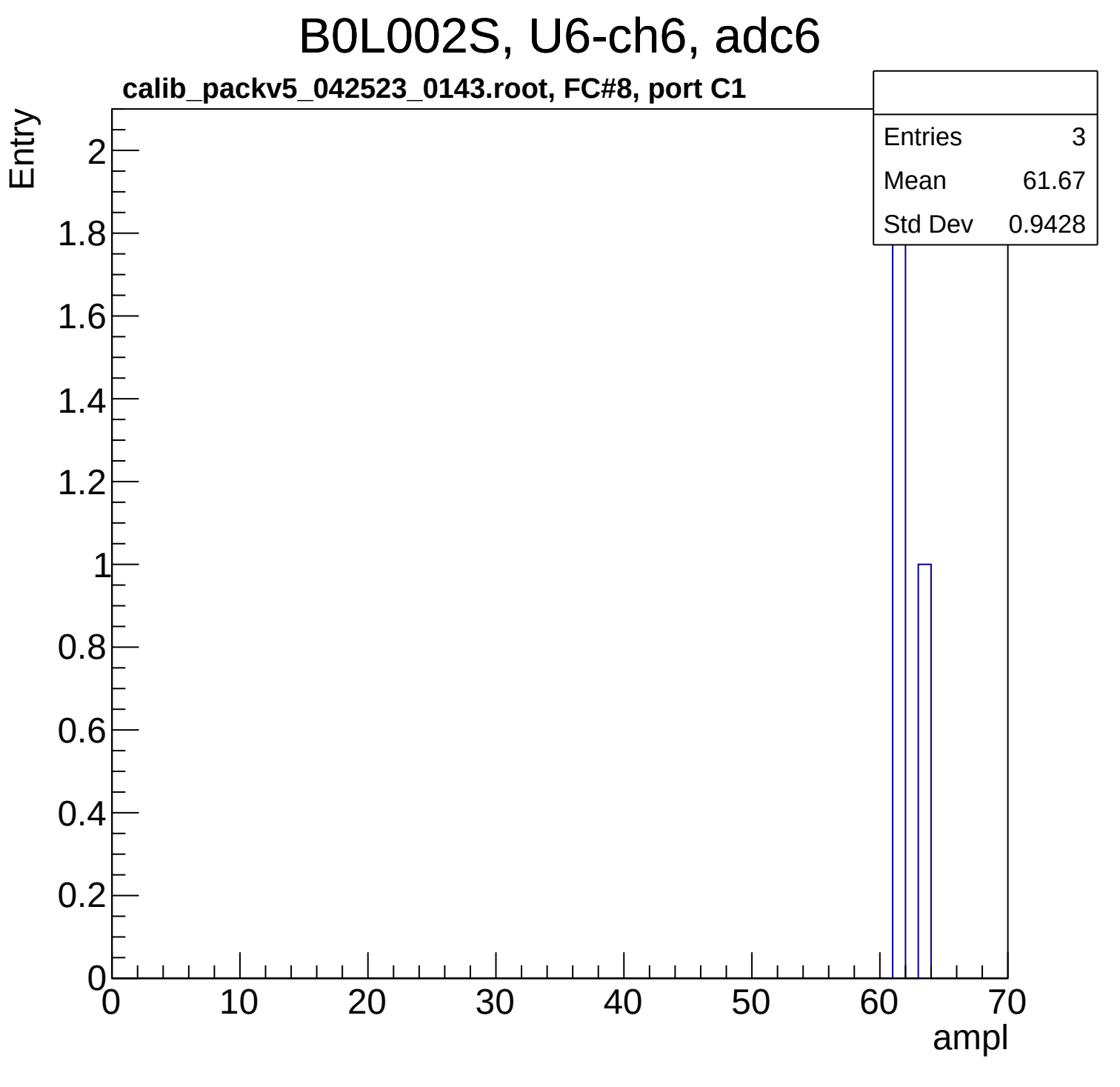

B0L002S, U6-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.89
Std Dev 11.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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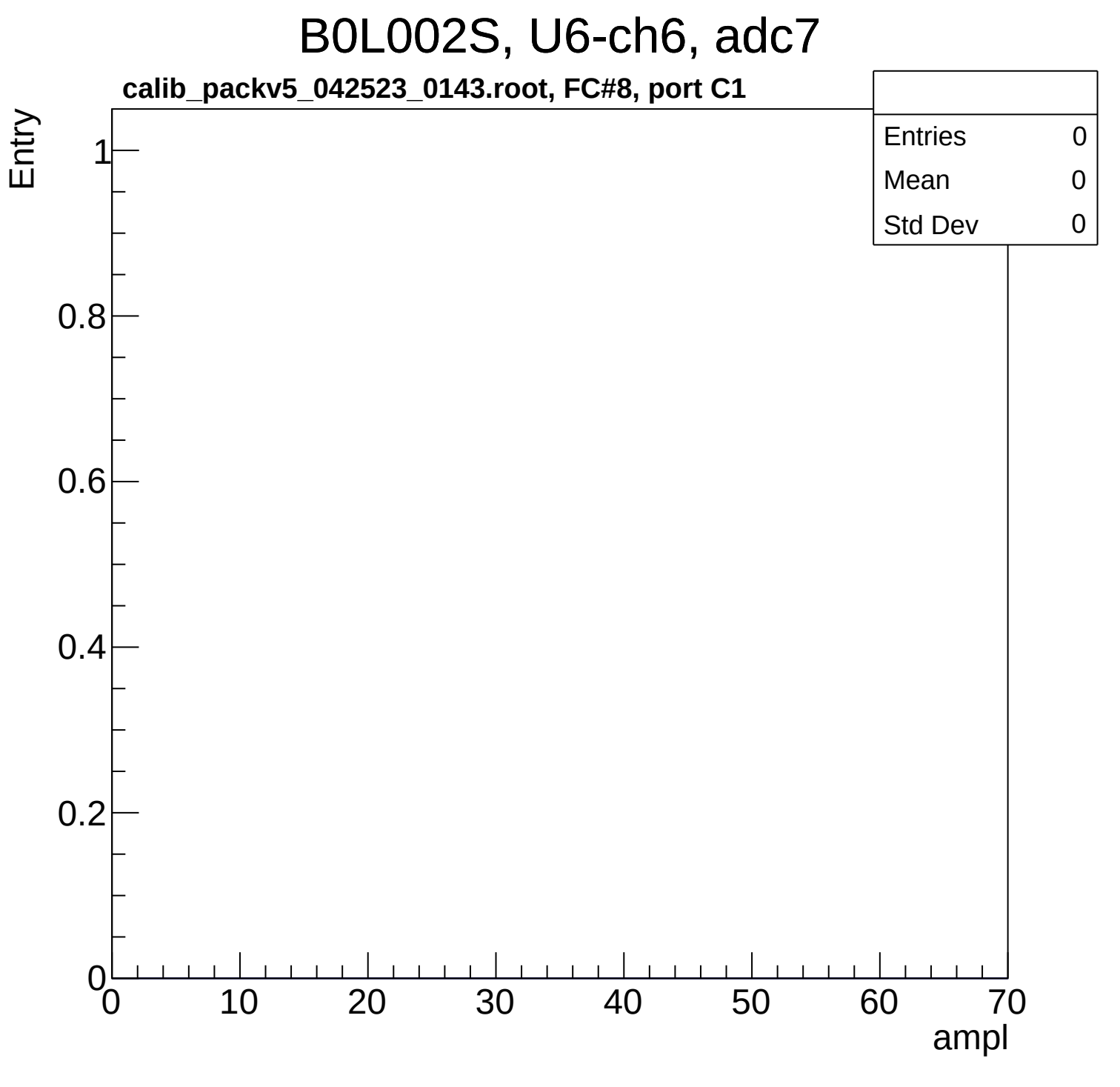
B0L002S, U6-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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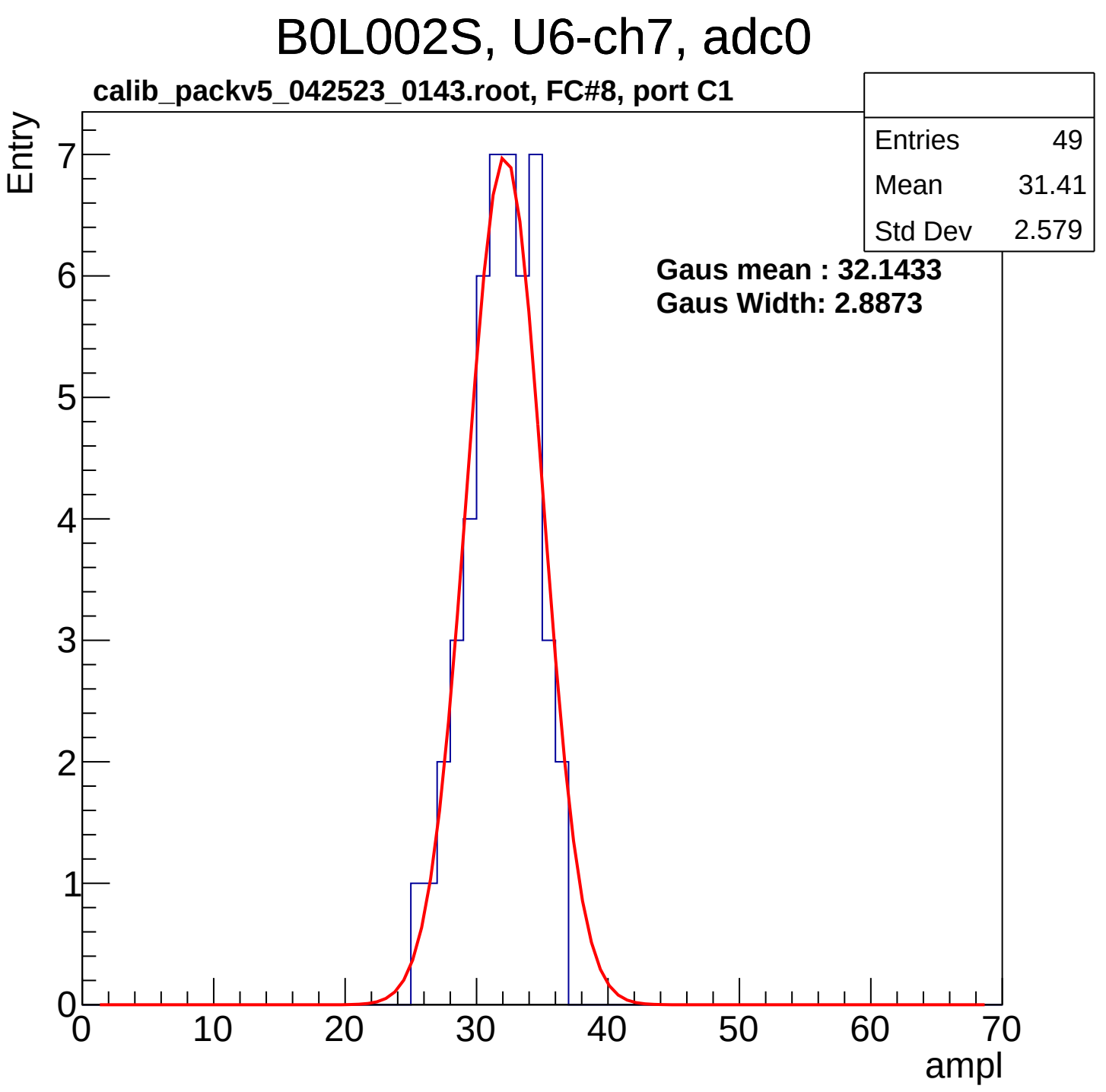
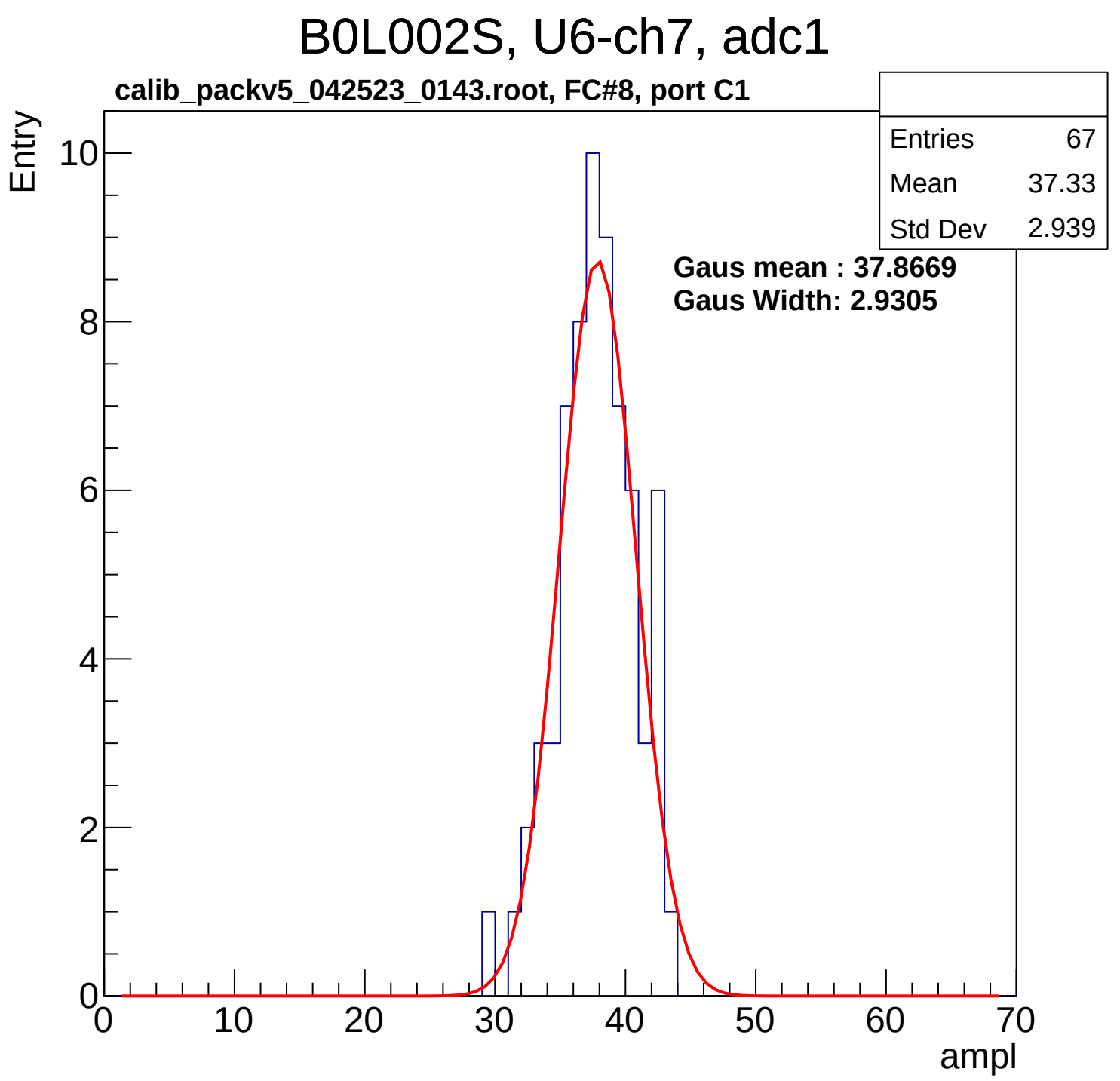
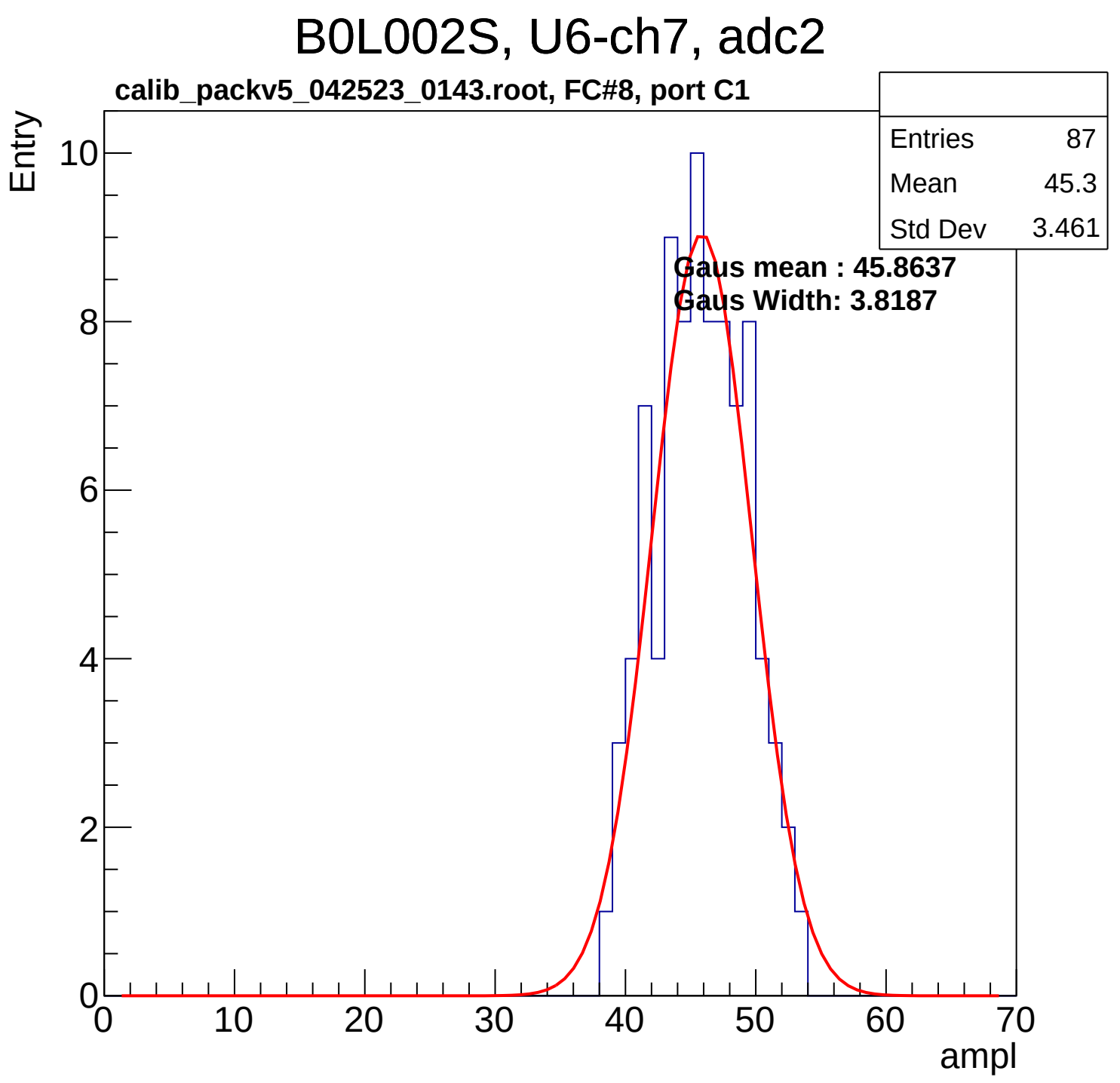
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 31.41
Std Dev 2.579
Gaus mean : 32.1433
Gaus Width: 2.8873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.33
Std Dev 2.939
Gaus mean : 37.8669
Gaus Width: 2.9305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 45.3
Std Dev 3.461
Gaus mean : 45.8637
Gaus Width: 3.8187
Entry
ampl
0
2
4
6
8
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0
10
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40
50
60
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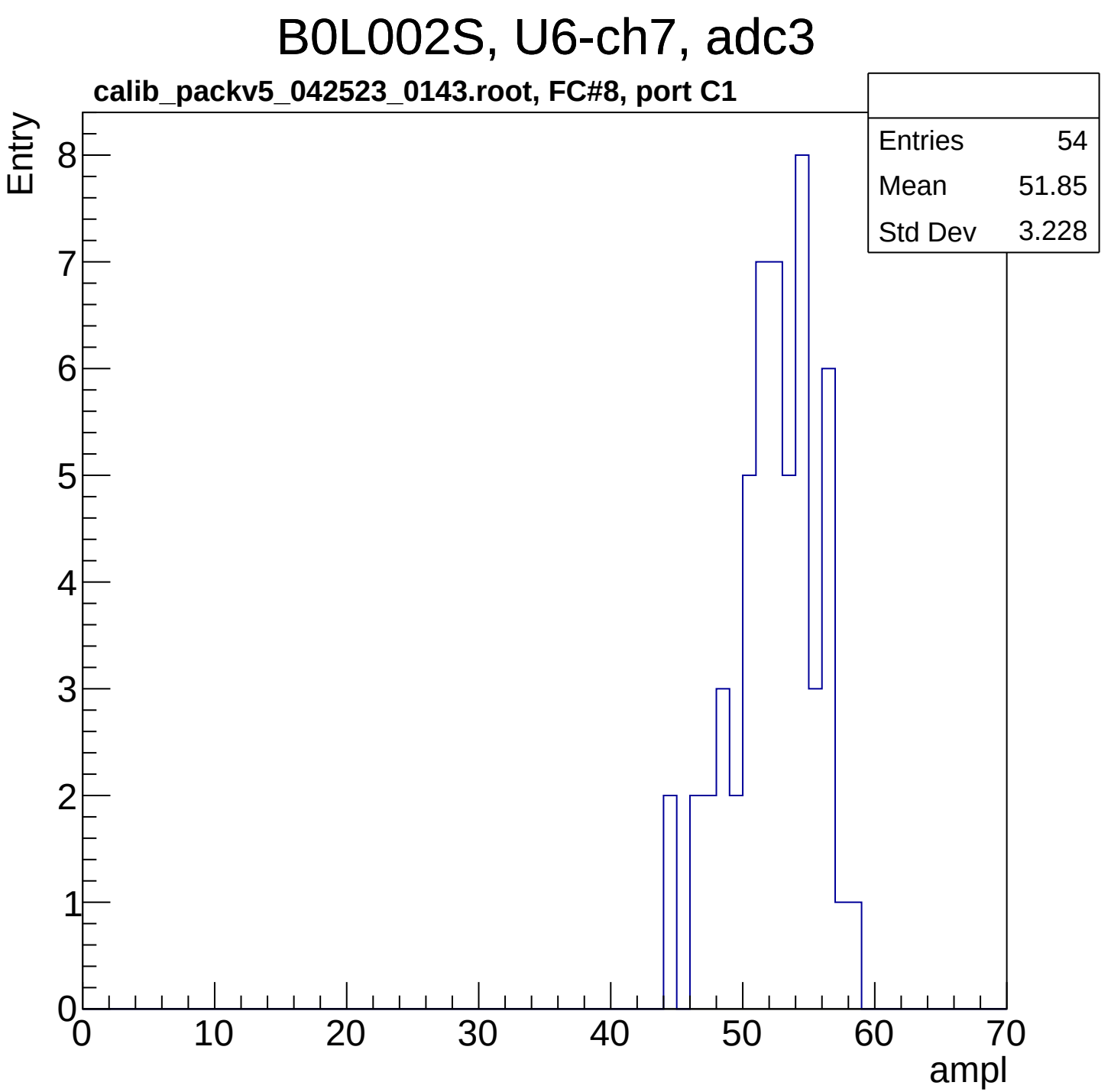
B0L002S, U6-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 51.85
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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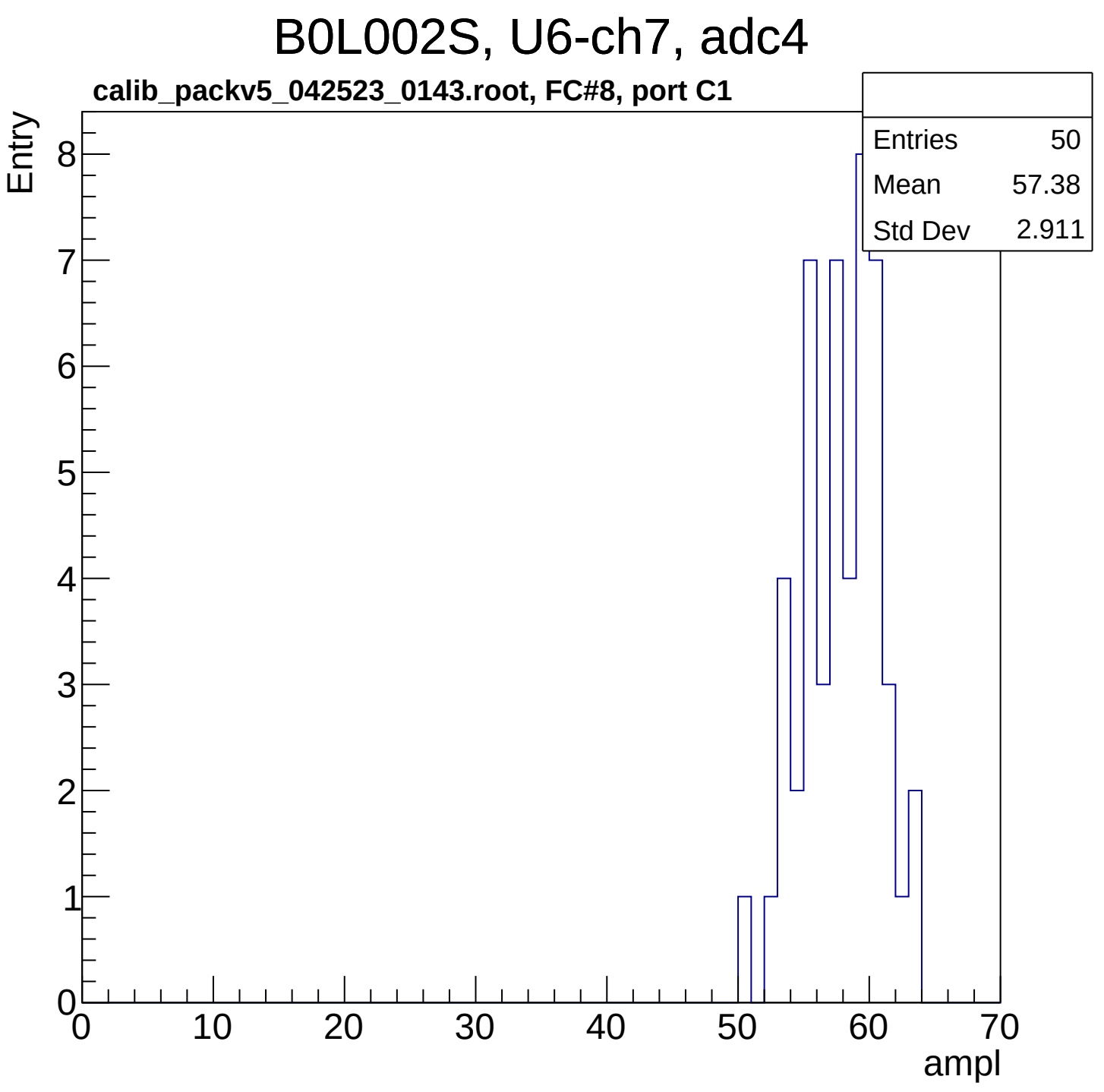
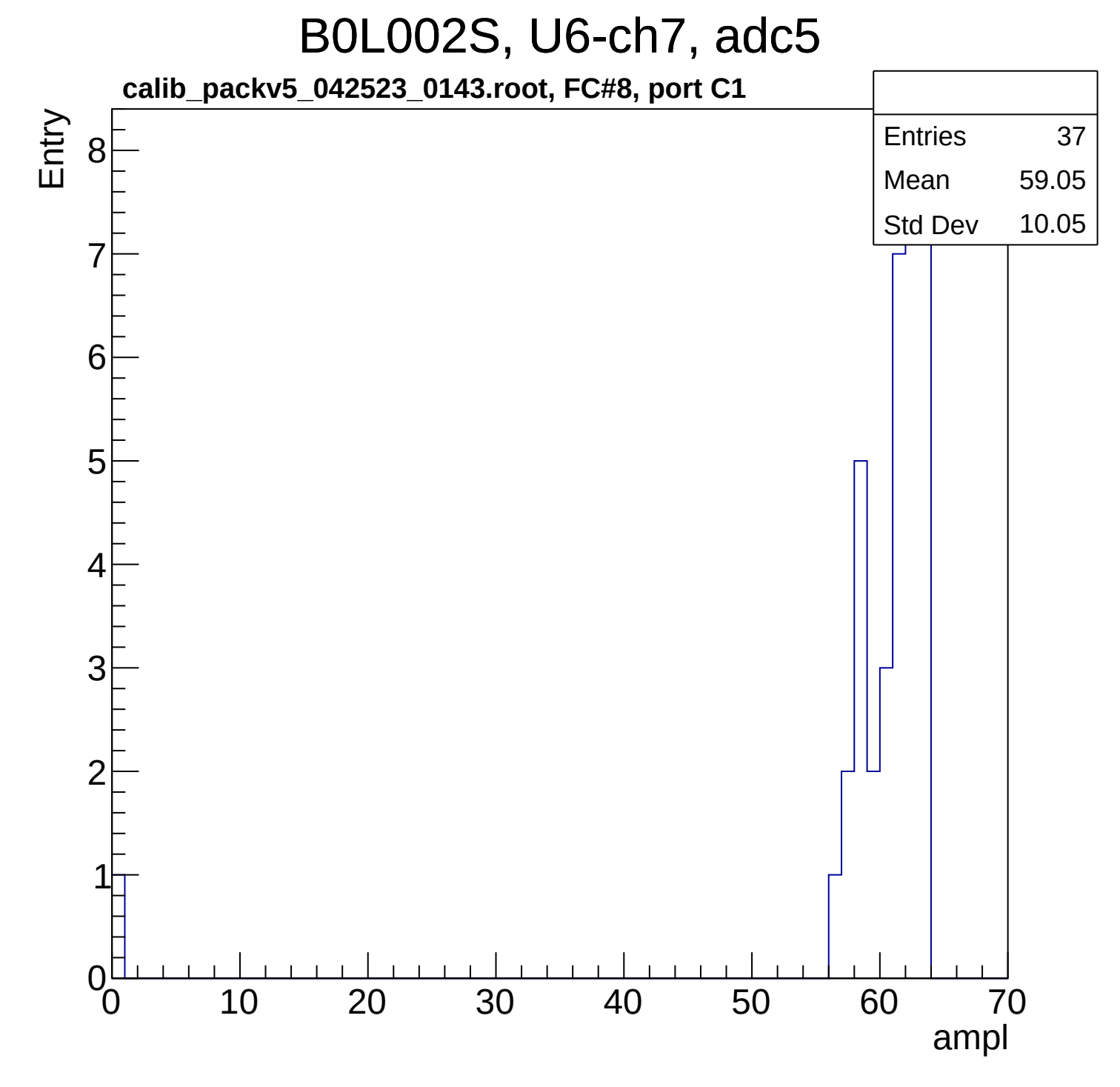

B0L002S, U6-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.38
Std Dev 2.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.05
Std Dev 10.05
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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60
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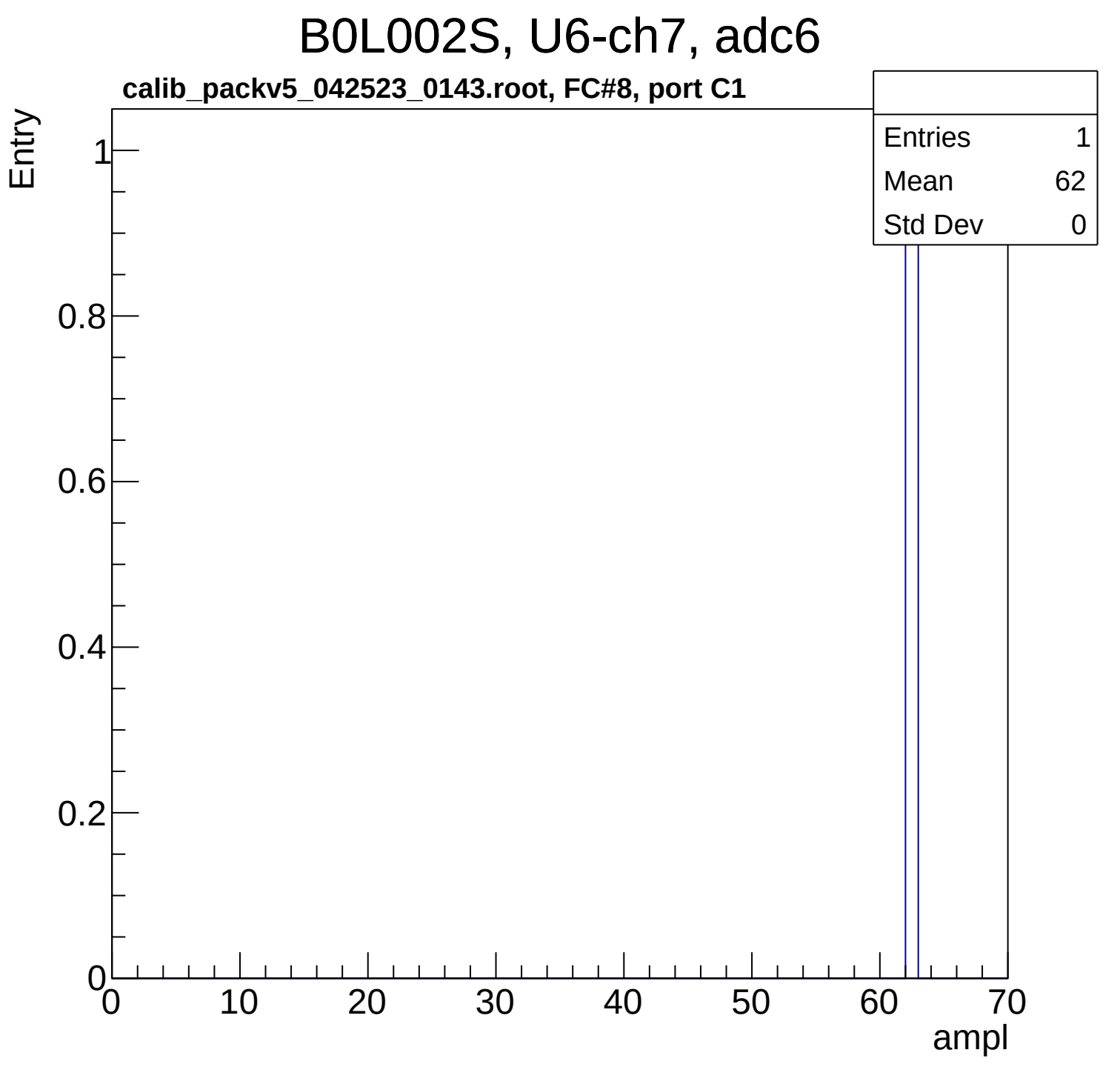
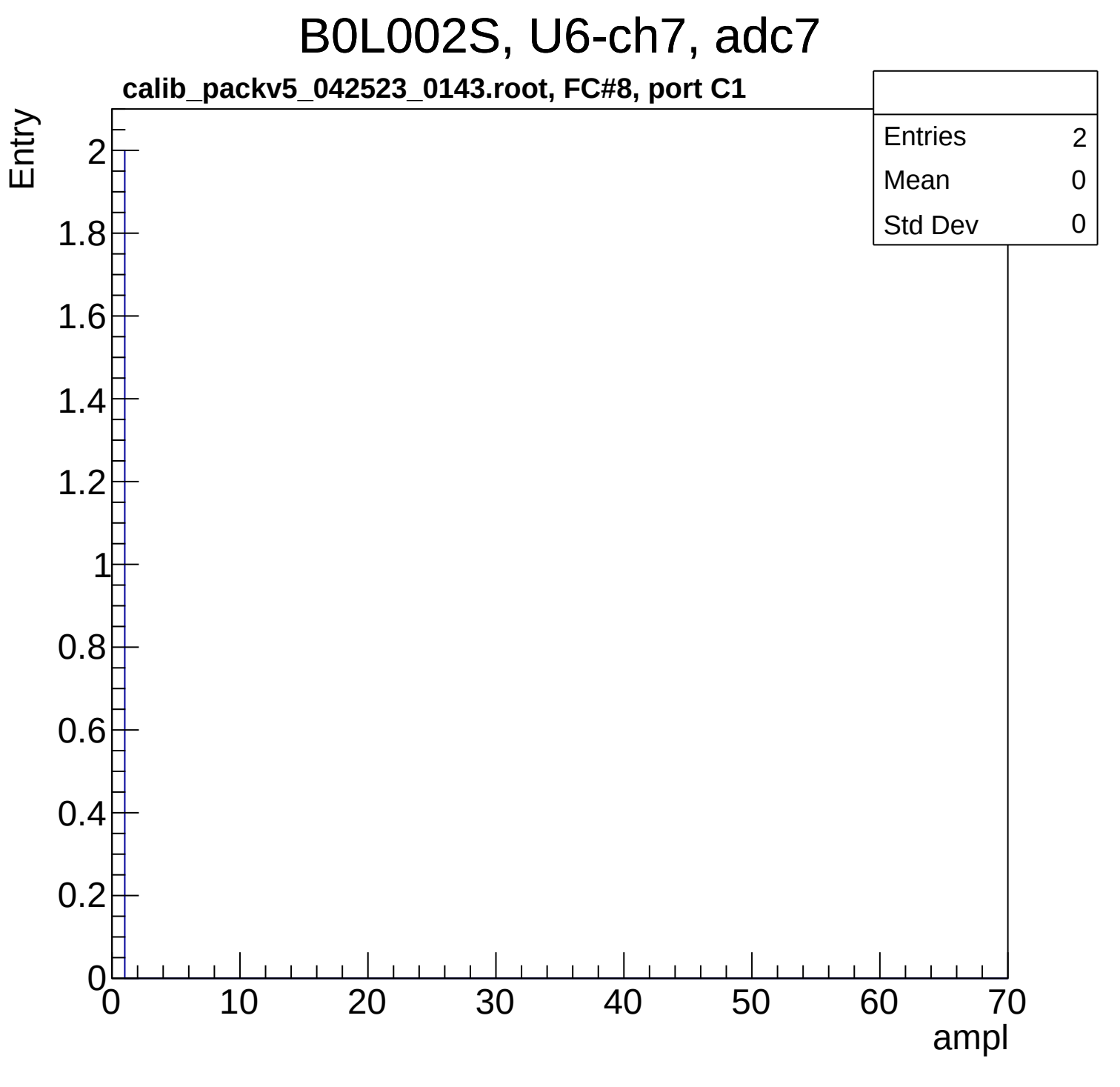
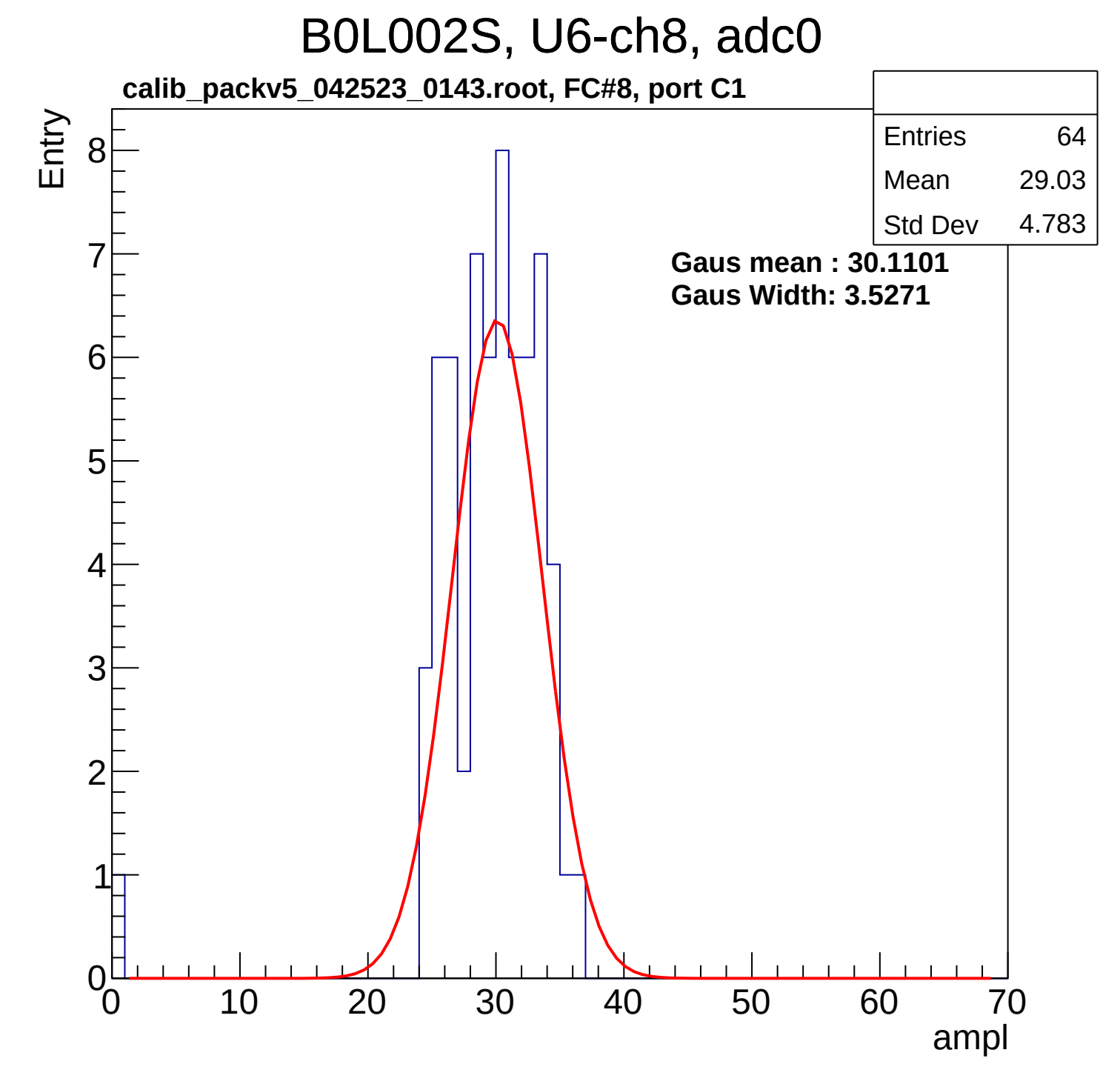

B0L002S, U6-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.03
Std Dev 4.783
Gaus mean : 30.1101
Gaus Width: 3.5271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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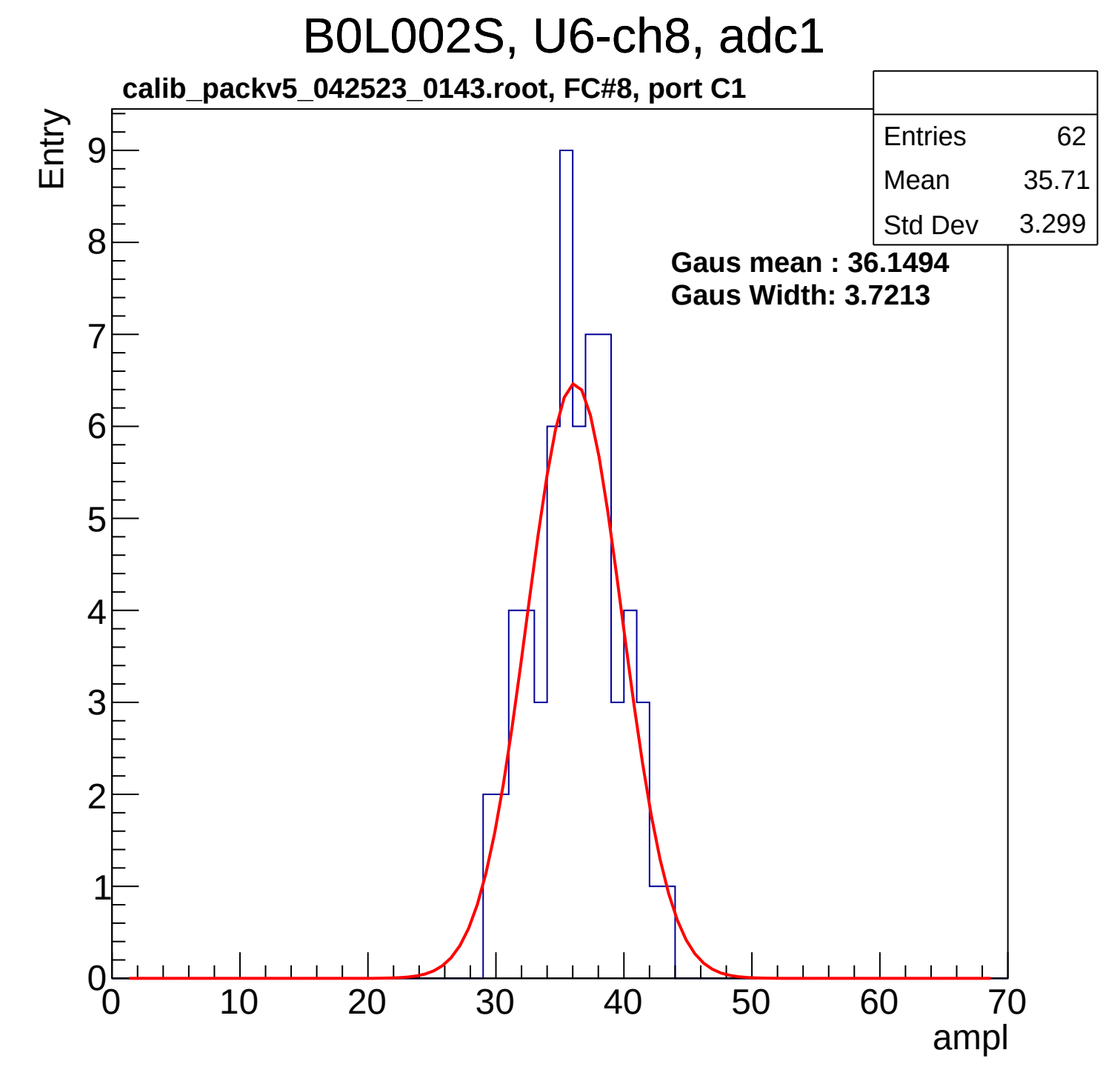
B0L002S, U6-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 35.71
Std Dev 3.299
Gaus mean : 36.1494
Gaus Width: 3.7213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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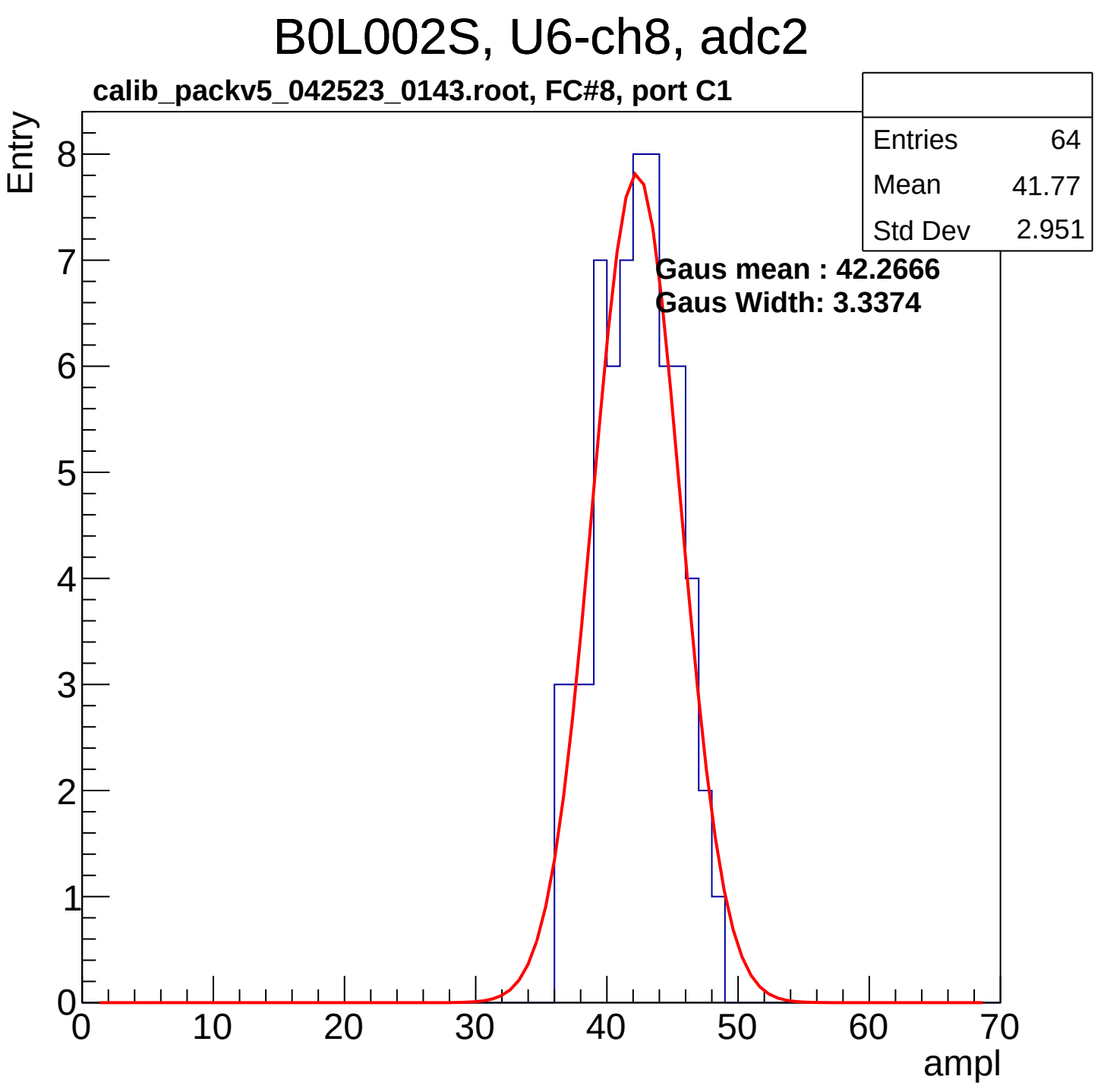
B0L002S, U6-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 41.77
Std Dev 2.951
Gaus mean : 42.2666
Gaus Width: 3.3374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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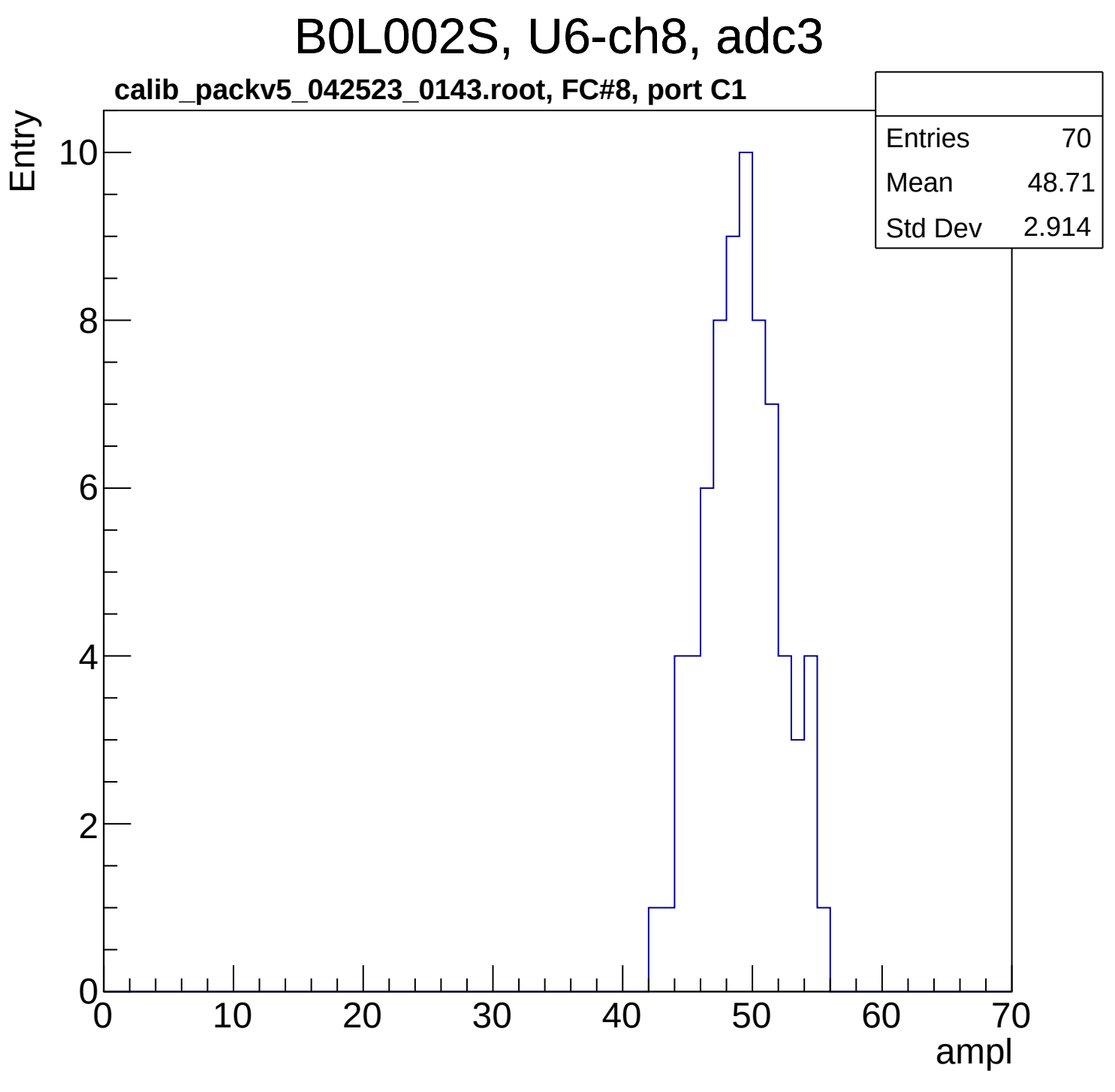
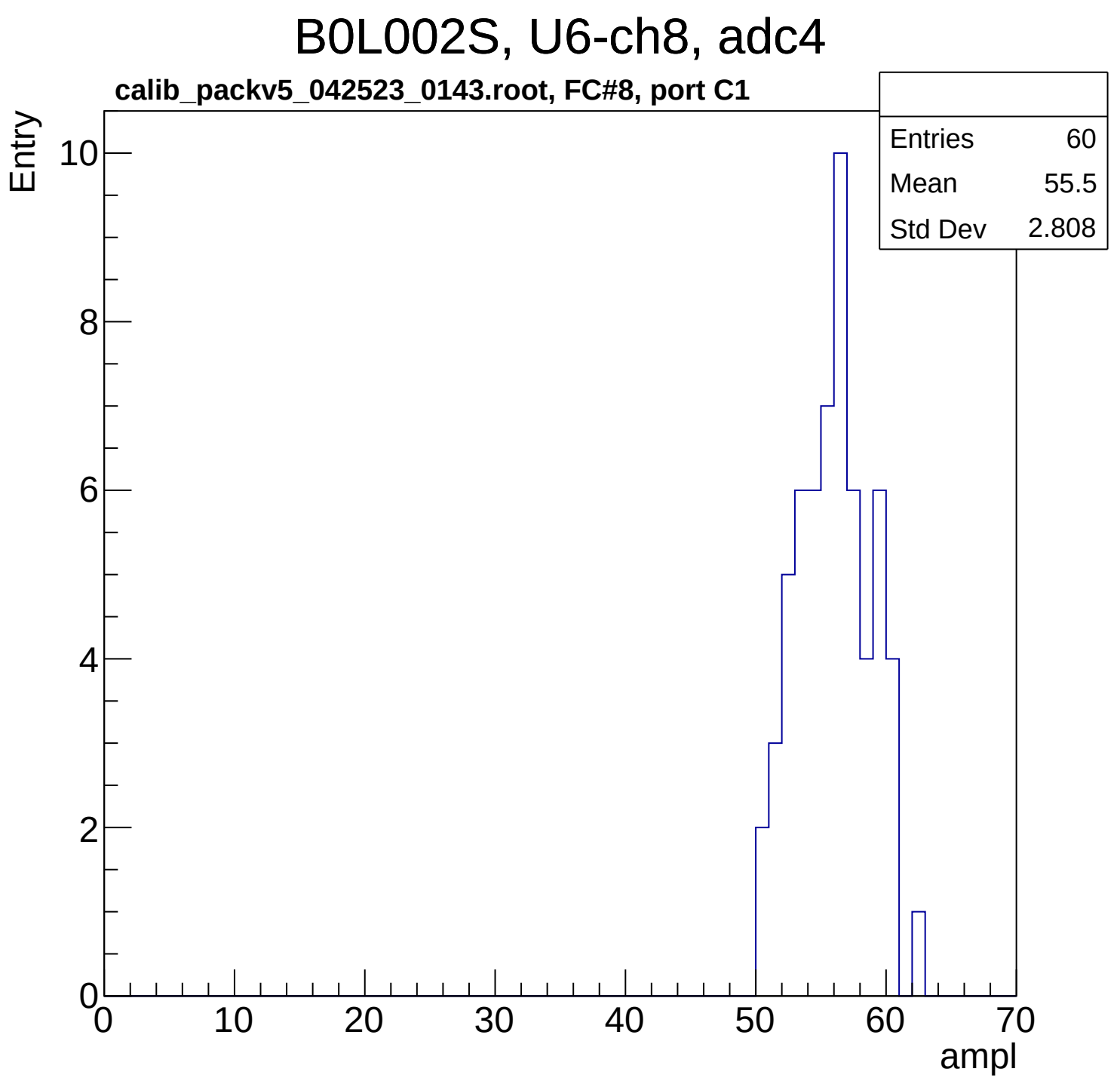

B0L002S, U6-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 48.71
Std Dev 2.914
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.5
Std Dev 2.808
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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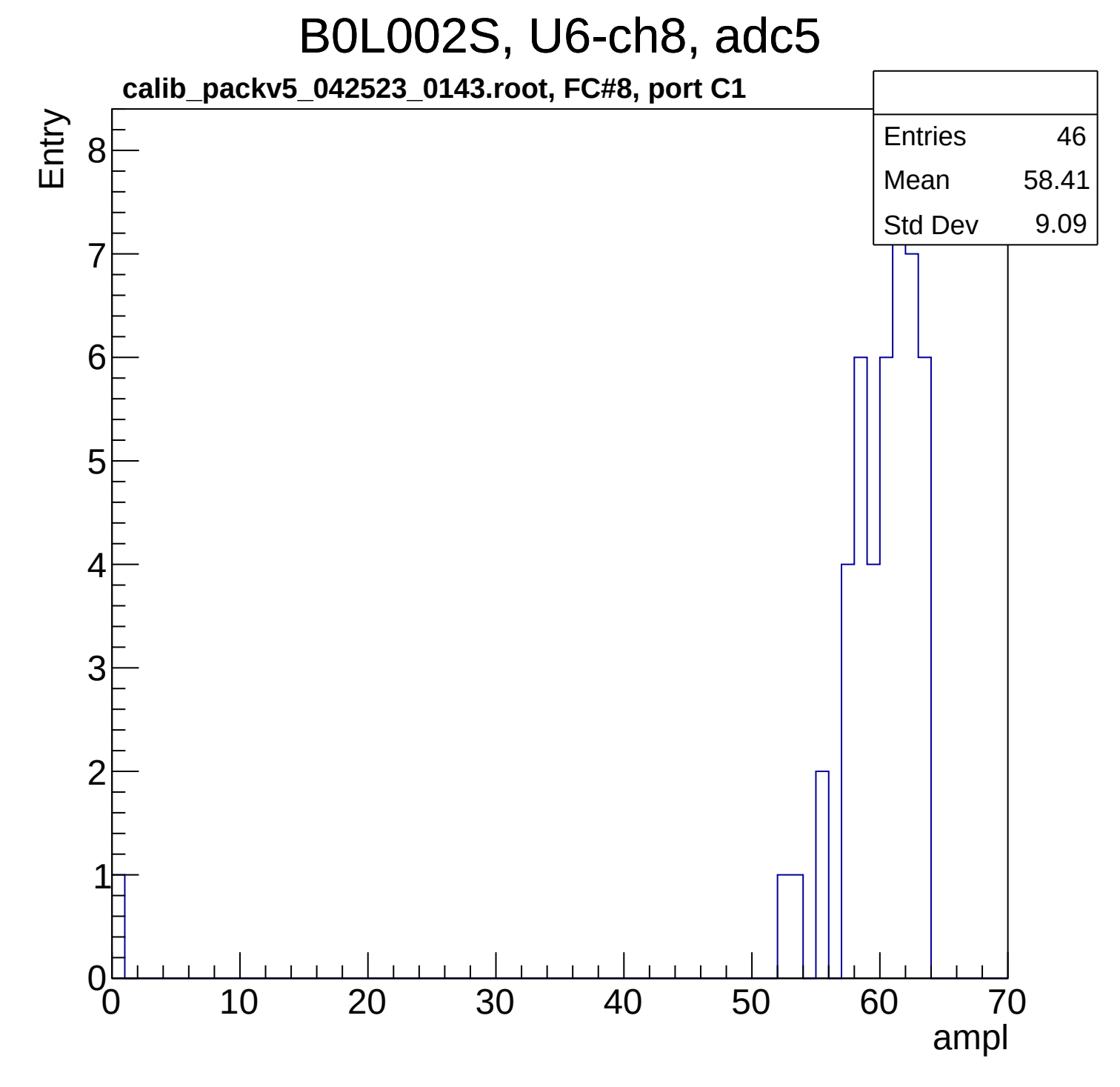
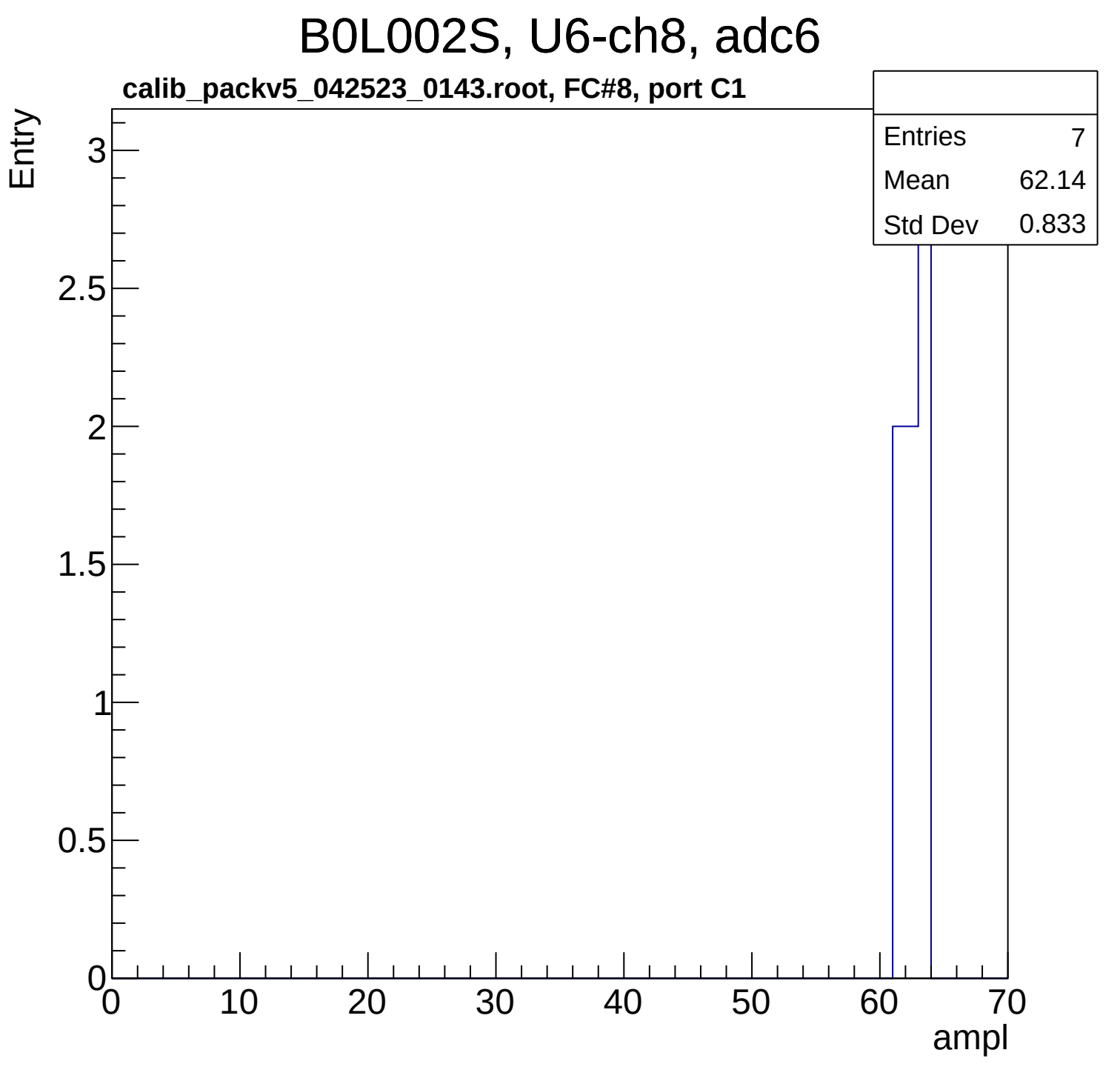
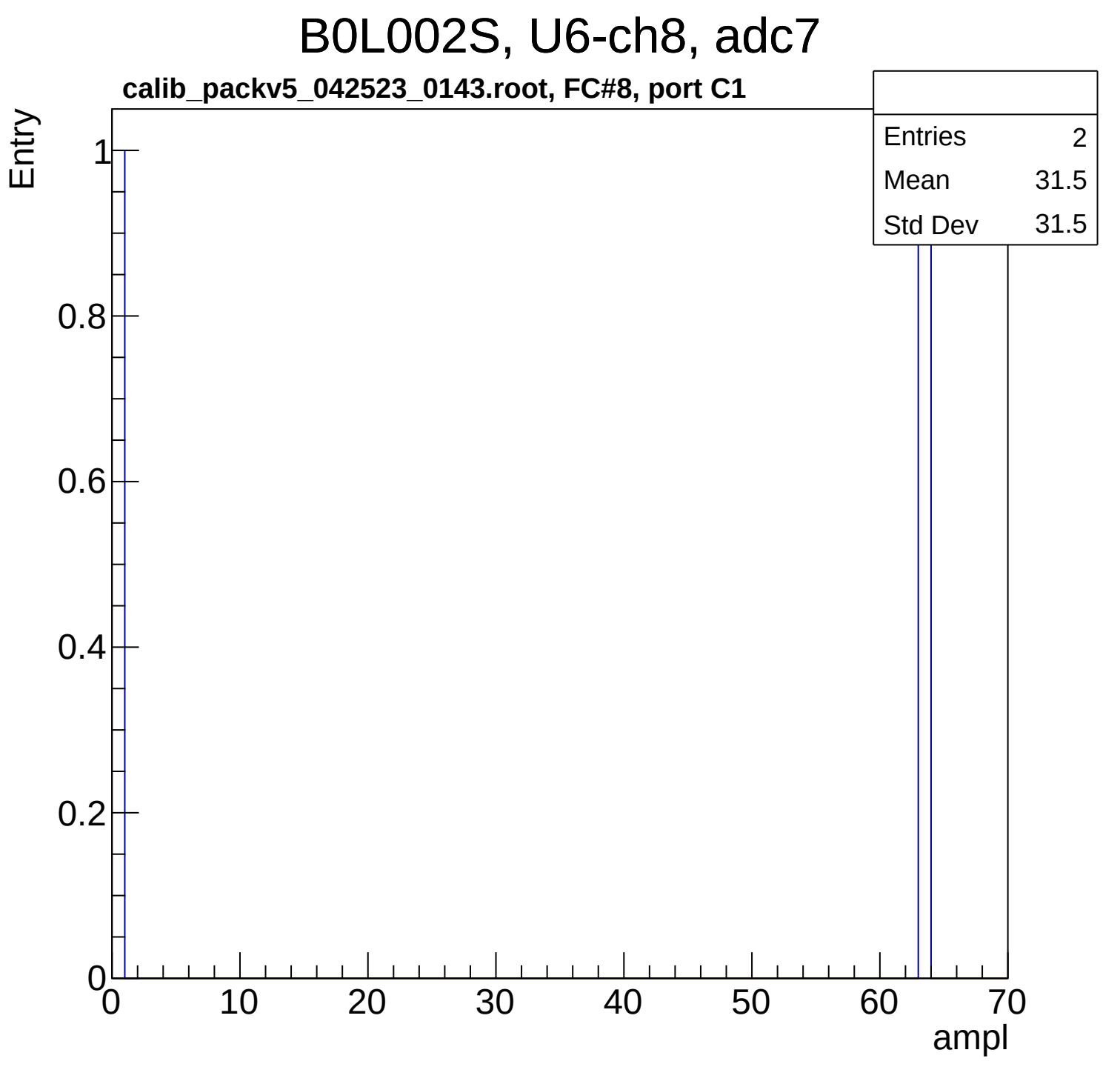
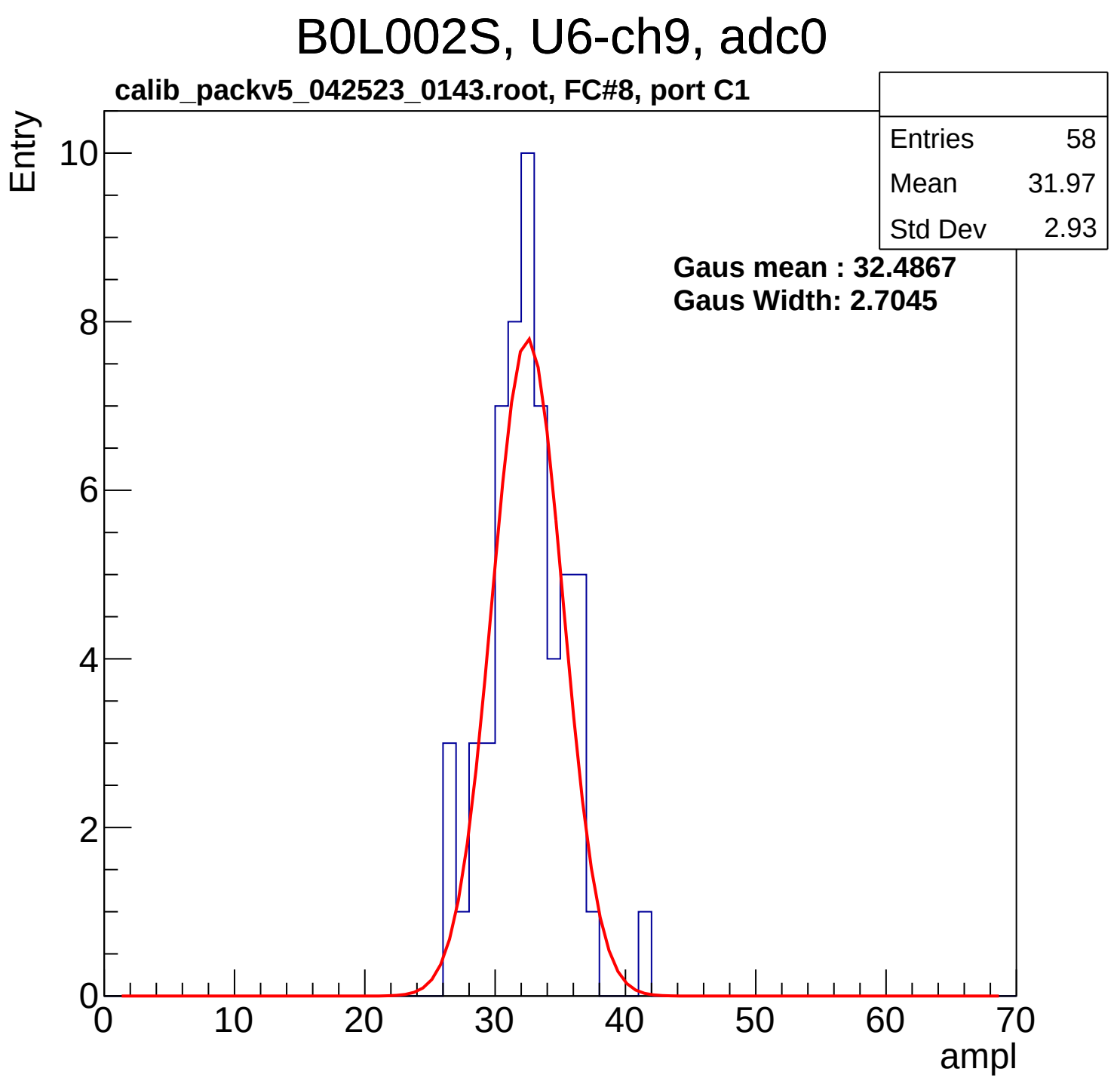
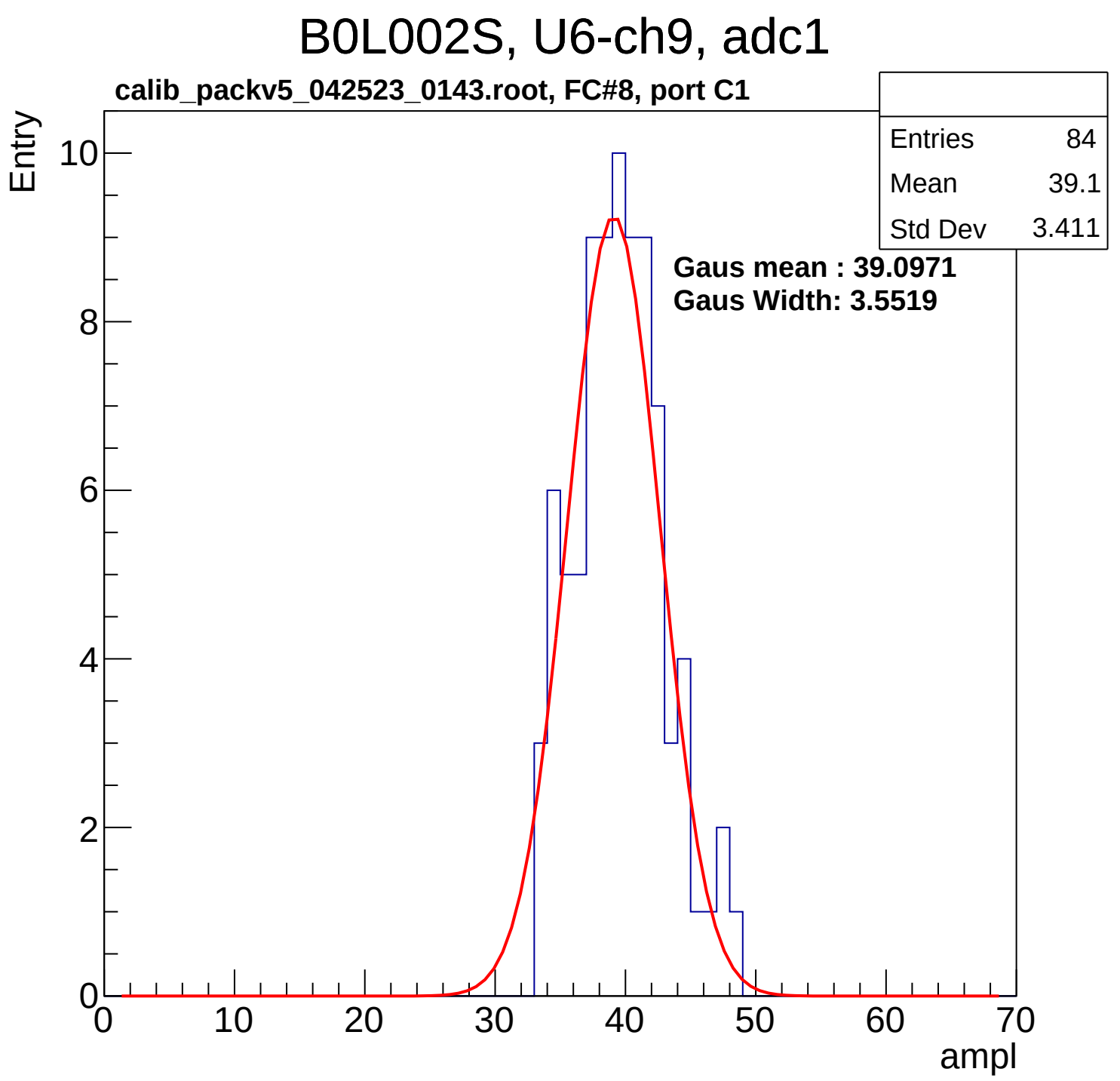
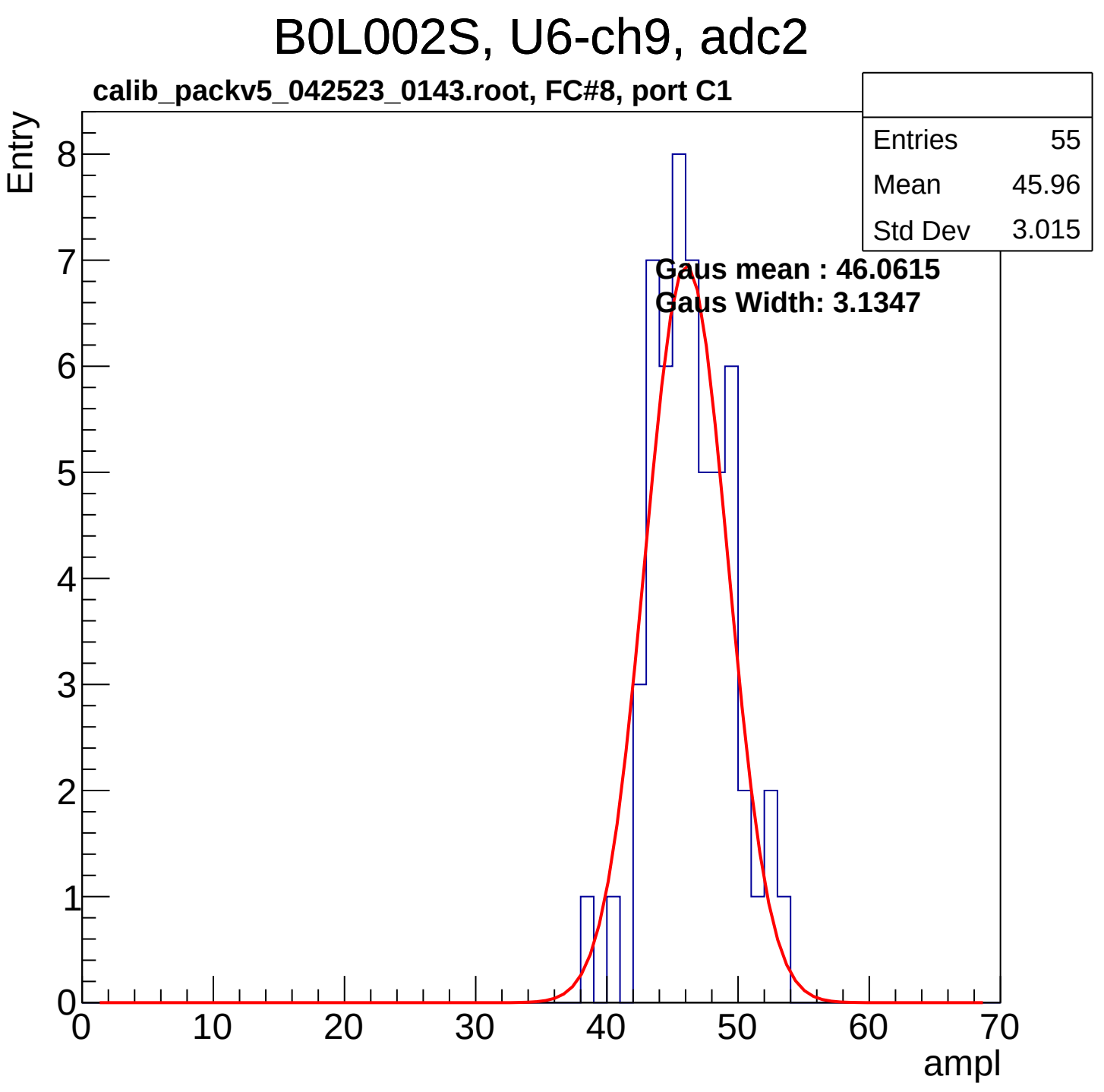

B0L002S, U6-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 58.41
Std Dev 9.09
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

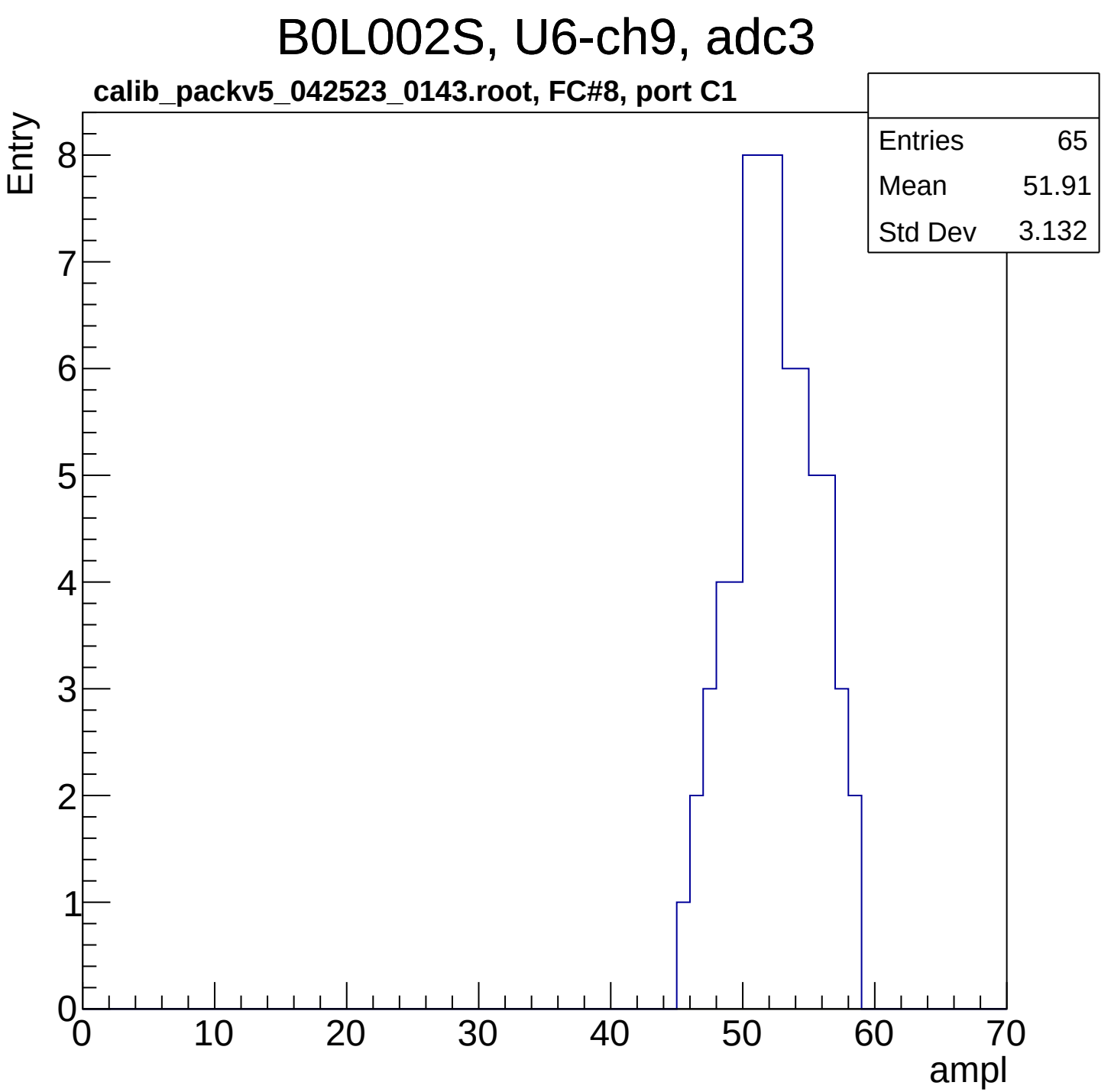

B0L002S, U6-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

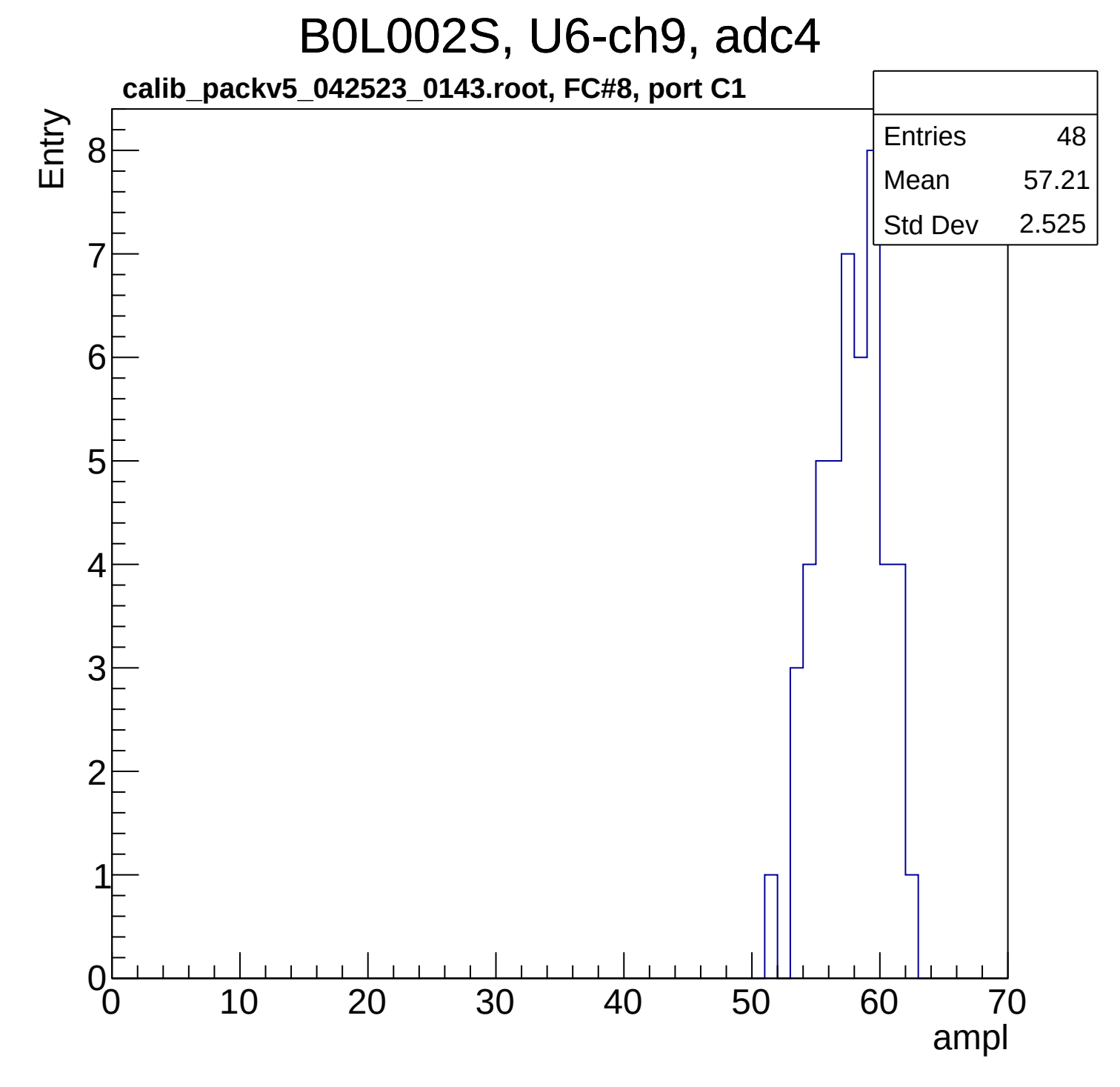

B0L002S, U6-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

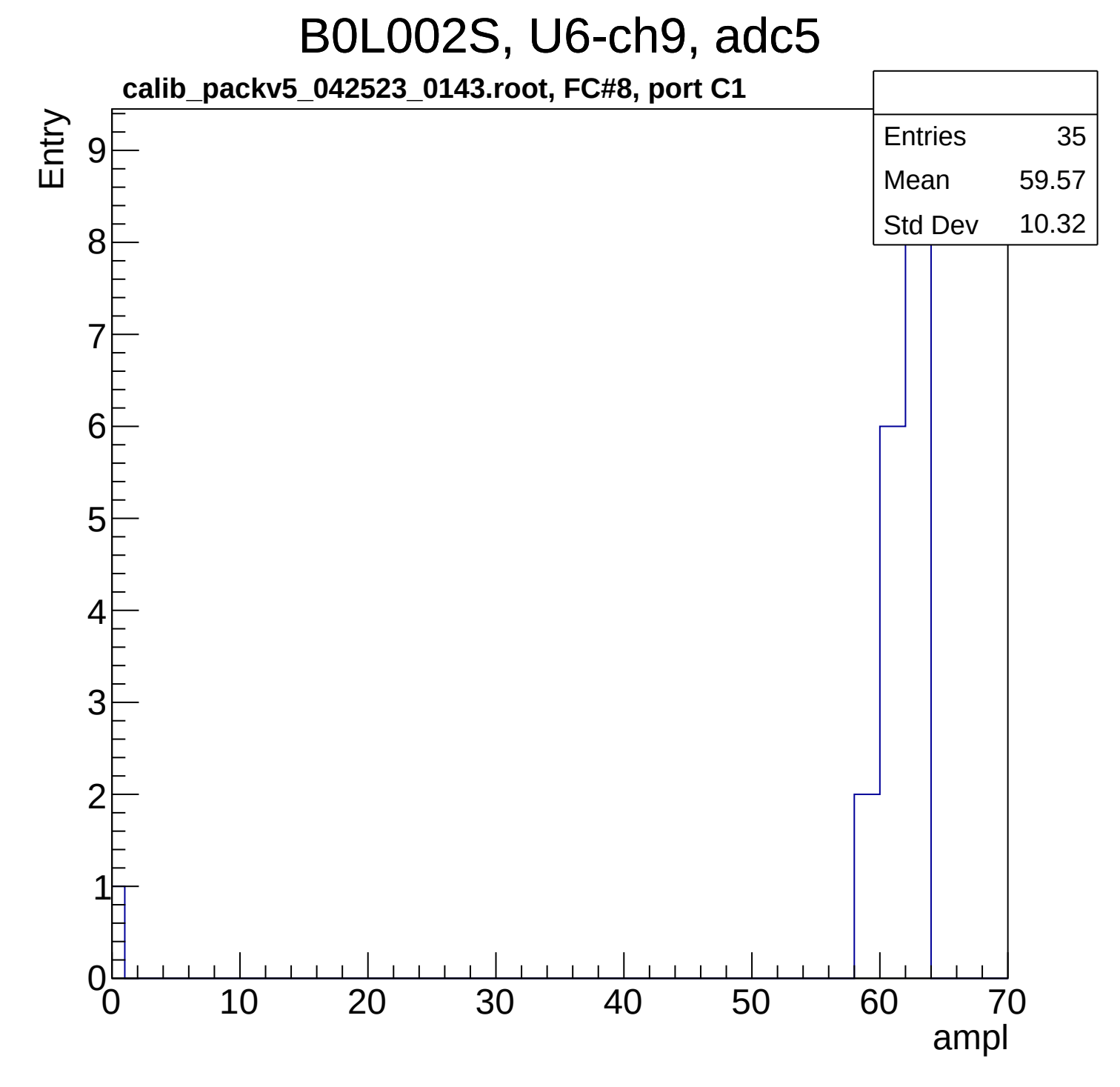

B0L002S, U6-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.97
Std Dev 2.93
Gaus mean : 32.4867
Gaus Width: 2.7045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

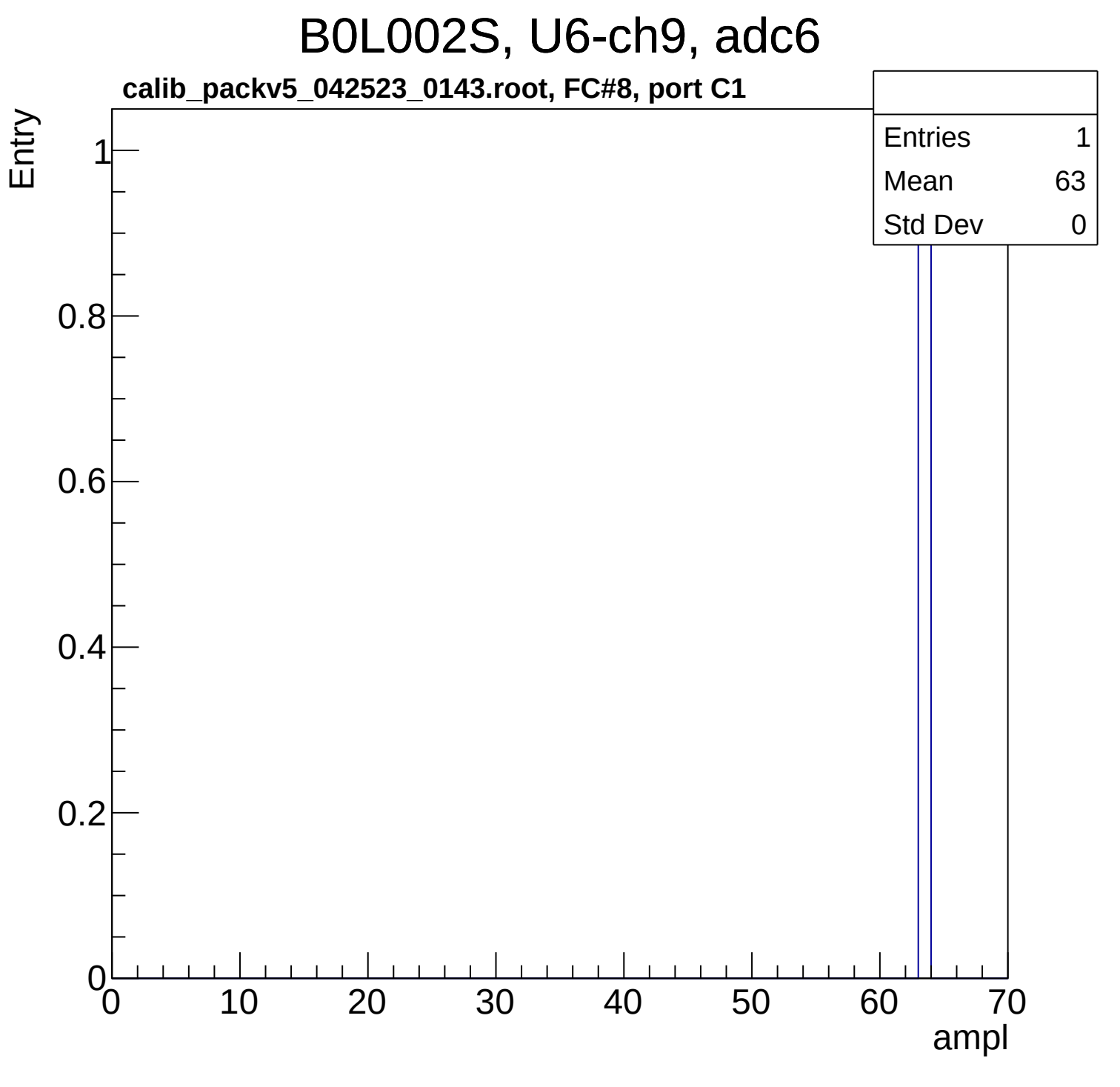

B0L002S, U6-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 39.1
Std Dev 3.411
Gaus mean : 39.0971
Gaus Width: 3.5519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

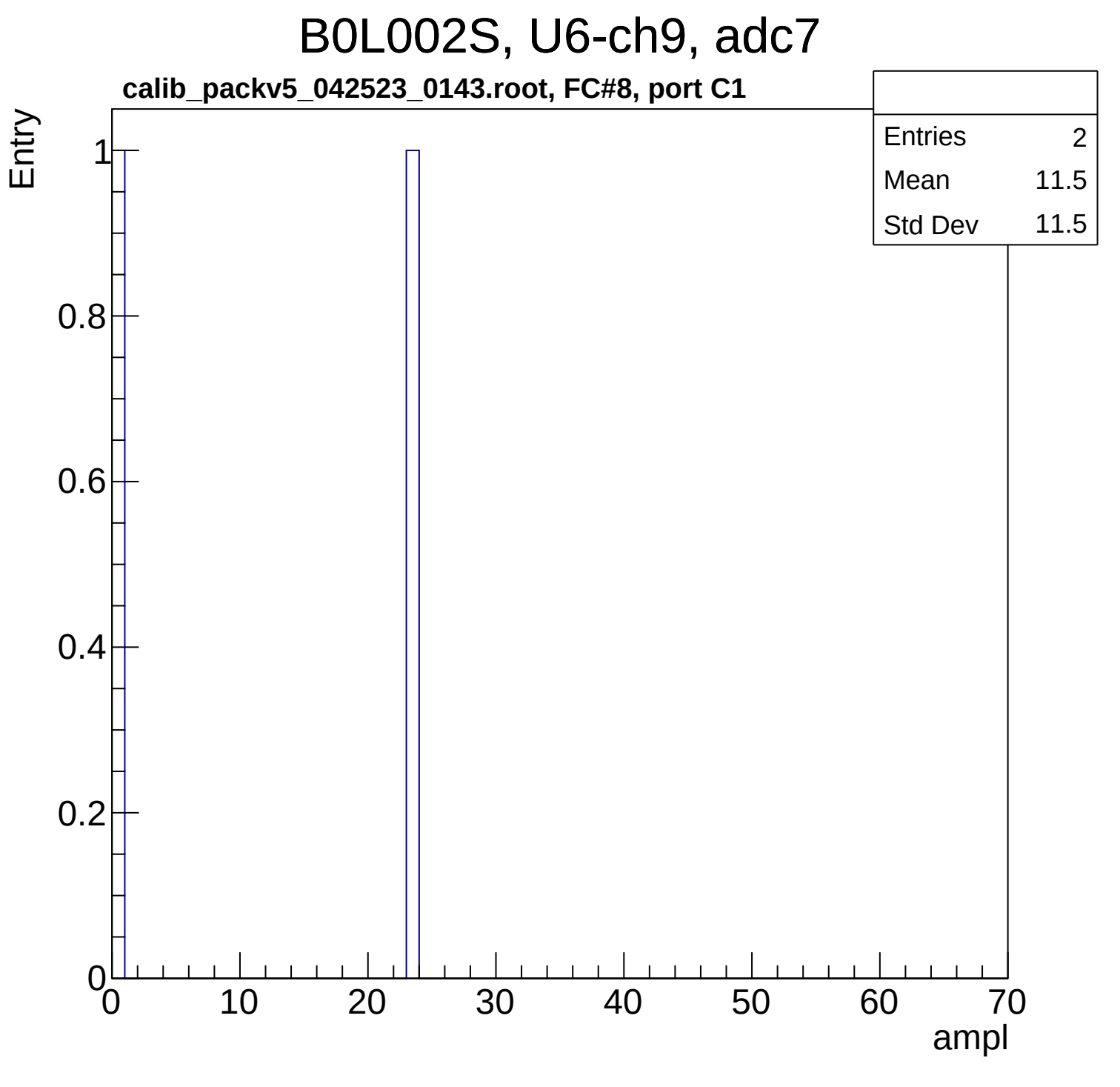

B0L002S, U6-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.96
Std Dev 3.015
Gaus mean : 46.0615
Gaus Width: 3.1347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

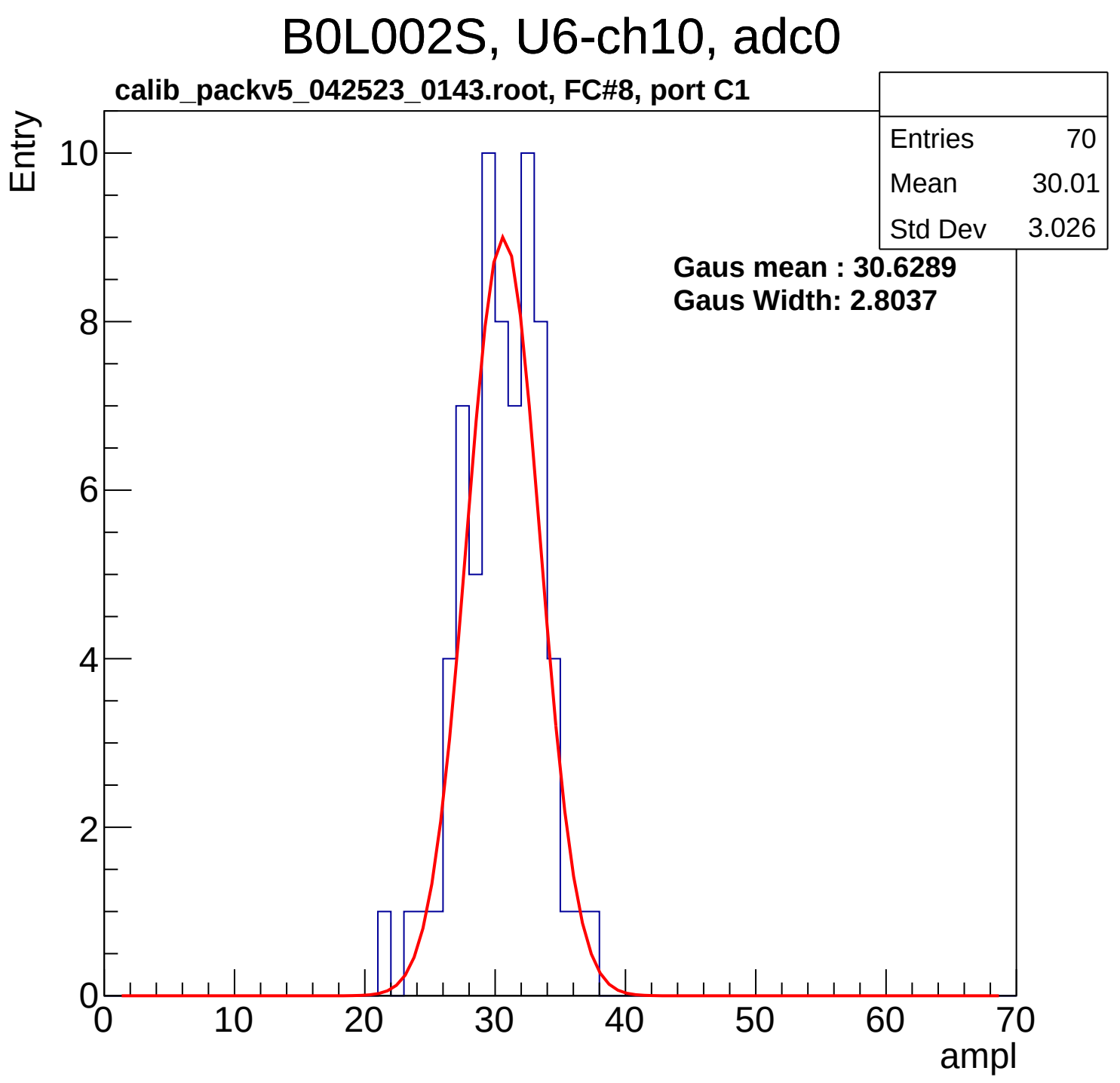

B0L002S, U6-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.91
Std Dev 3.132
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

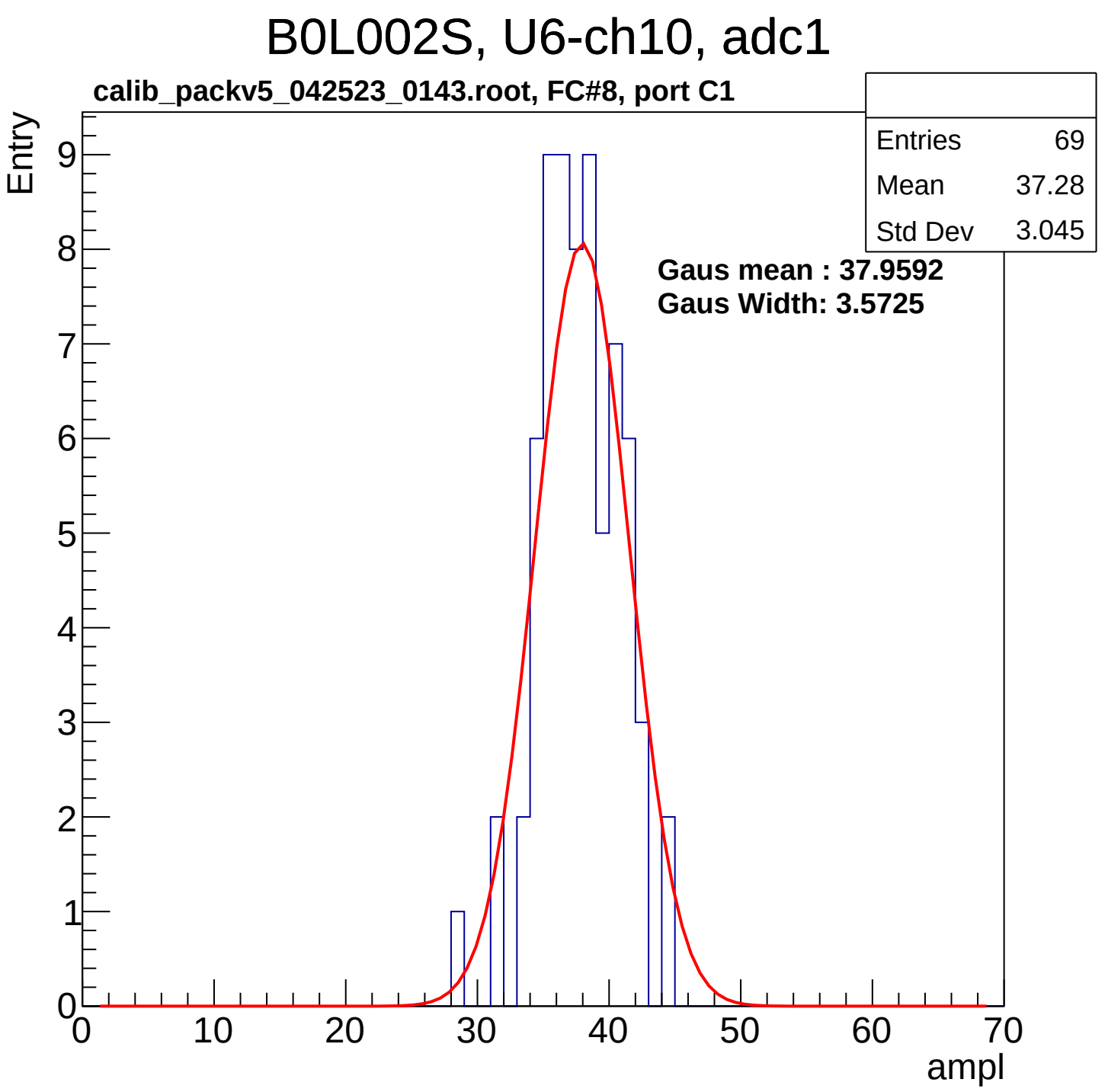

B0L002S, U6-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.21
Std Dev 2.525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.57
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.01
Std Dev 3.026
Gaus mean : 30.6289
Gaus Width: 2.8037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.28
Std Dev 3.045
Gaus mean : 37.9592
Gaus Width: 3.5725
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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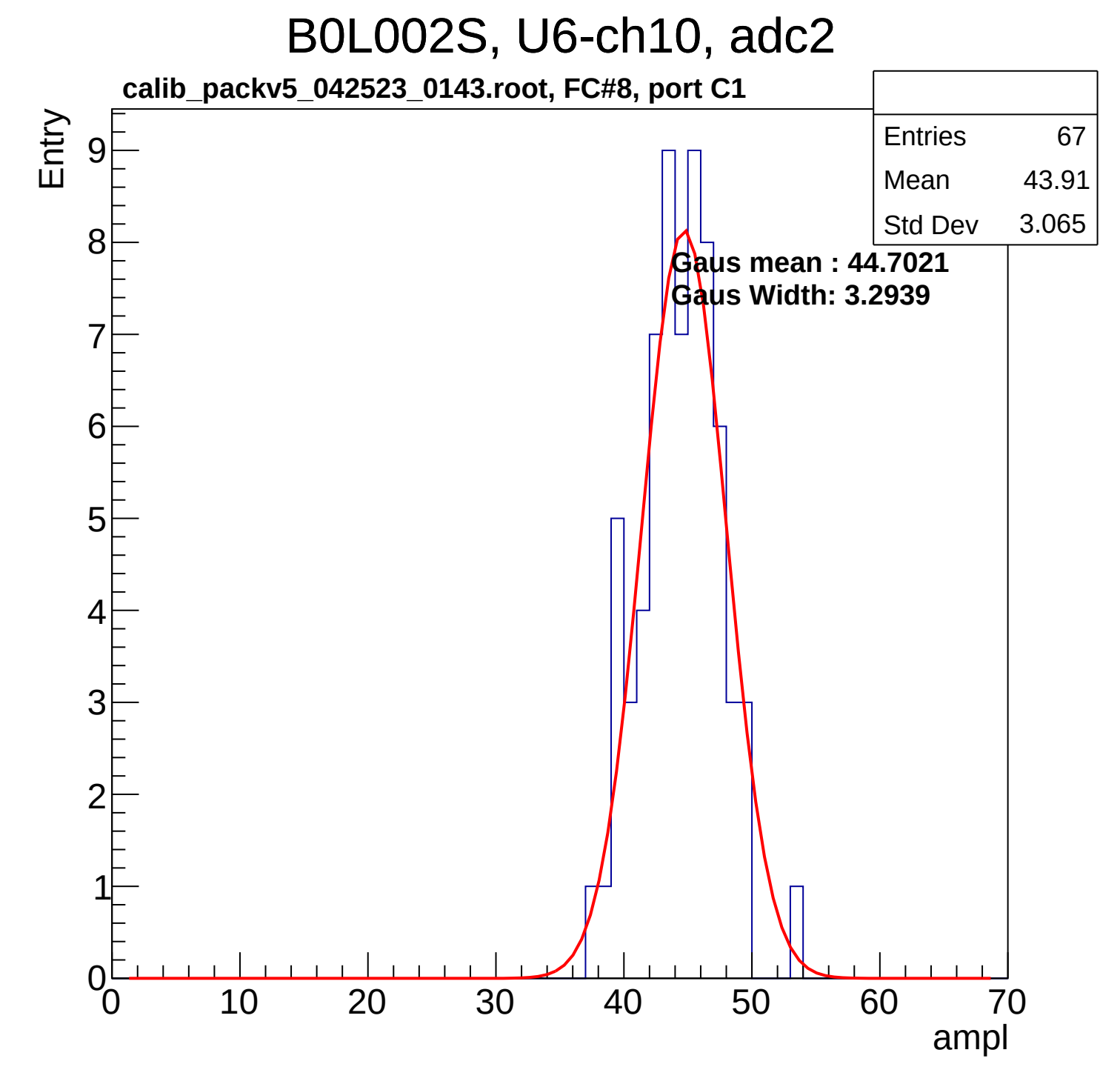
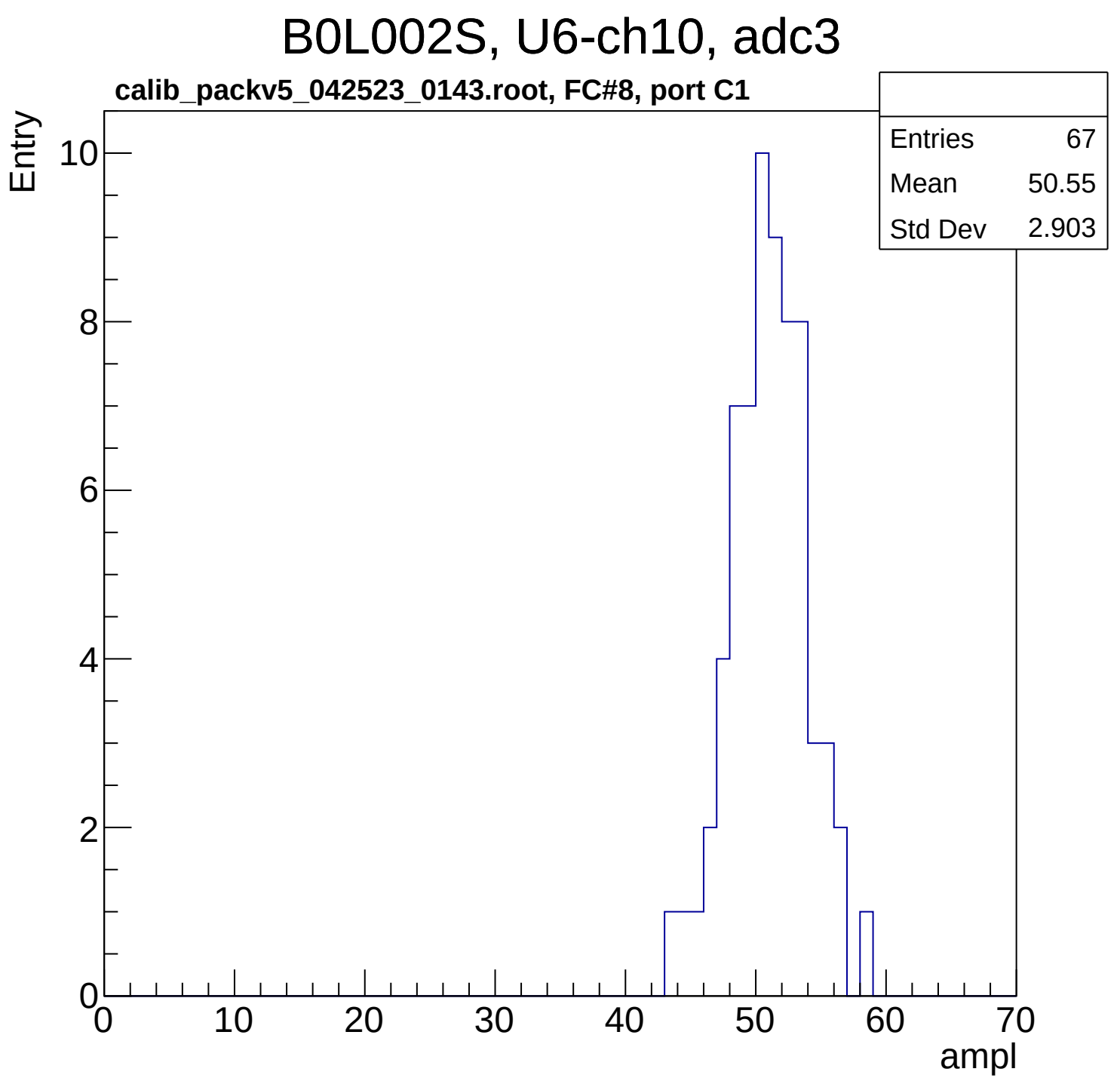

B0L002S, U6-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.91
Std Dev 3.065
Gaus mean : 44.7021
Gaus Width: 3.2939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.55
Std Dev 2.903
Entry
ampl
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4
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8
10
0
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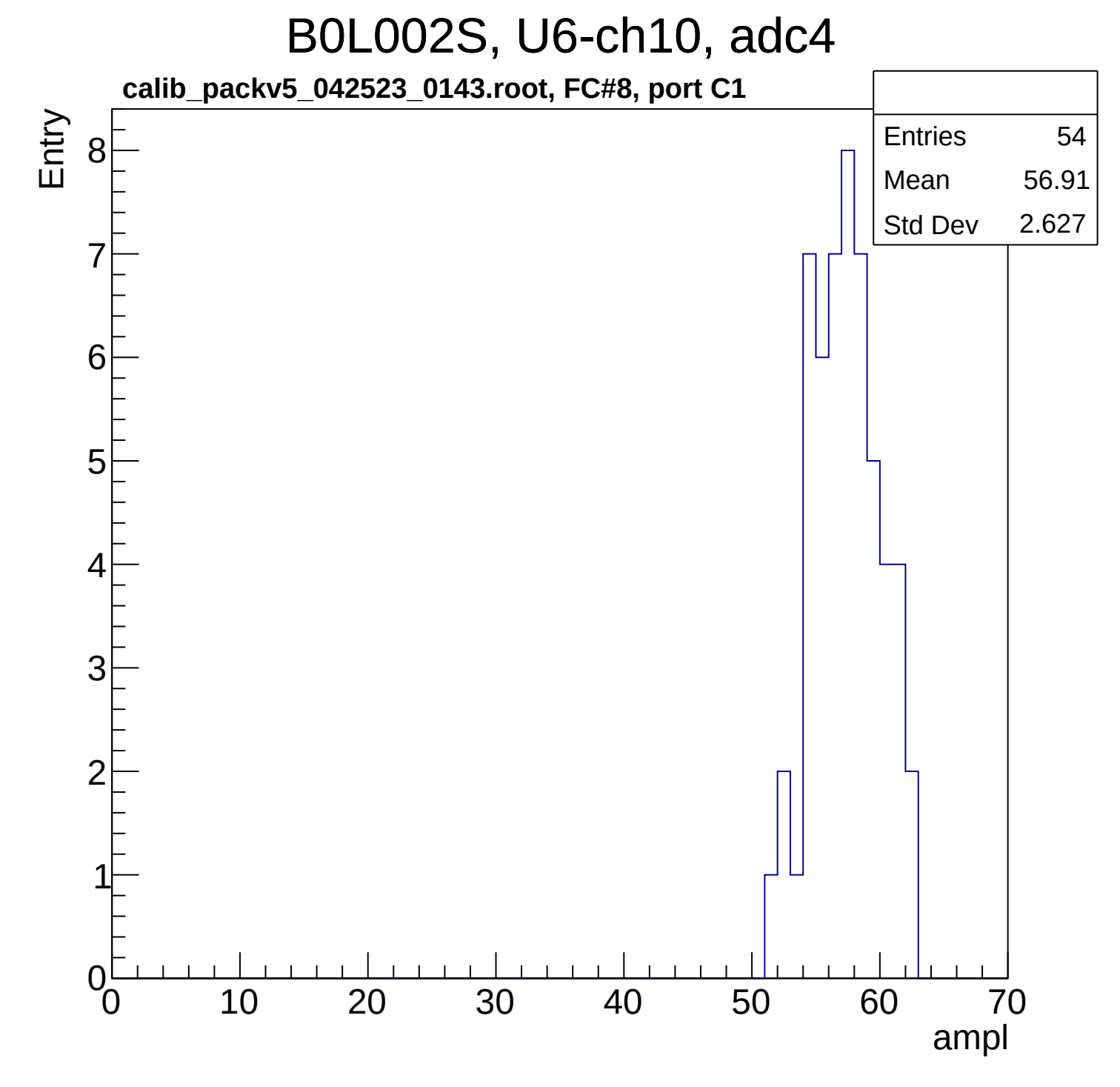
20
30
40
50
60
70


B0L002S, U6-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.91
Std Dev 2.627
Entry
ampl
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2
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4
5
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7
8
0
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20
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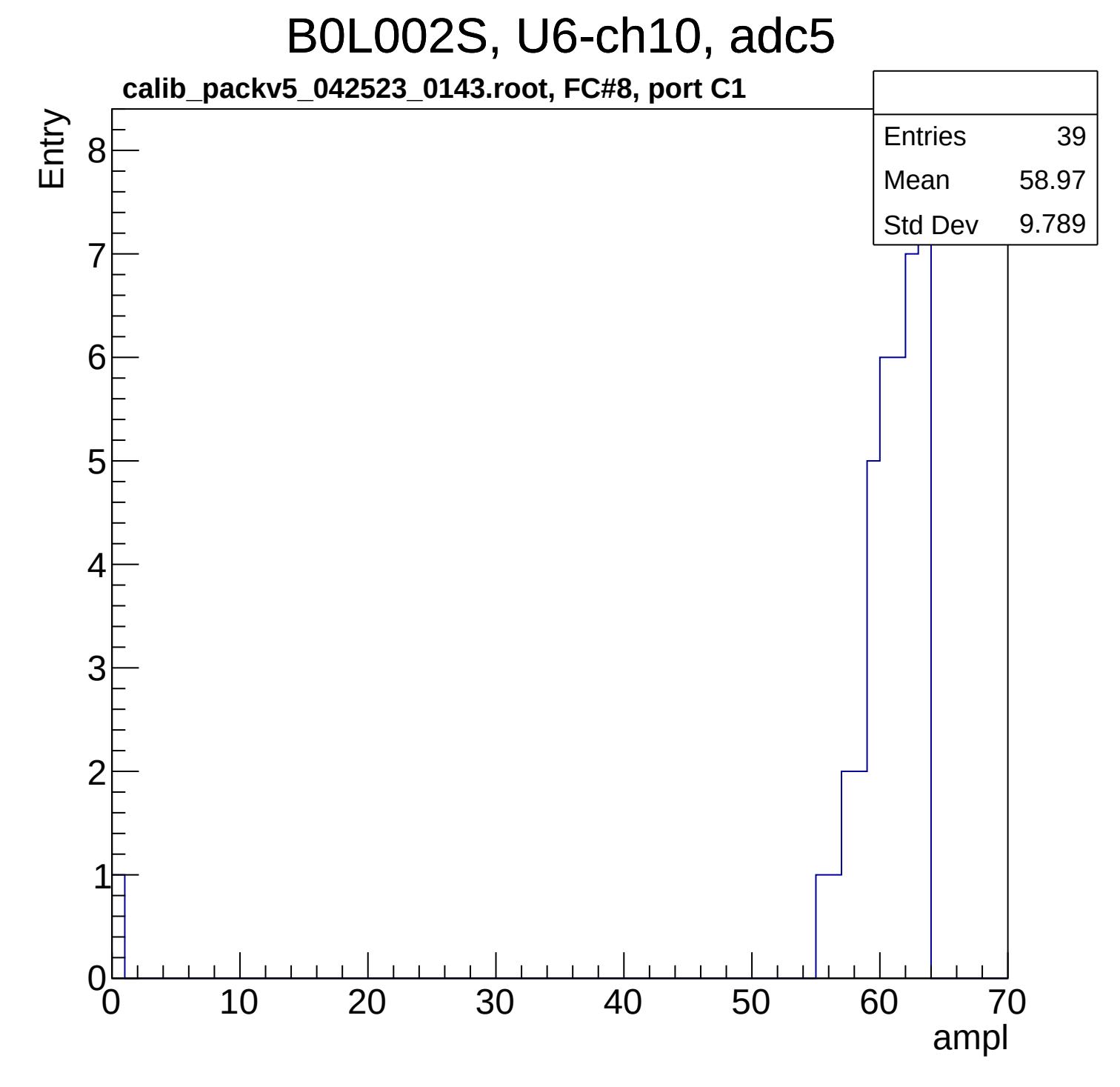
60
70


B0L002S, U6-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.97
Std Dev 9.789
Entry
ampl
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7
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0
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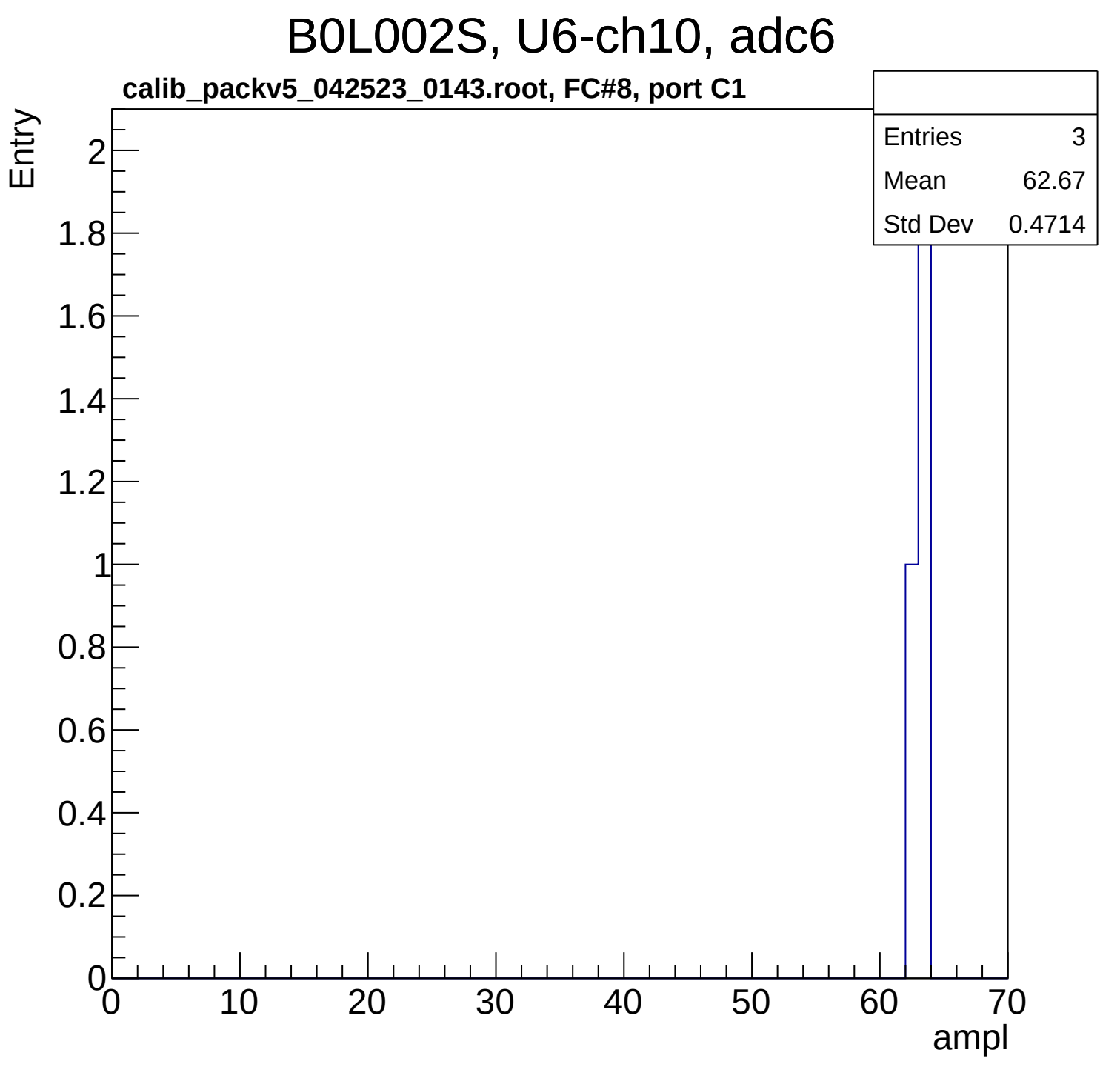
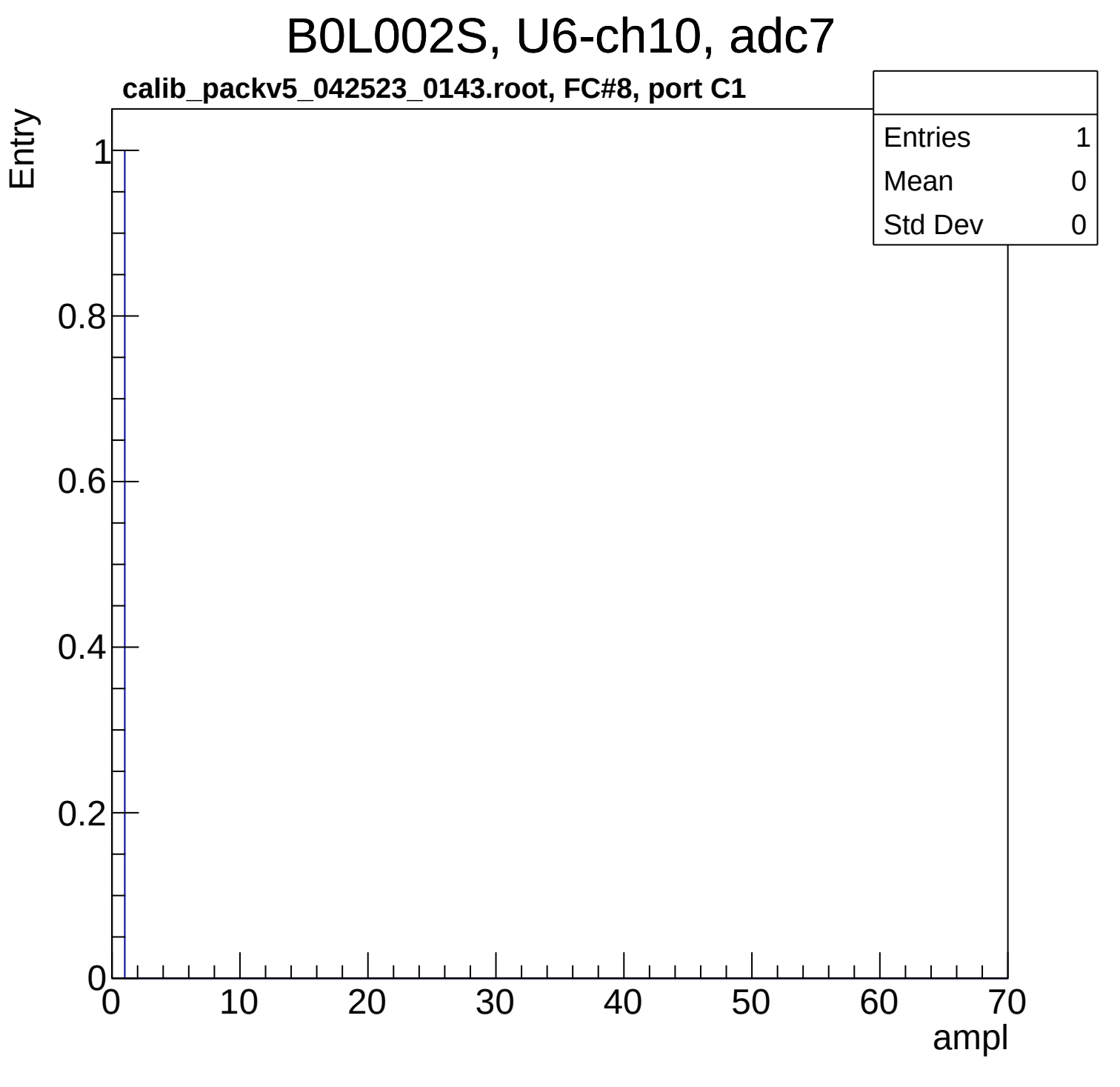
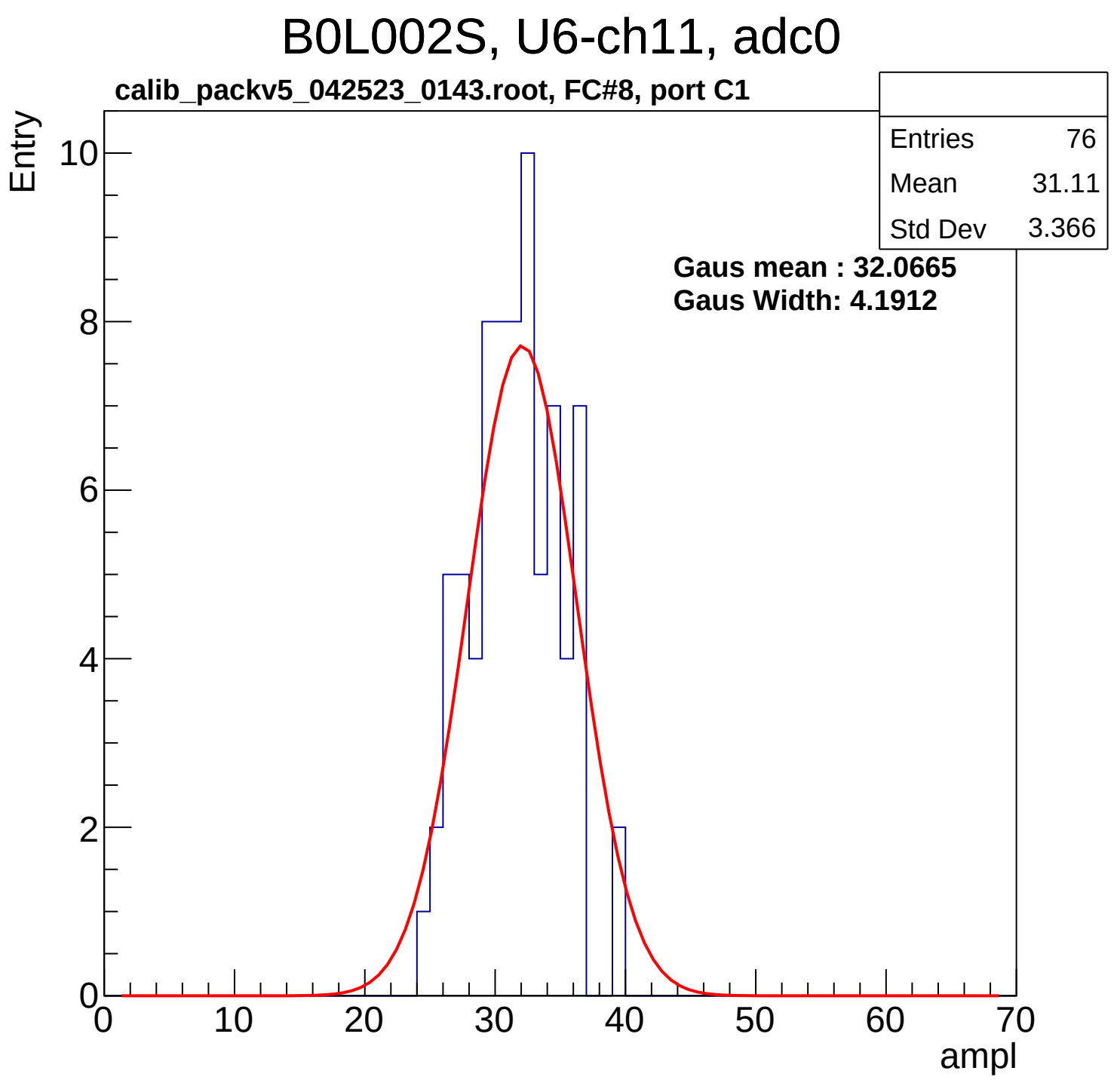
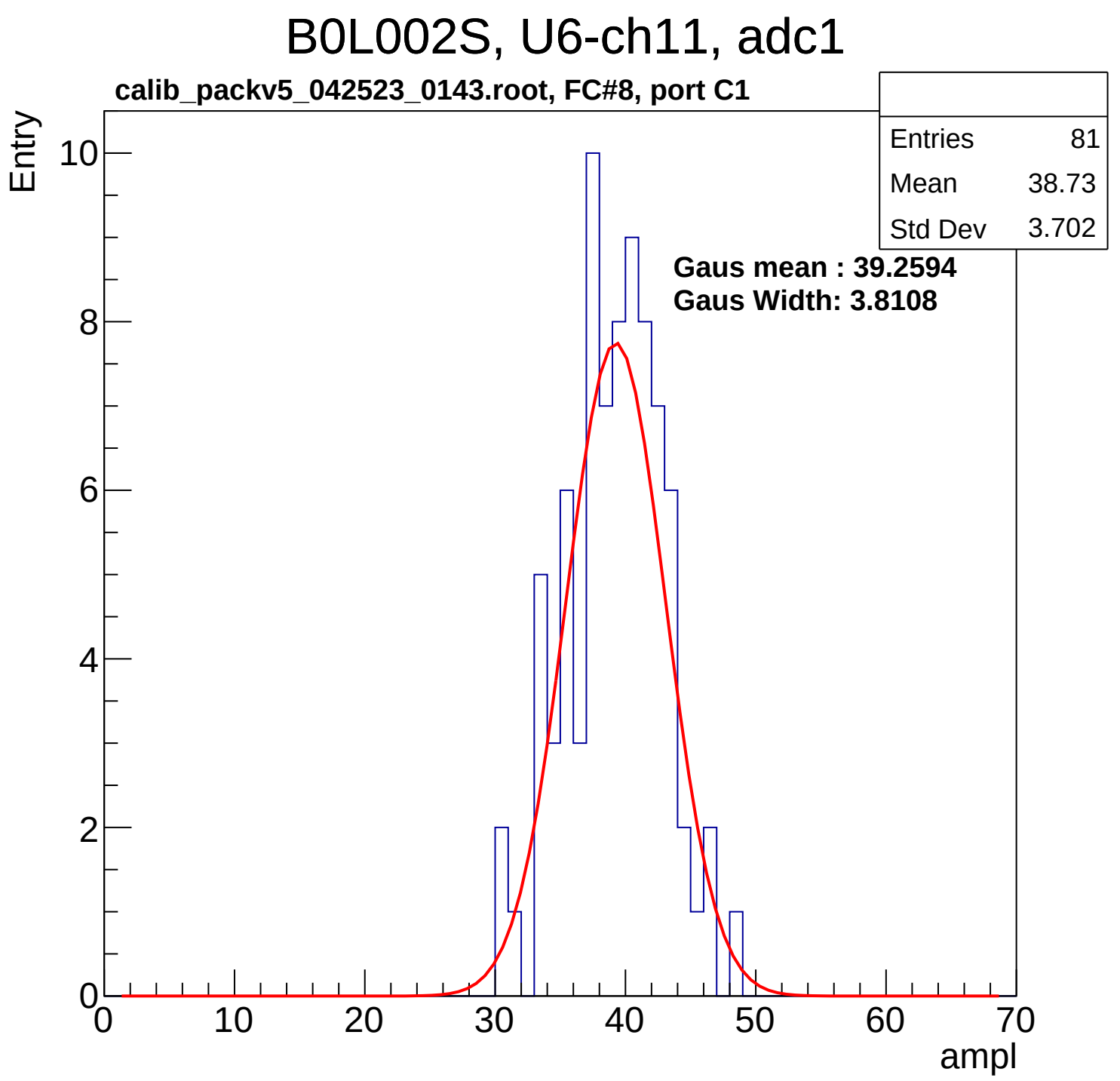
40
50
60
70


B0L002S, U6-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 31.11
Std Dev 3.366
Gaus mean : 32.0665
Gaus Width: 4.1912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 38.73
Std Dev 3.702
Gaus mean : 39.2594
Gaus Width: 3.8108
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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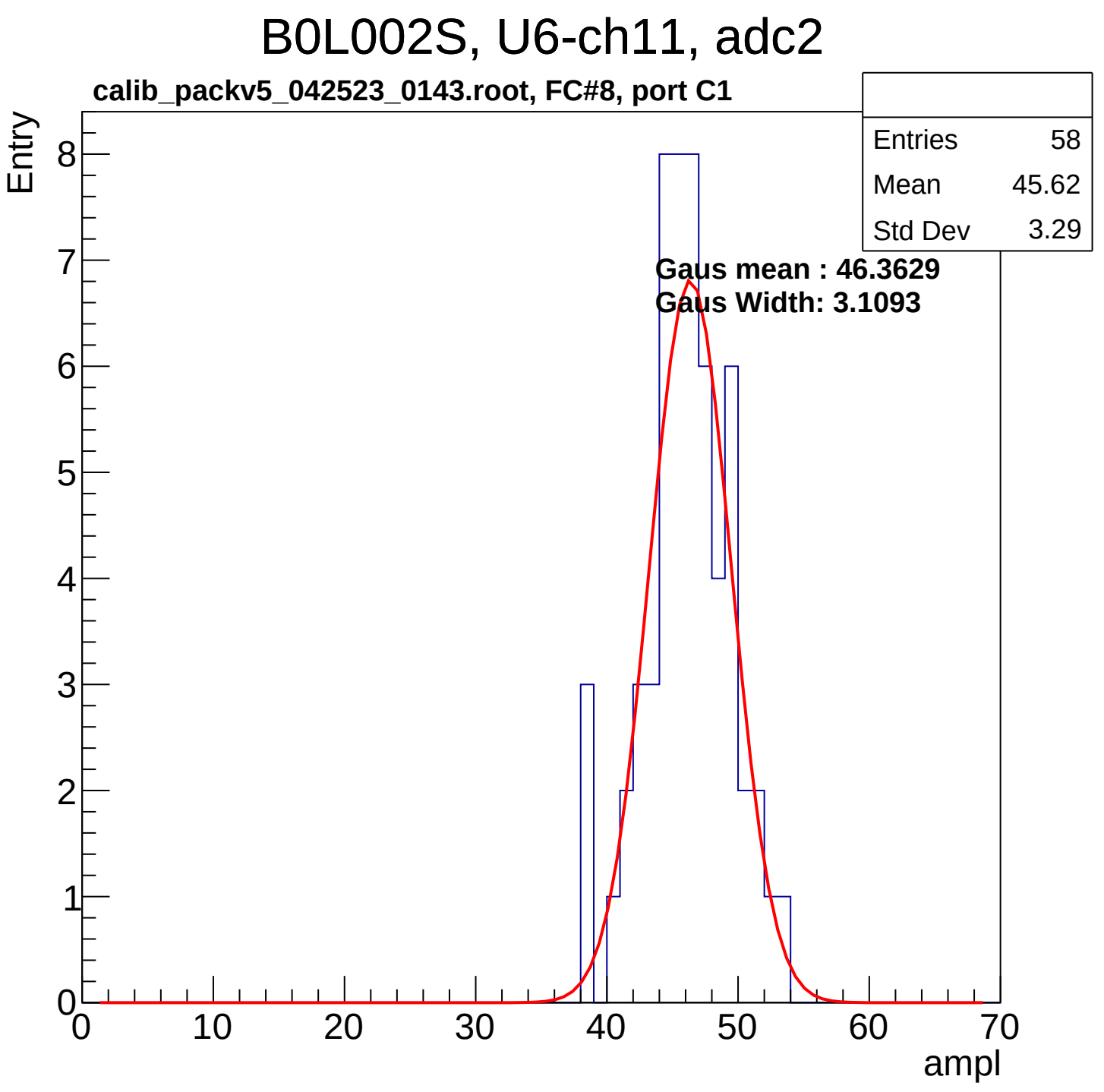
B0L002S, U6-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 45.62
Std Dev 3.29
Gaus mean : 46.3629
Gaus Width: 3.1093
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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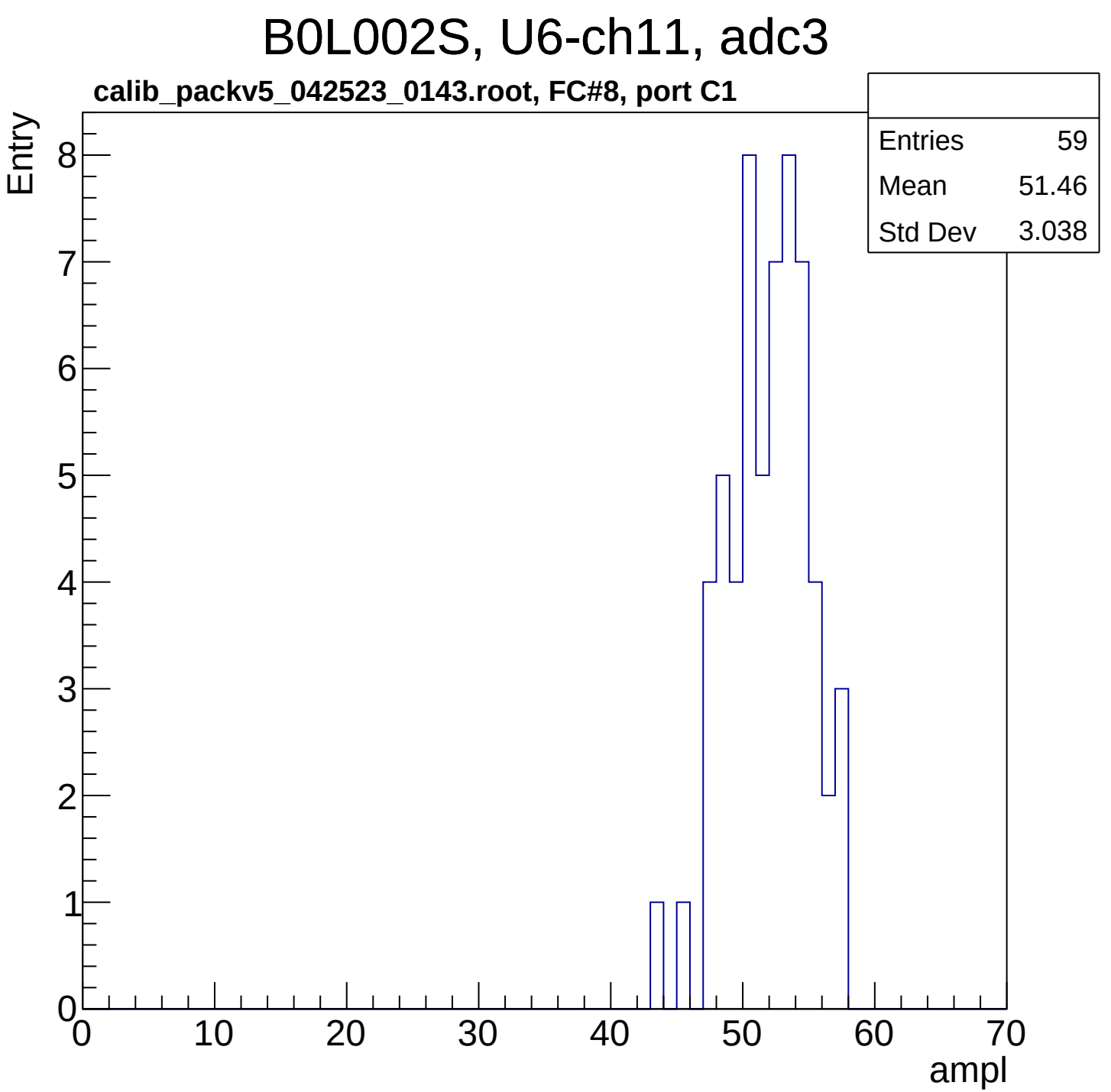
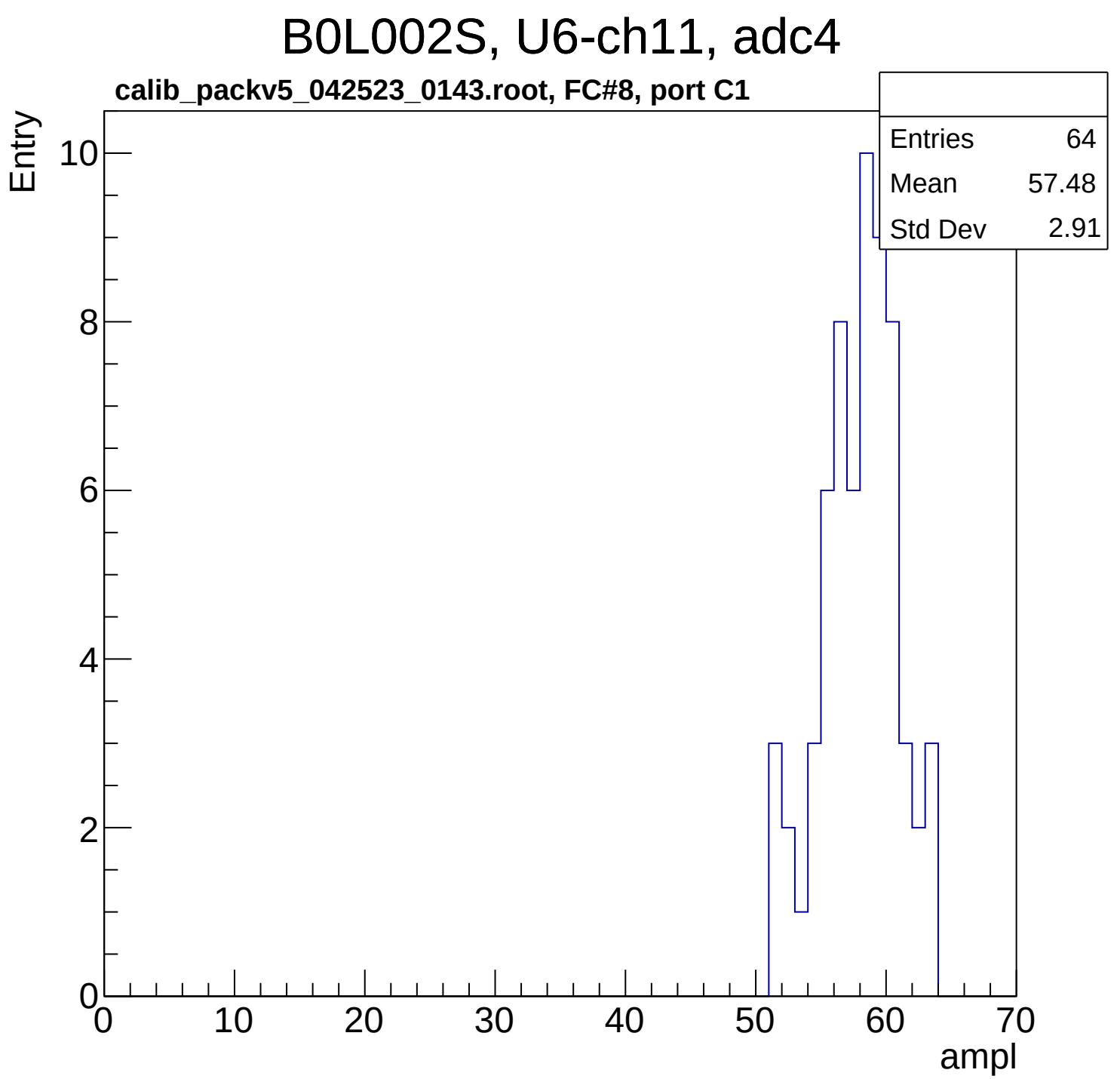

B0L002S, U6-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.46
Std Dev 3.038
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 57.48
Std Dev 2.91
Entry
0
2
4
6
8
10
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10
20
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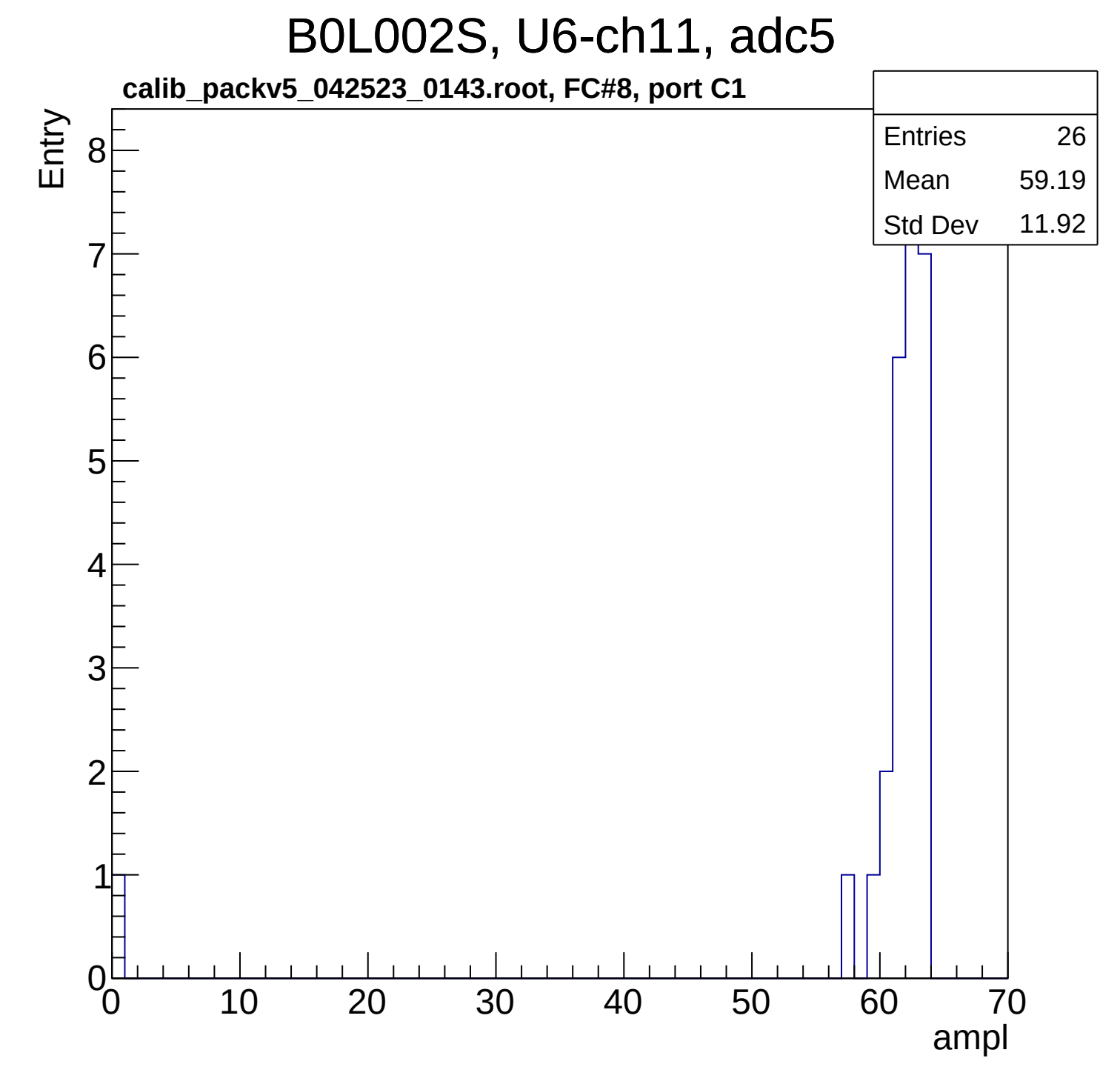
50
60
70
ampl


B0L002S, U6-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59.19
Std Dev 11.92
Entry
ampl
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1
2
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4
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60
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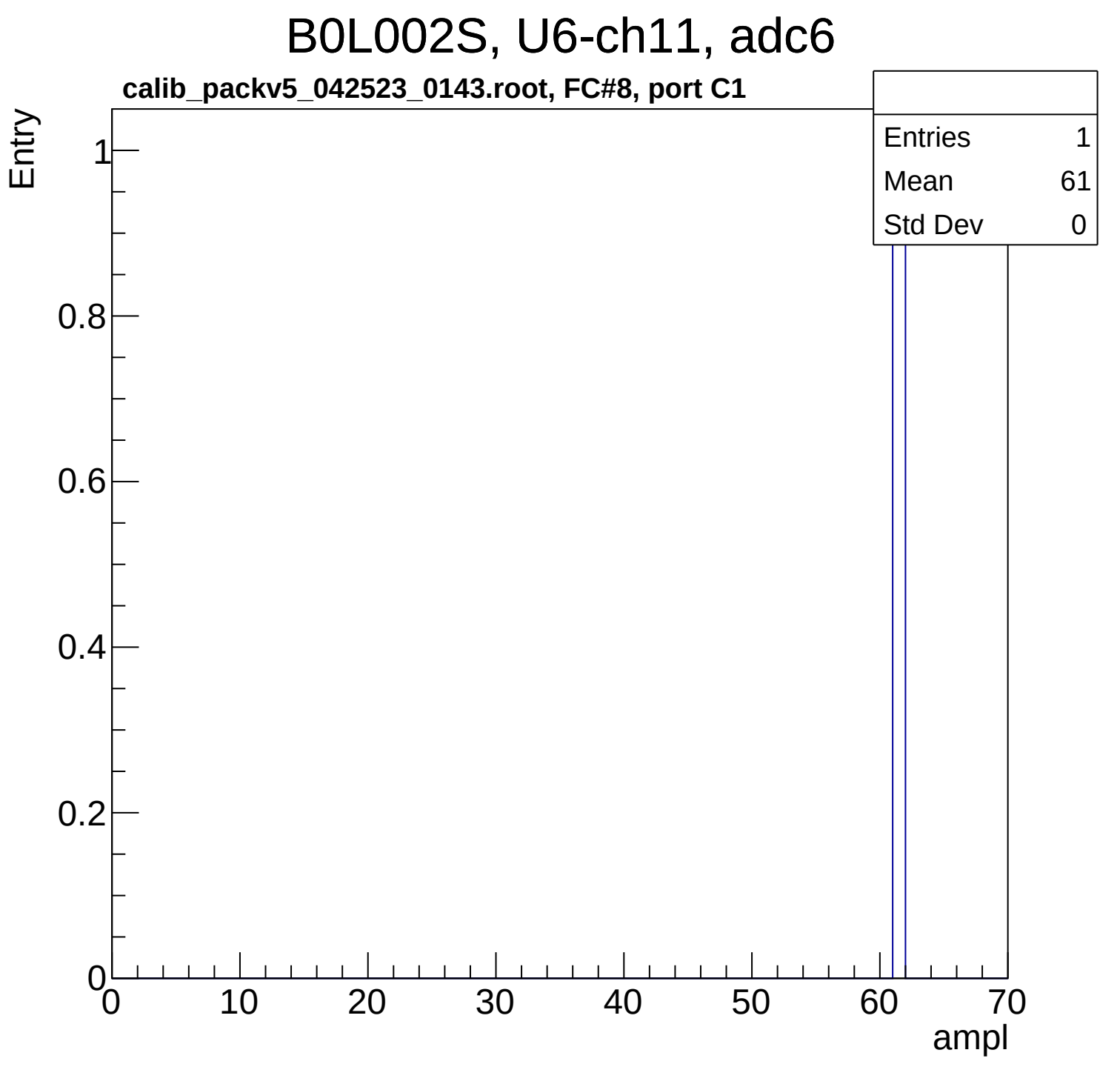
B0L002S, U6-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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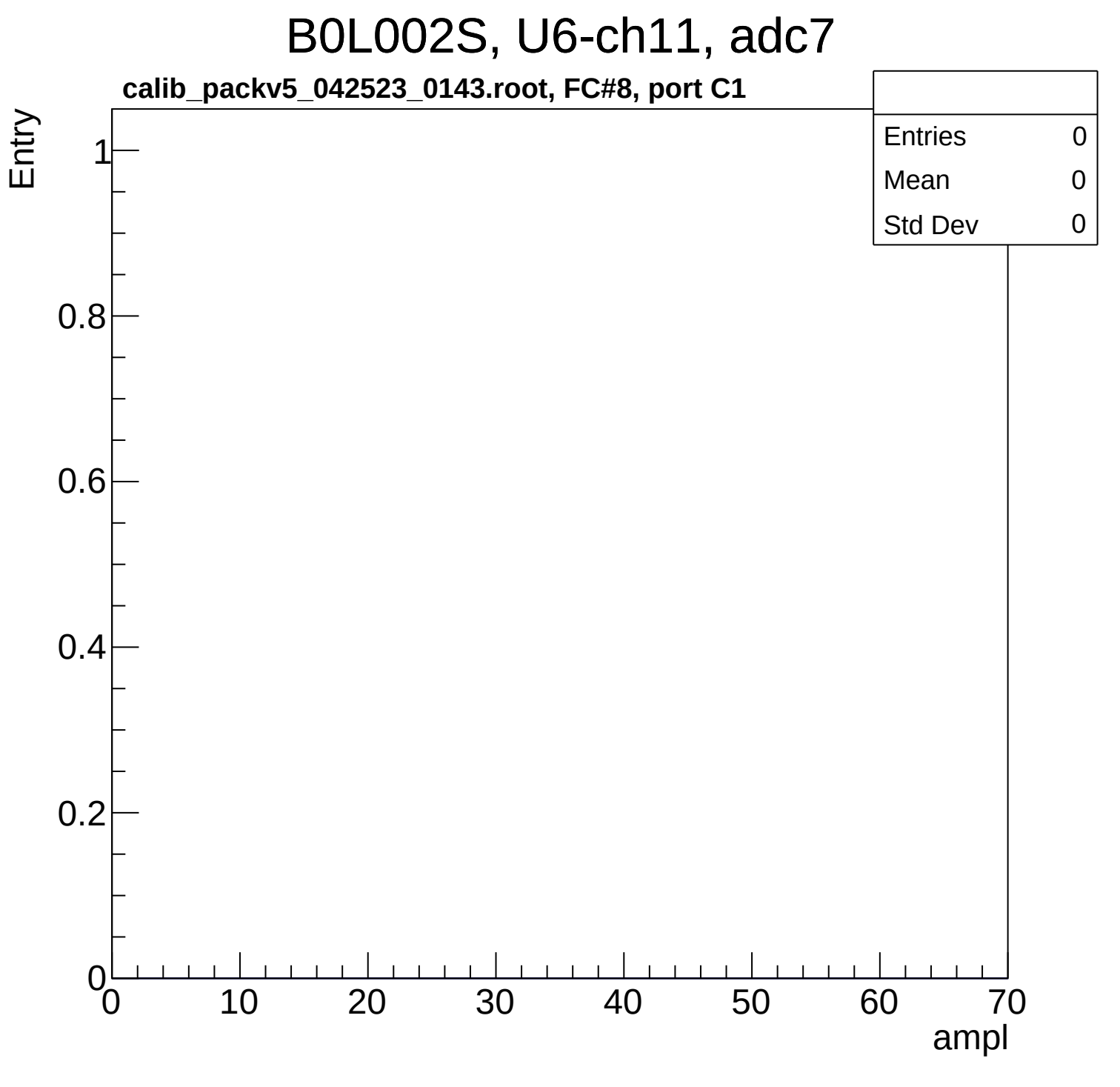
40
50
60
70
ampl


B0L002S, U6-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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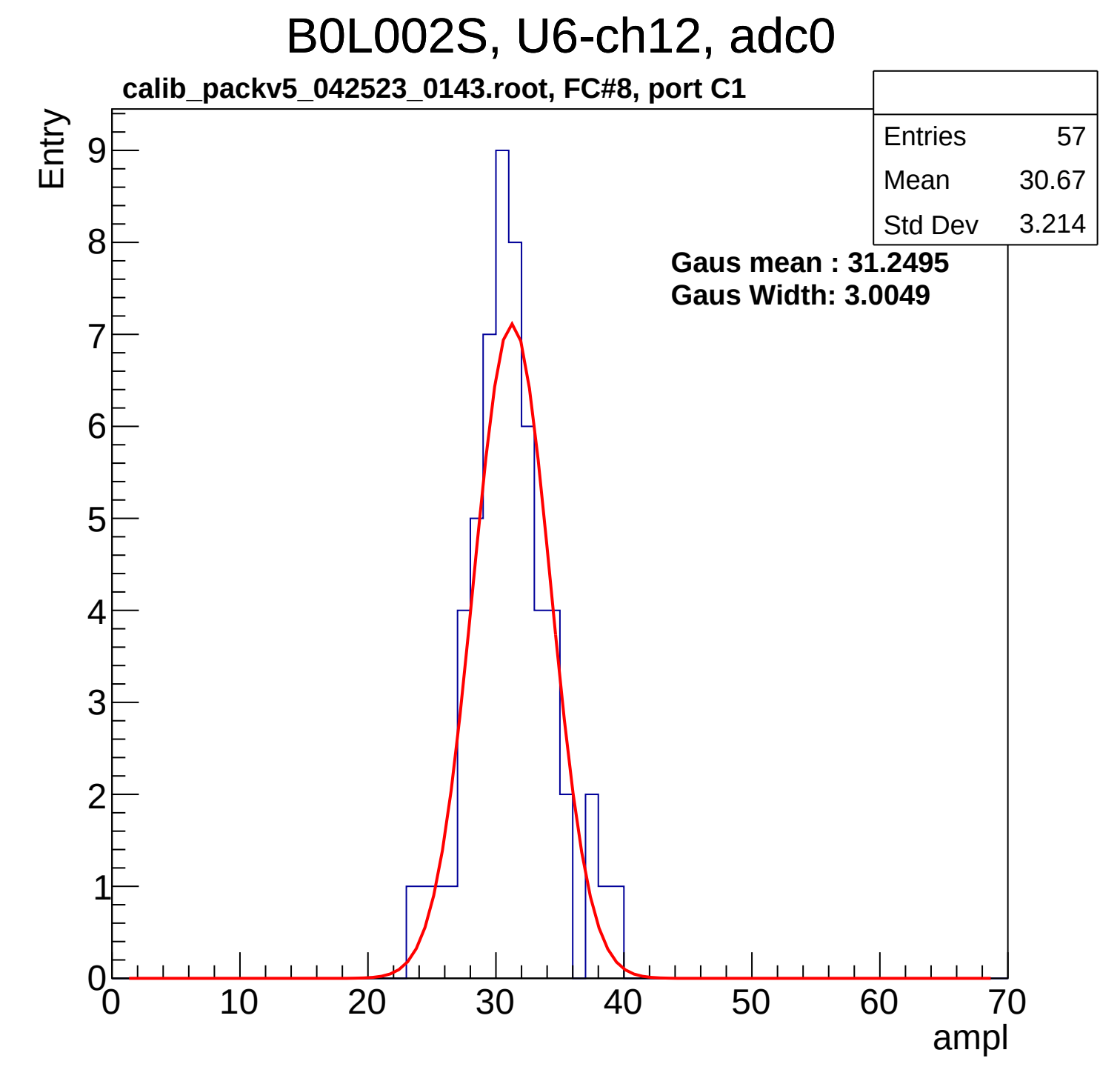
40
50
60
70
ampl


B0L002S, U6-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 30.67
Std Dev 3.214
Gaus mean : 31.2495
Gaus Width: 3.0049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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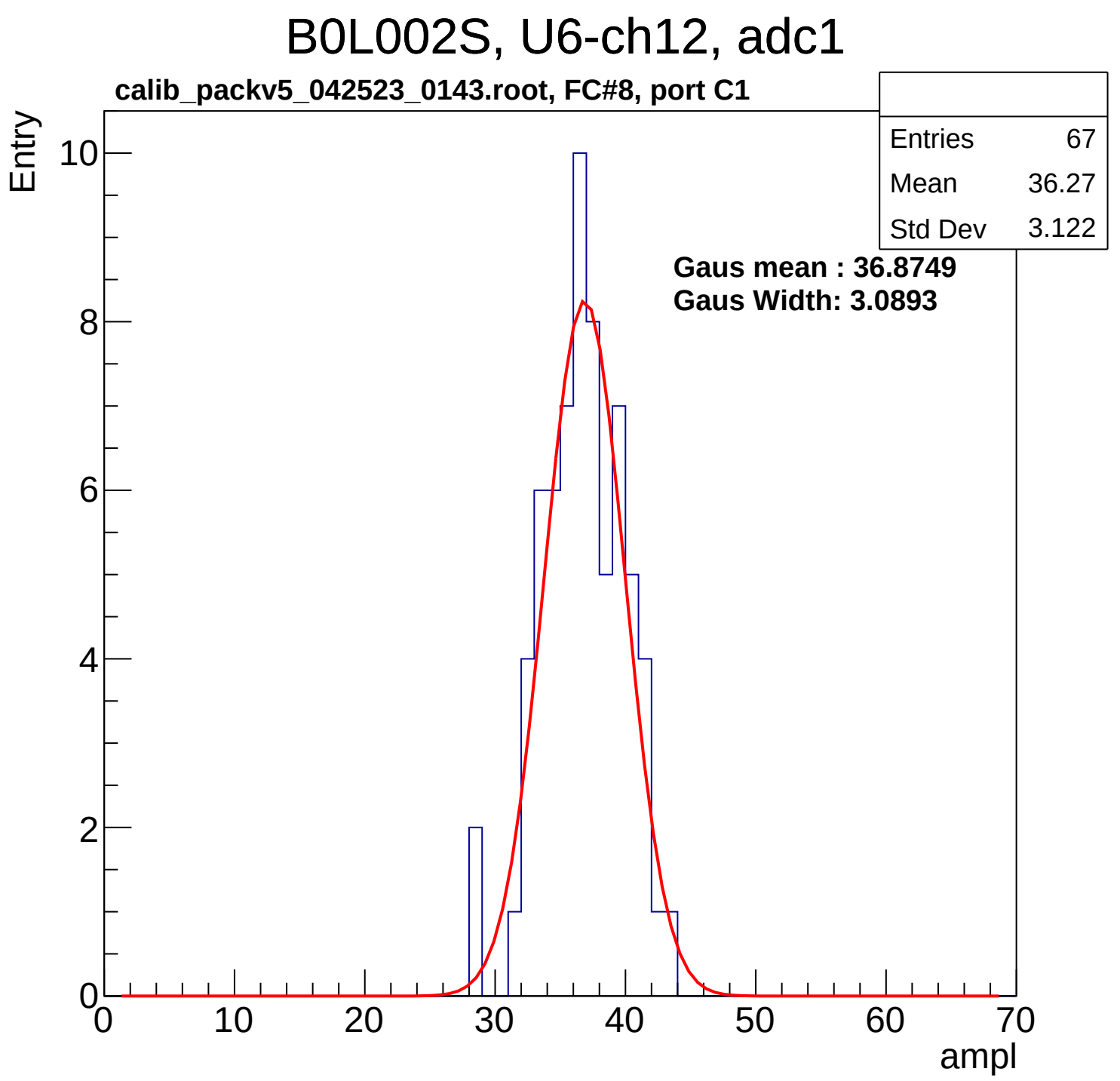
B0L002S, U6-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.27
Std Dev 3.122
Gaus mean : 36.8749
Gaus Width: 3.0893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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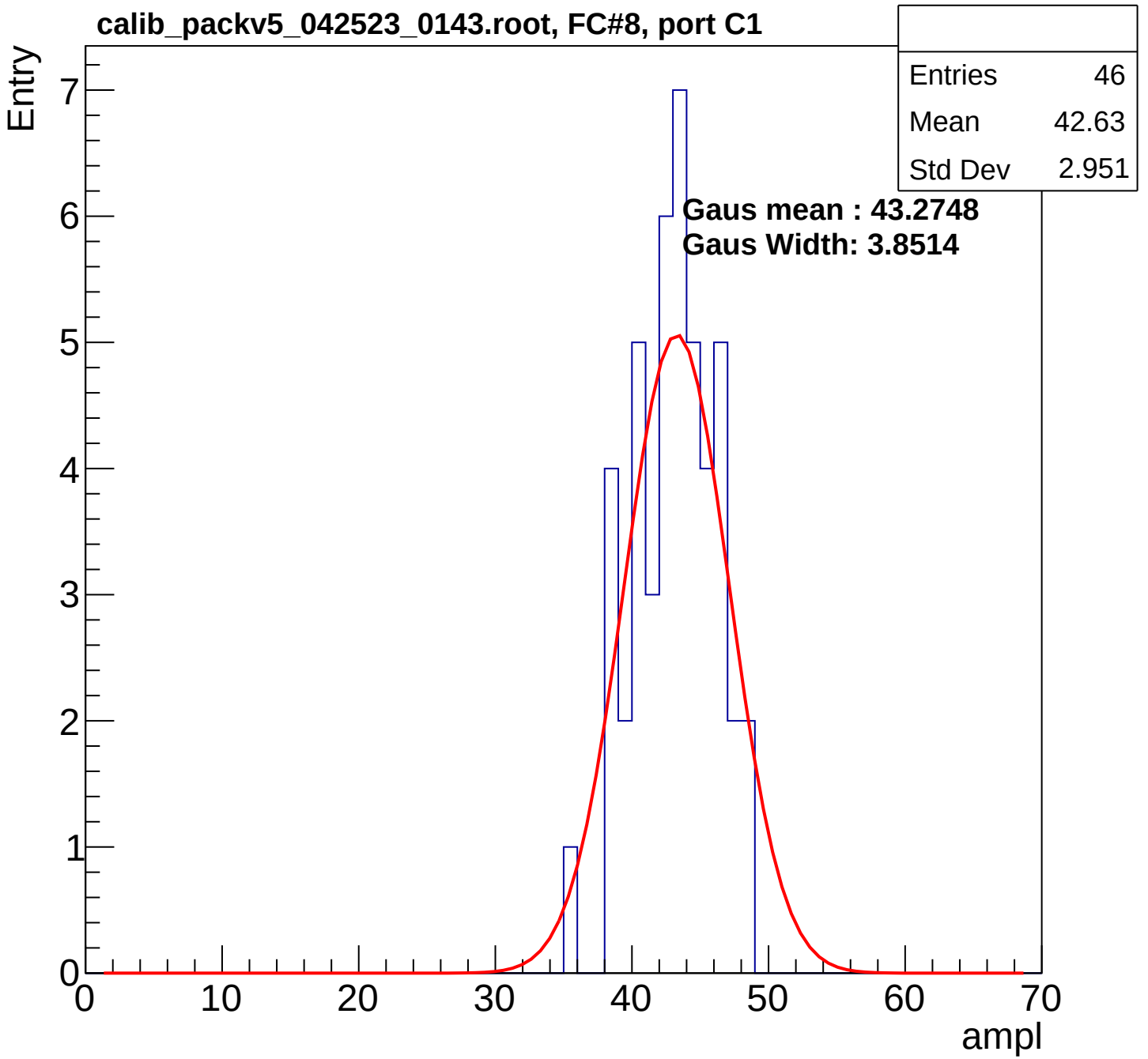
B0L002S, U6-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 42.63
Std Dev 2.951
Gaus mean : 43.2748
Gaus Width: 3.8514
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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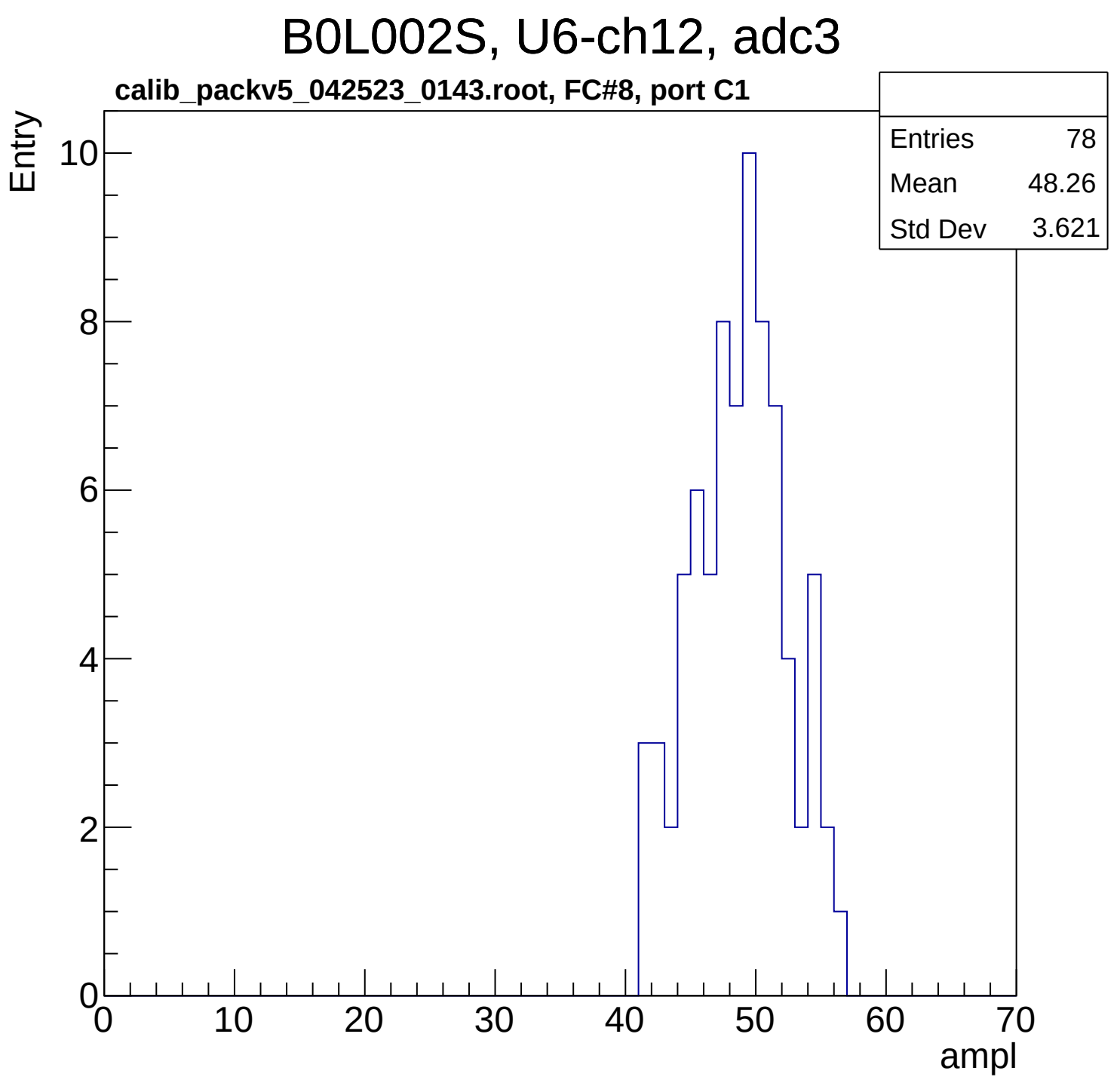
B0L002S, U6-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 48.26
Std Dev 3.621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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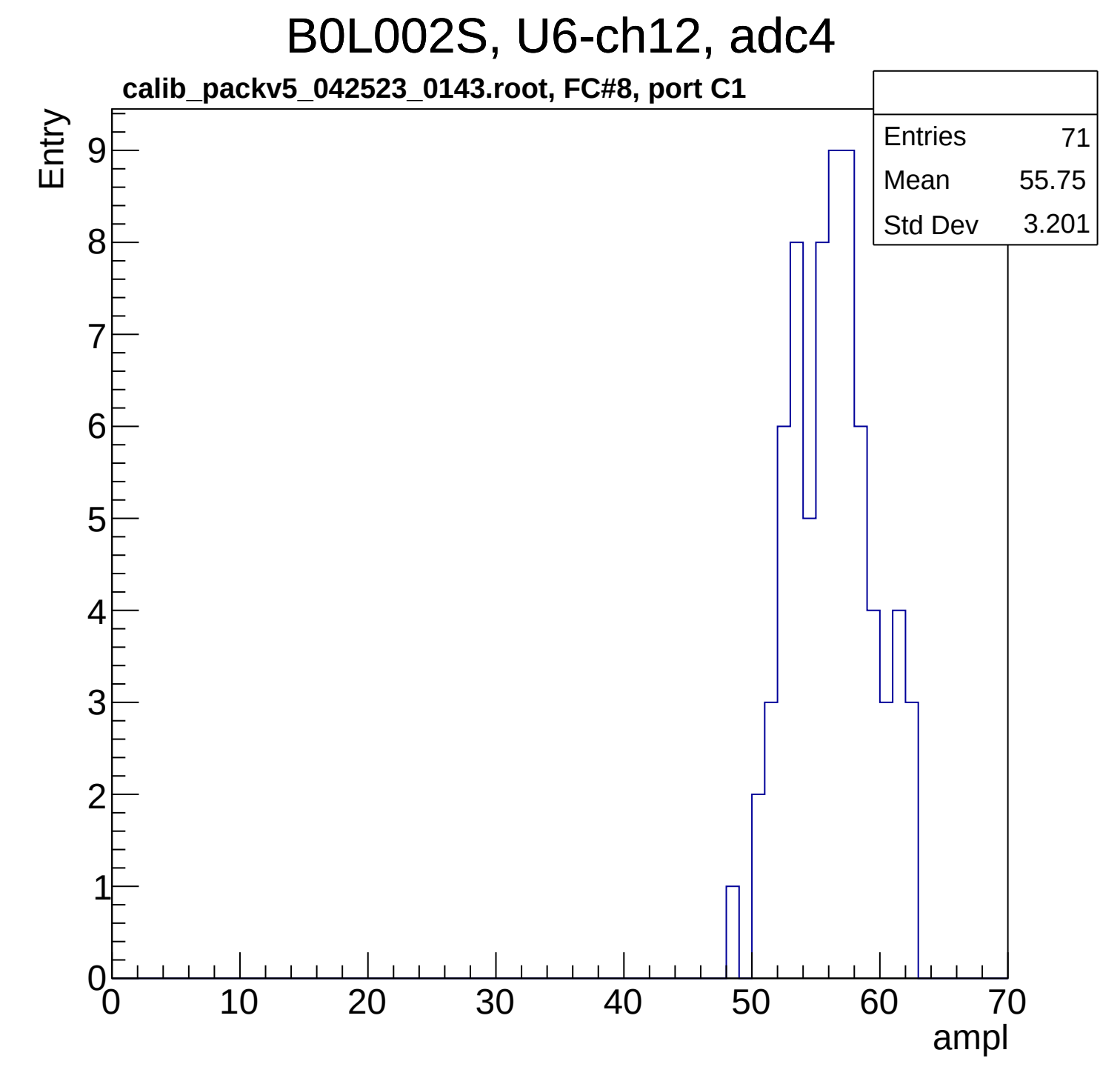
B0L002S, U6-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 55.75
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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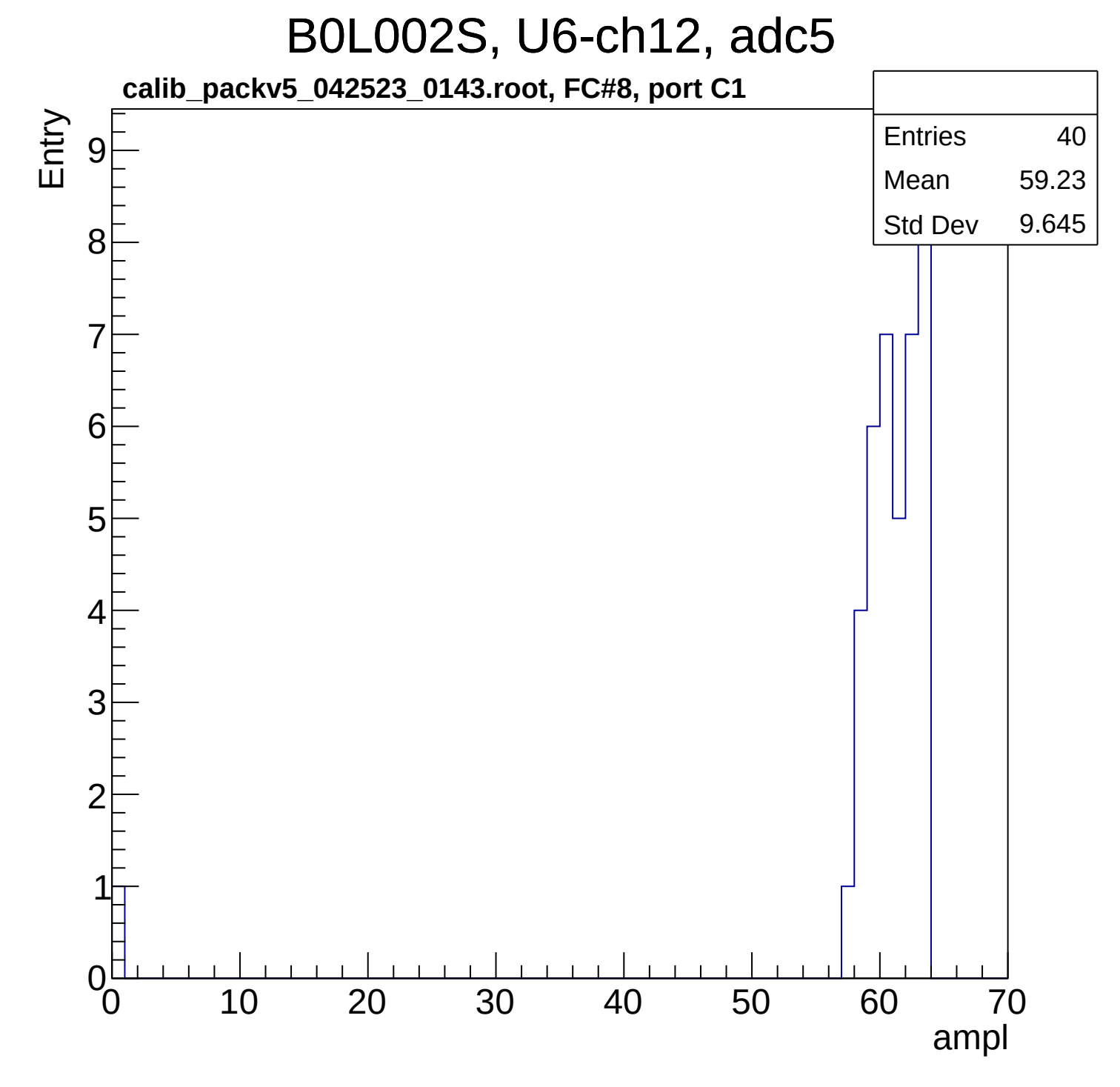
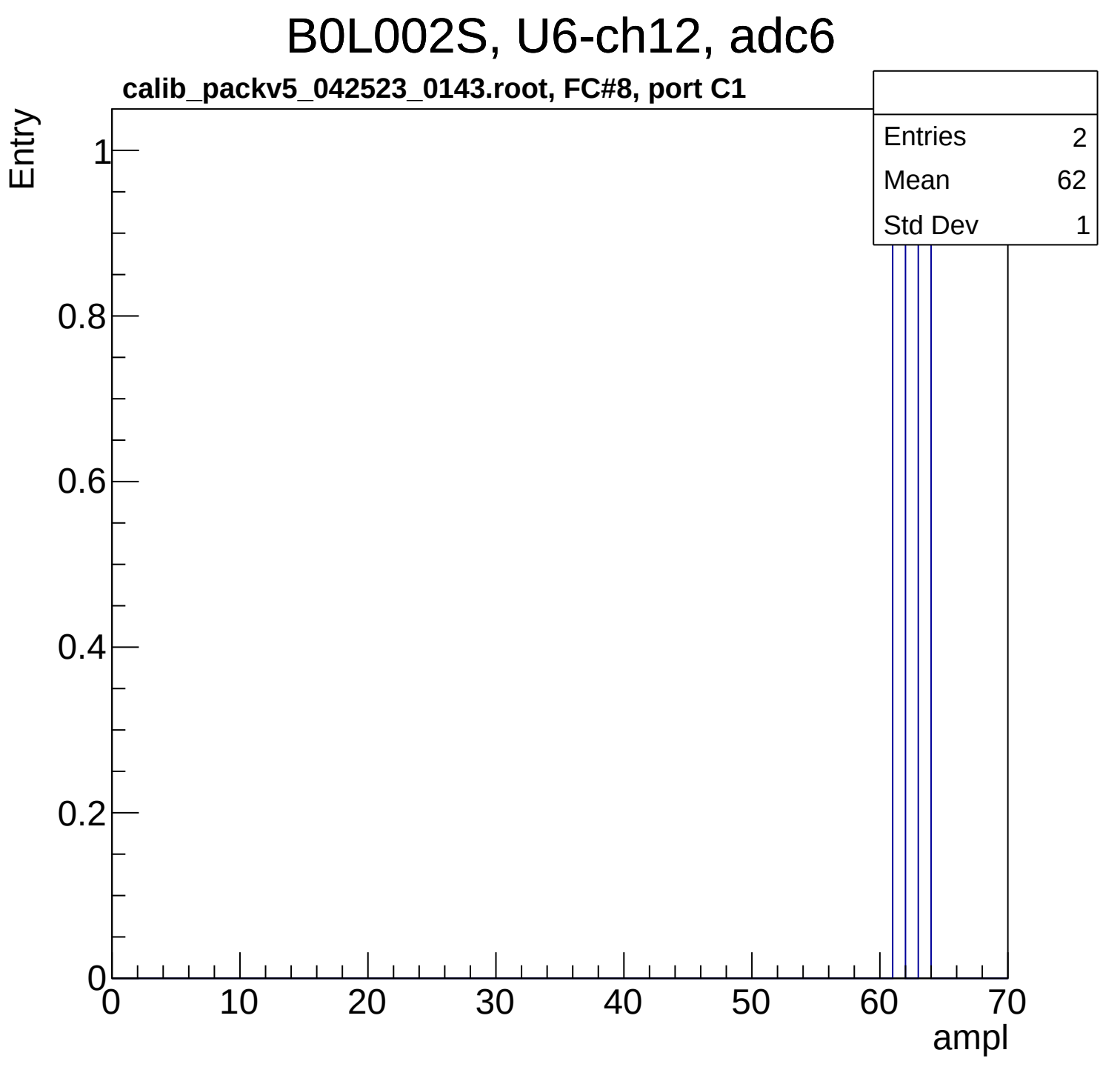
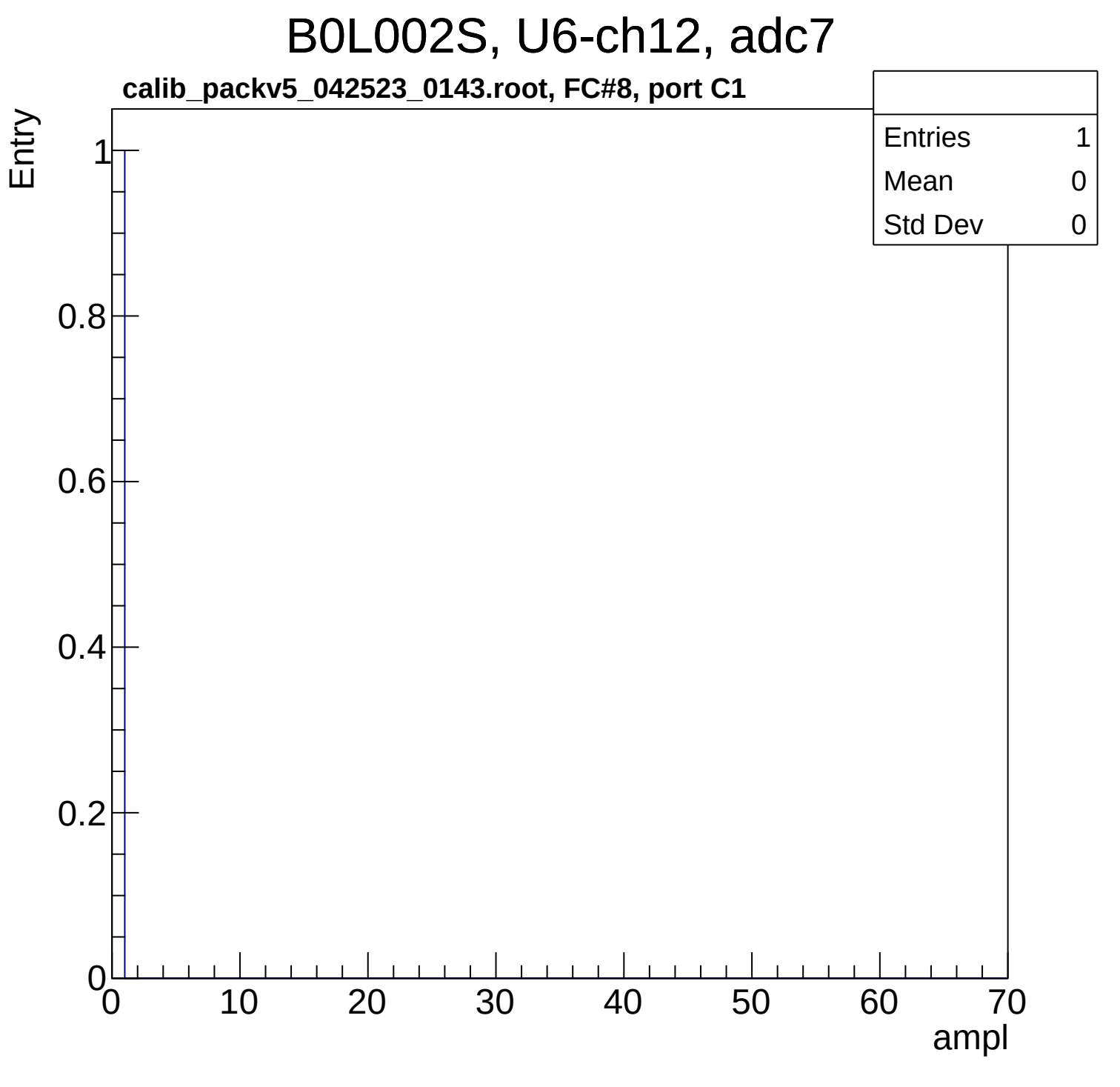

B0L002S, U6-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.23
Std Dev 9.645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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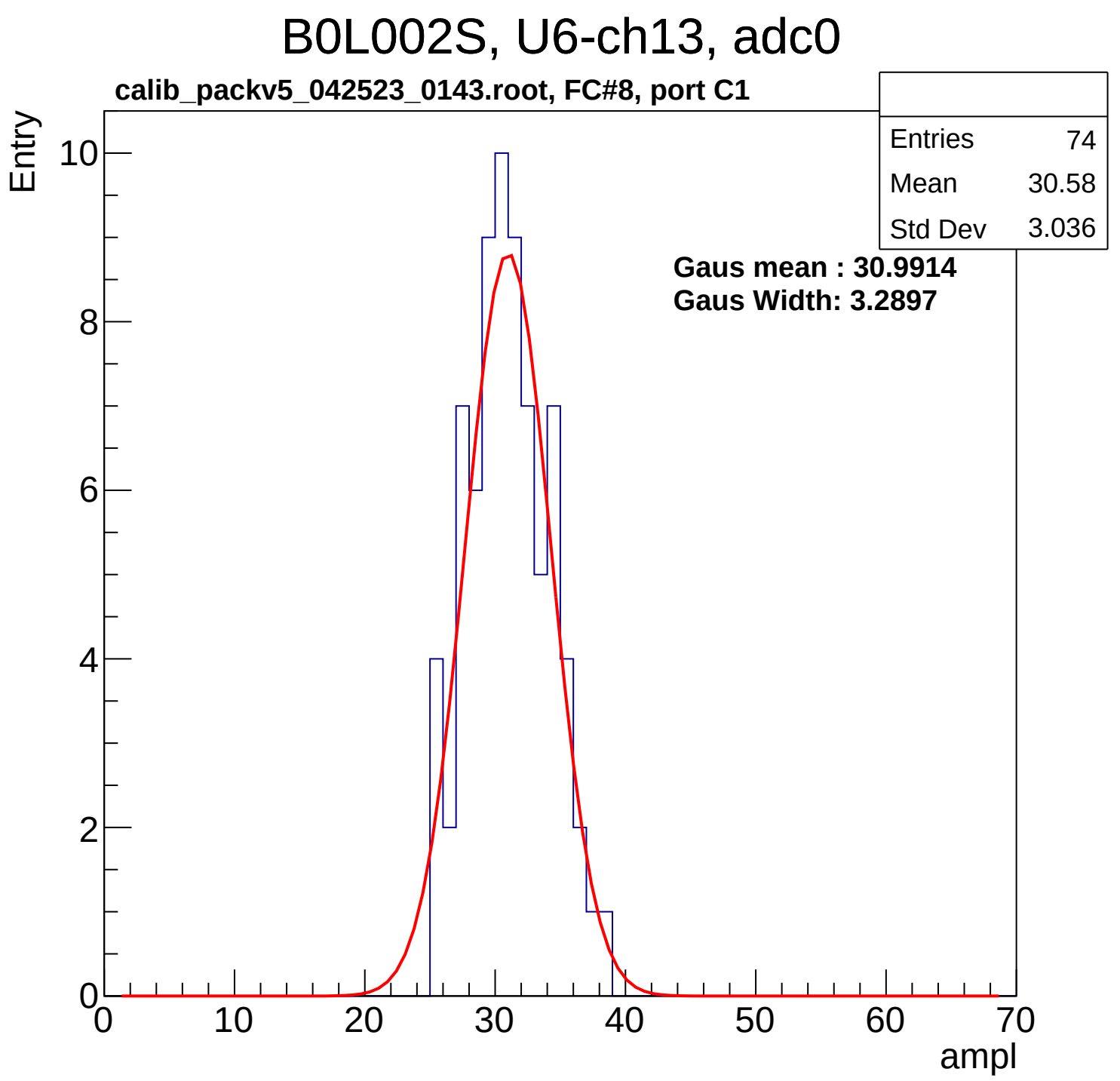
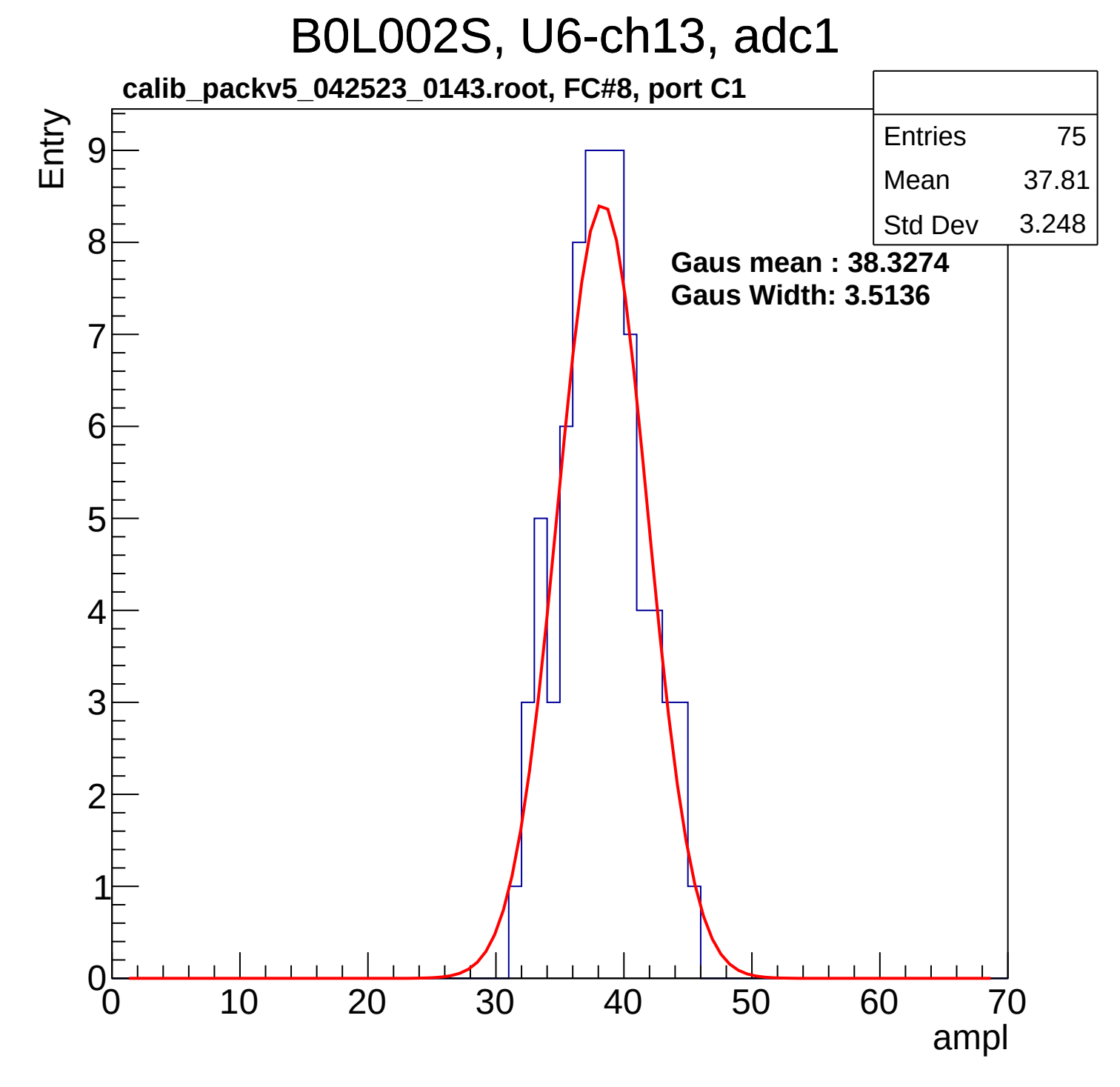
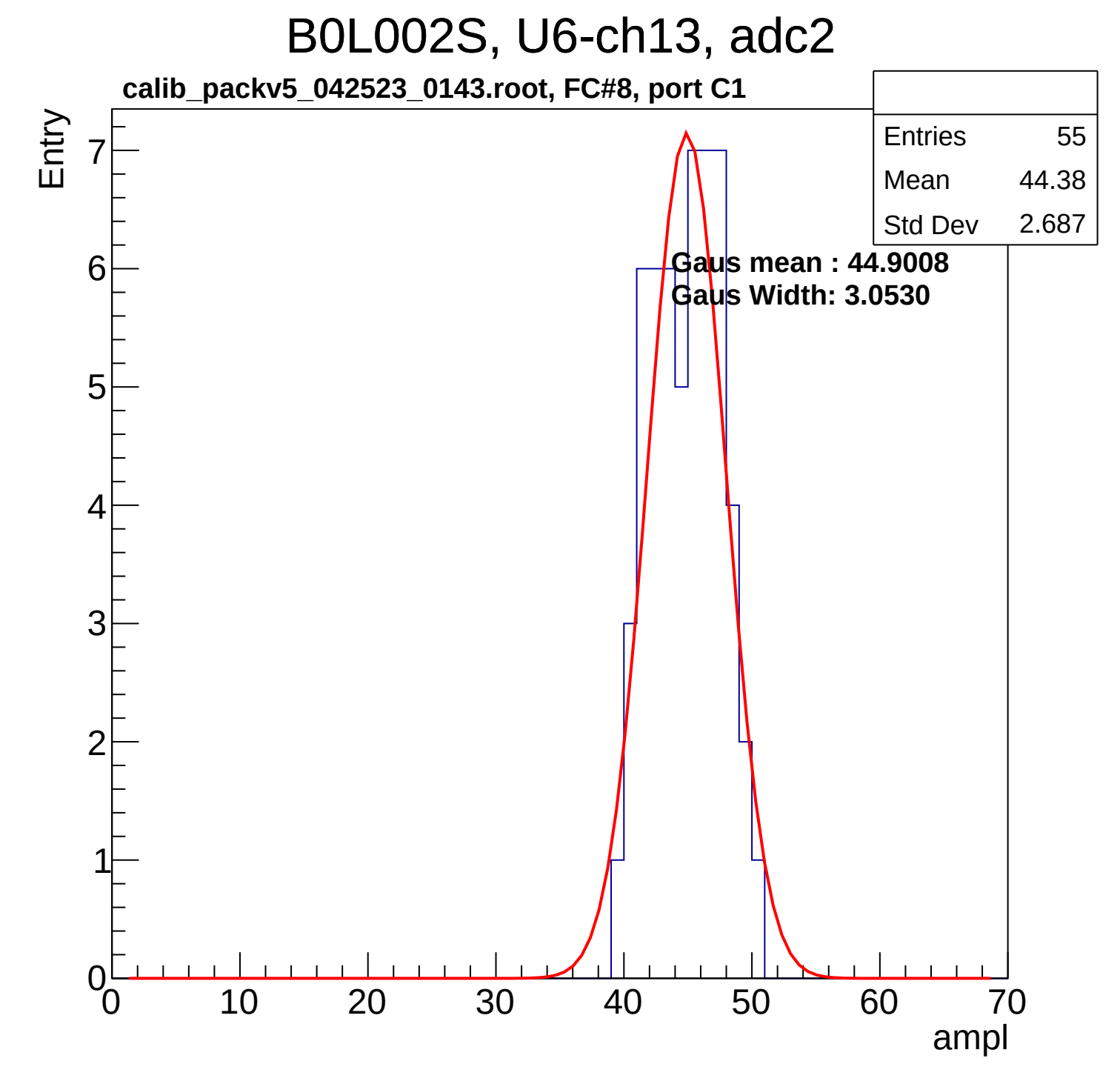
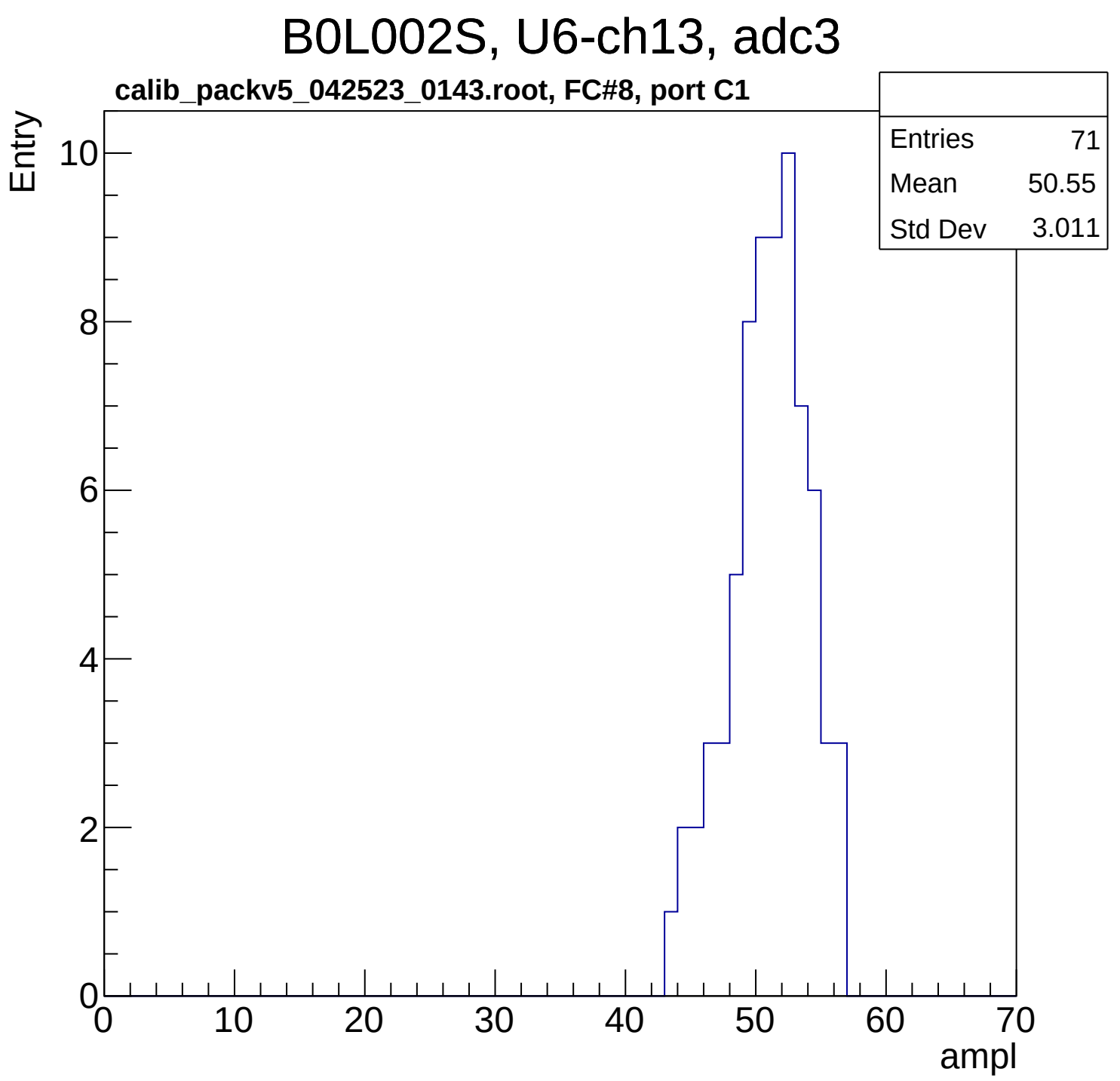
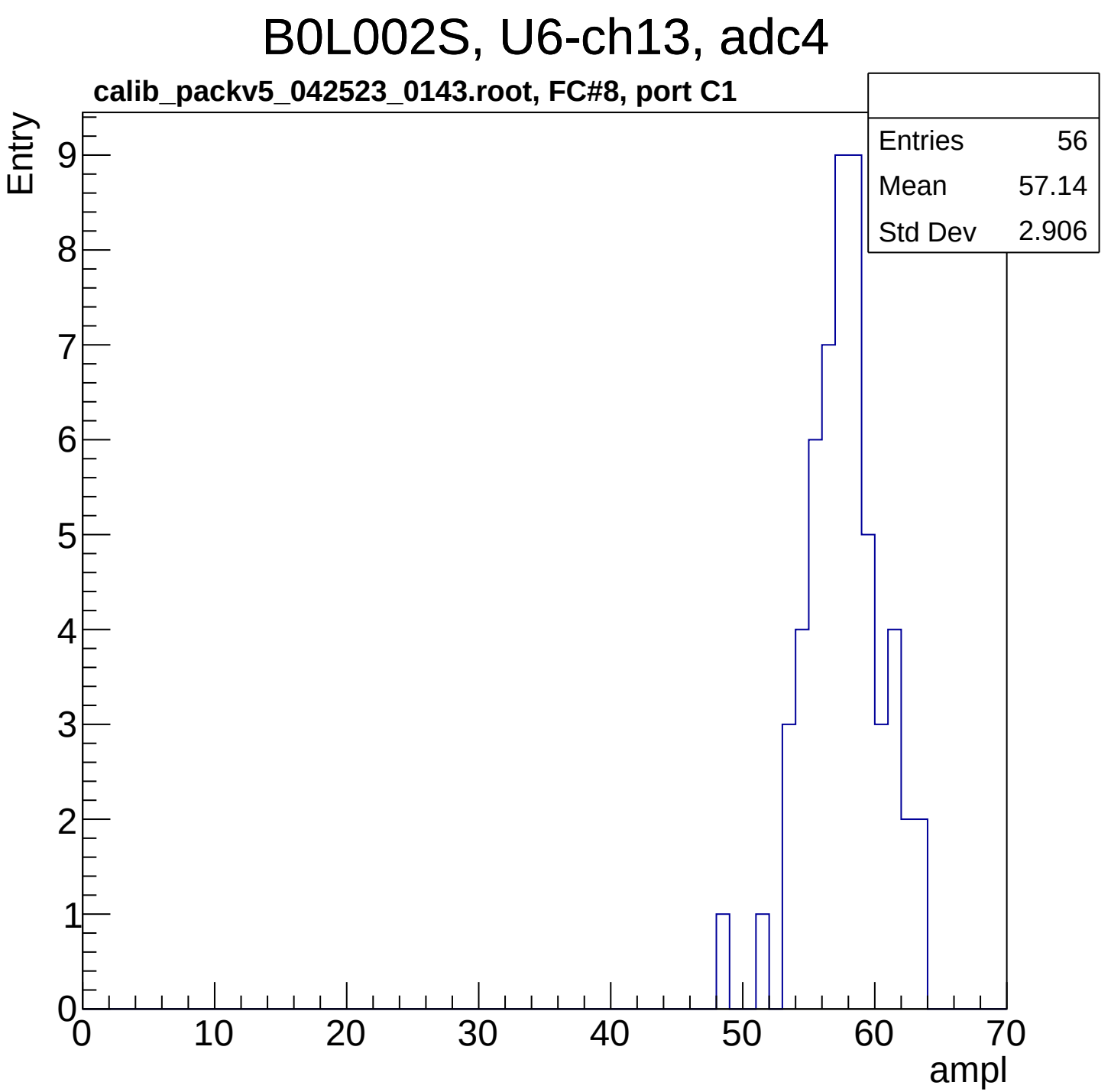
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

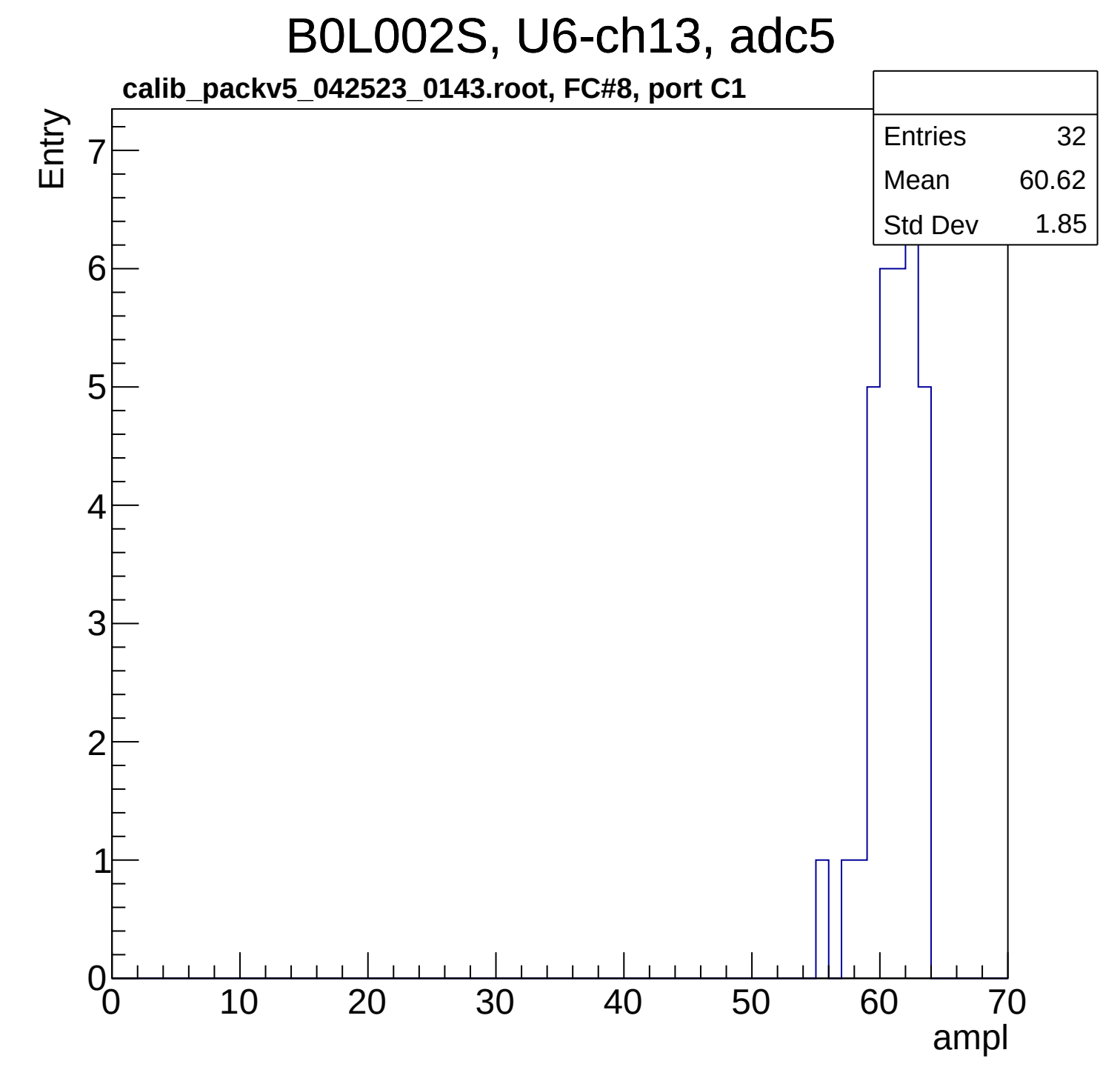

B0L002S, U6-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 30.58
Std Dev 3.036
Gaus mean : 30.9914
Gaus Width: 3.2897
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

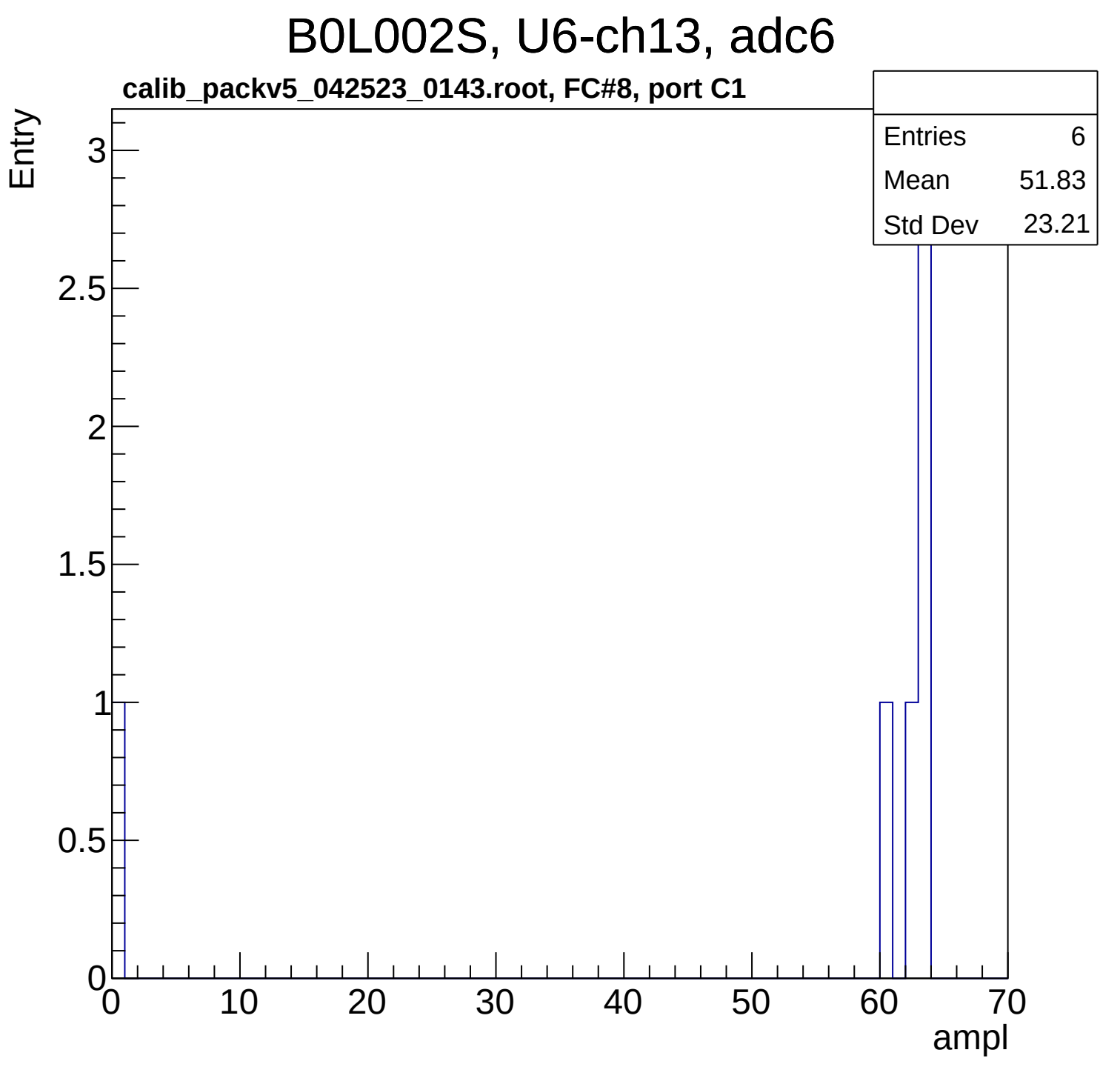

B0L002S, U6-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.81
Std Dev 3.248
Gaus mean : 38.3274
Gaus Width: 3.5136
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.38
Std Dev 2.687
Gaus mean : 44.9008
Gaus Width: 3.0530
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.55
Std Dev 3.011
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.14
Std Dev 2.906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 60.62
Std Dev 1.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
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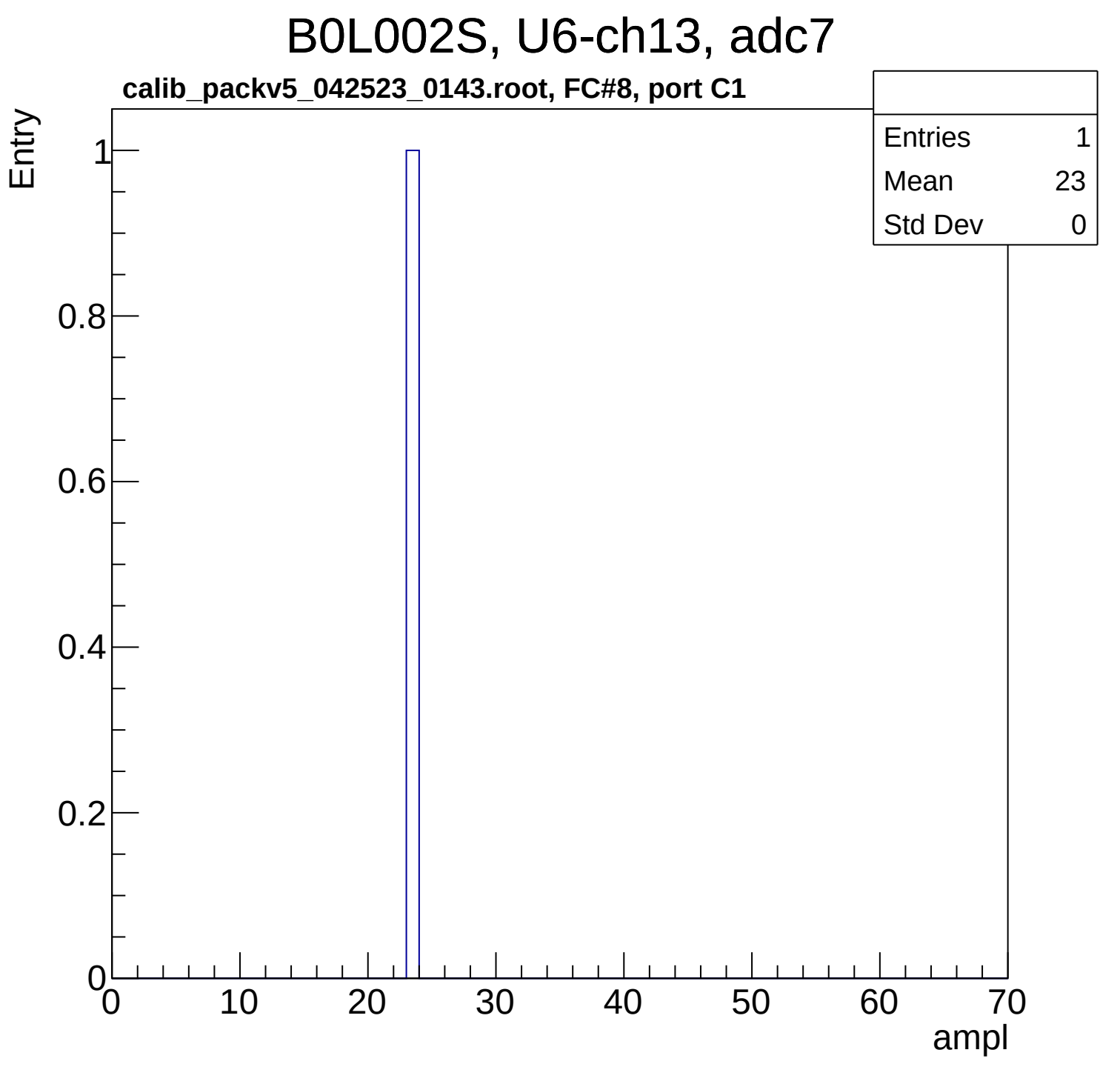
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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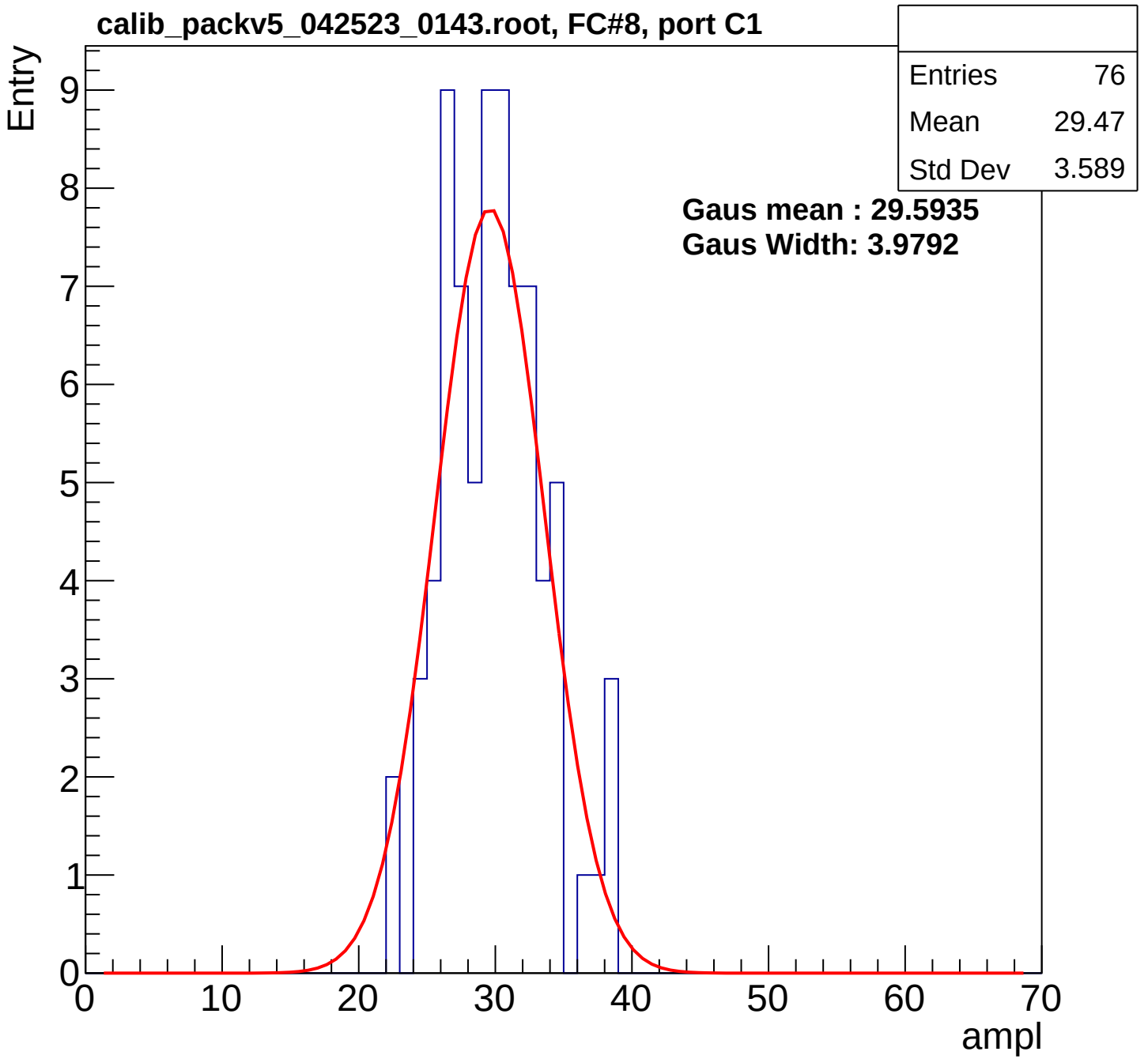
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 29.47
Std Dev 3.589
Gaus mean : 29.5935
Gaus Width: 3.9792
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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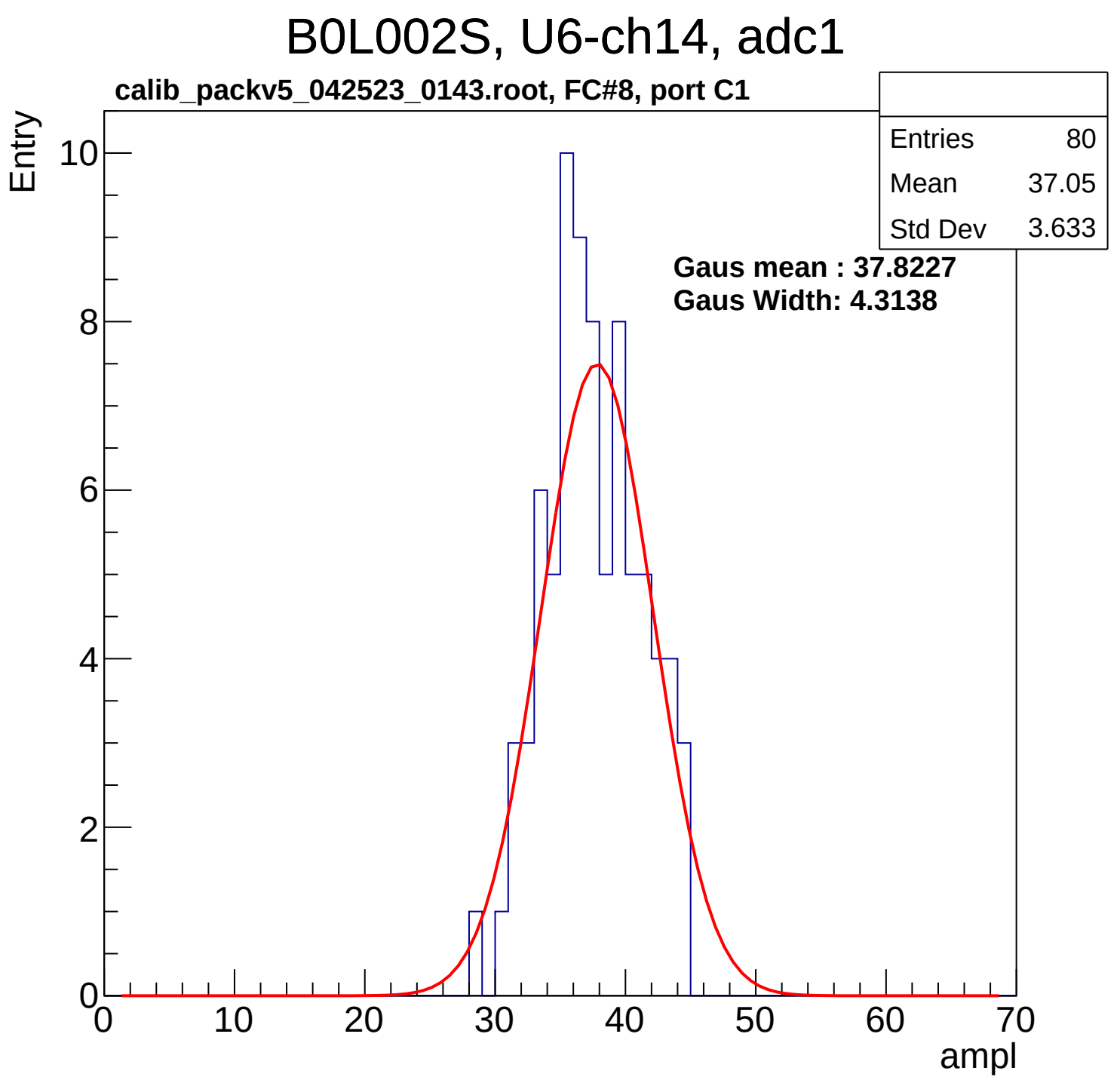
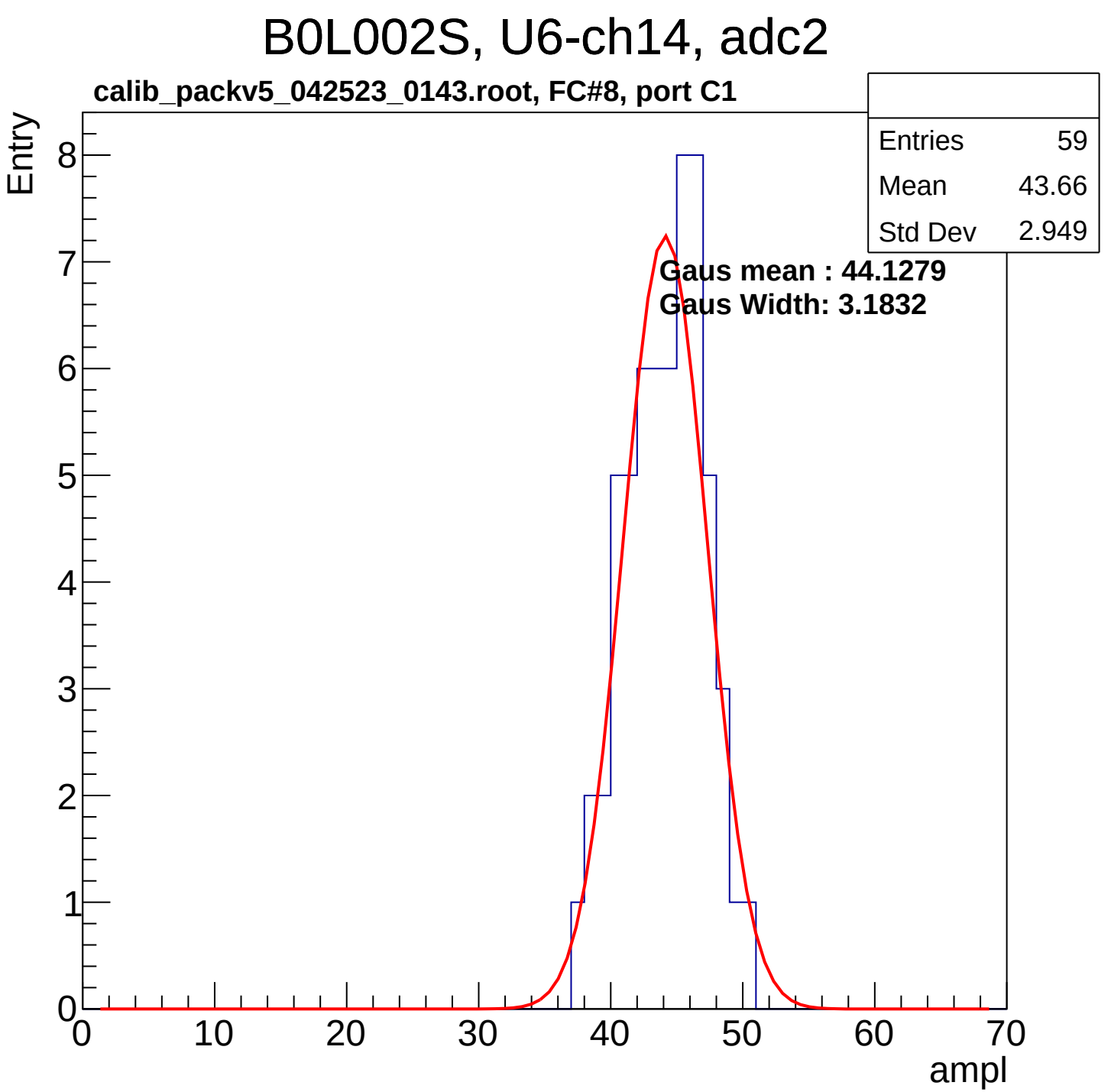
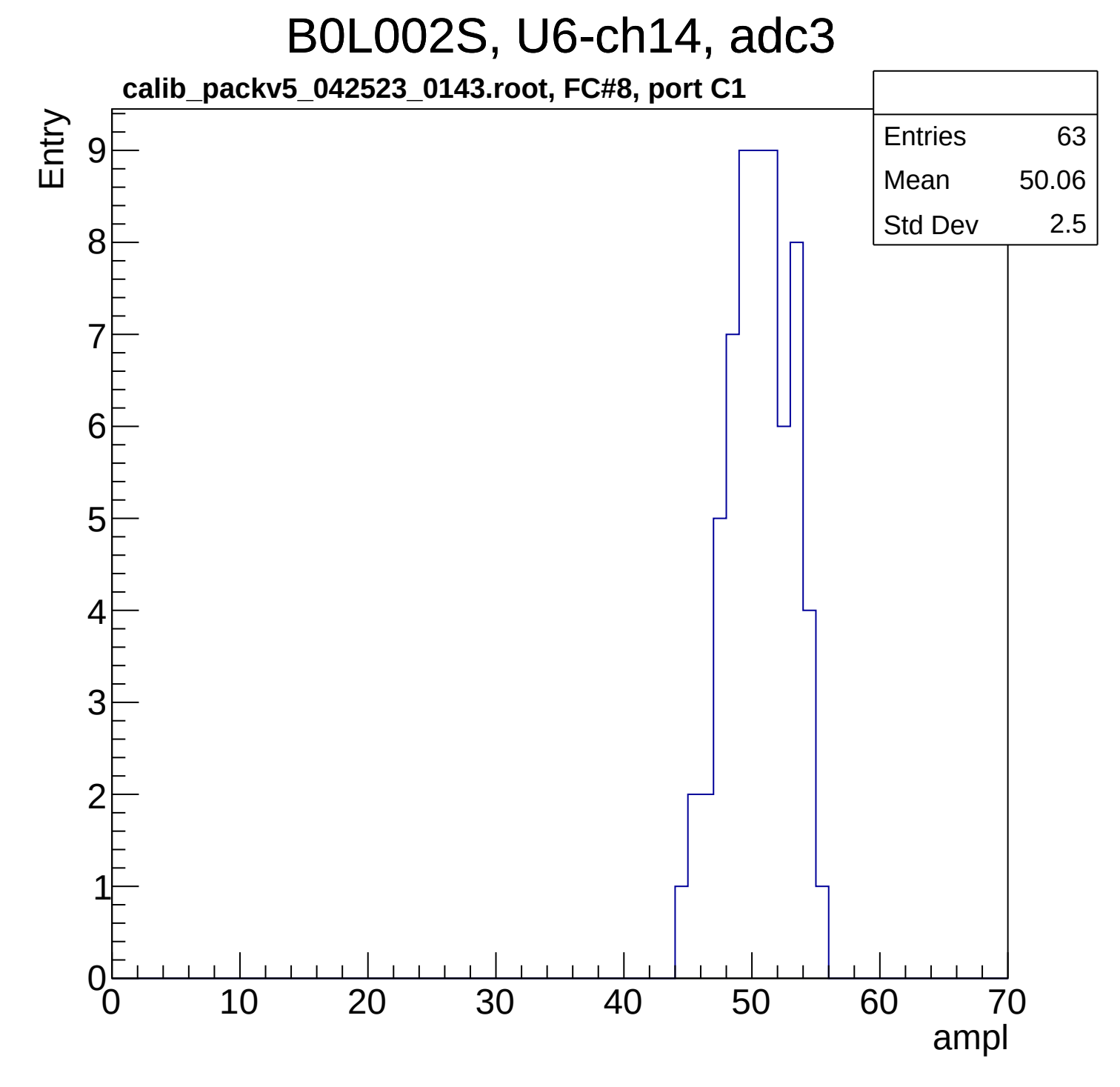

B0L002S, U6-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.05
Std Dev 3.633
Gaus mean : 37.8227
Gaus Width: 4.3138
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.66
Std Dev 2.949
Gaus mean : 44.1279
Gaus Width: 3.1832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.06
Std Dev 2.5
Entry
ampl
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2
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4
5
6
7
8
9
0
10
20
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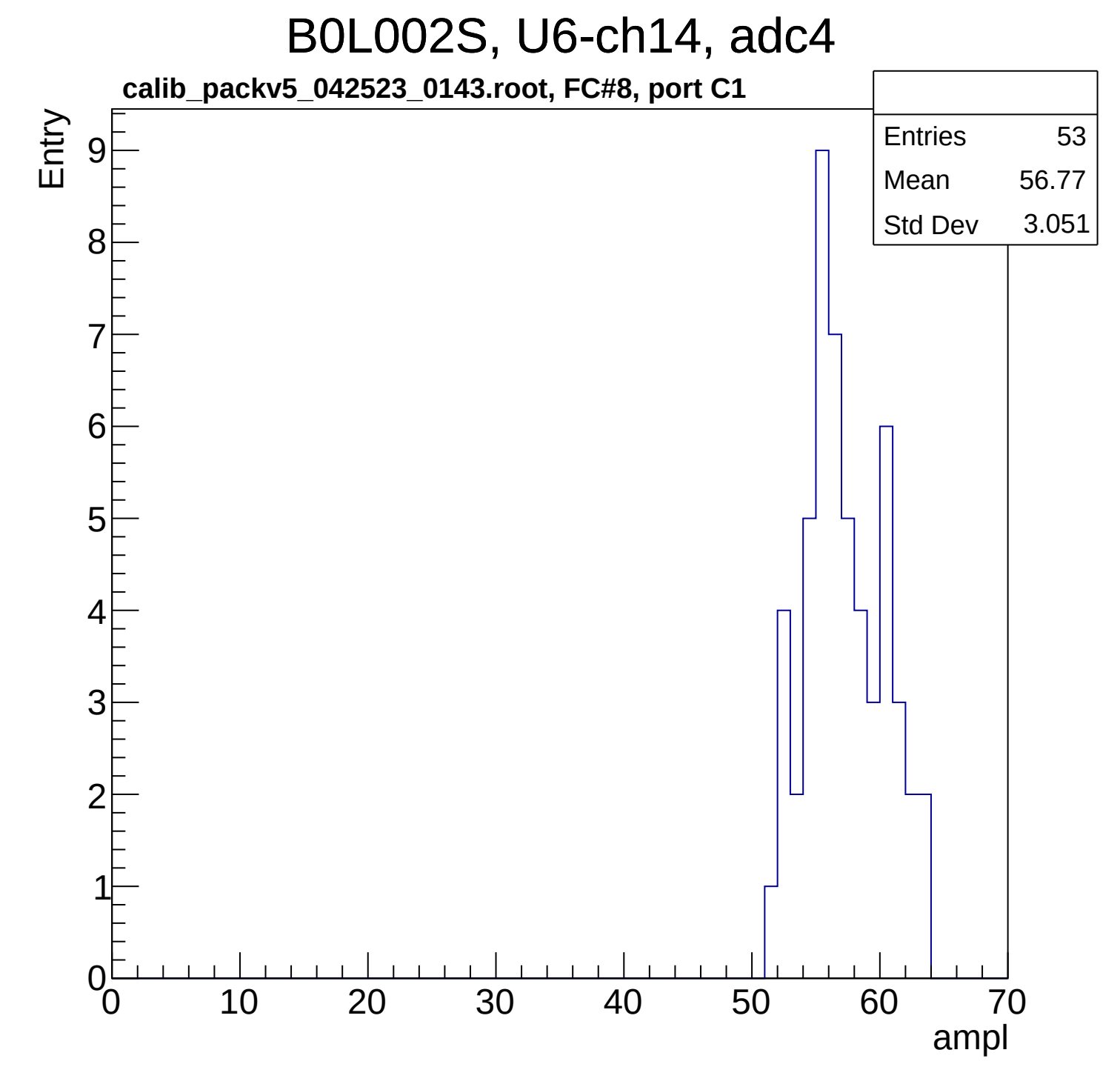
40
50
60
70


B0L002S, U6-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.77
Std Dev 3.051
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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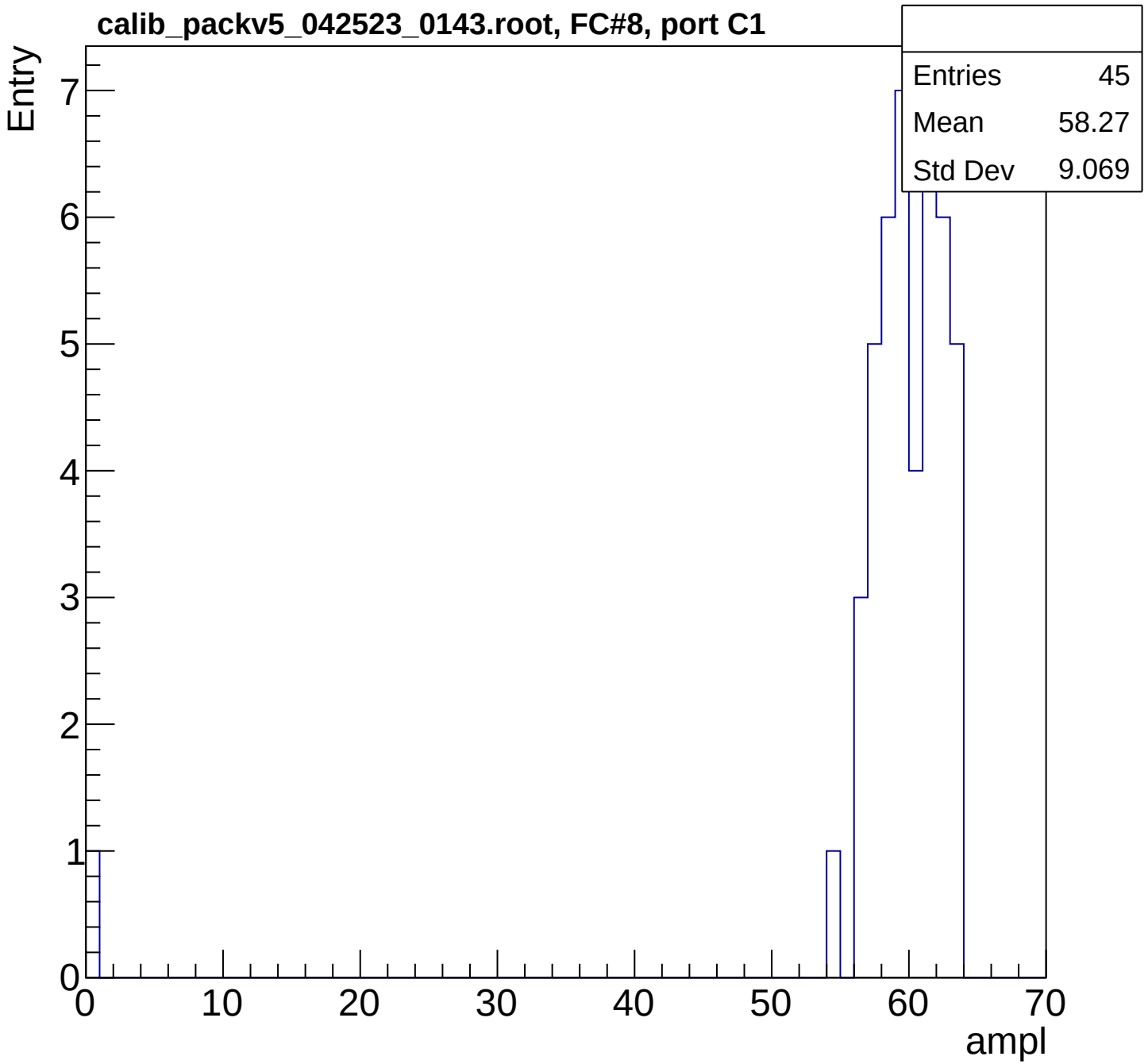
B0L002S, U6-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.27
Std Dev 9.069
Entry
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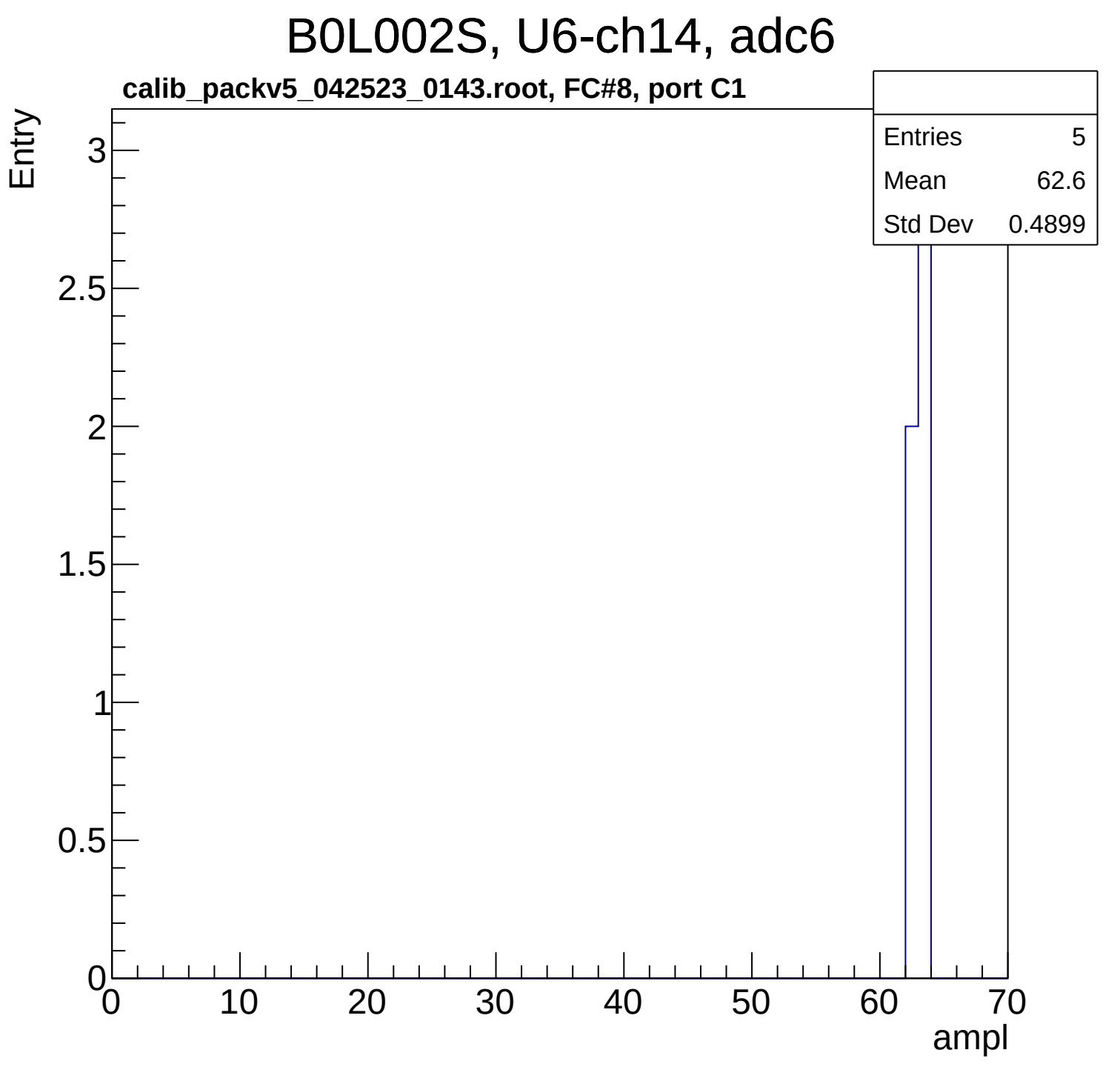
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
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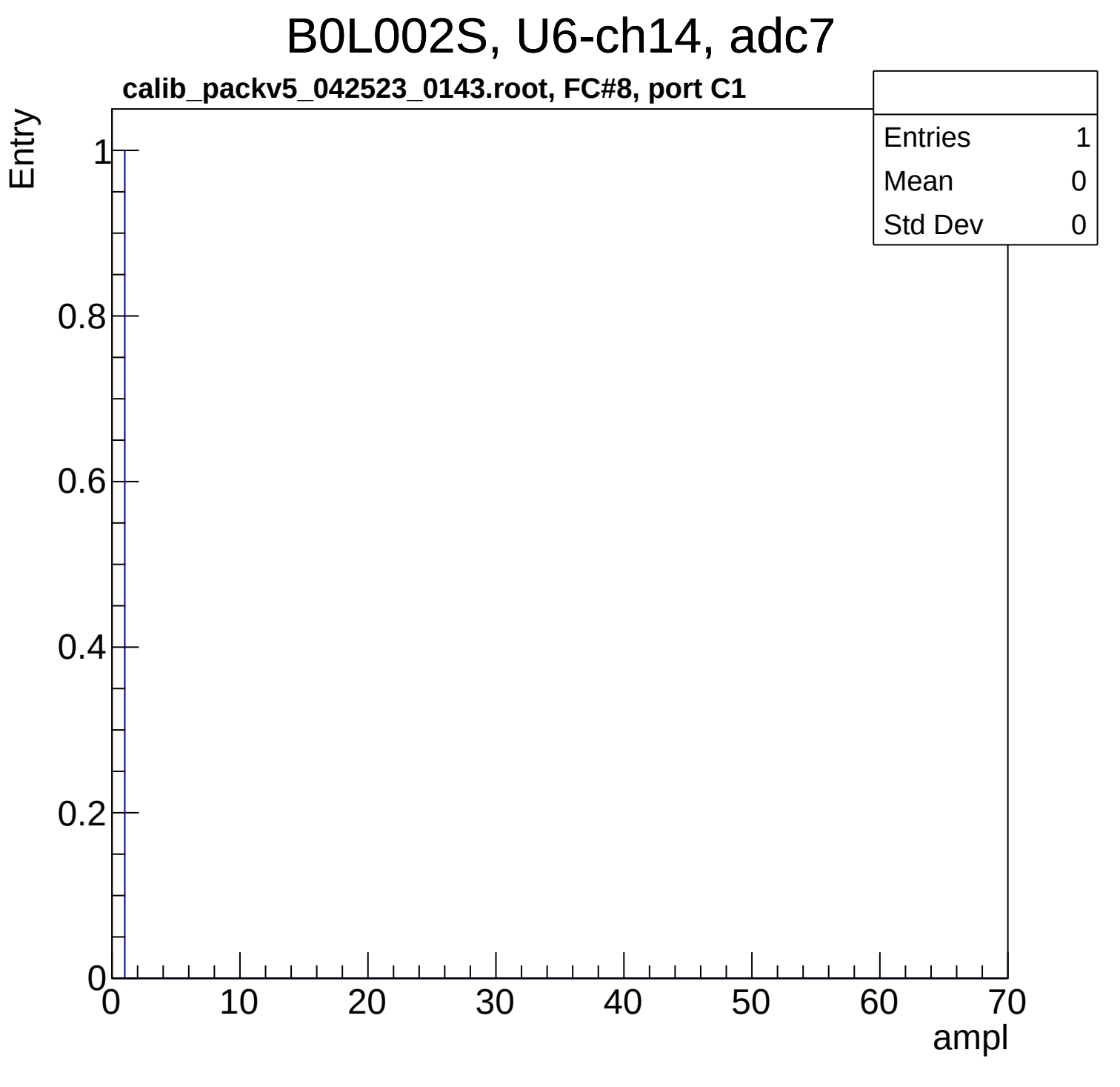
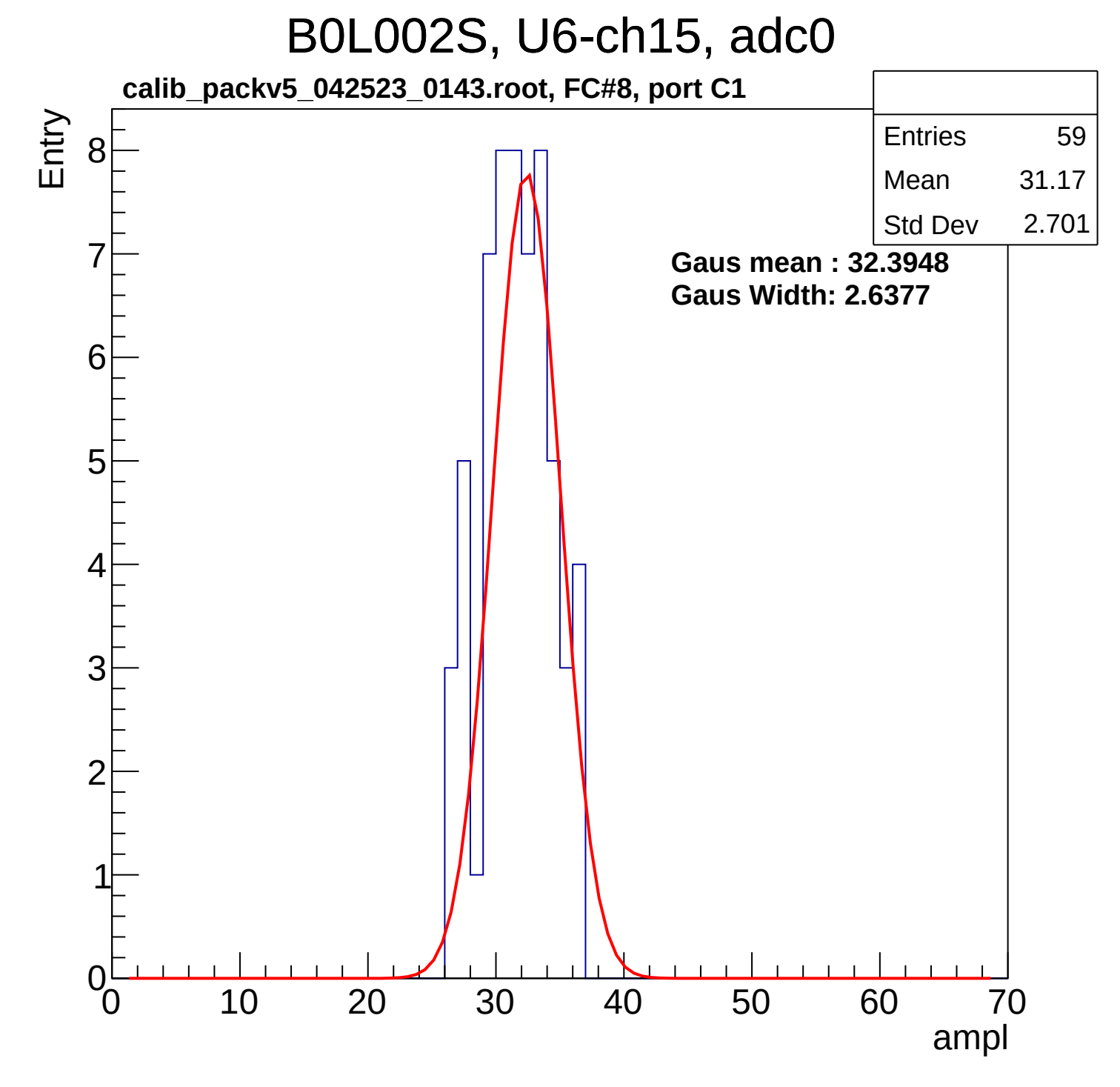
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 31.17
Std Dev 2.701
Gaus mean : 32.3948
Gaus Width: 2.6377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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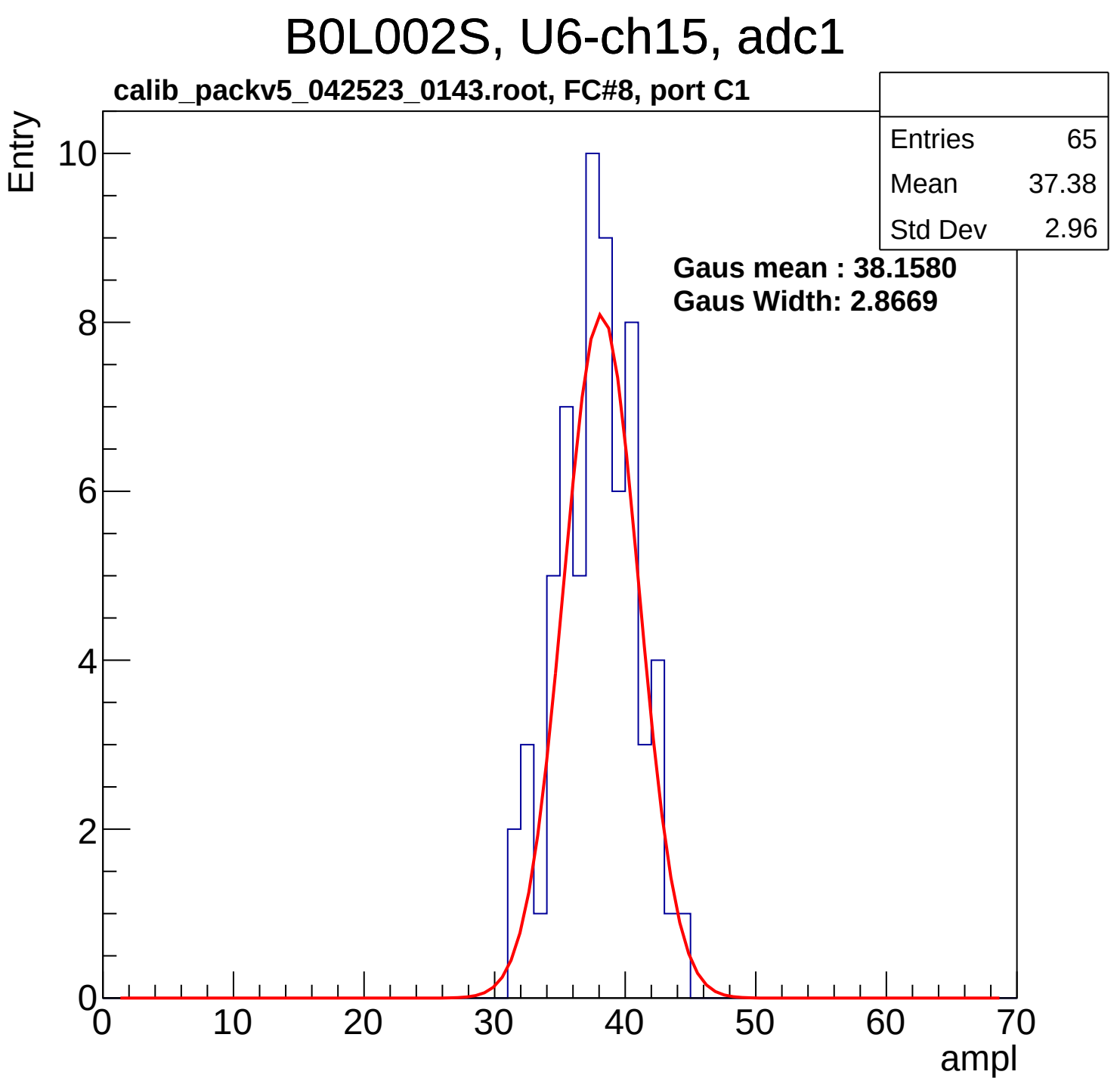
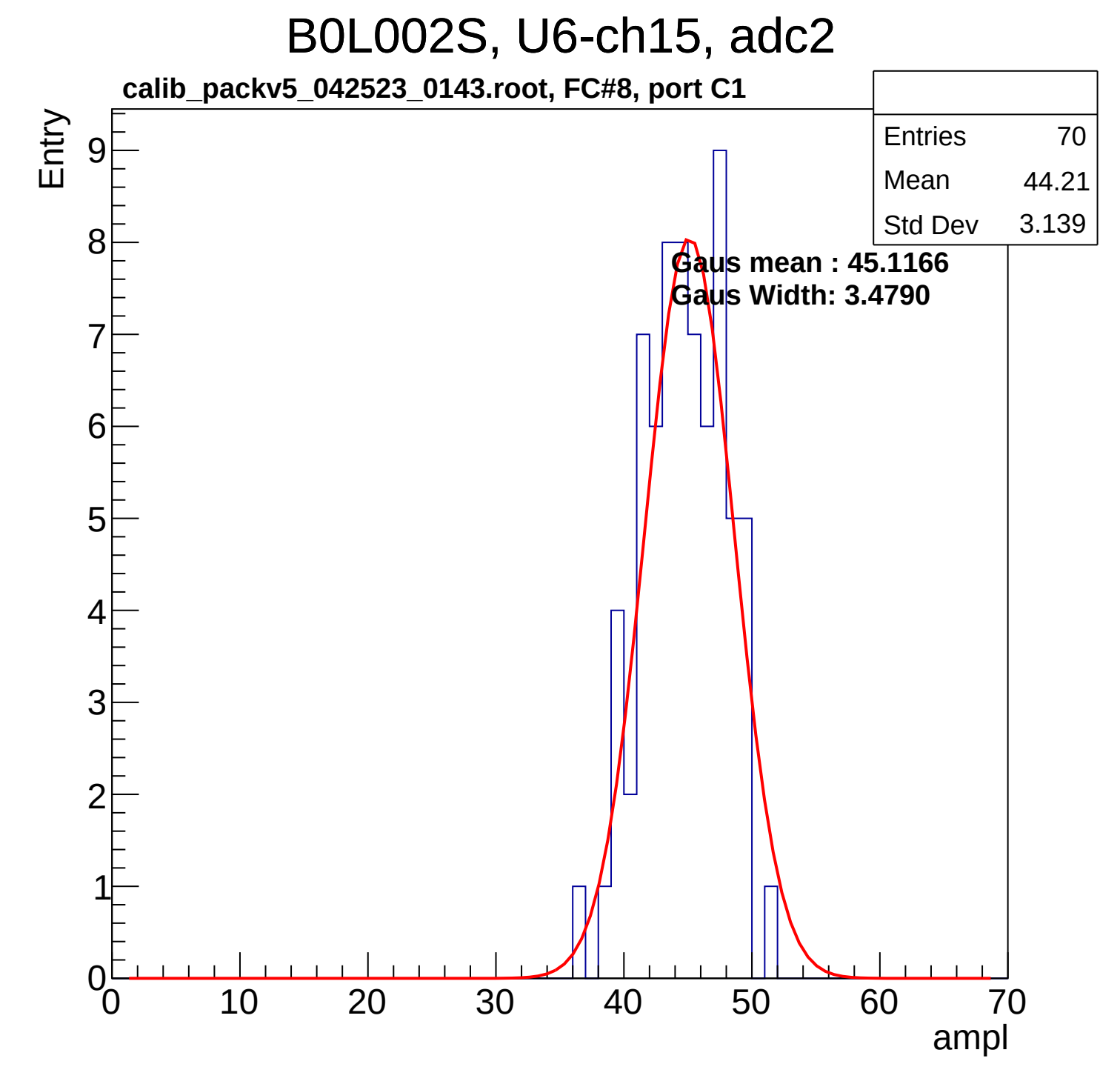
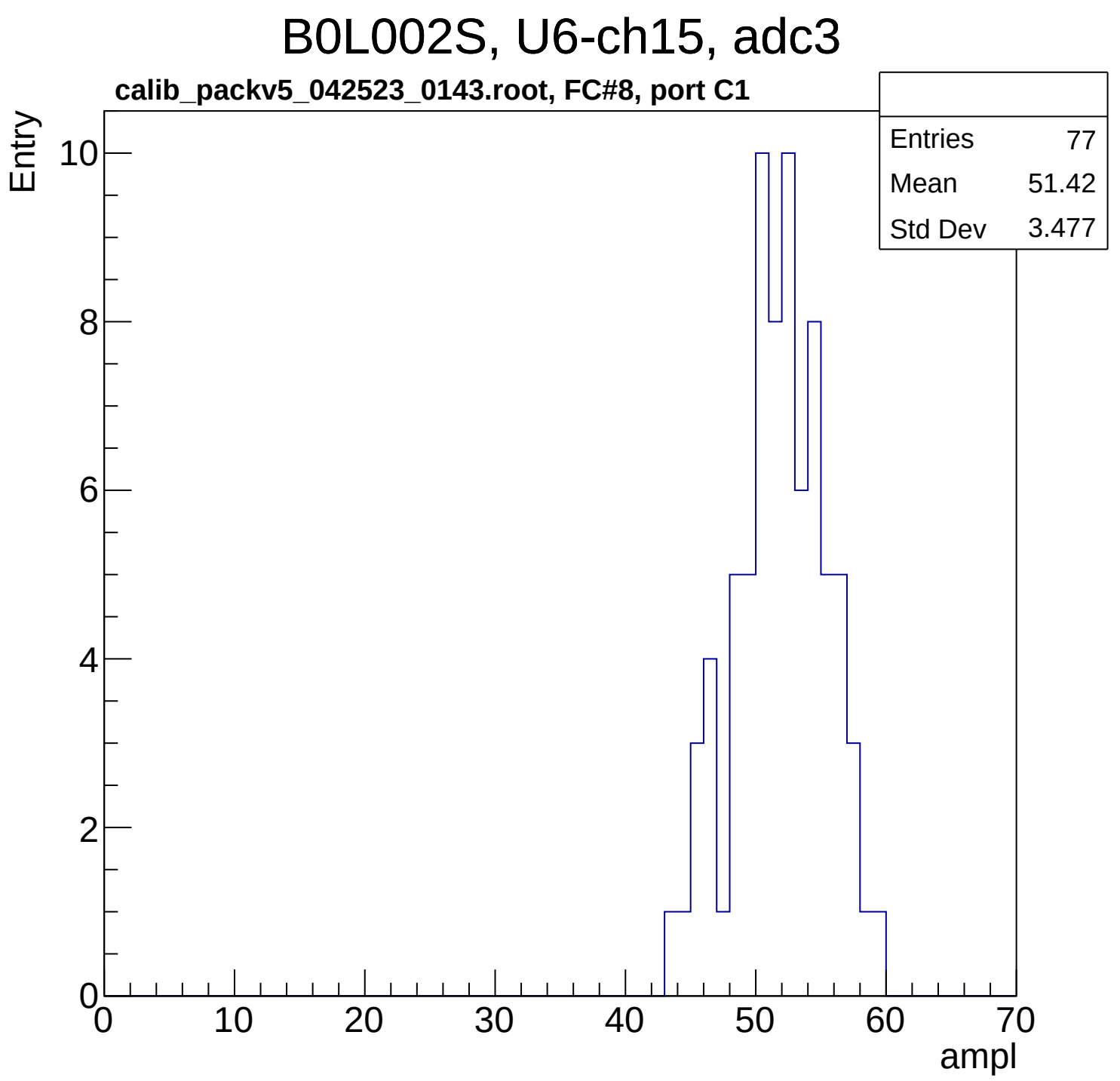
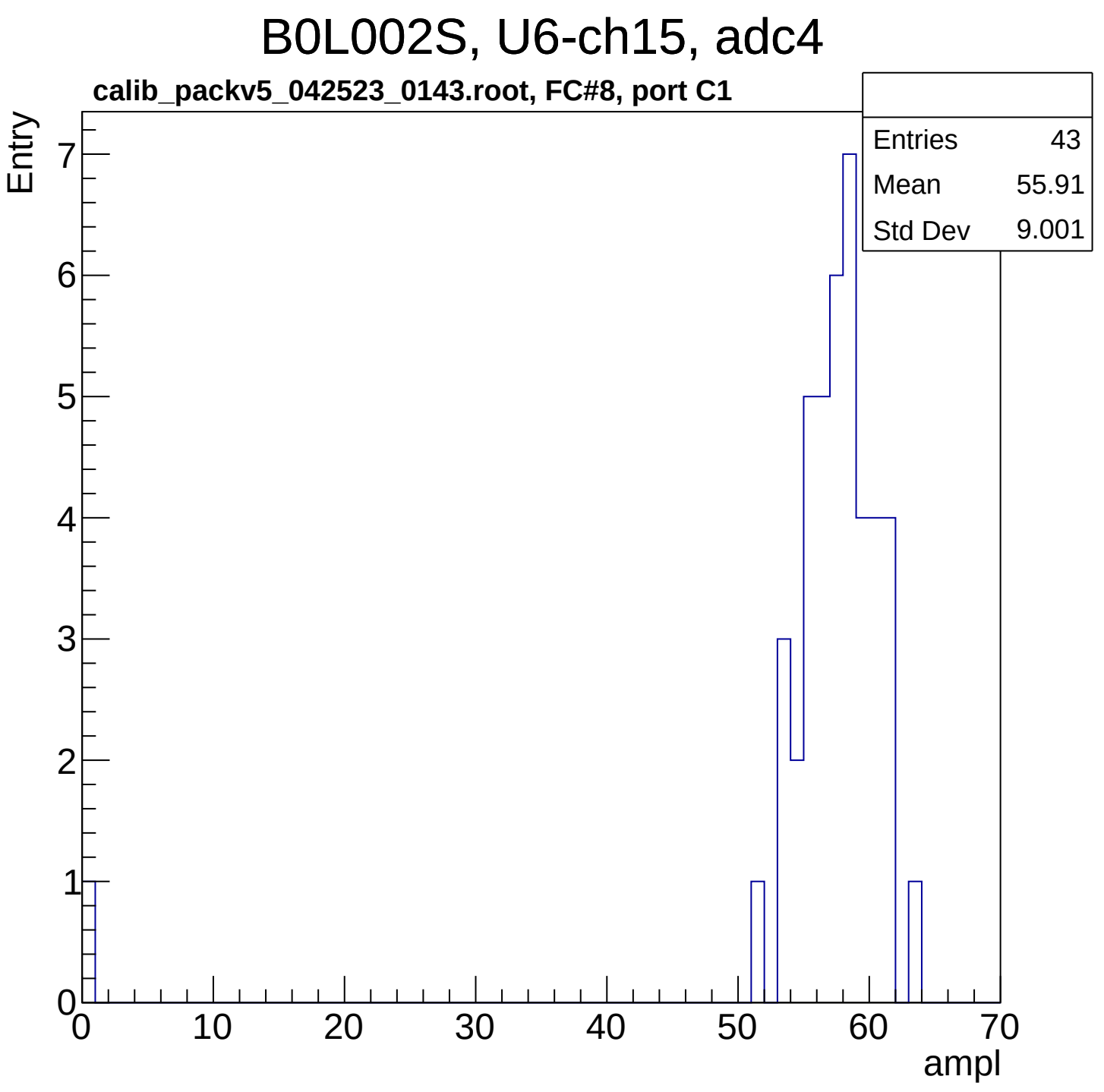
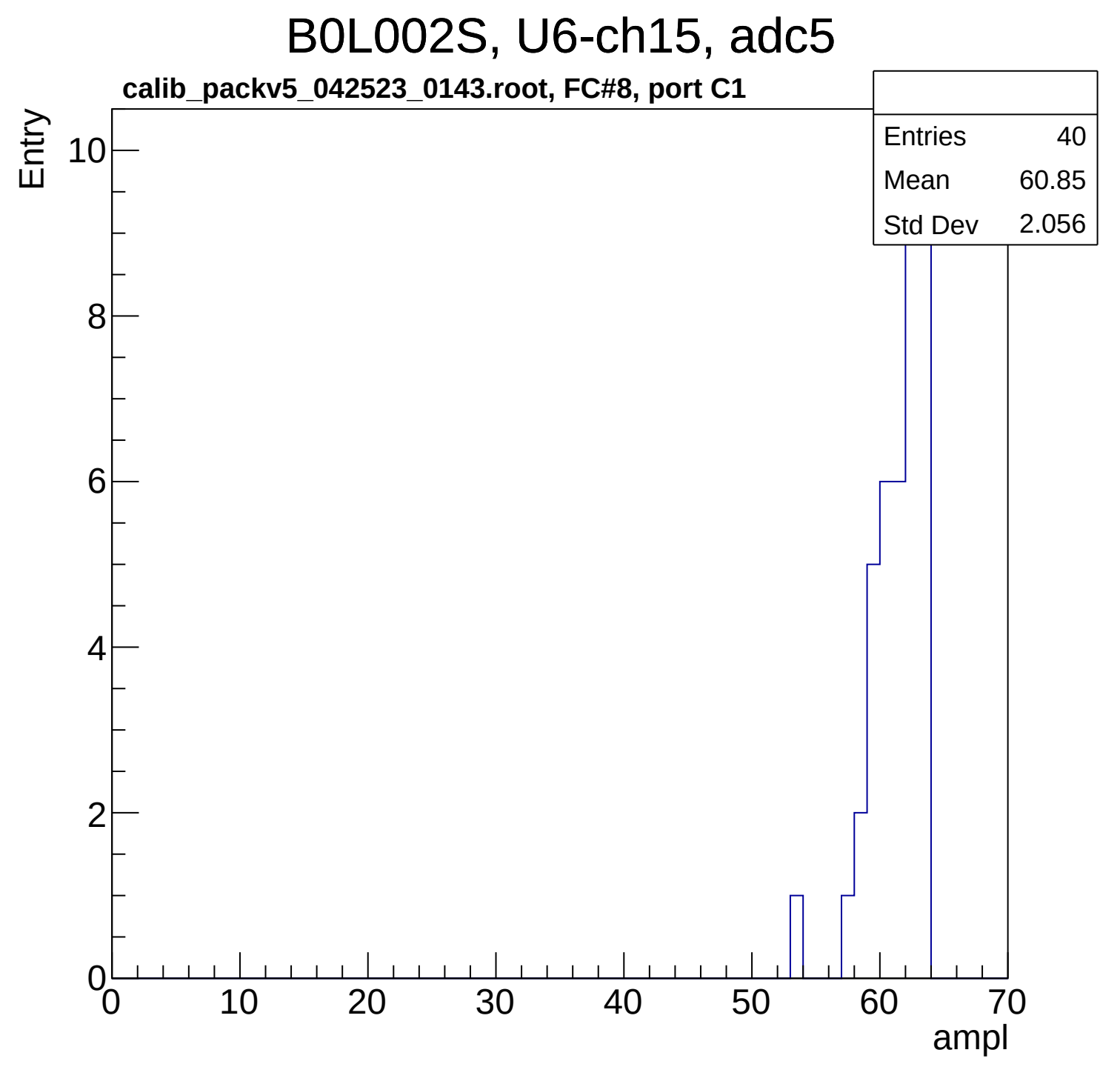
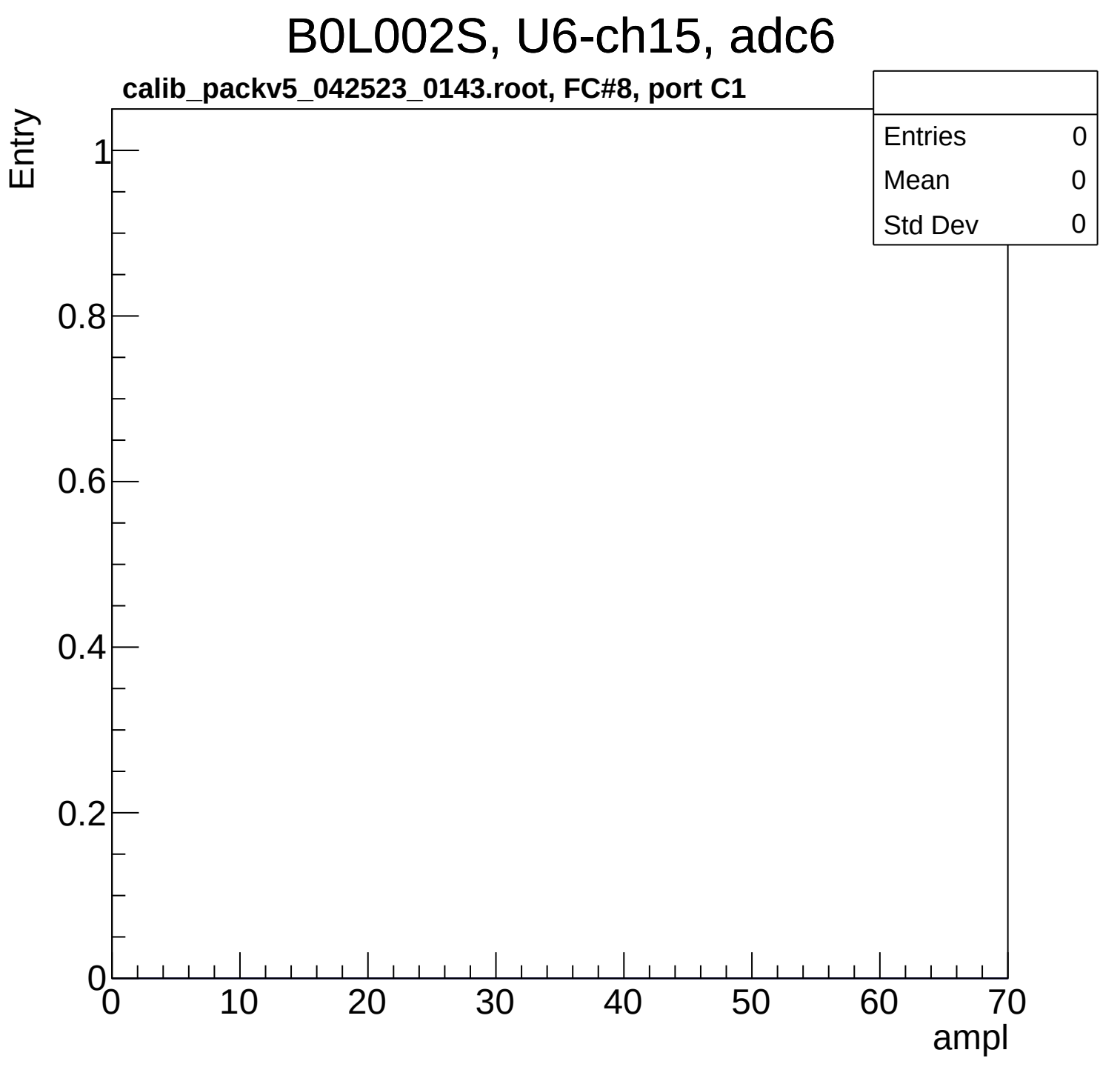

B0L002S, U6-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.38
Std Dev 2.96
Gaus mean : 38.1580
Gaus Width: 2.8669
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

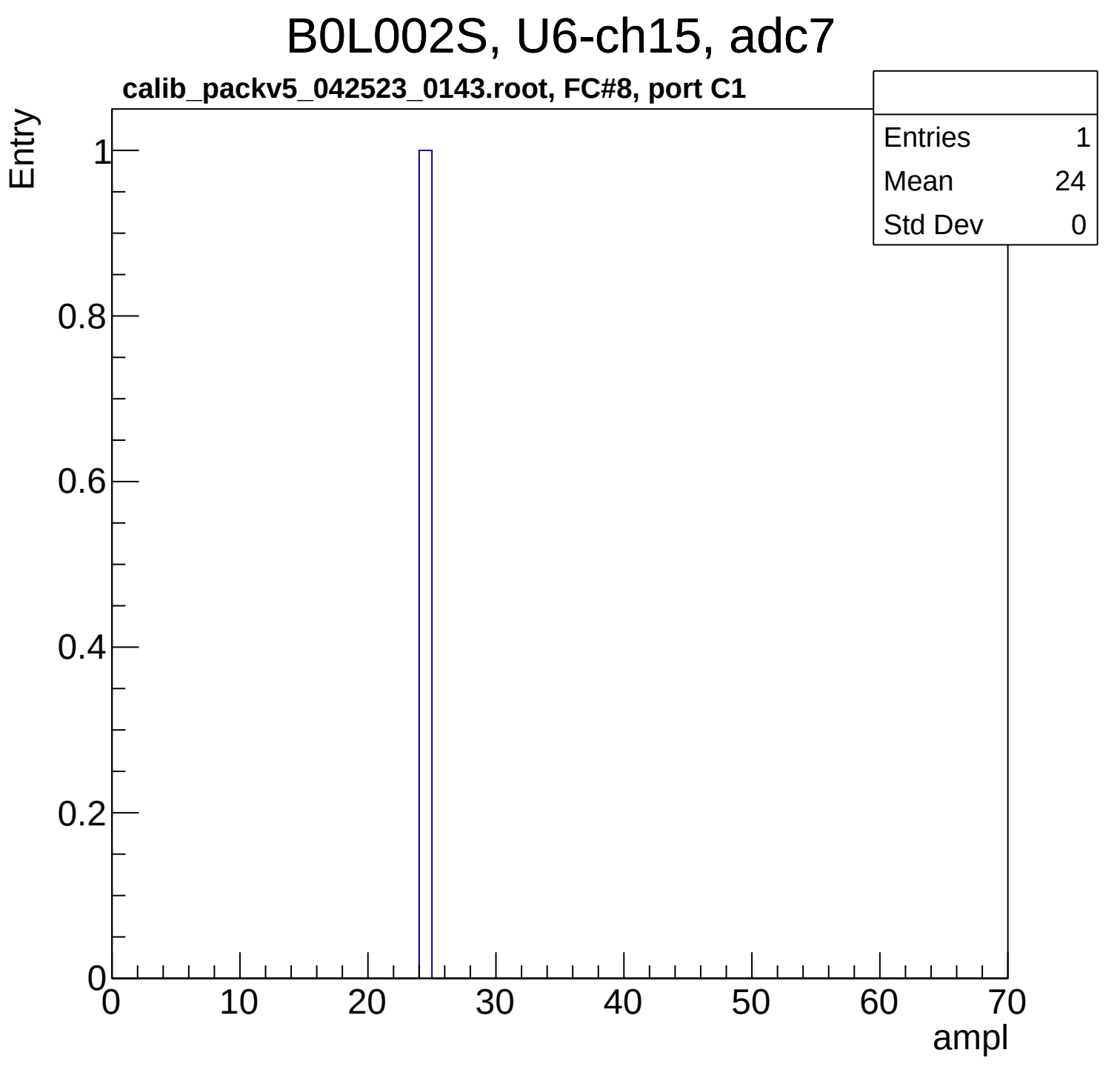

B0L002S, U6-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.21
Std Dev 3.139
Gaus mean : 45.1166
Gaus Width: 3.4790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.42
Std Dev 3.477
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 55.91
Std Dev 9.001
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 60.85
Std Dev 2.056
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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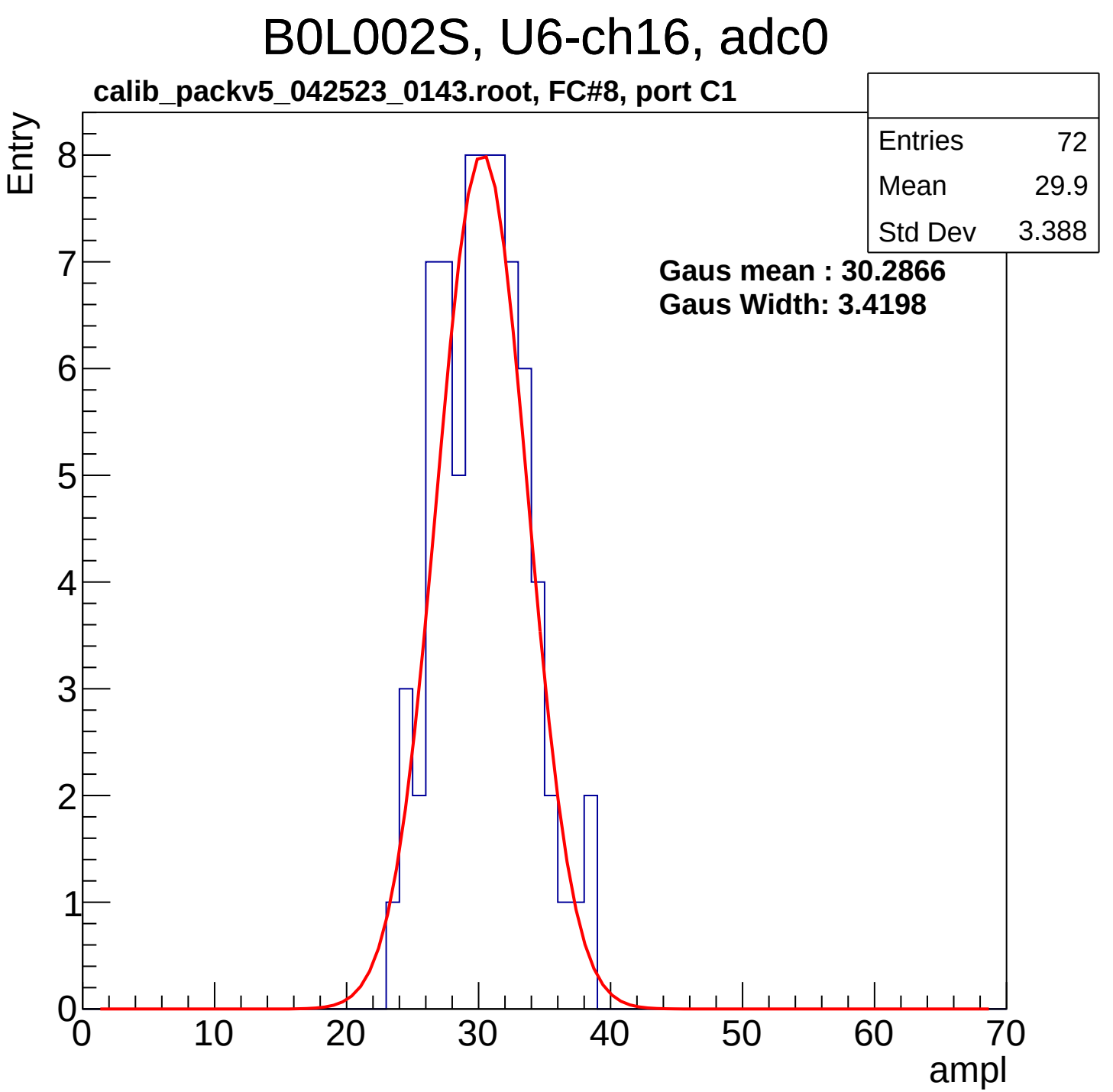
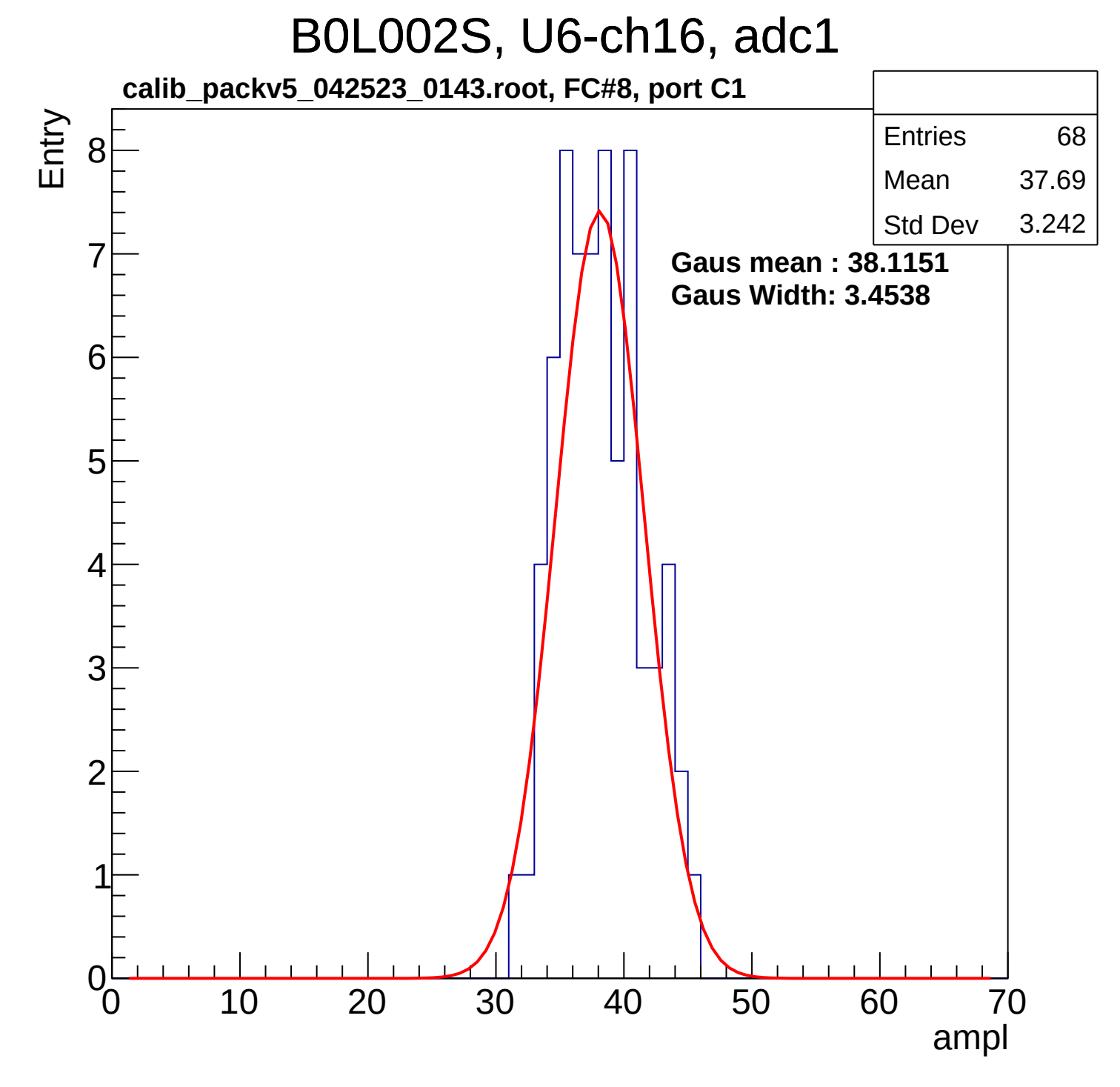
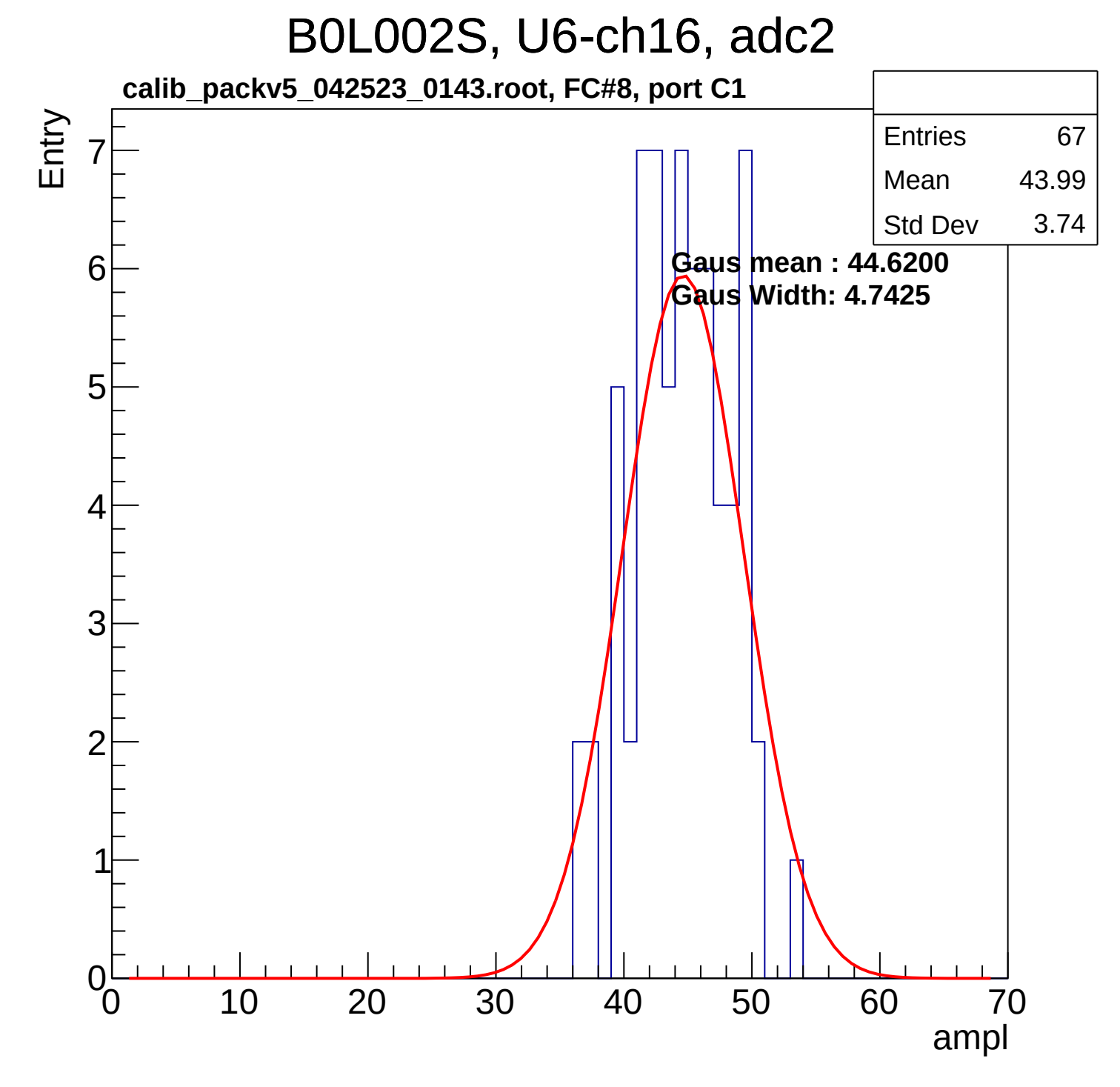
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.9
Std Dev 3.388
Gaus mean : 30.2866
Gaus Width: 3.4198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.69
Std Dev 3.242
Gaus mean : 38.1151
Gaus Width: 3.4538
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.99
Std Dev 3.74
Gaus mean : 44.6200
Gaus Width: 4.7425
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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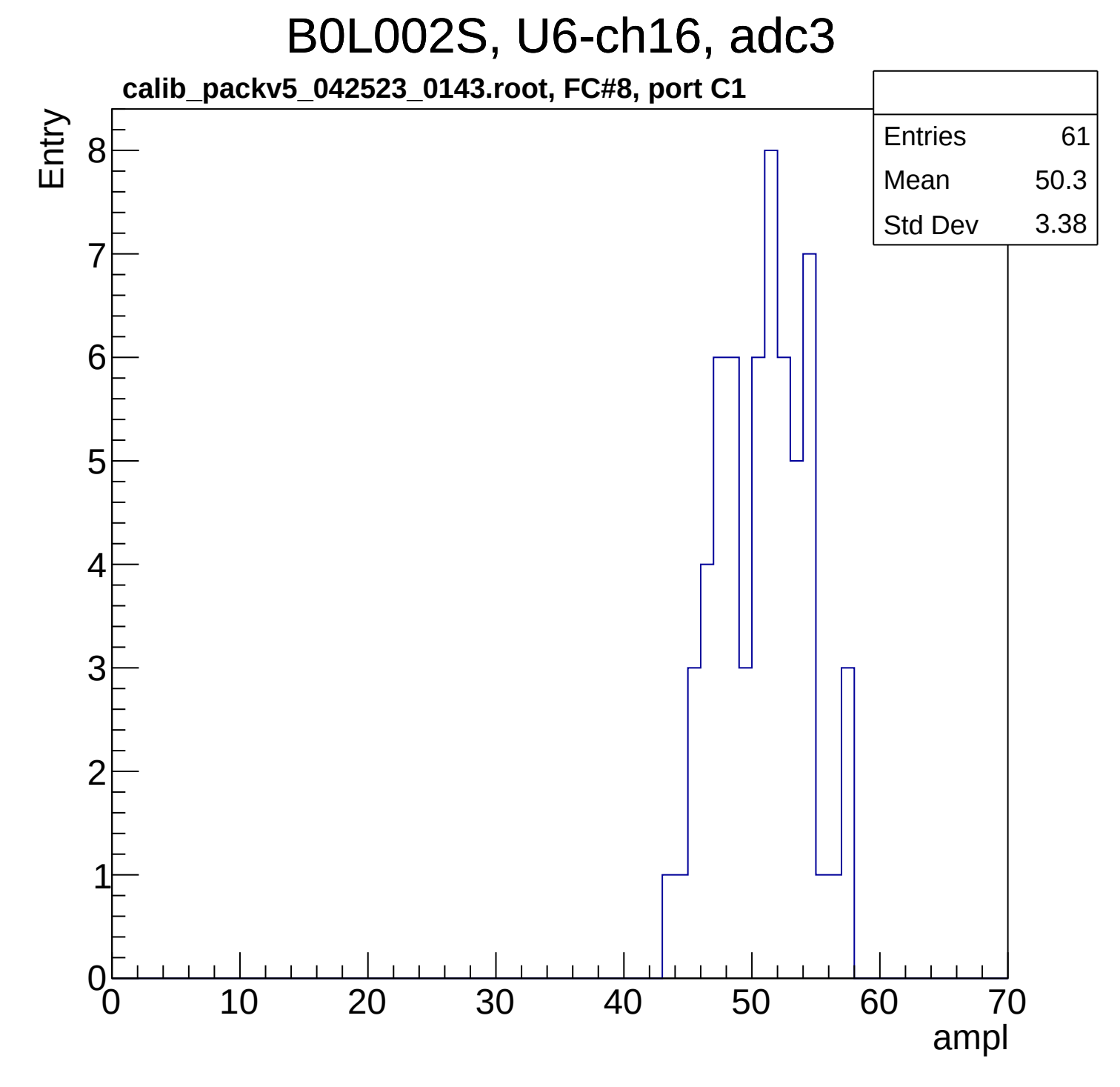
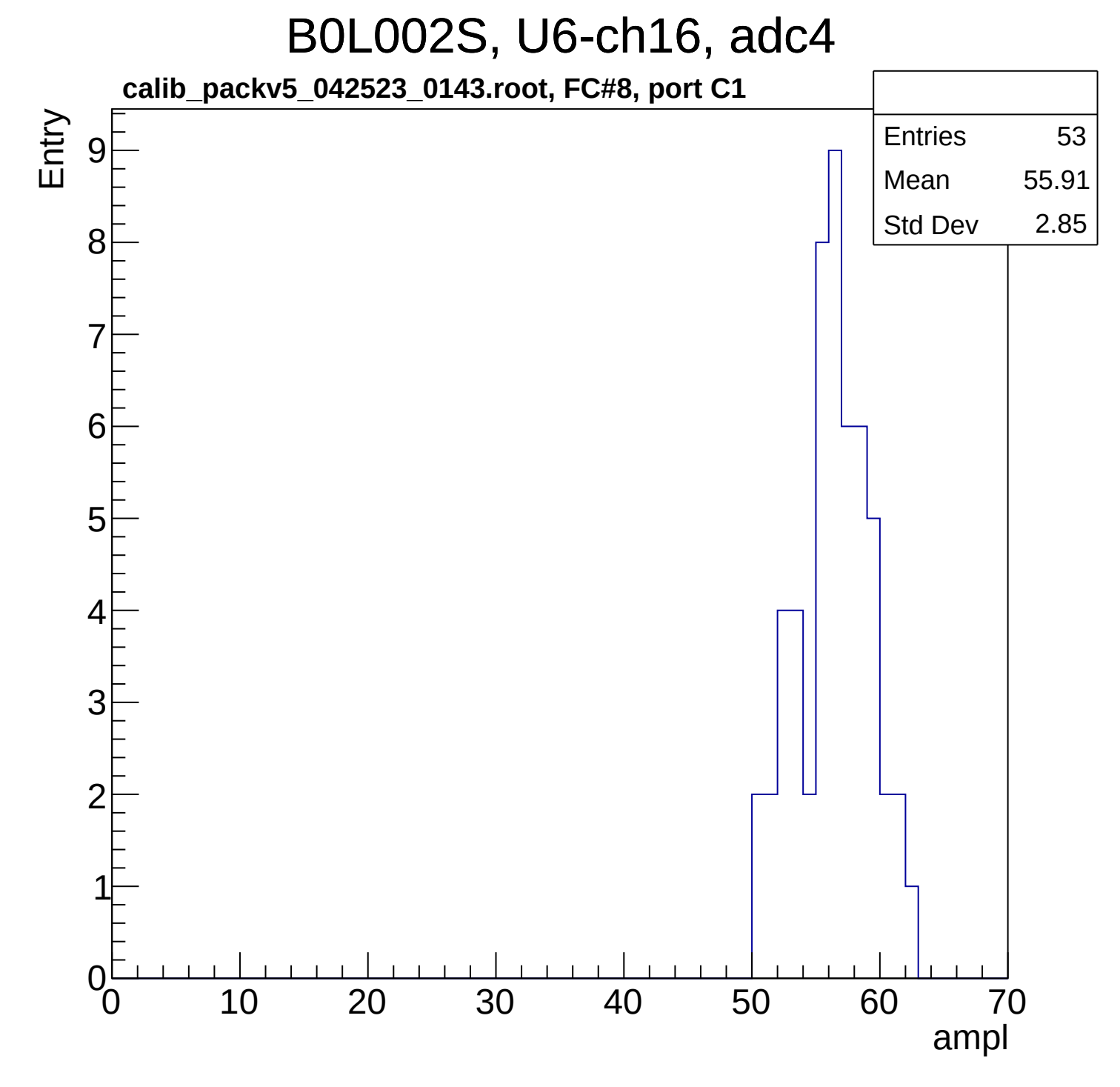
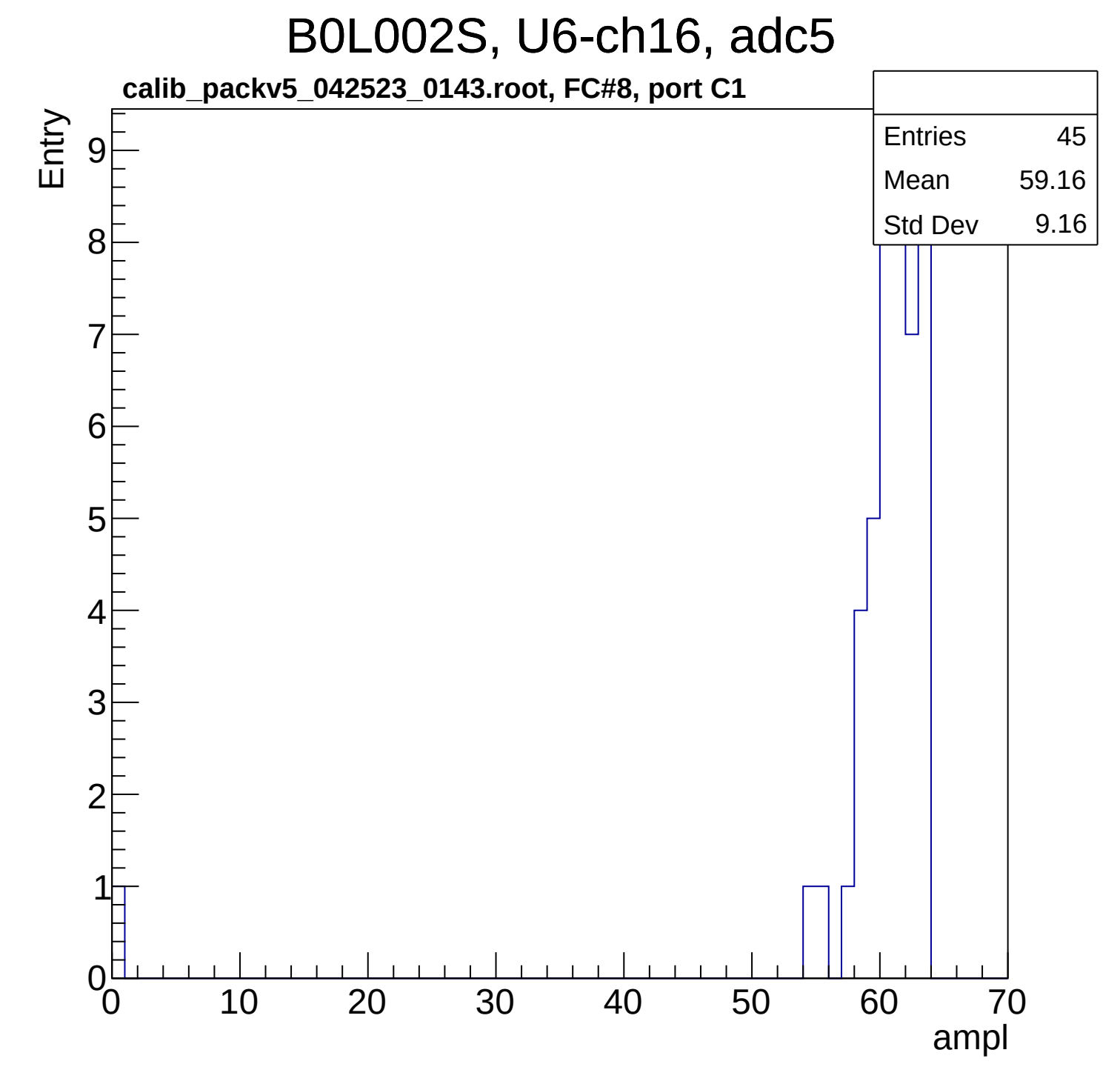
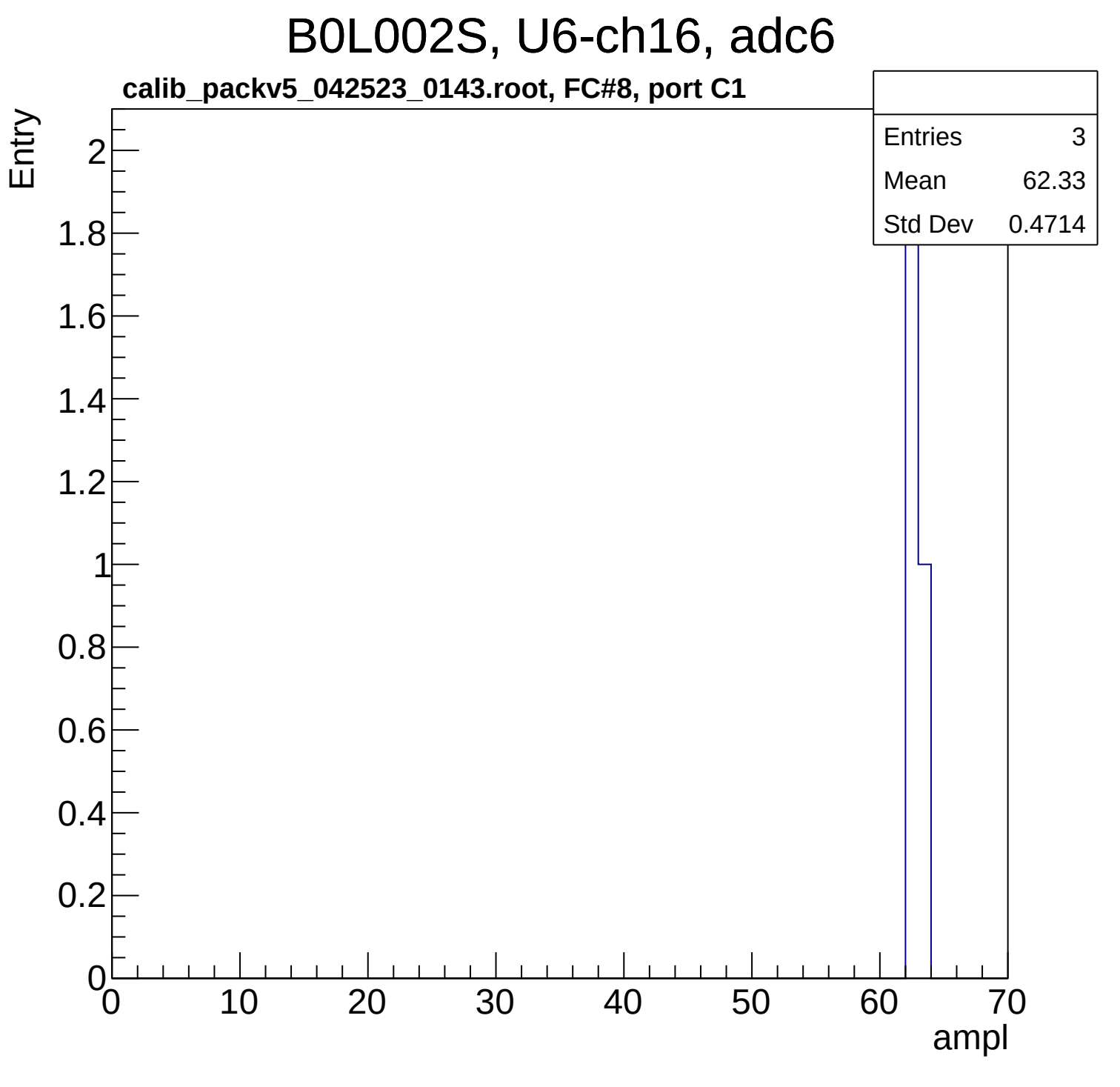
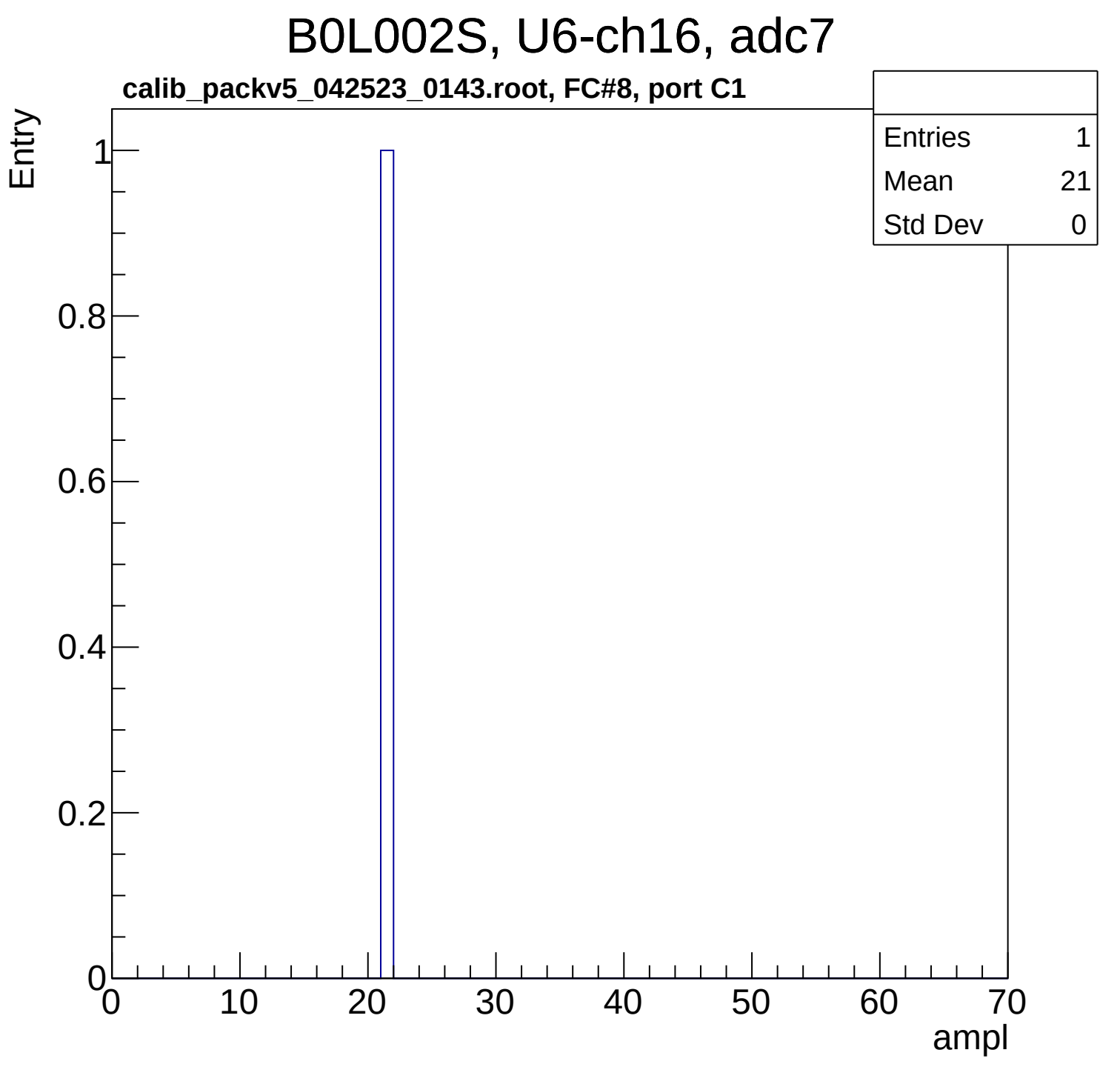
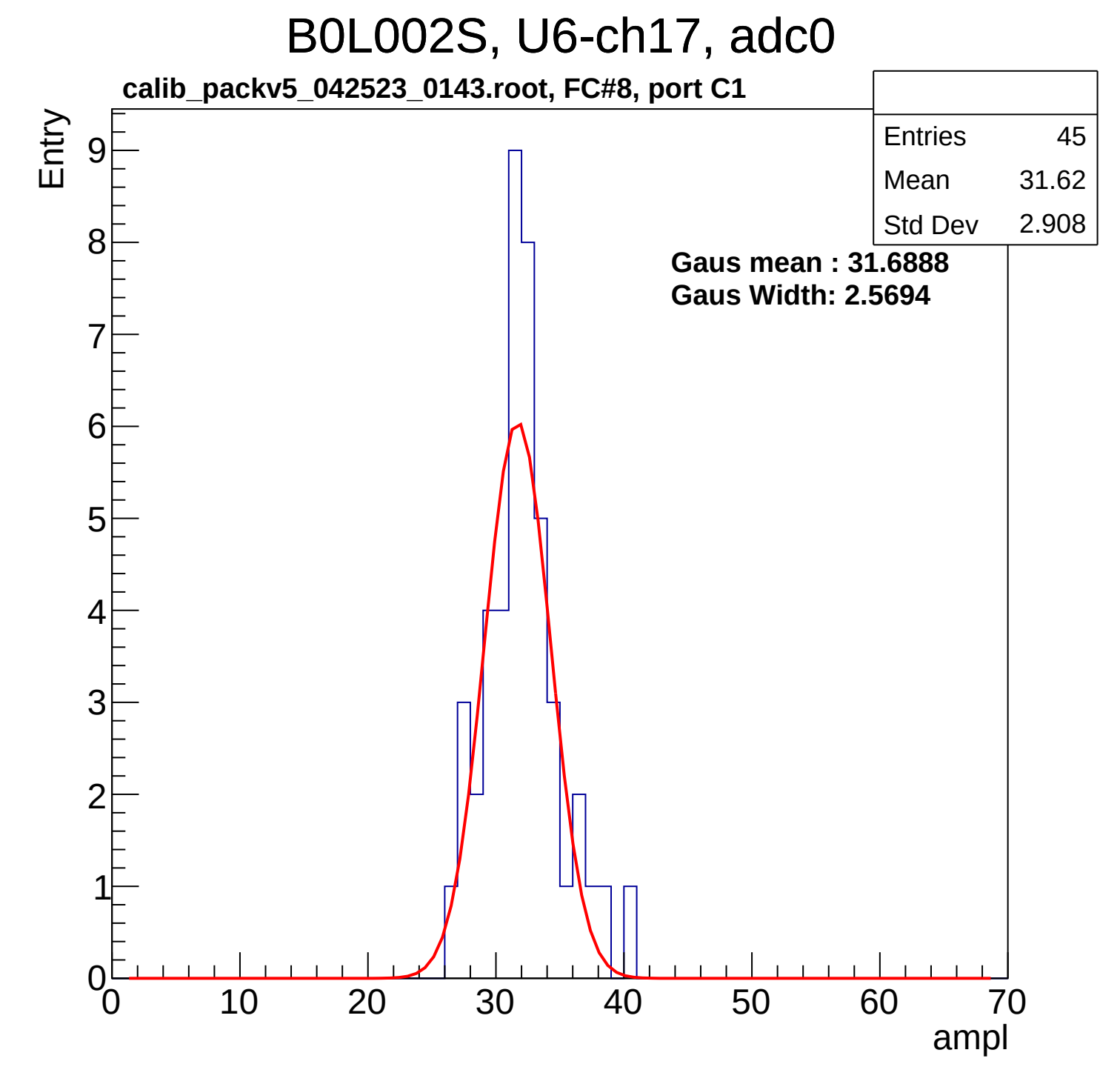

B0L002S, U6-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.3
Std Dev 3.38
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

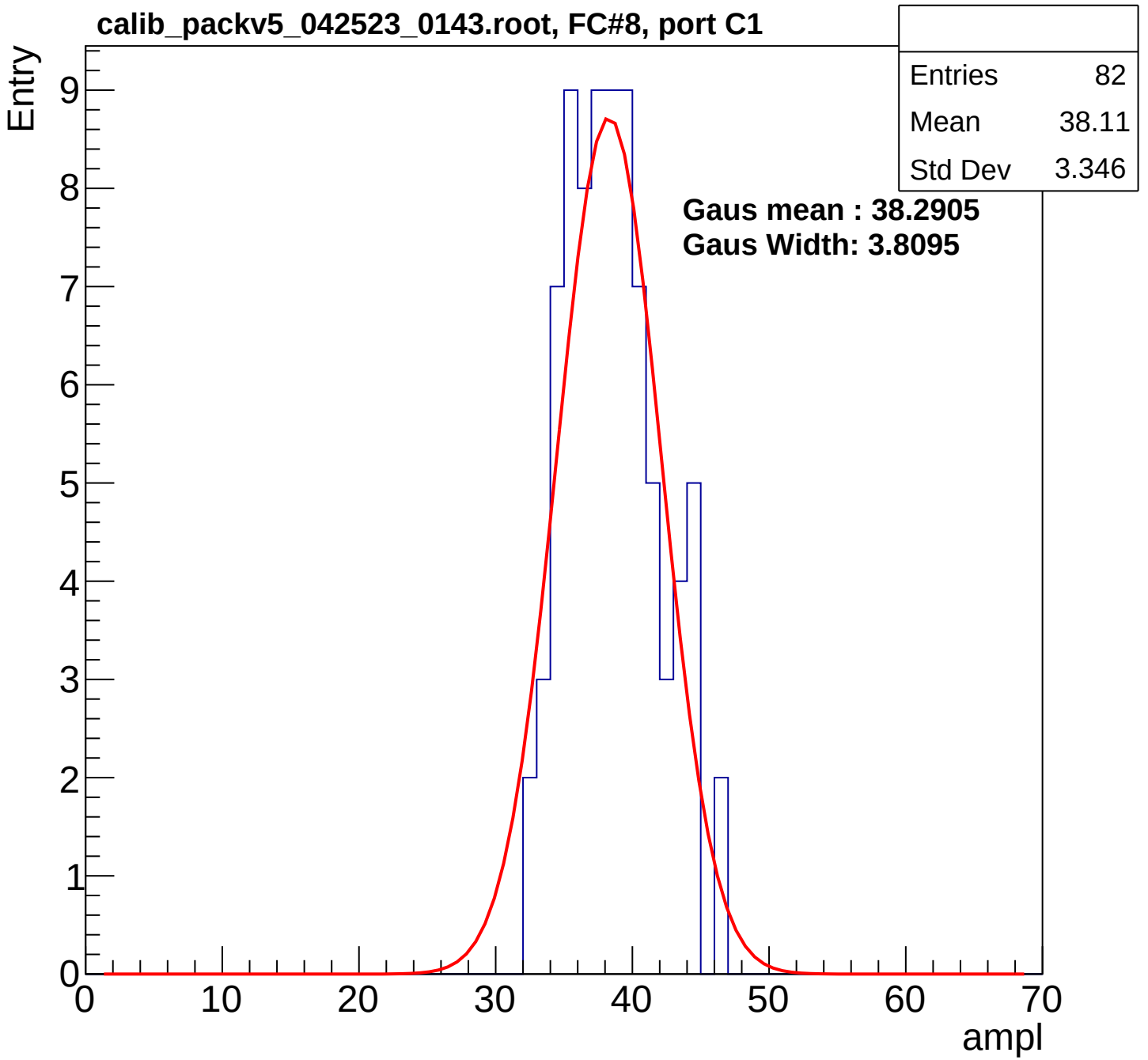

B0L002S, U6-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 55.91
Std Dev 2.85
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

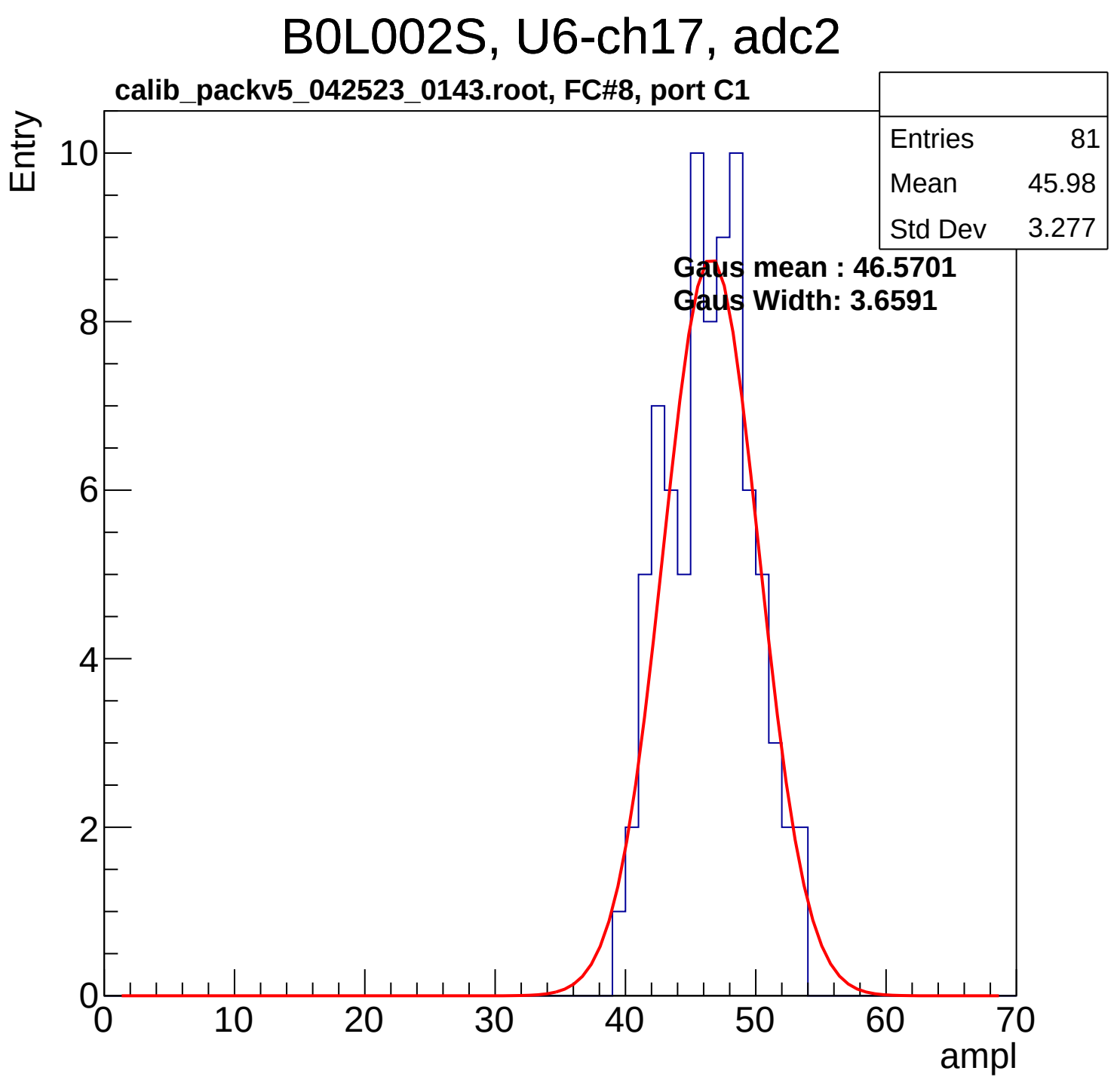

B0L002S, U6-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.16
Std Dev 9.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 31.62
Std Dev 2.908
Gaus mean : 31.6888
Gaus Width: 2.5694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 38.11
Std Dev 3.346
Gaus mean : 38.2905
Gaus Width: 3.8095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 45.98
Std Dev 3.277
Gaus mean : 46.5701
Gaus Width: 3.6591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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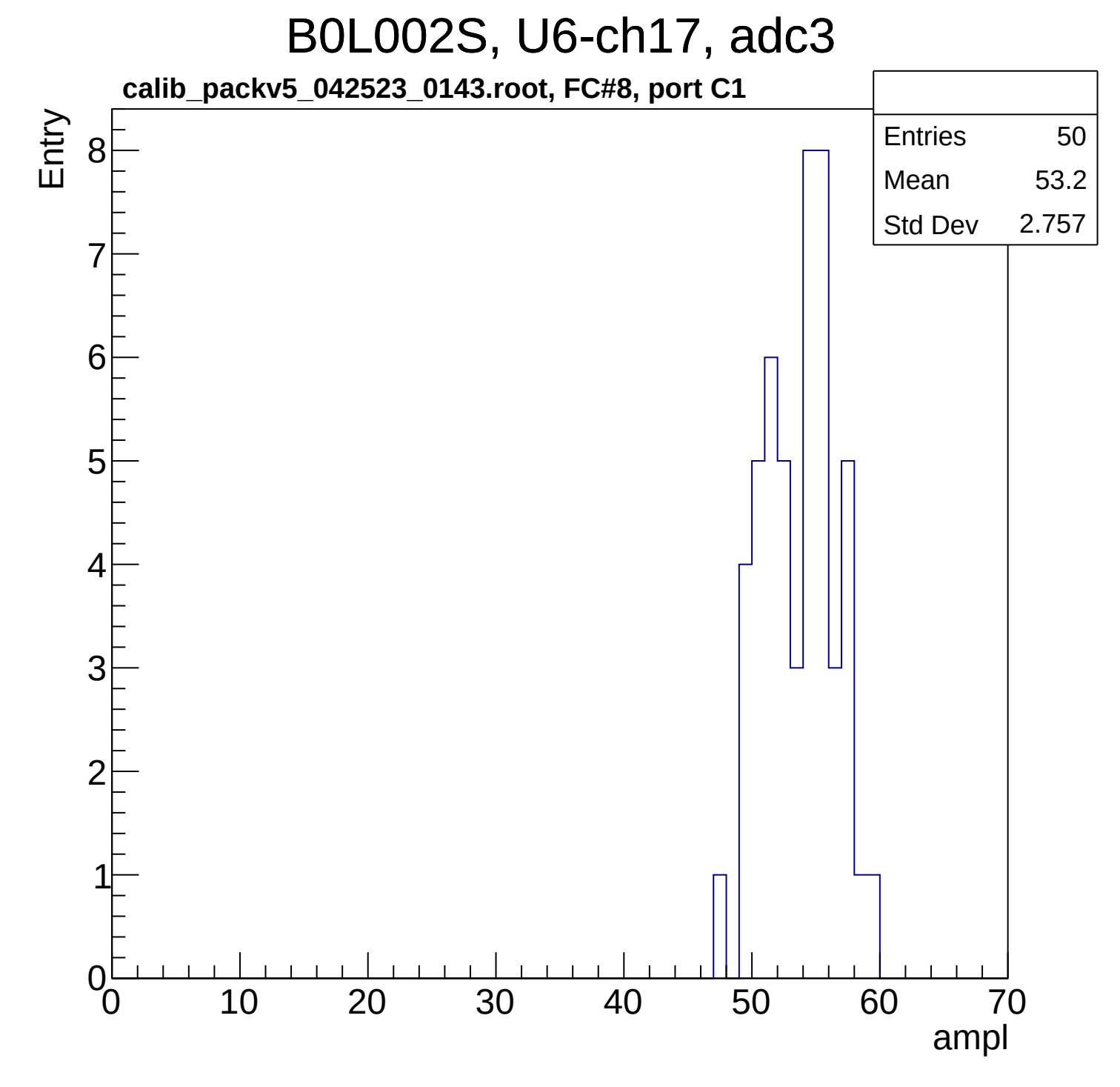
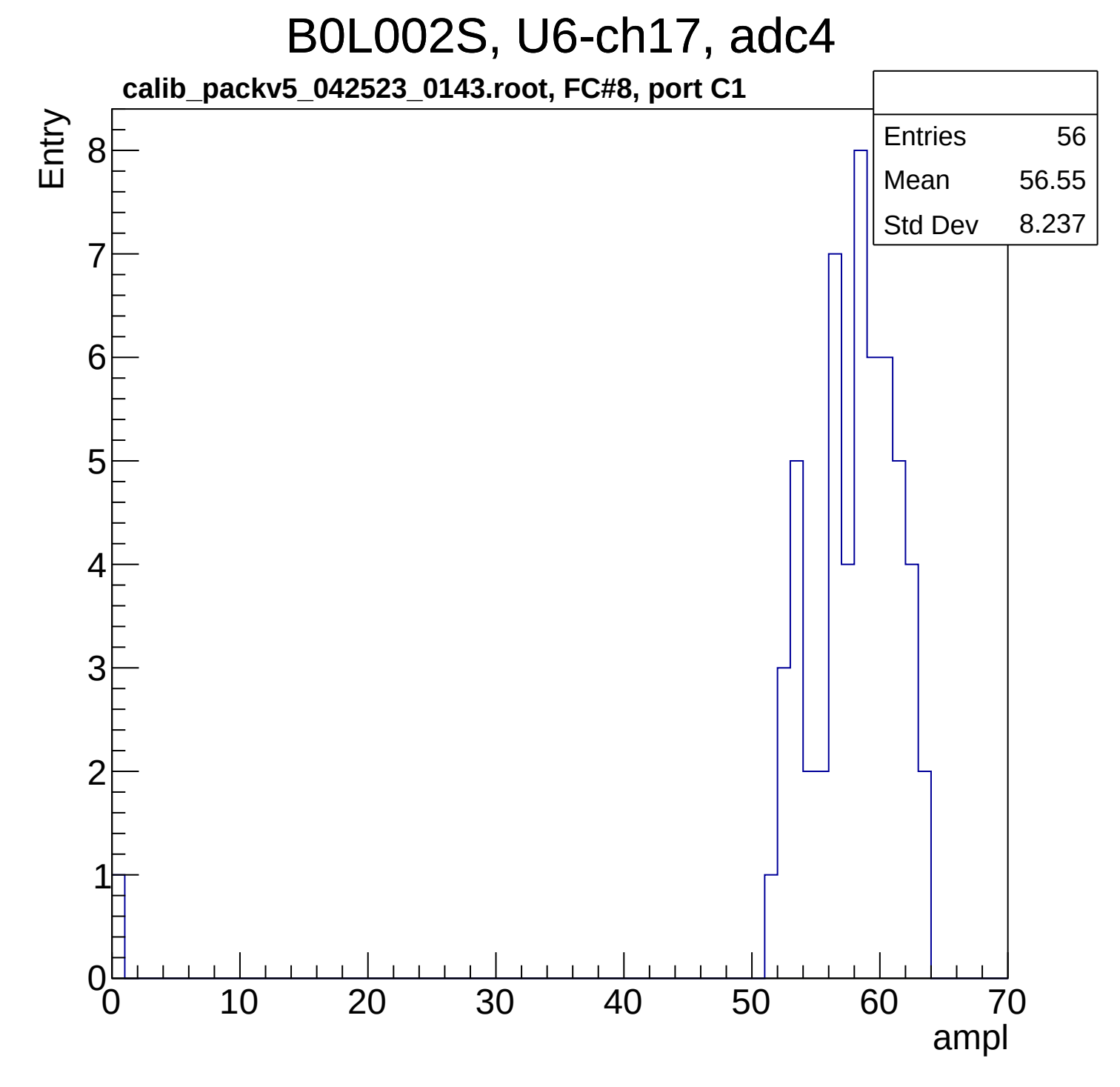
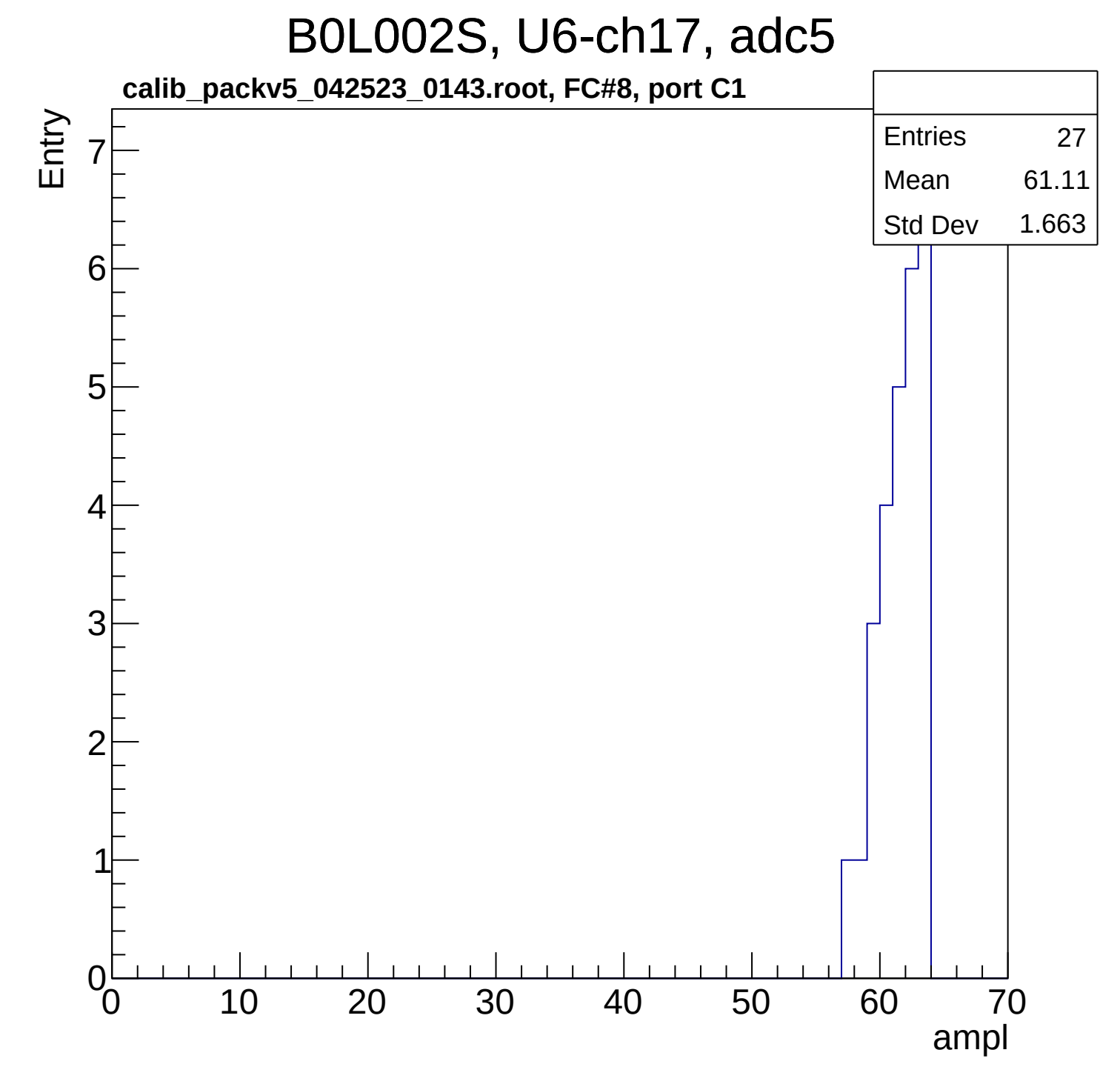
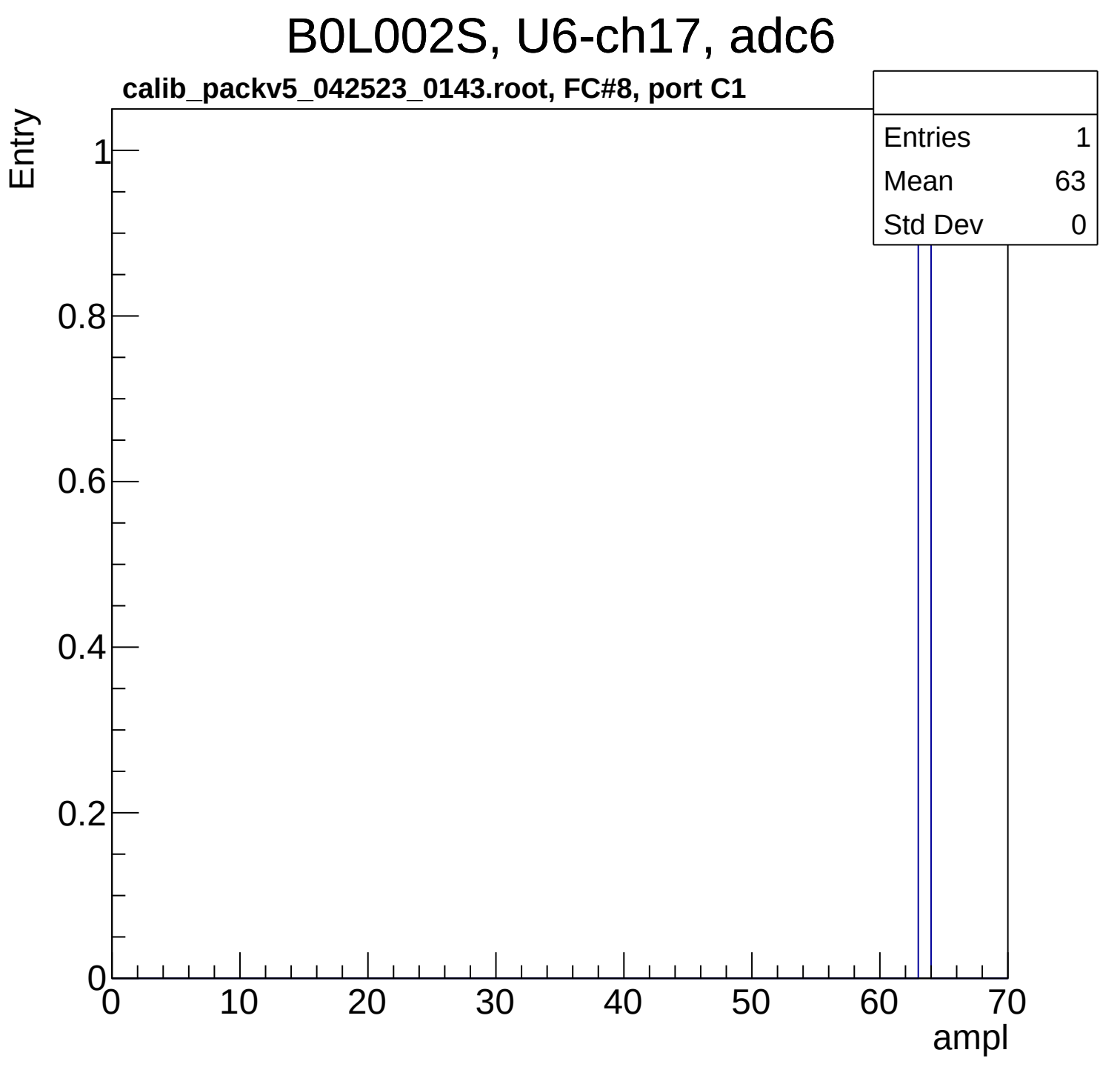
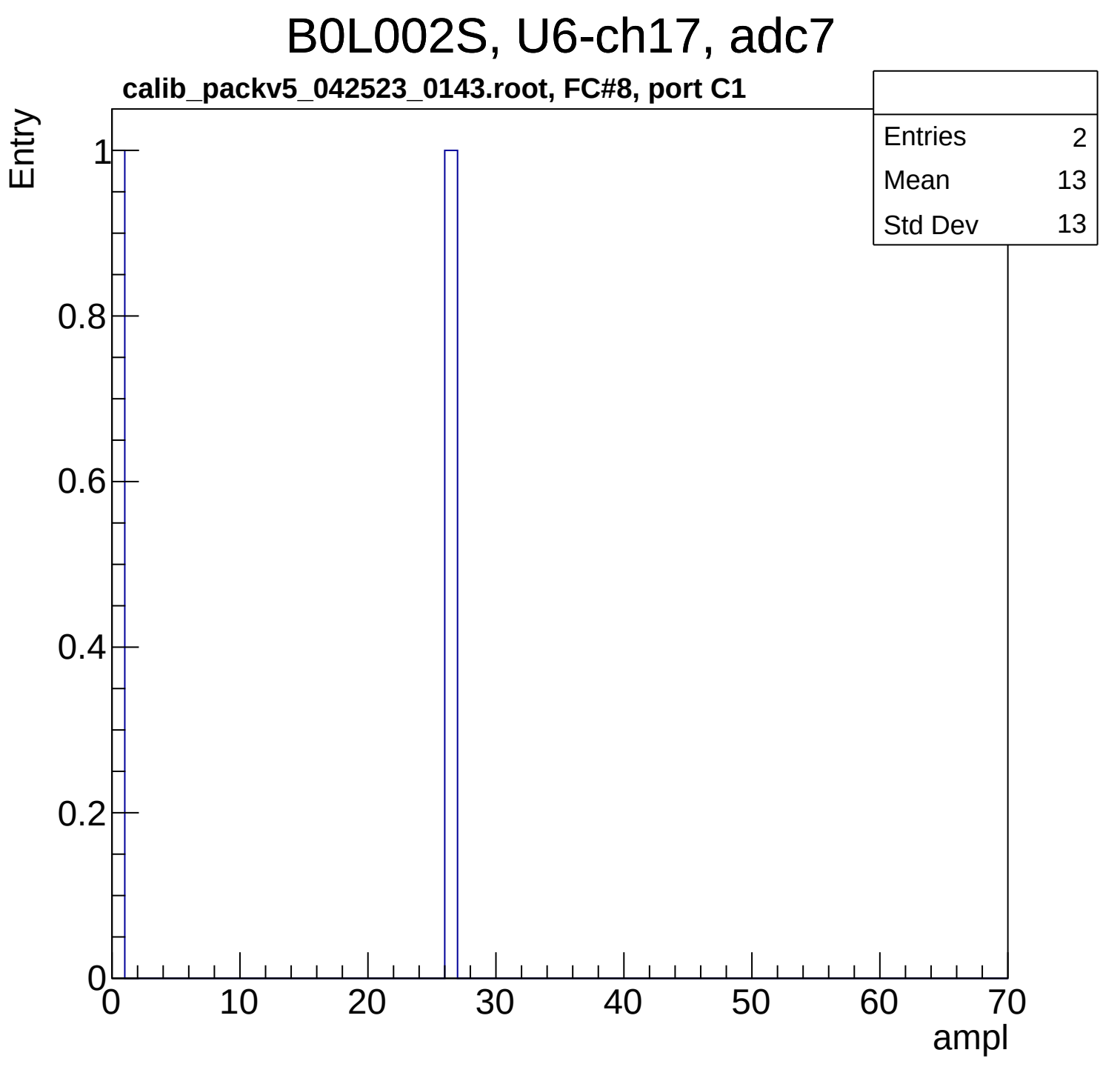
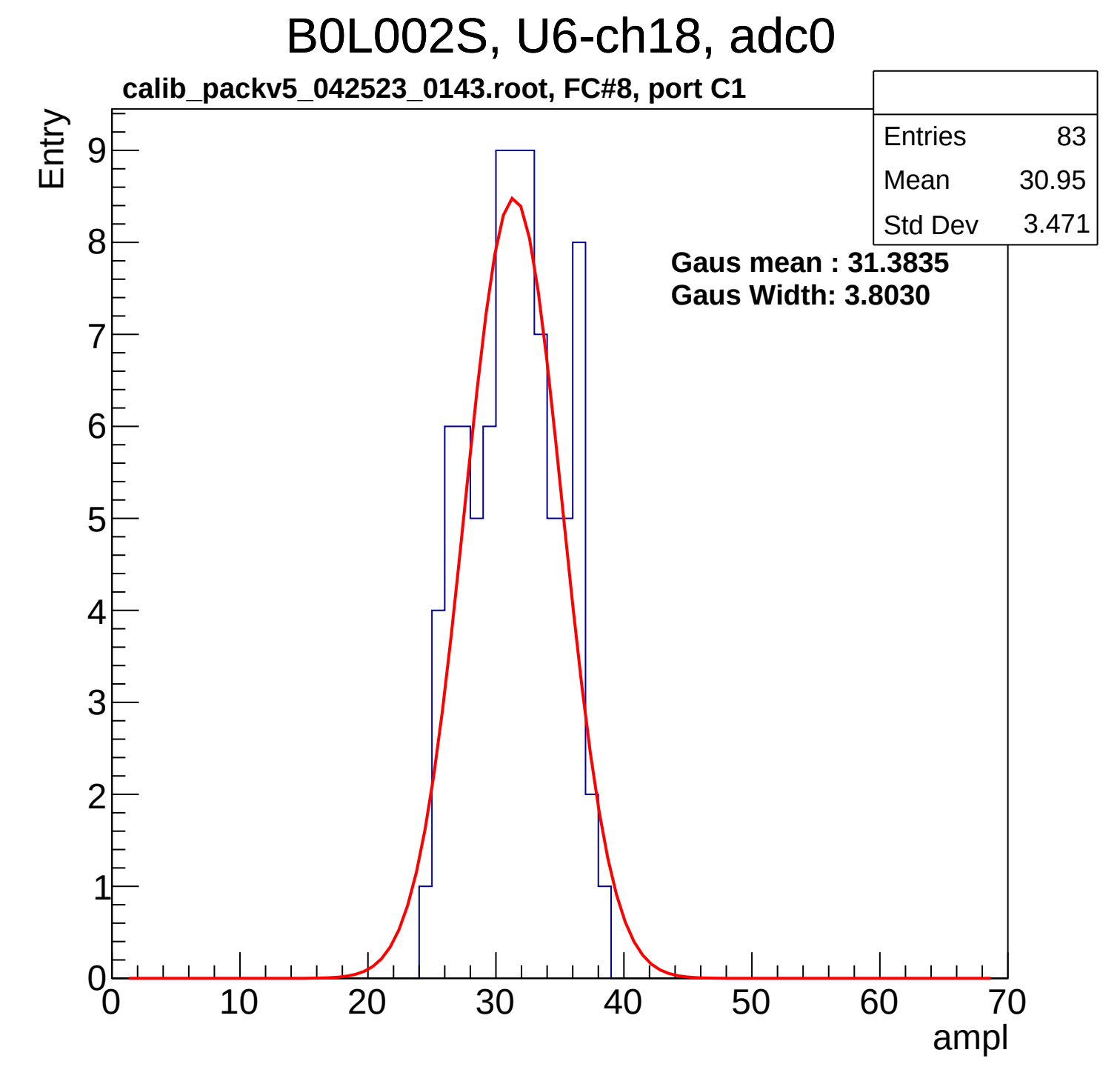

B0L002S, U6-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 53.2
Std Dev 2.757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

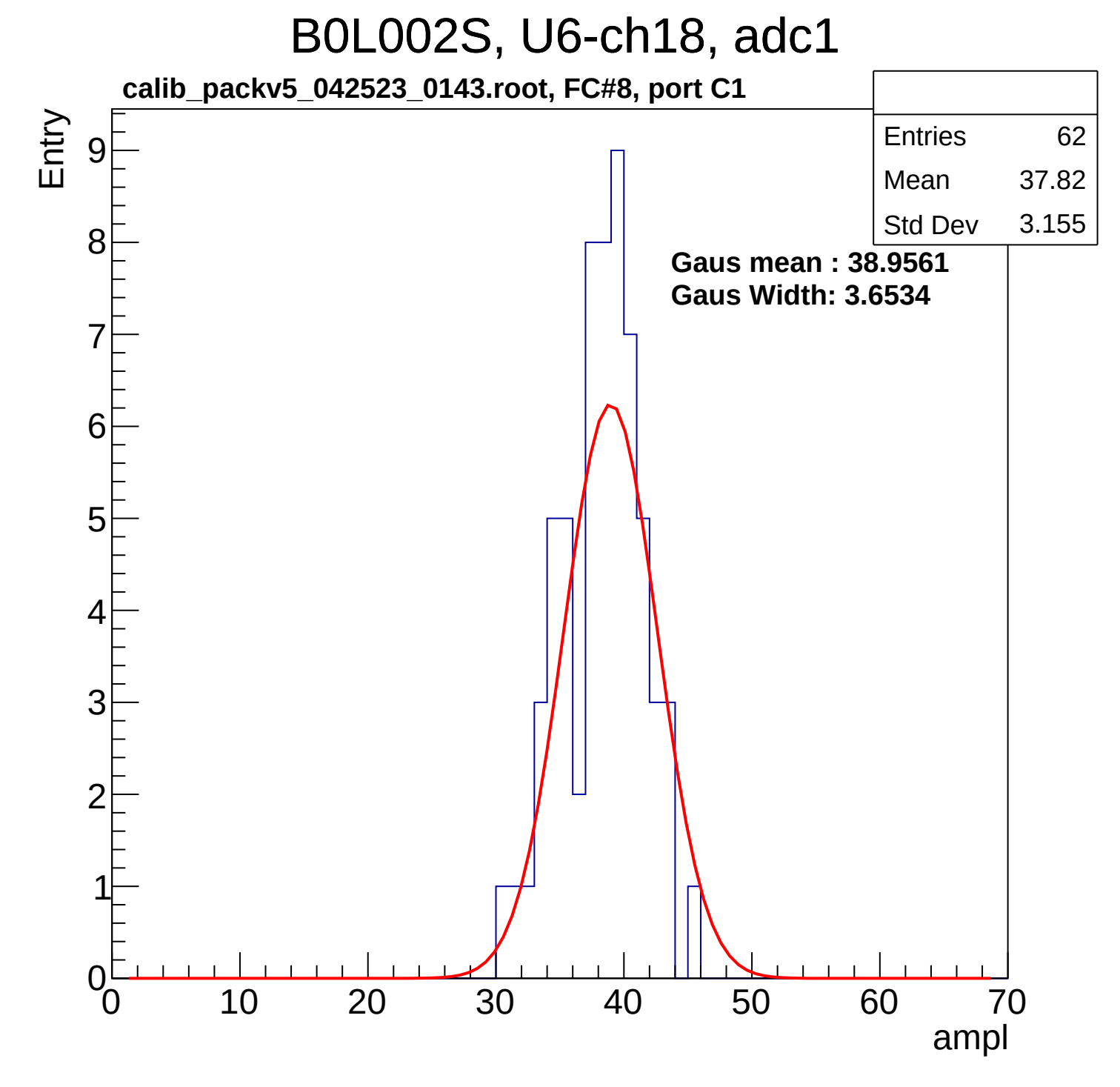

B0L002S, U6-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.55
Std Dev 8.237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

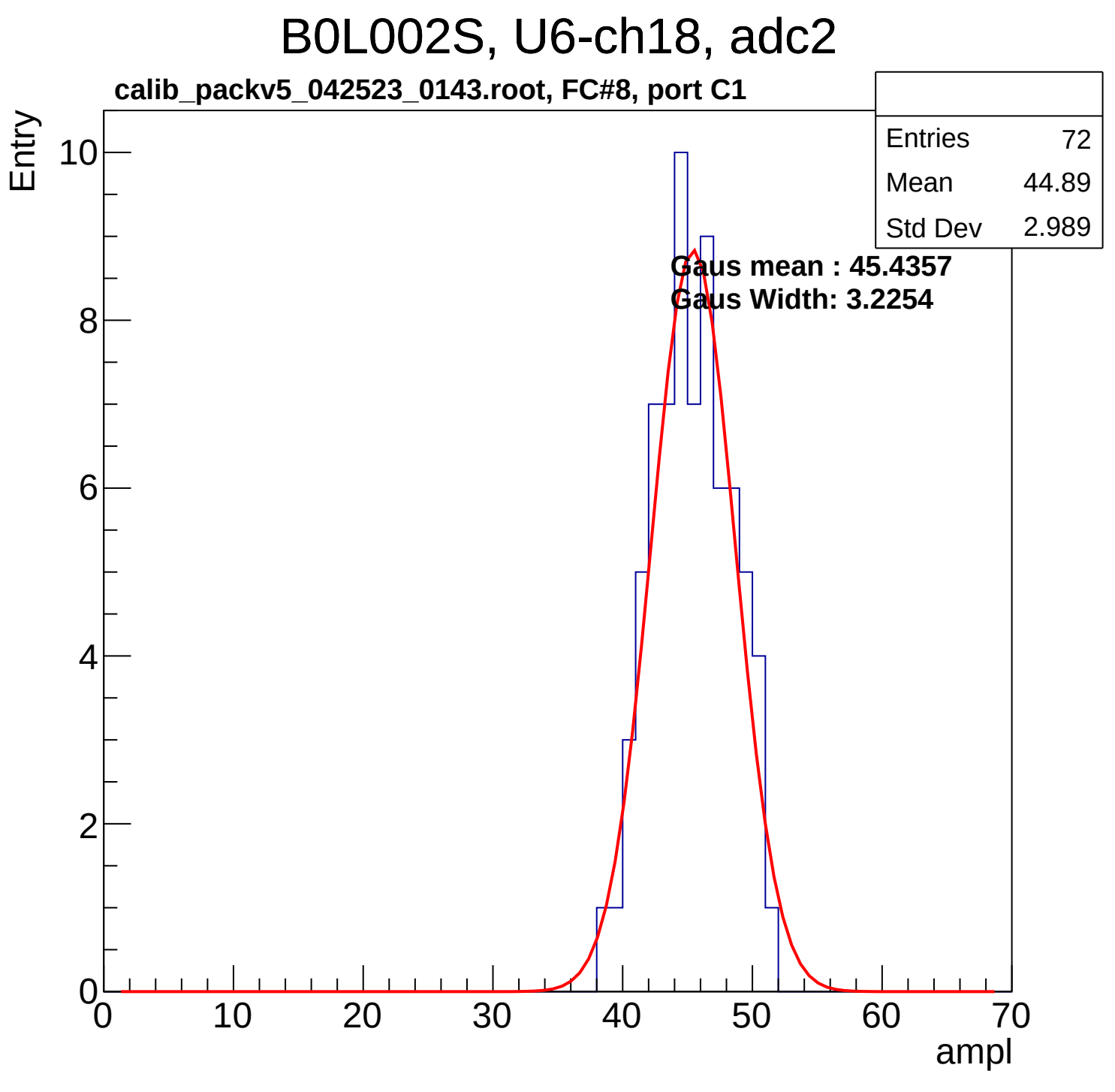

B0L002S, U6-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 61.11
Std Dev 1.663
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

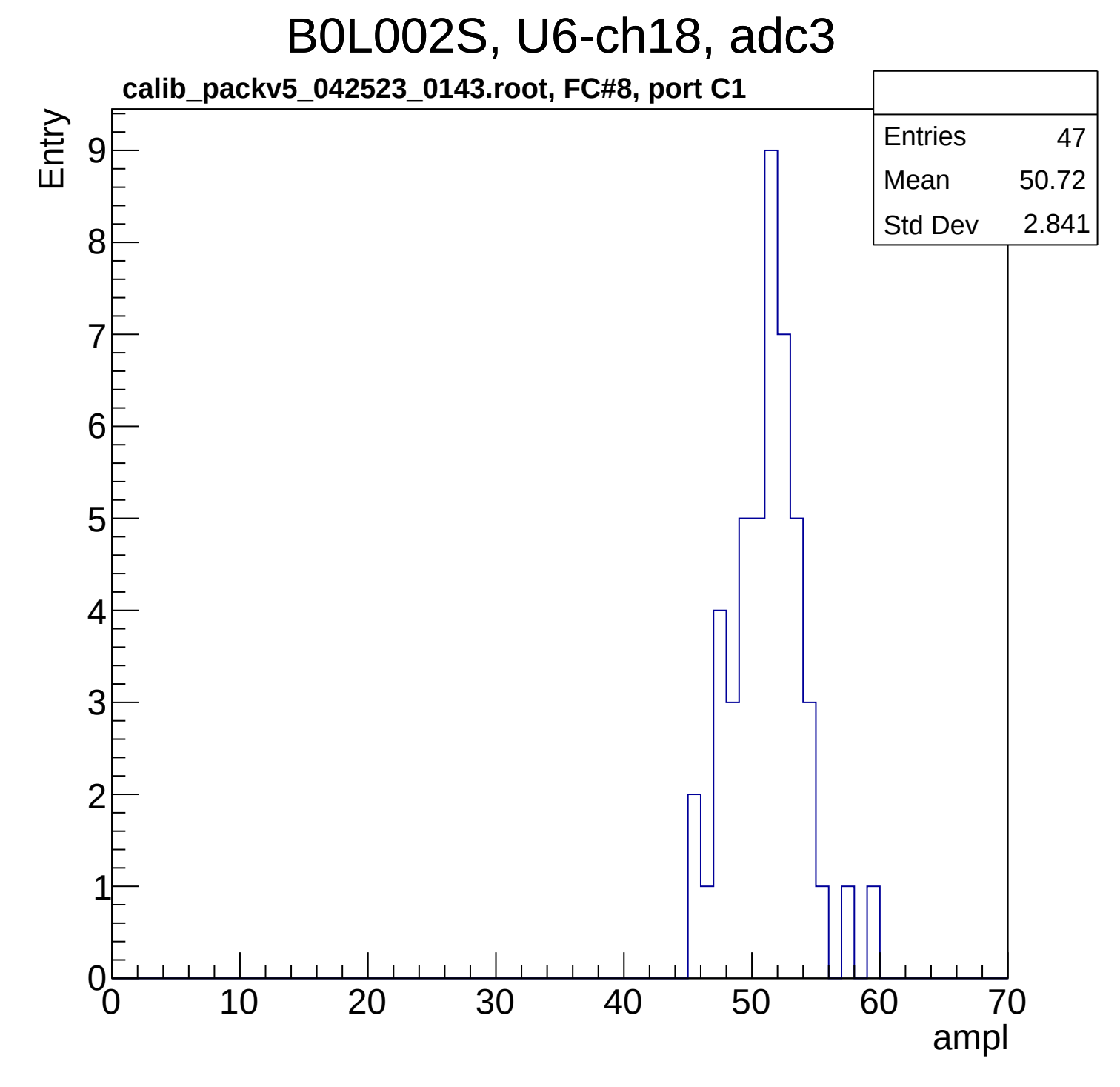

B0L002S, U6-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 30.95
Std Dev 3.471
Gaus mean : 31.3835
Gaus Width: 3.8030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.82
Std Dev 3.155
Gaus mean : 38.9561
Gaus Width: 3.6534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.89
Std Dev 2.989
Gaus mean : 45.4357
Gaus Width: 3.2254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 50.72
Std Dev 2.841
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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60
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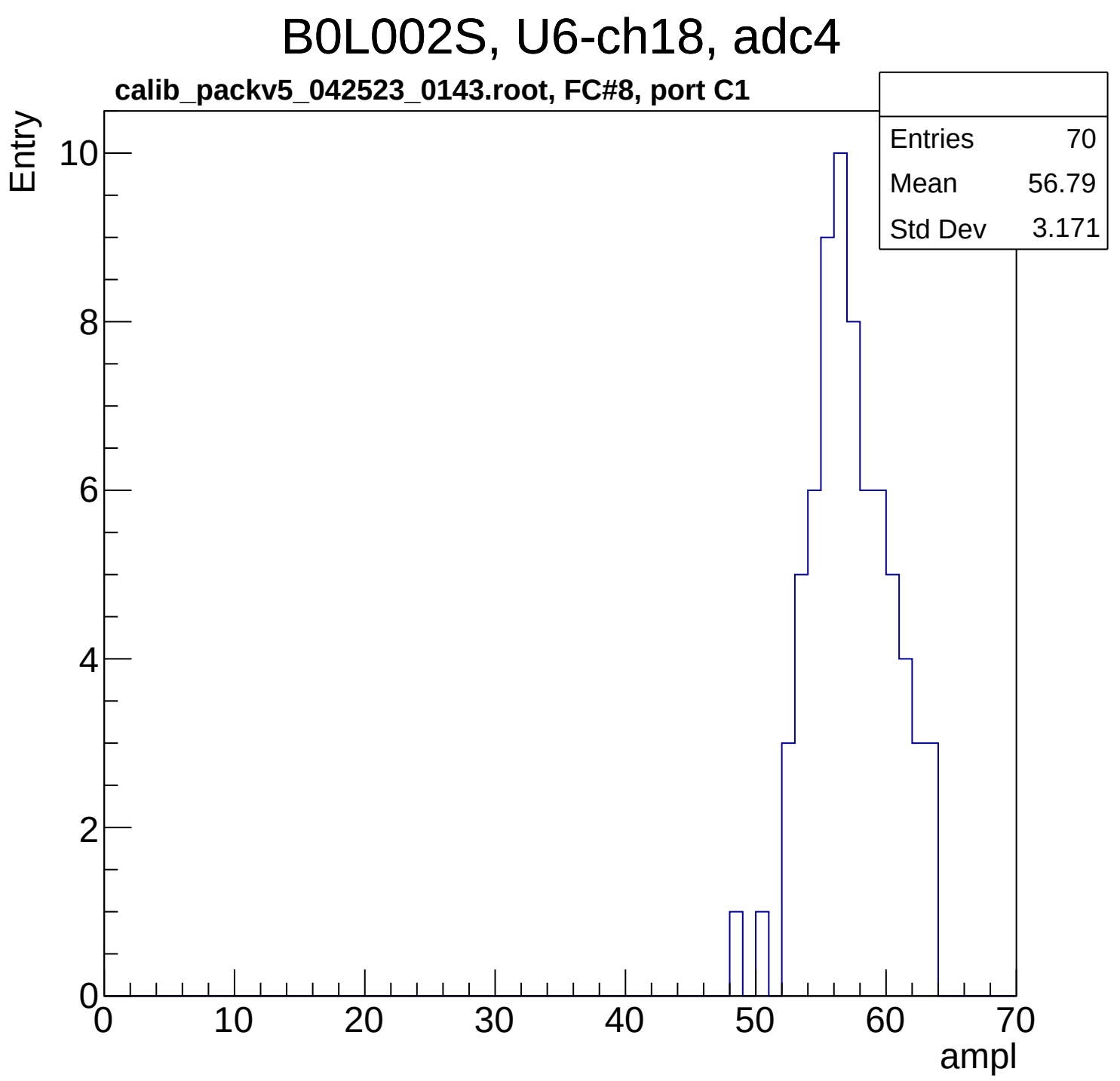
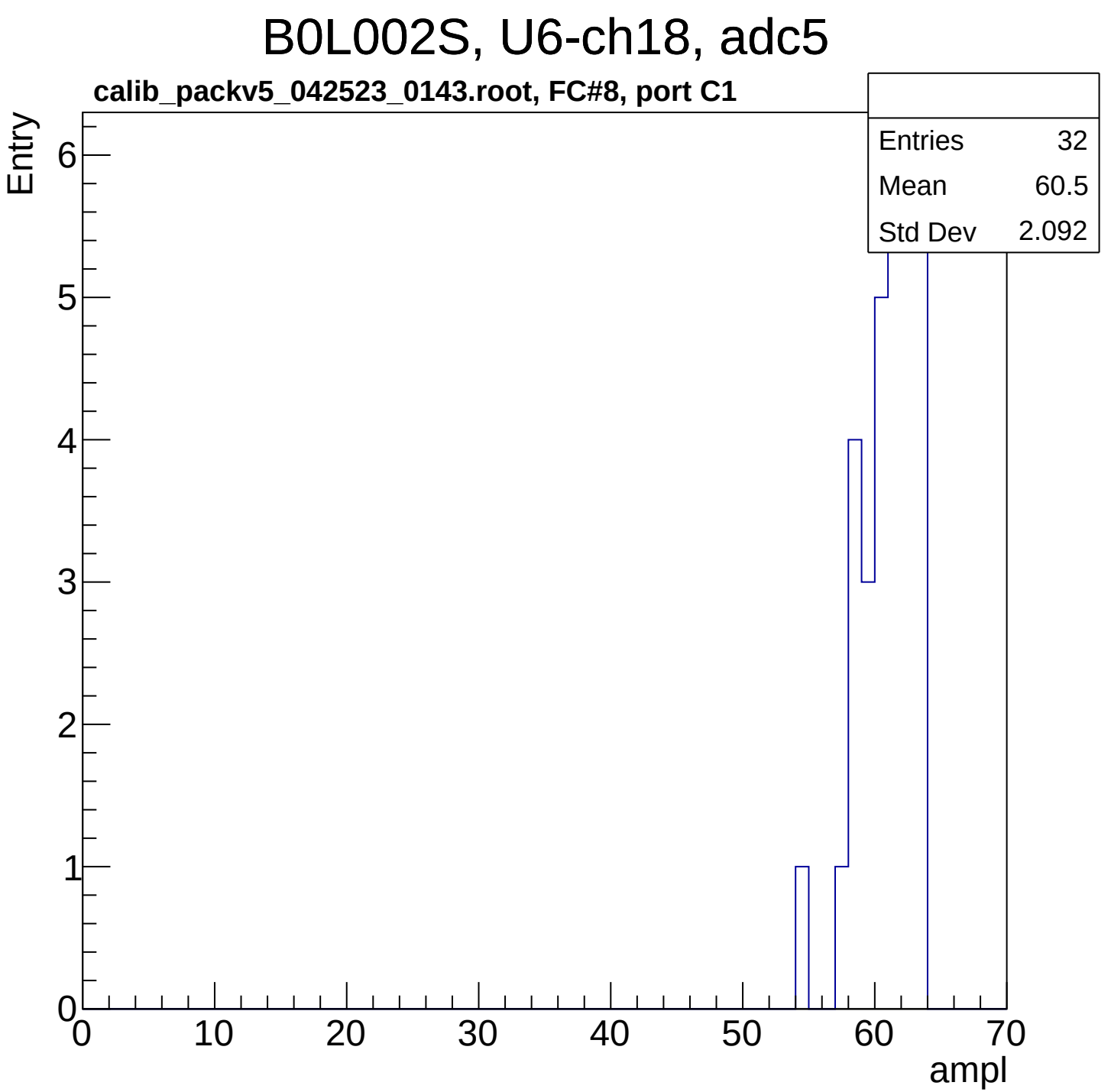
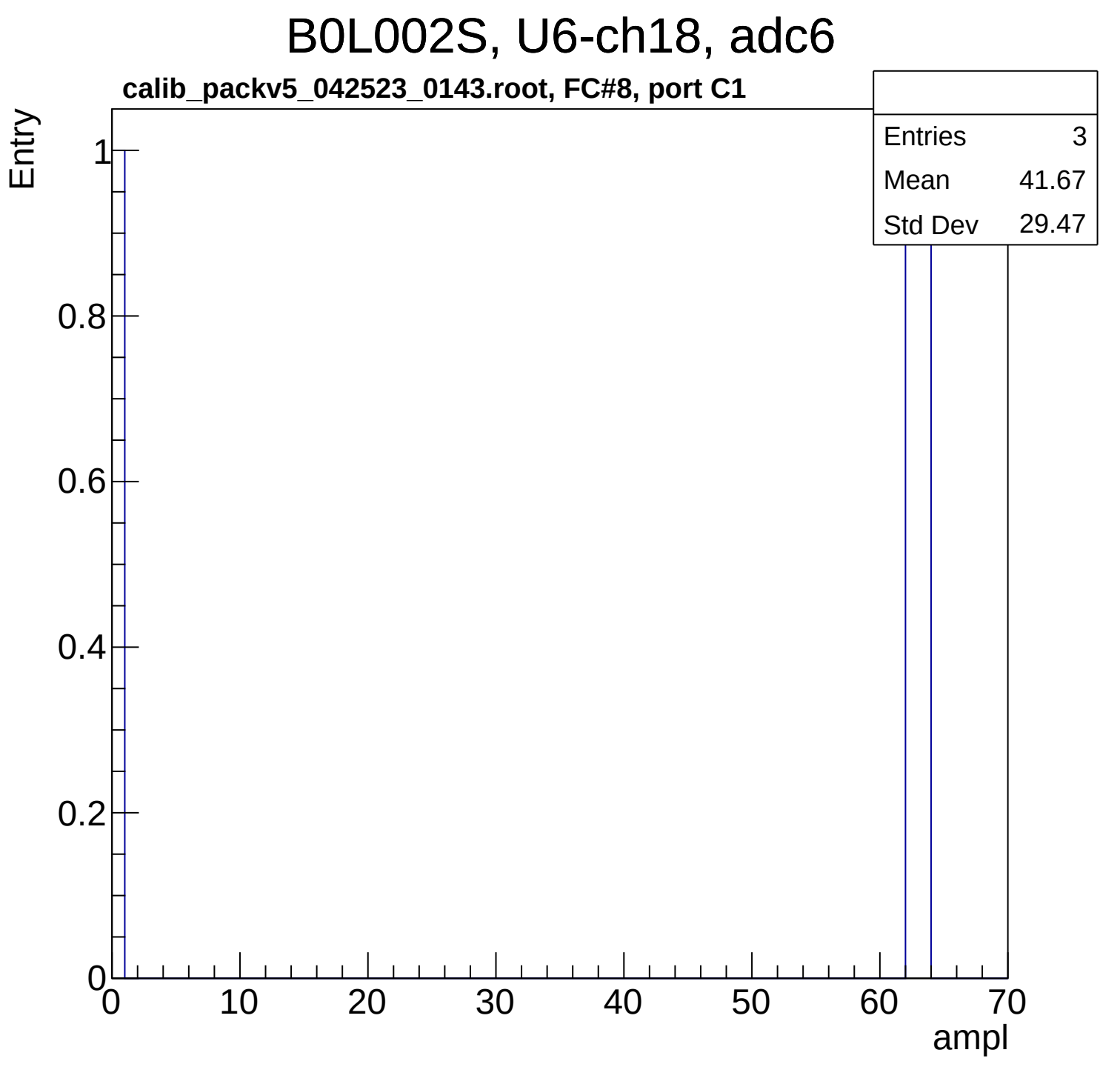
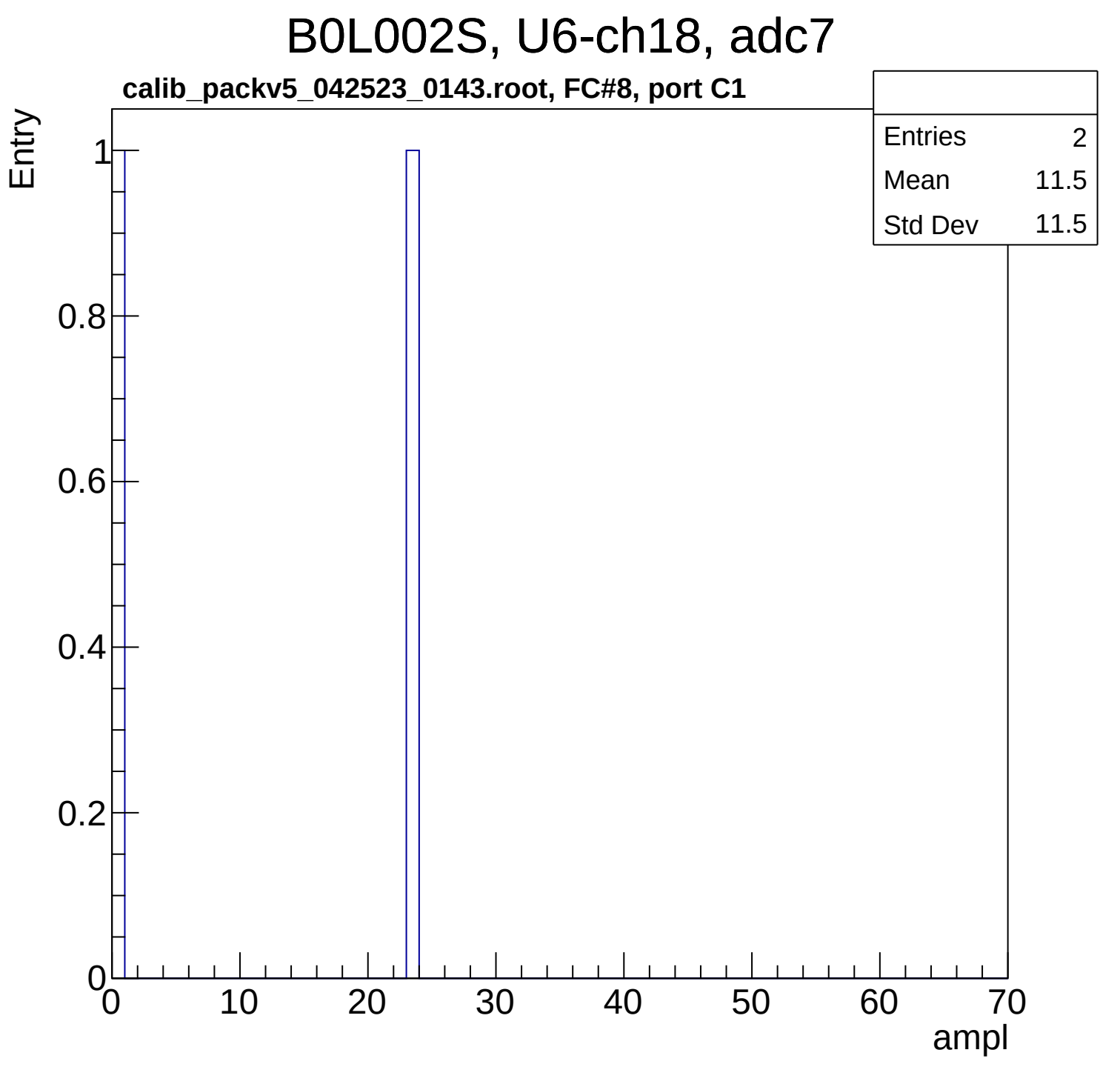

B0L002S, U6-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 56.79
Std Dev 3.171
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 60.5
Std Dev 2.092
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
0
0
10
20
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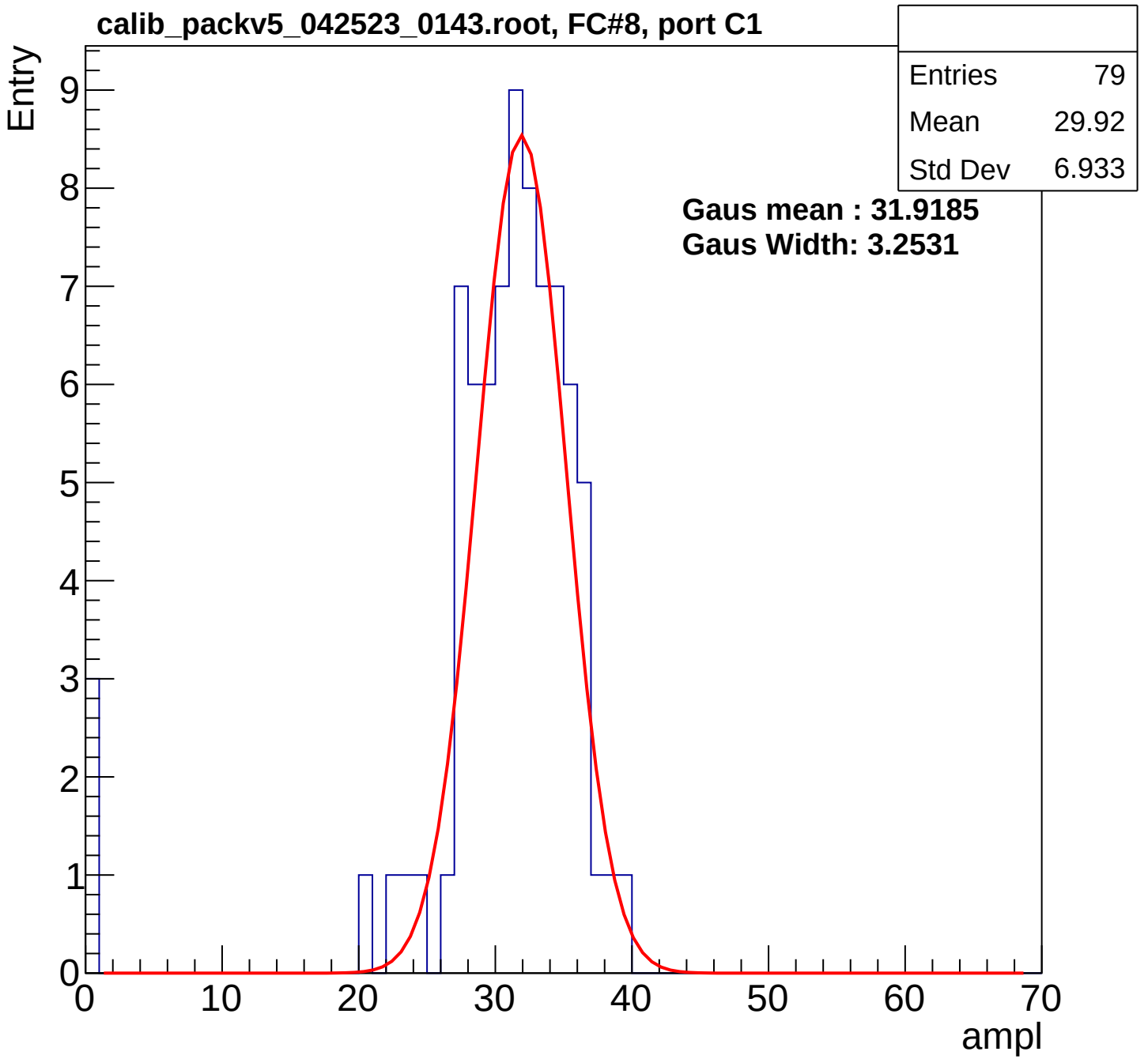
50
60
70
ampl


B0L002S, U6-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.92
Std Dev 6.933
Gaus mean : 31.9185
Gaus Width: 3.2531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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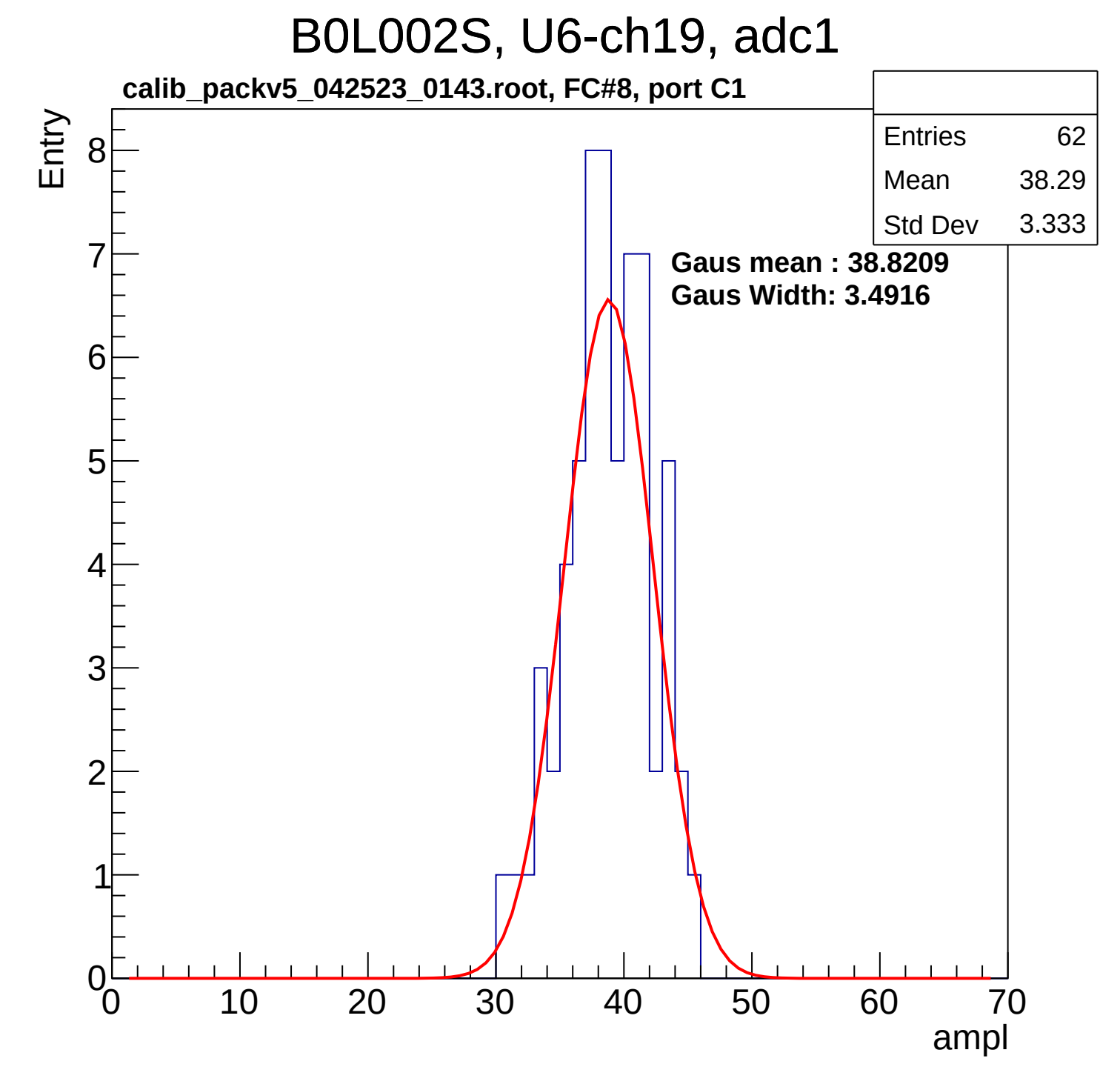
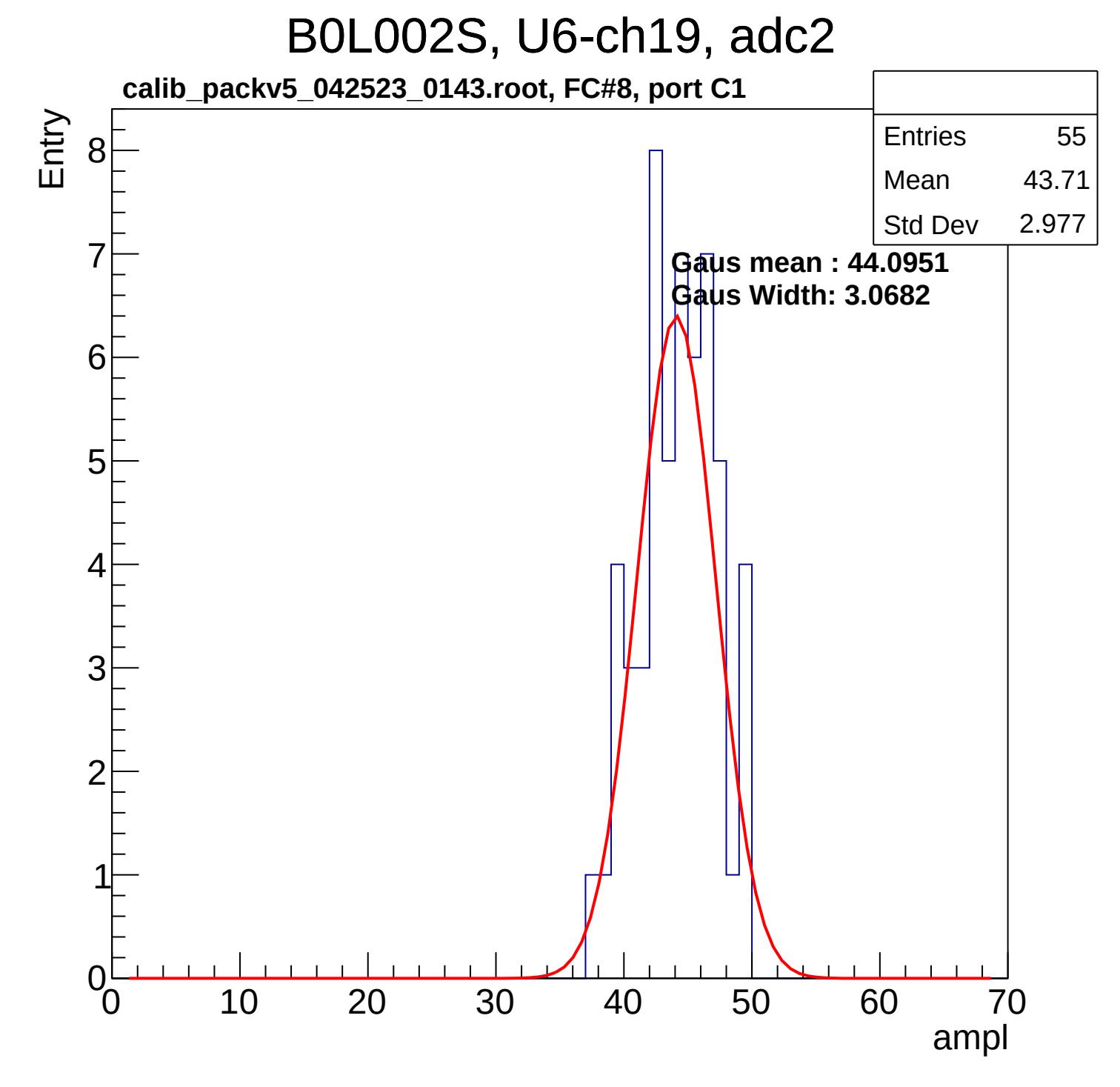

B0L002S, U6-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 38.29
Std Dev 3.333
Gaus mean : 38.8209
Gaus Width: 3.4916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 43.71
Std Dev 2.977
Gaus mean : 44.0951
Gaus Width: 3.0682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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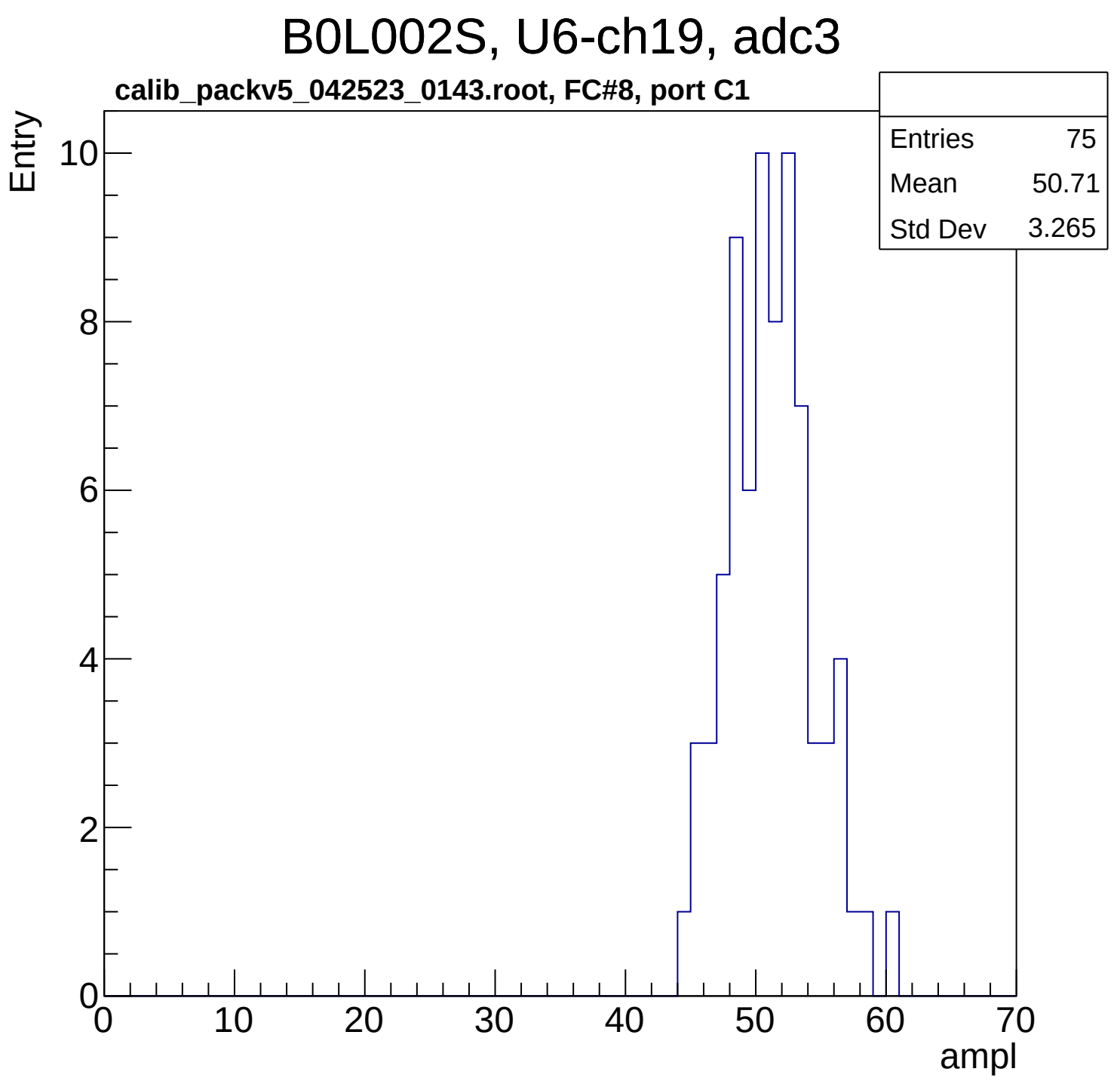
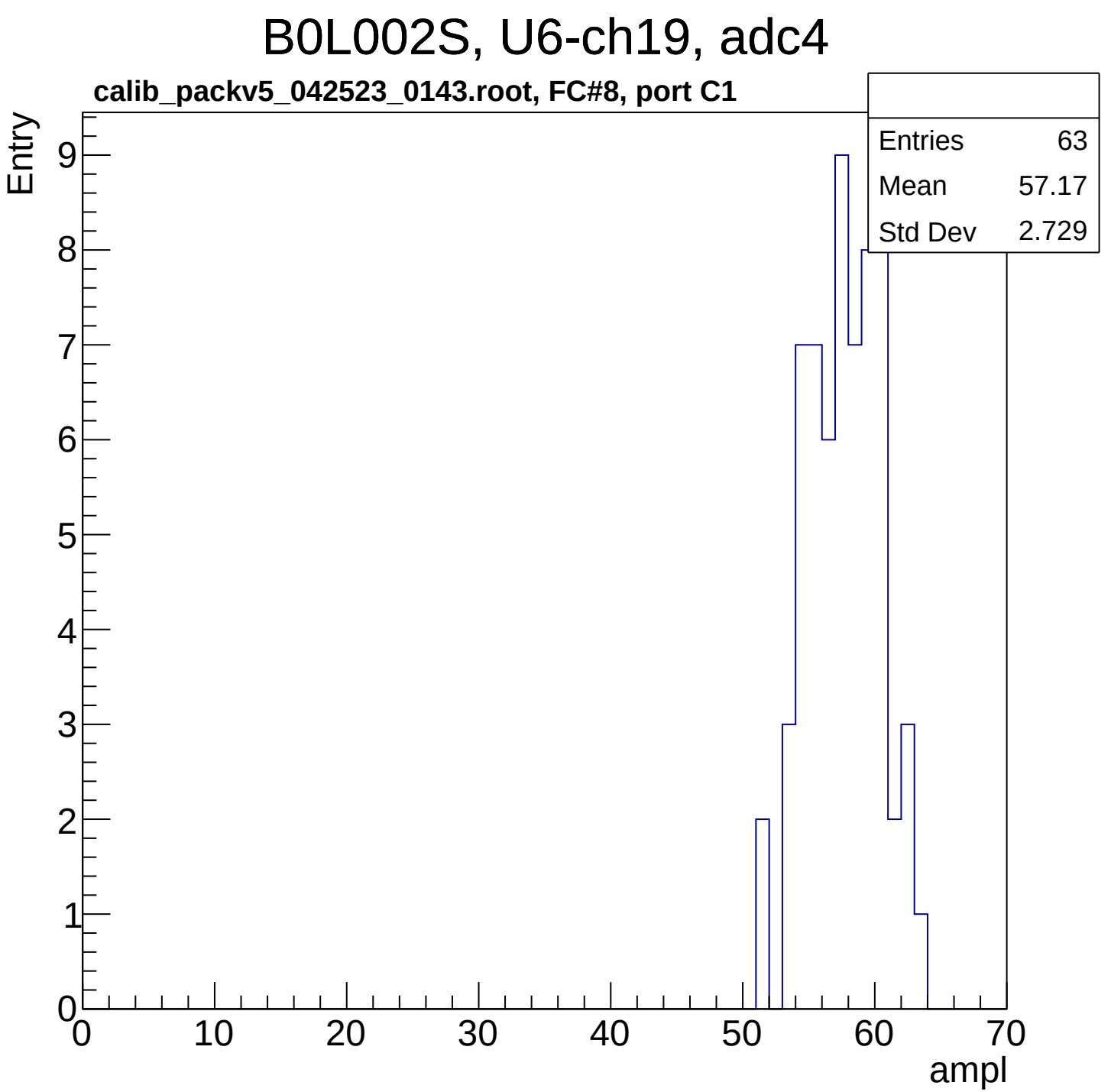

B0L002S, U6-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.71
Std Dev 3.265
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.17
Std Dev 2.729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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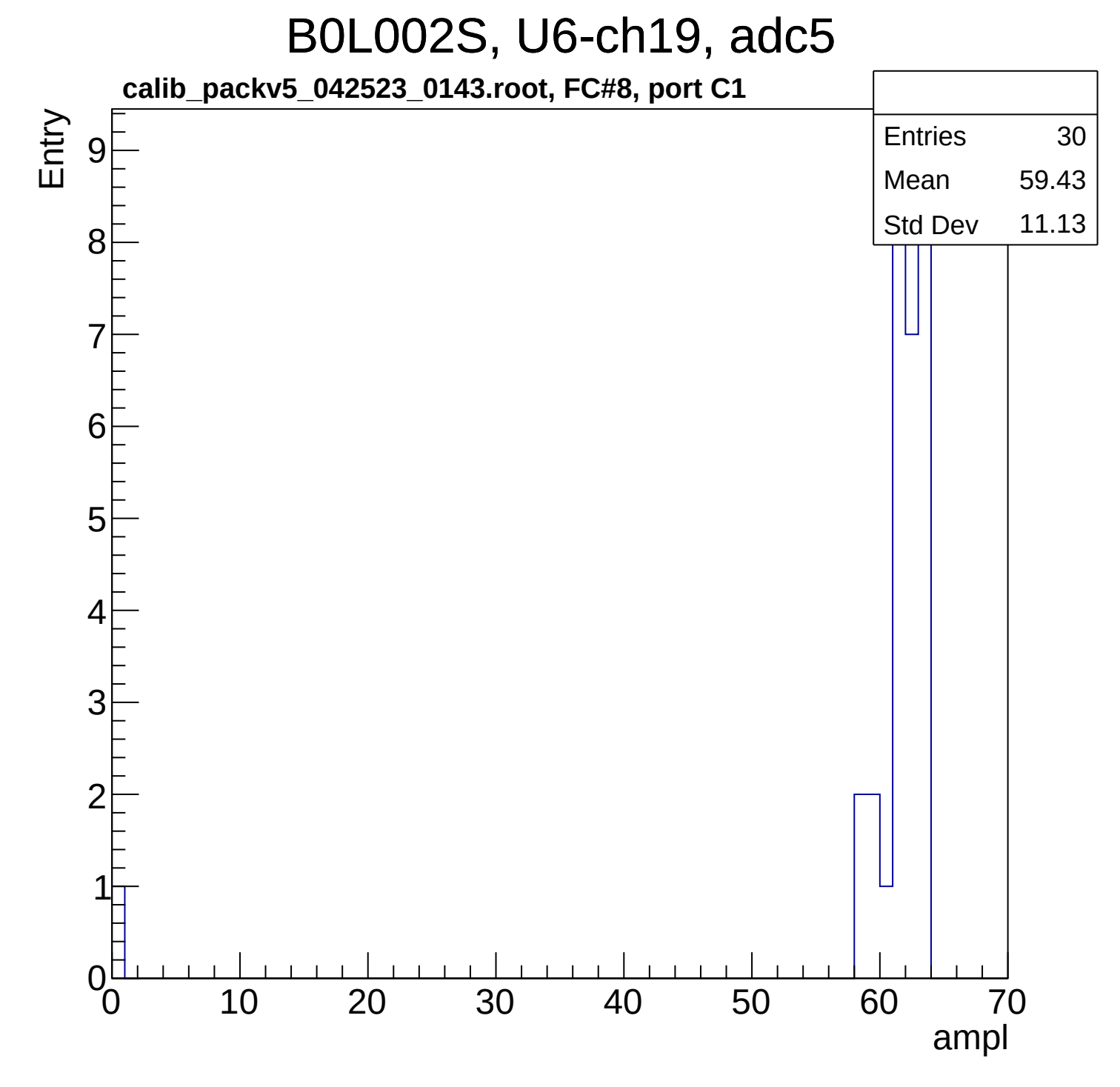
B0L002S, U6-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.43
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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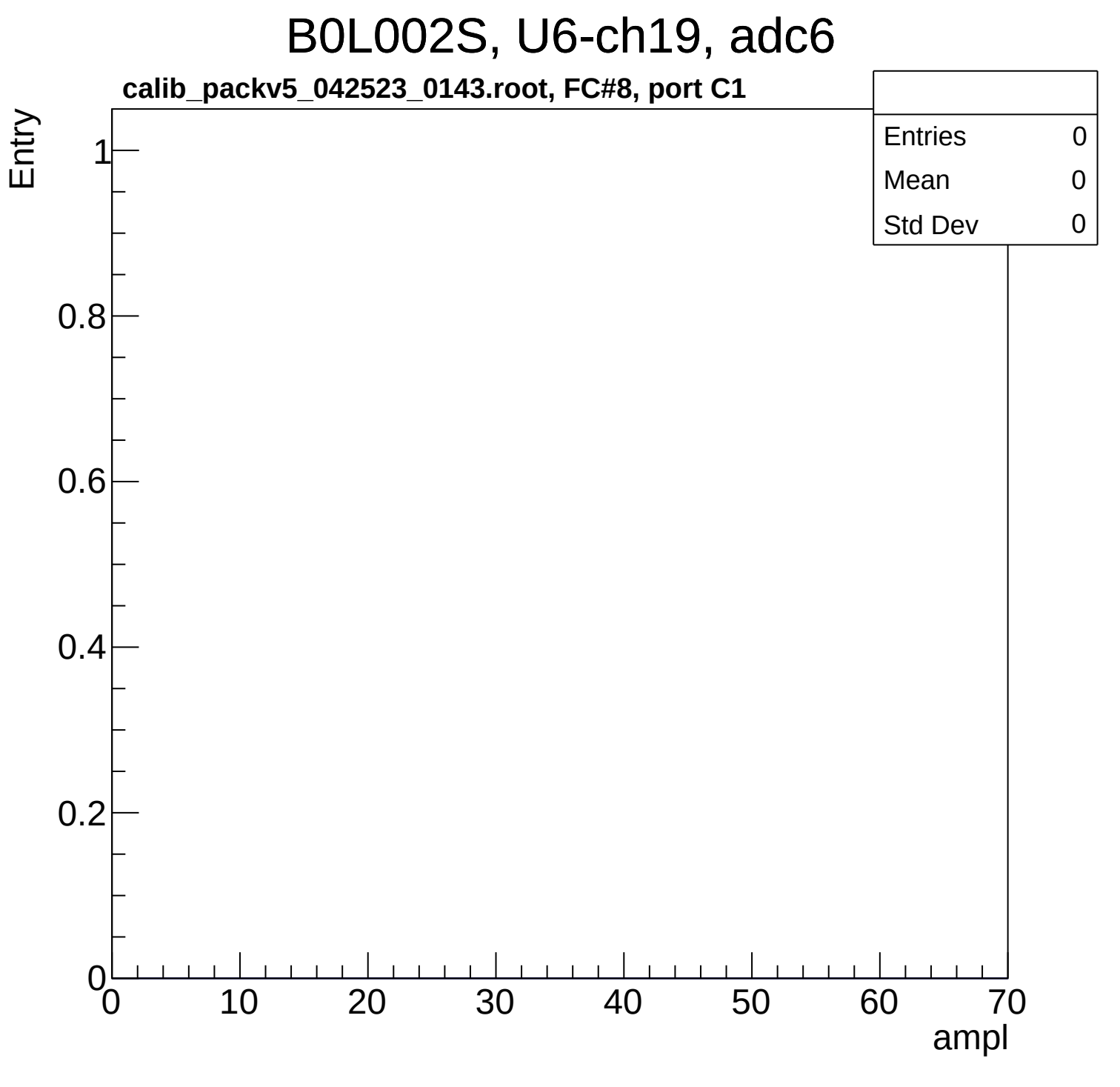
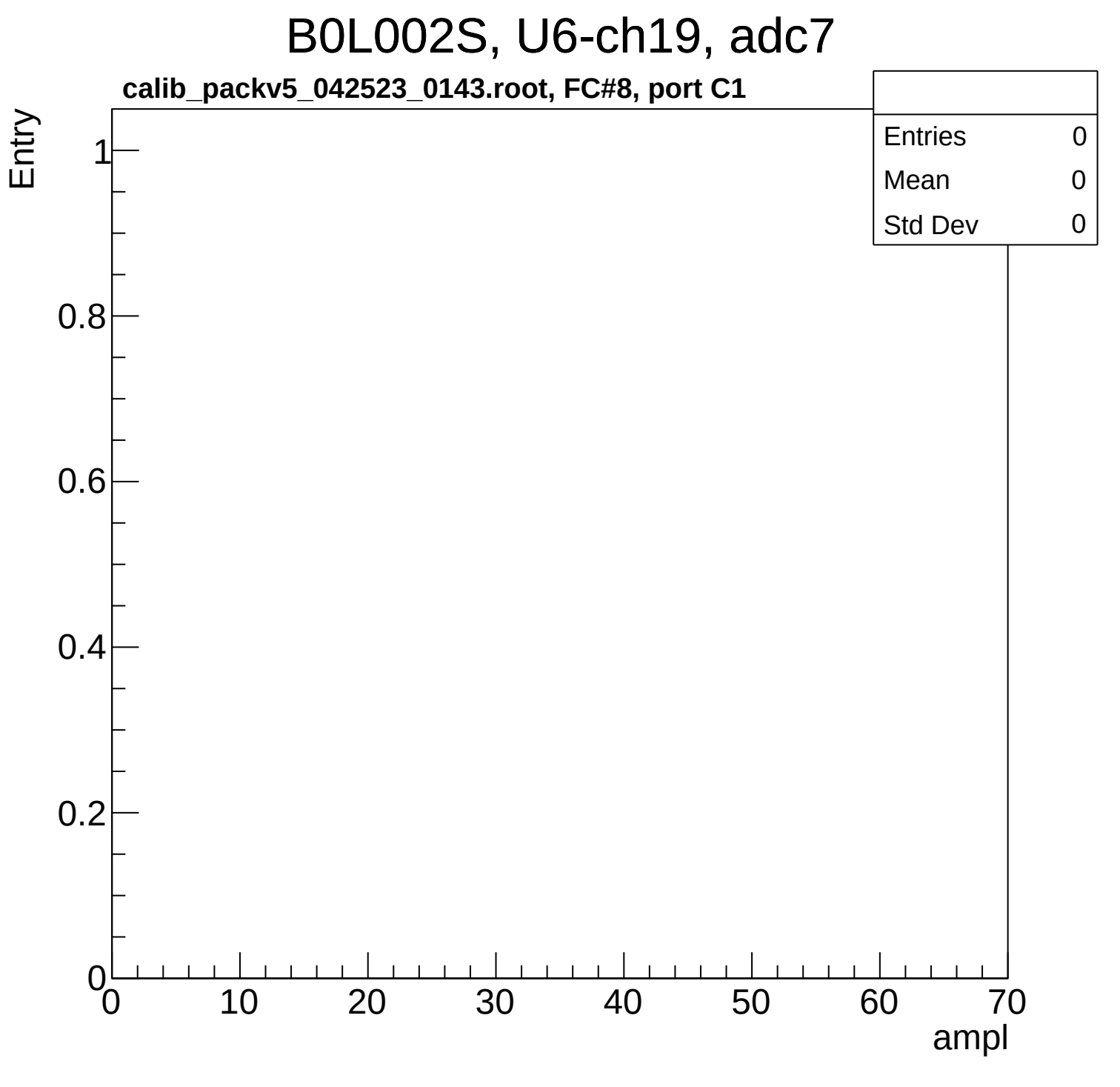
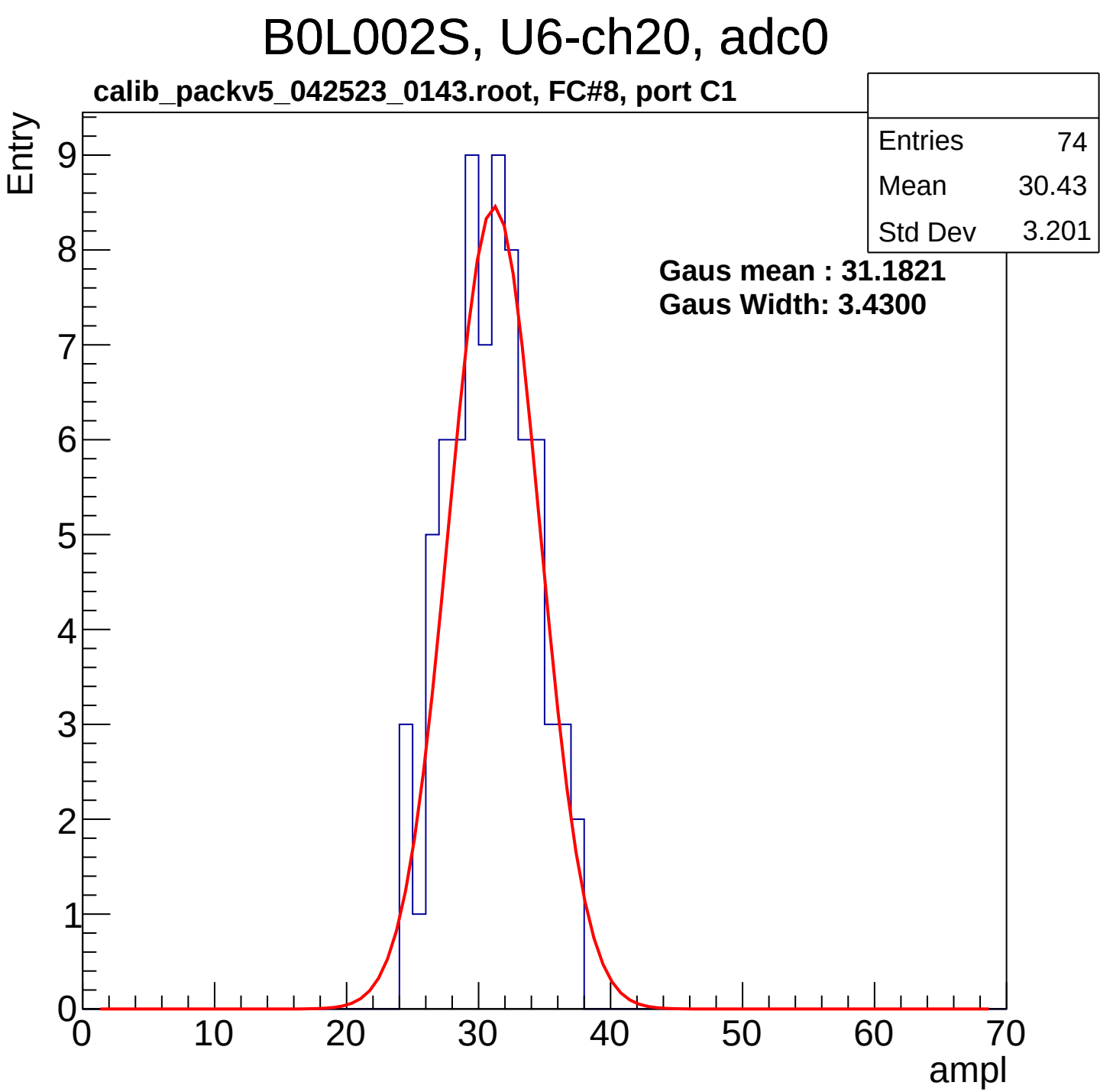
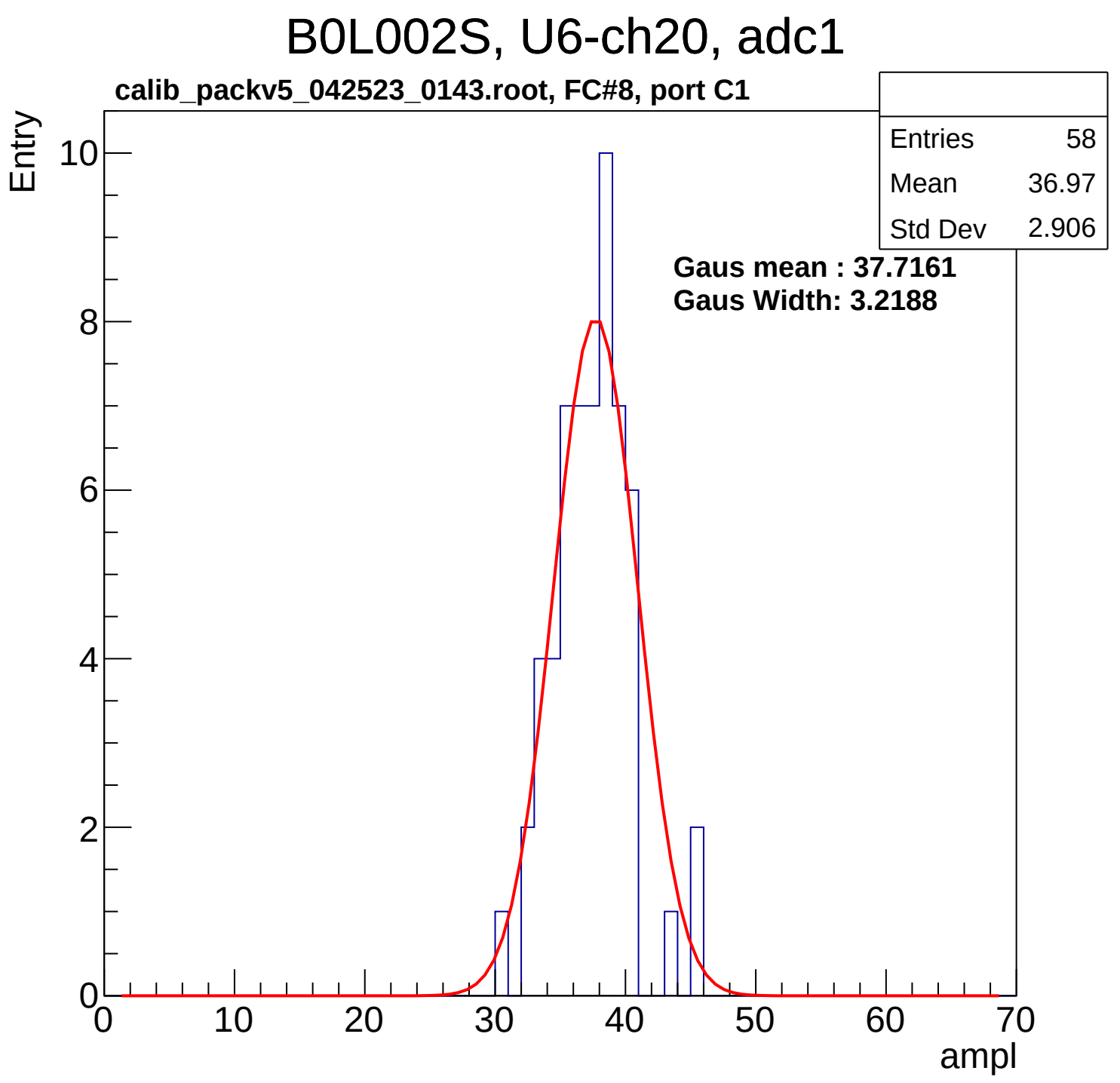

B0L002S, U6-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 30.43
Std Dev 3.201
Gaus mean : 31.1821
Gaus Width: 3.4300
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 36.97
Std Dev 2.906
Gaus mean : 37.7161
Gaus Width: 3.2188
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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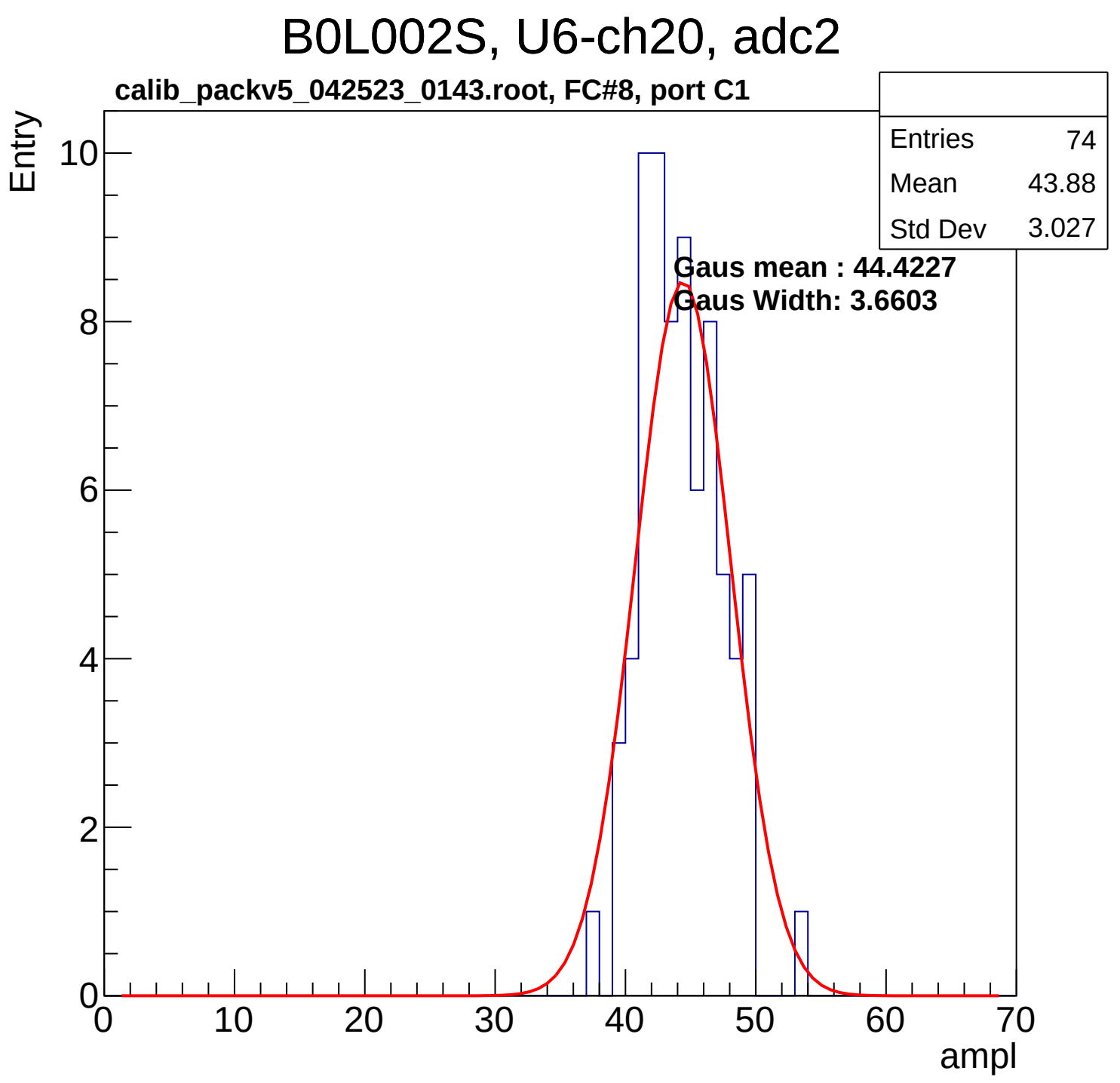
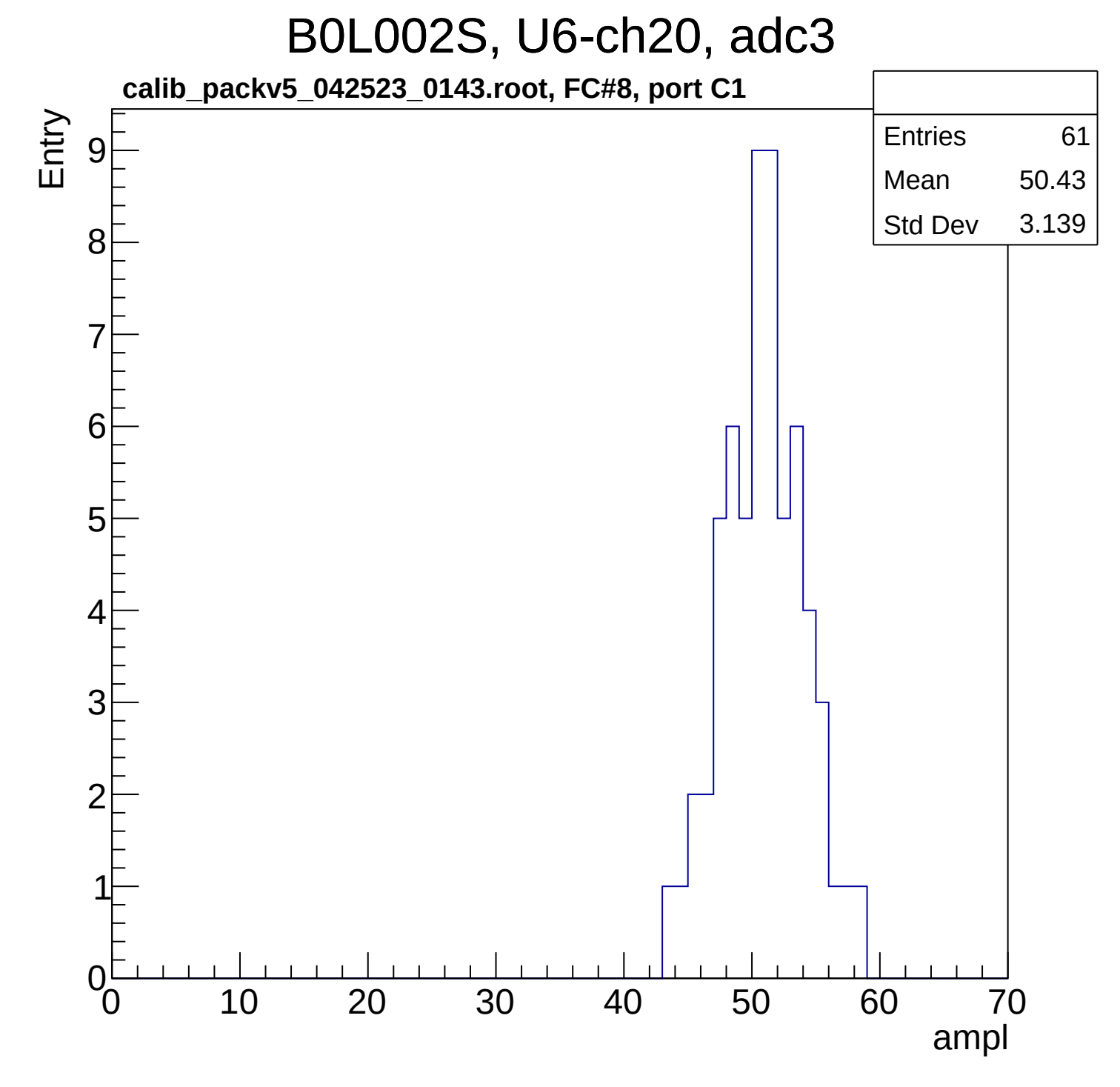
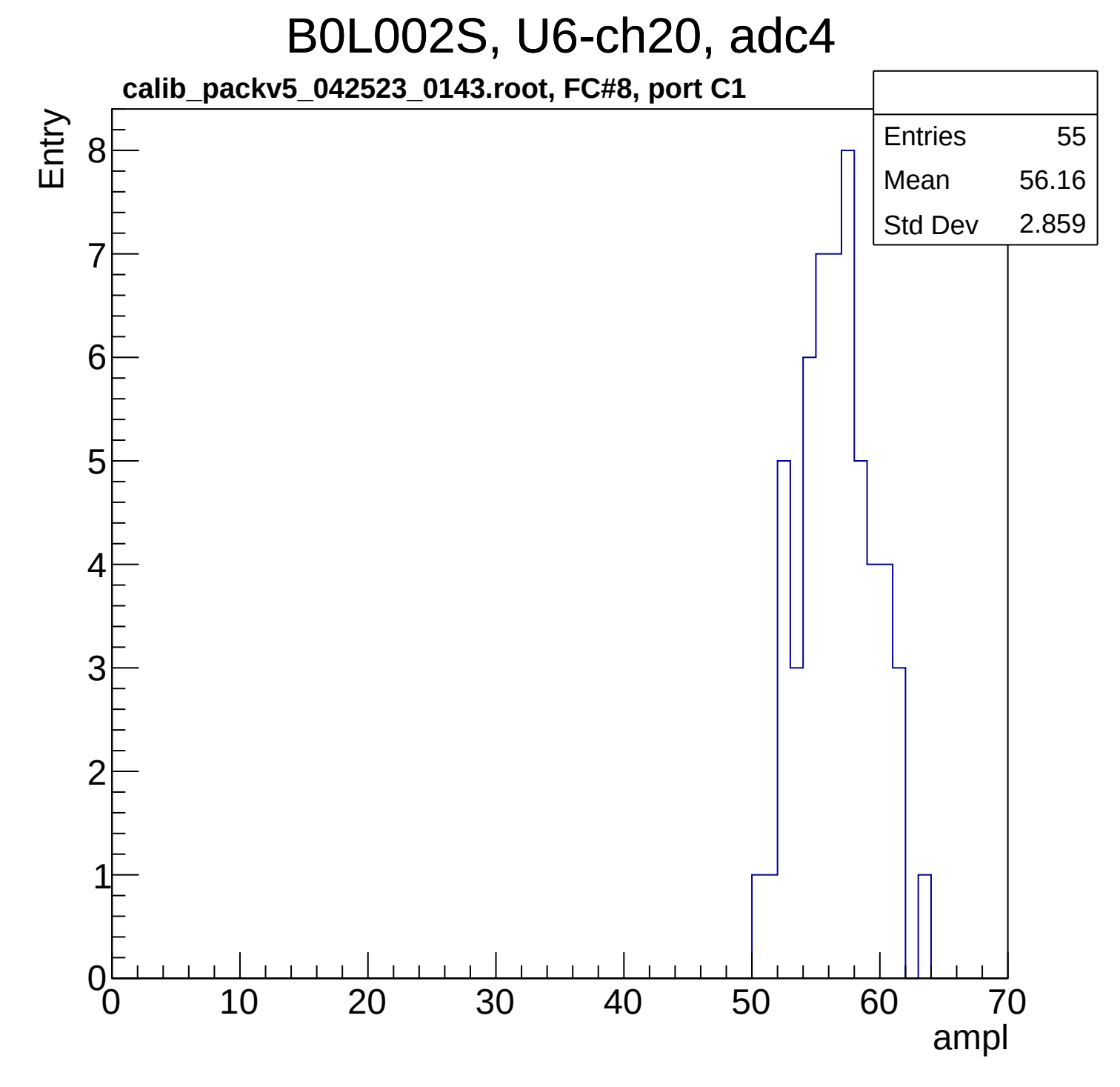

B0L002S, U6-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.88
Std Dev 3.027
Gaus mean : 44.4227
Gaus Width: 3.6603
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.43
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.16
Std Dev 2.859
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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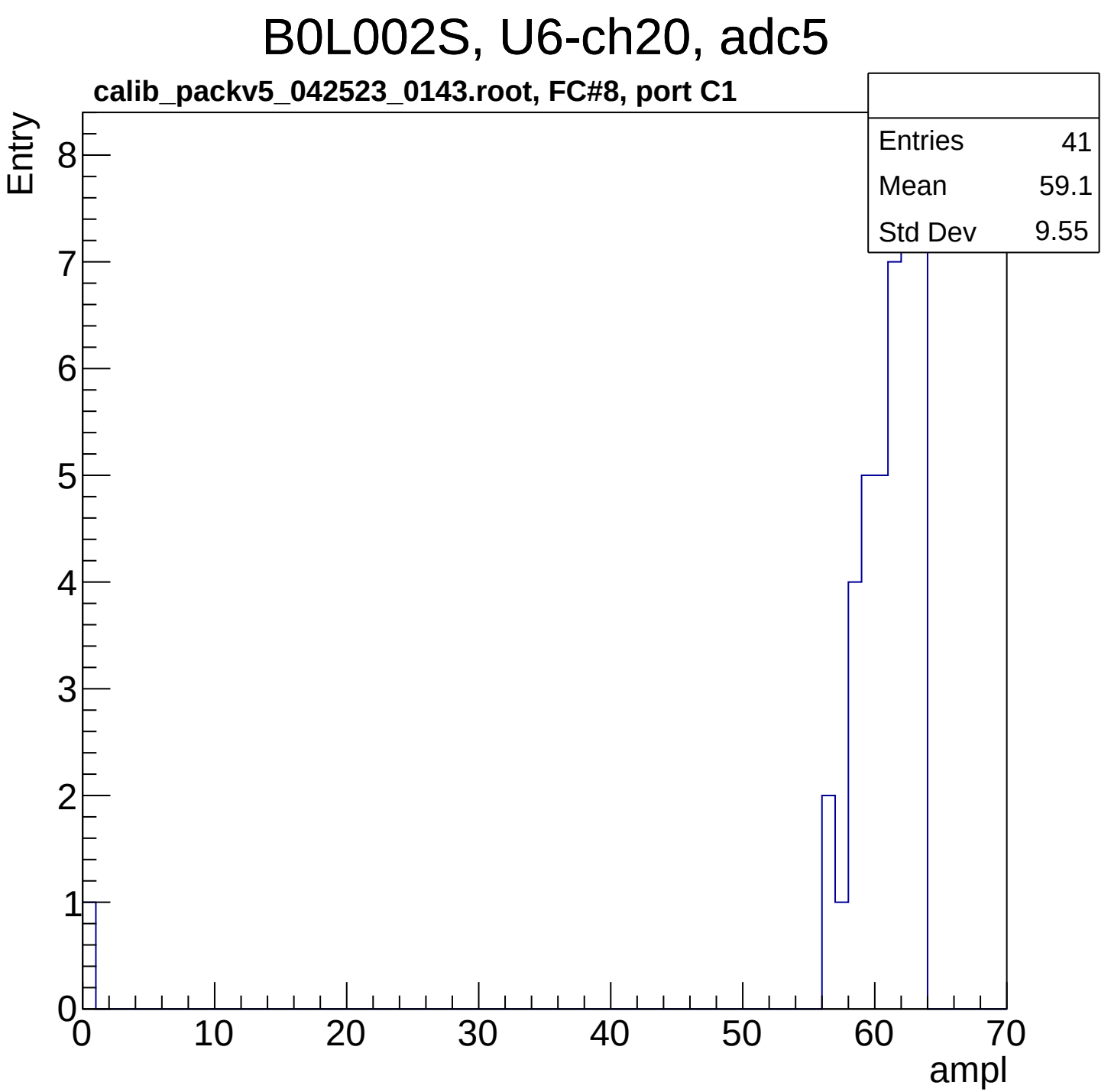
B0L002S, U6-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.1
Std Dev 9.55
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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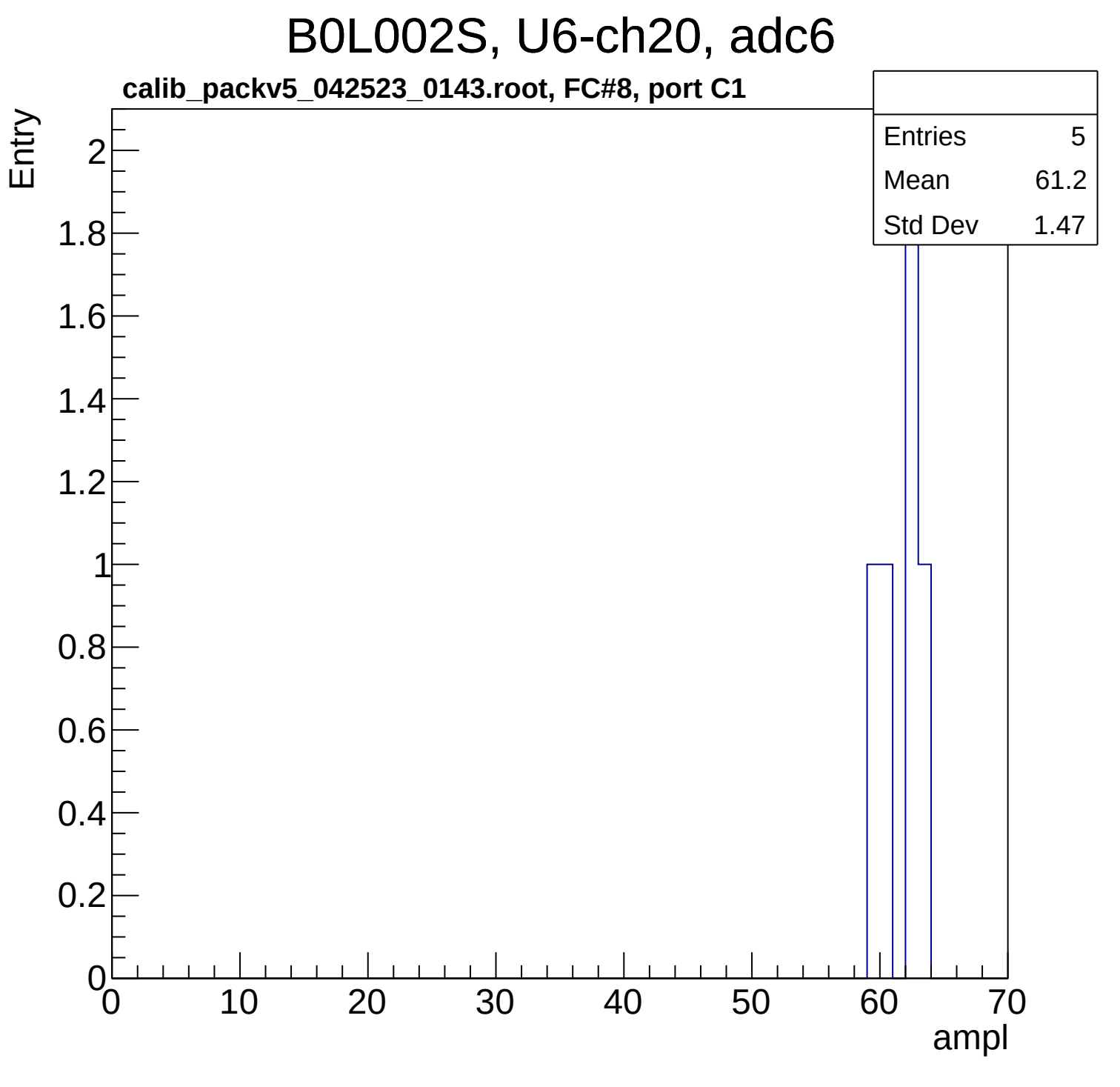
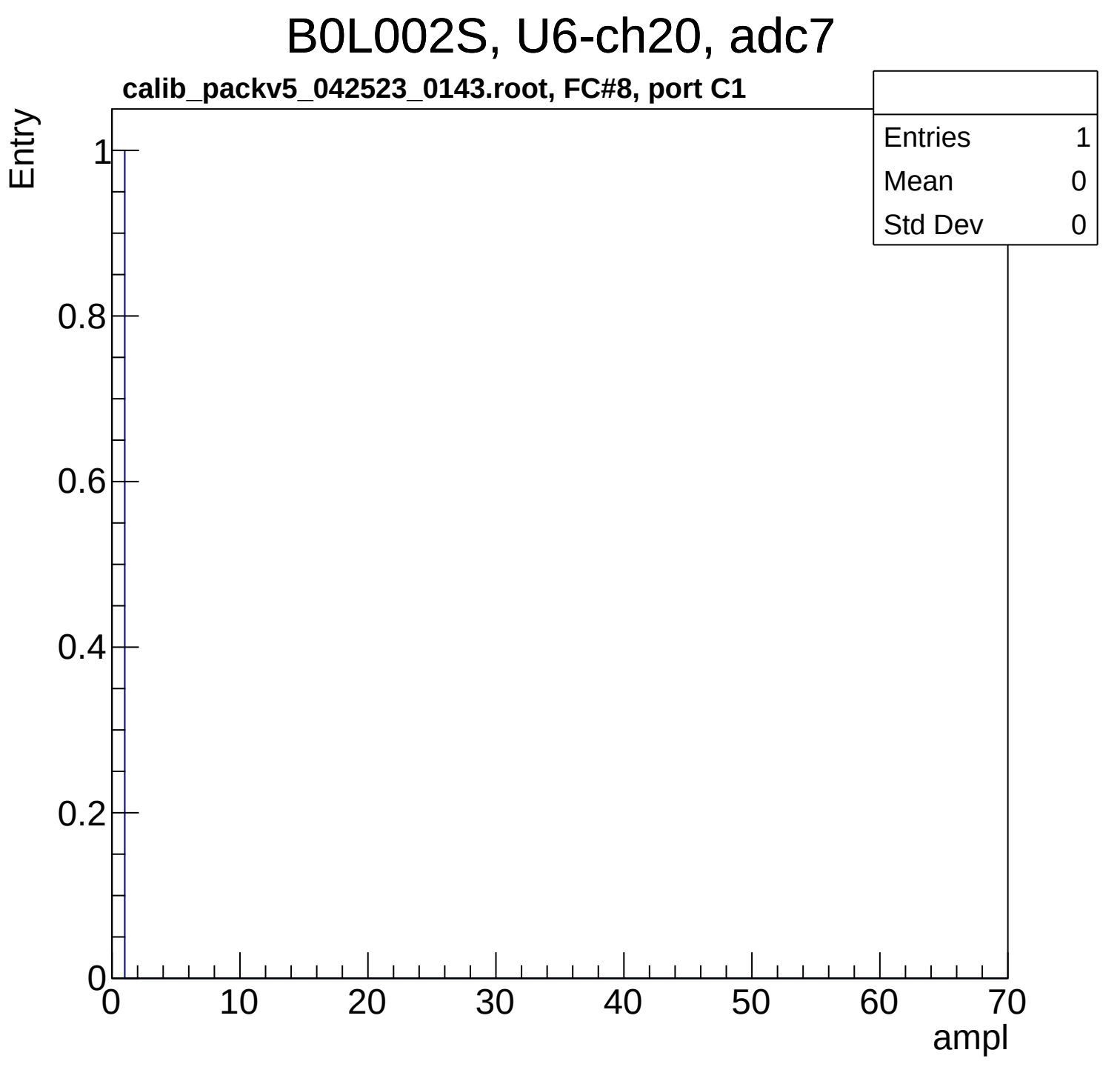
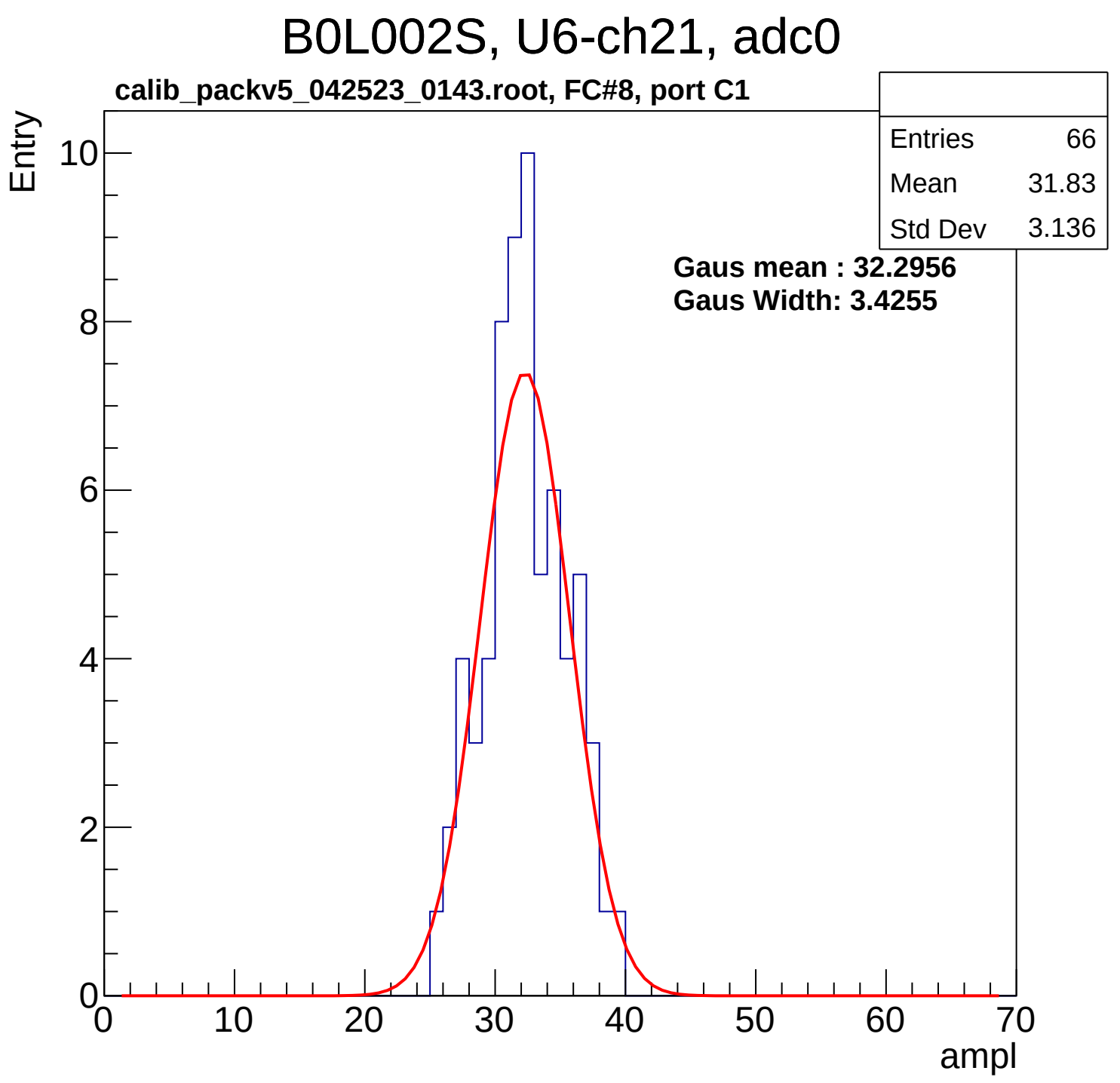
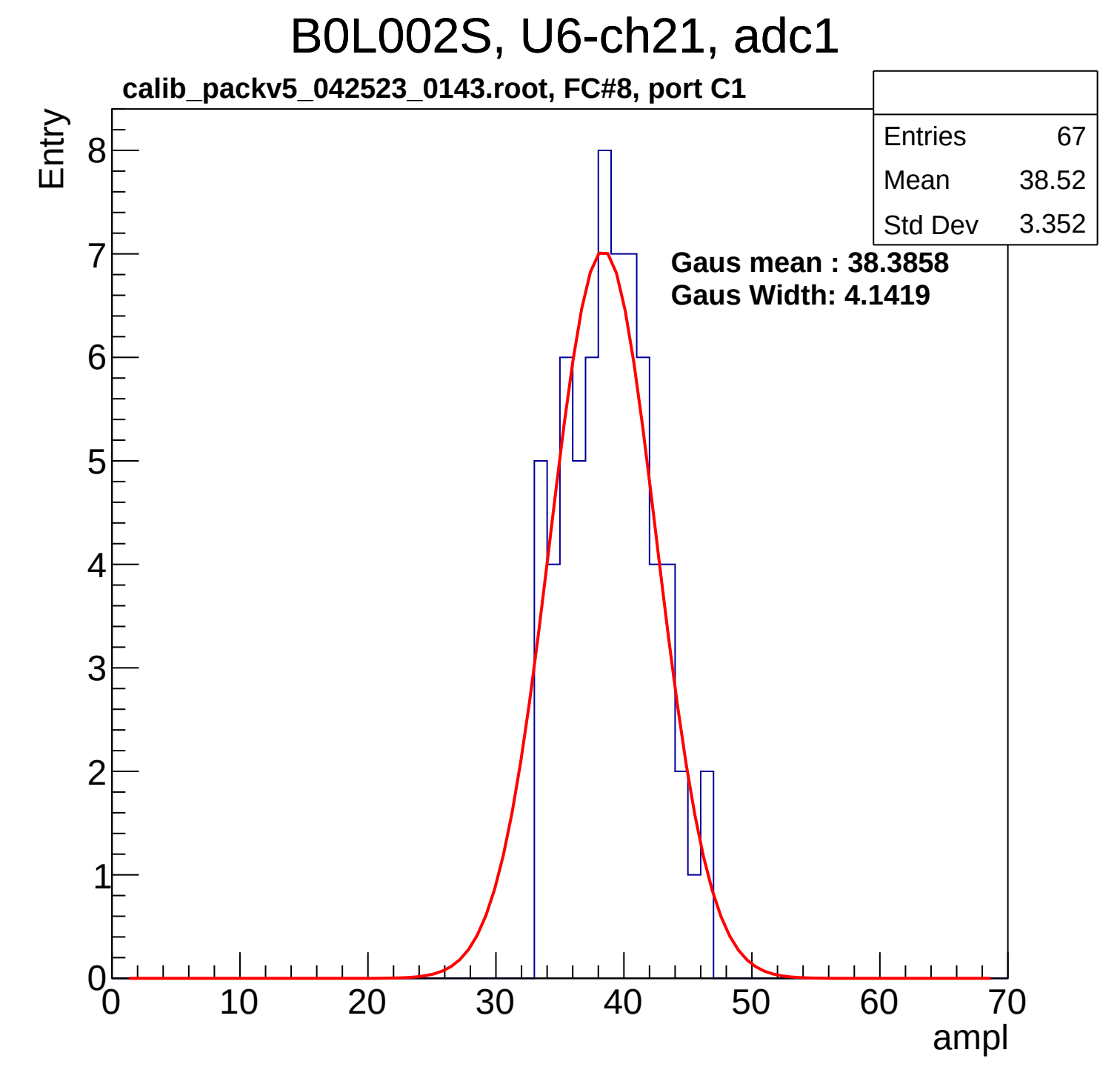
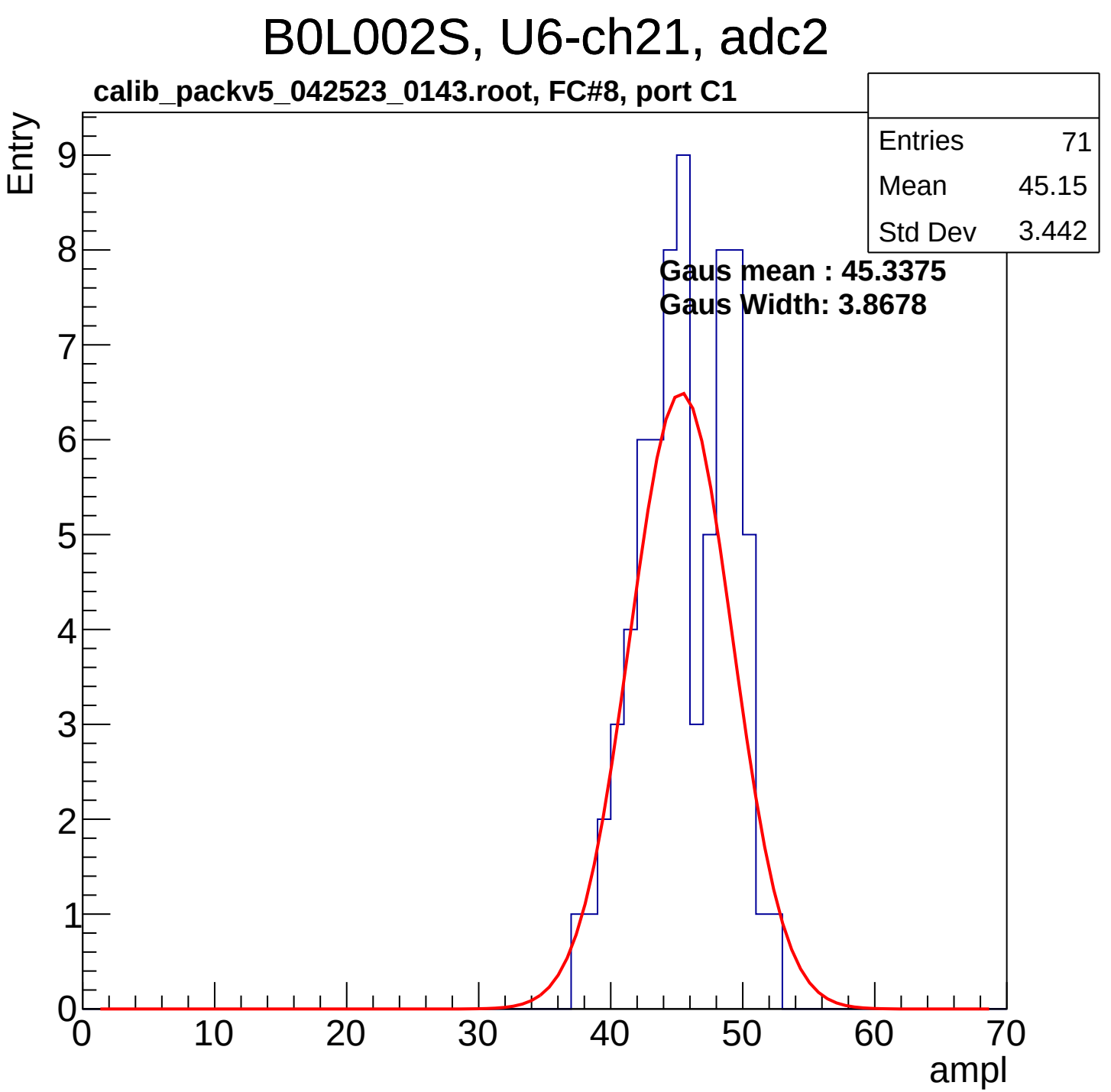

B0L002S, U6-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.2
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.83
Std Dev 3.136
Gaus mean : 32.2956
Gaus Width: 3.4255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.52
Std Dev 3.352
Gaus mean : 38.3858
Gaus Width: 4.1419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 45.15
Std Dev 3.442
Gaus mean : 45.3375
Gaus Width: 3.8678
Entry
ampl
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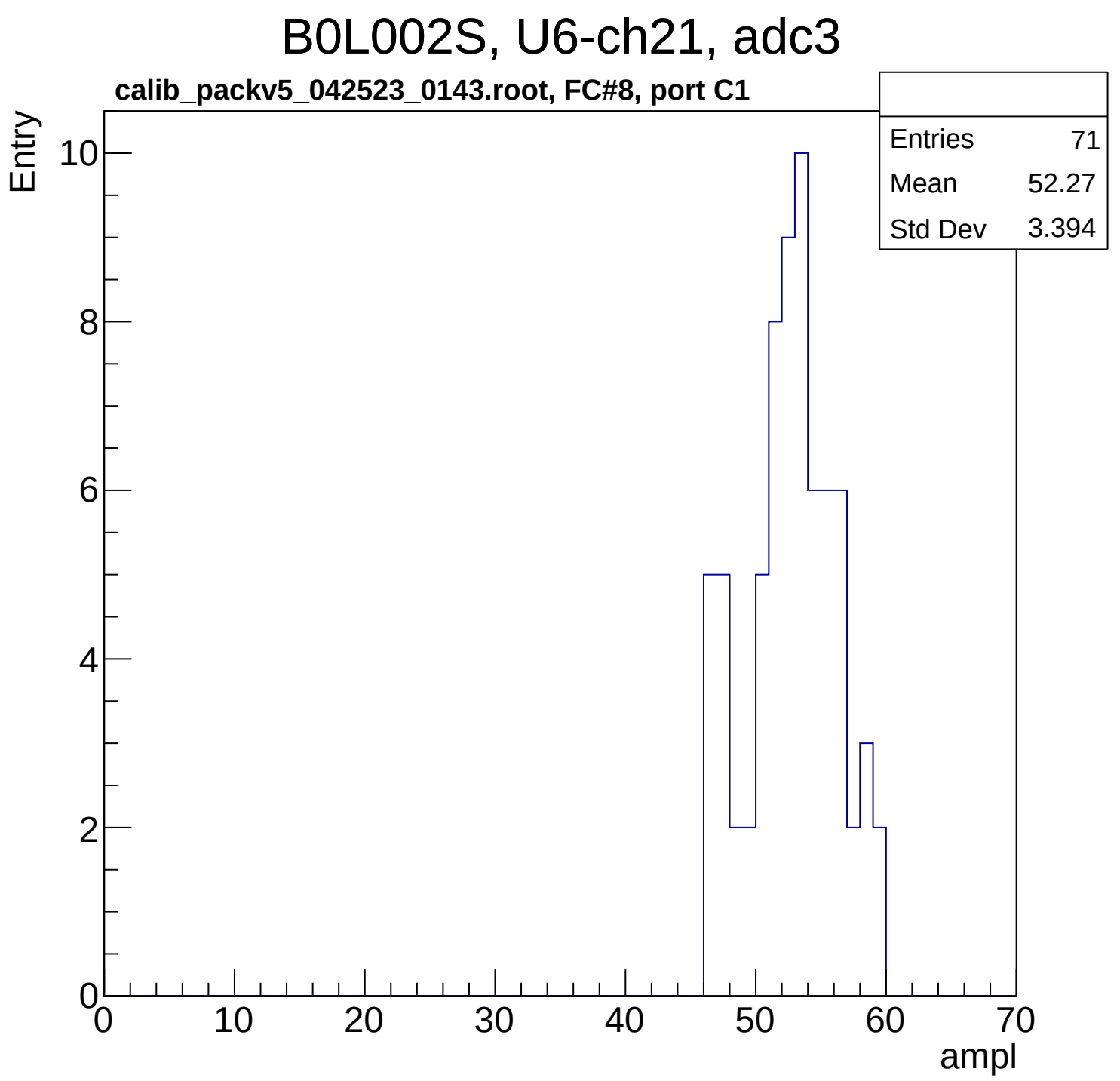
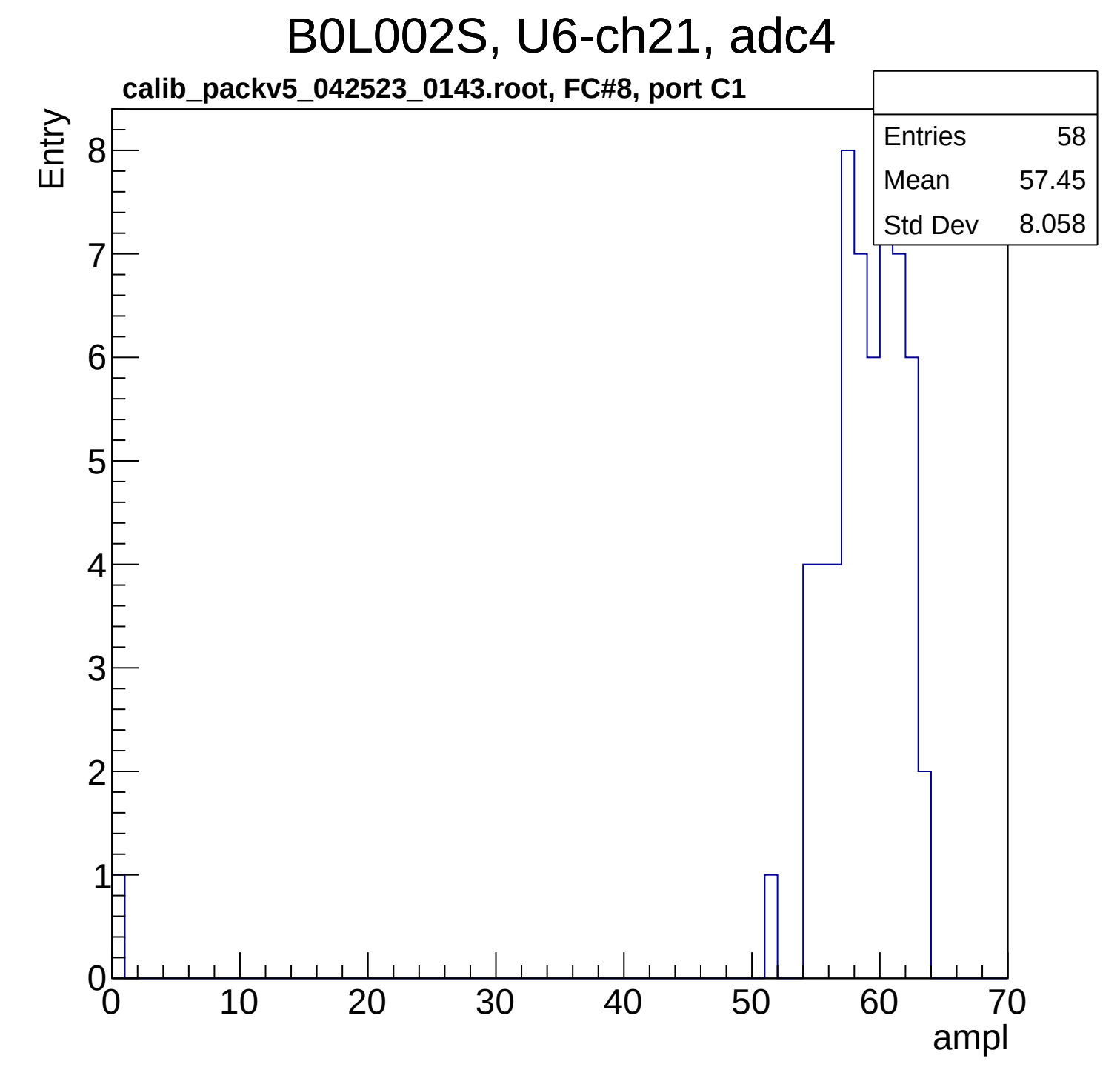
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.27
Std Dev 3.394
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.45
Std Dev 8.058
Entry
ampl
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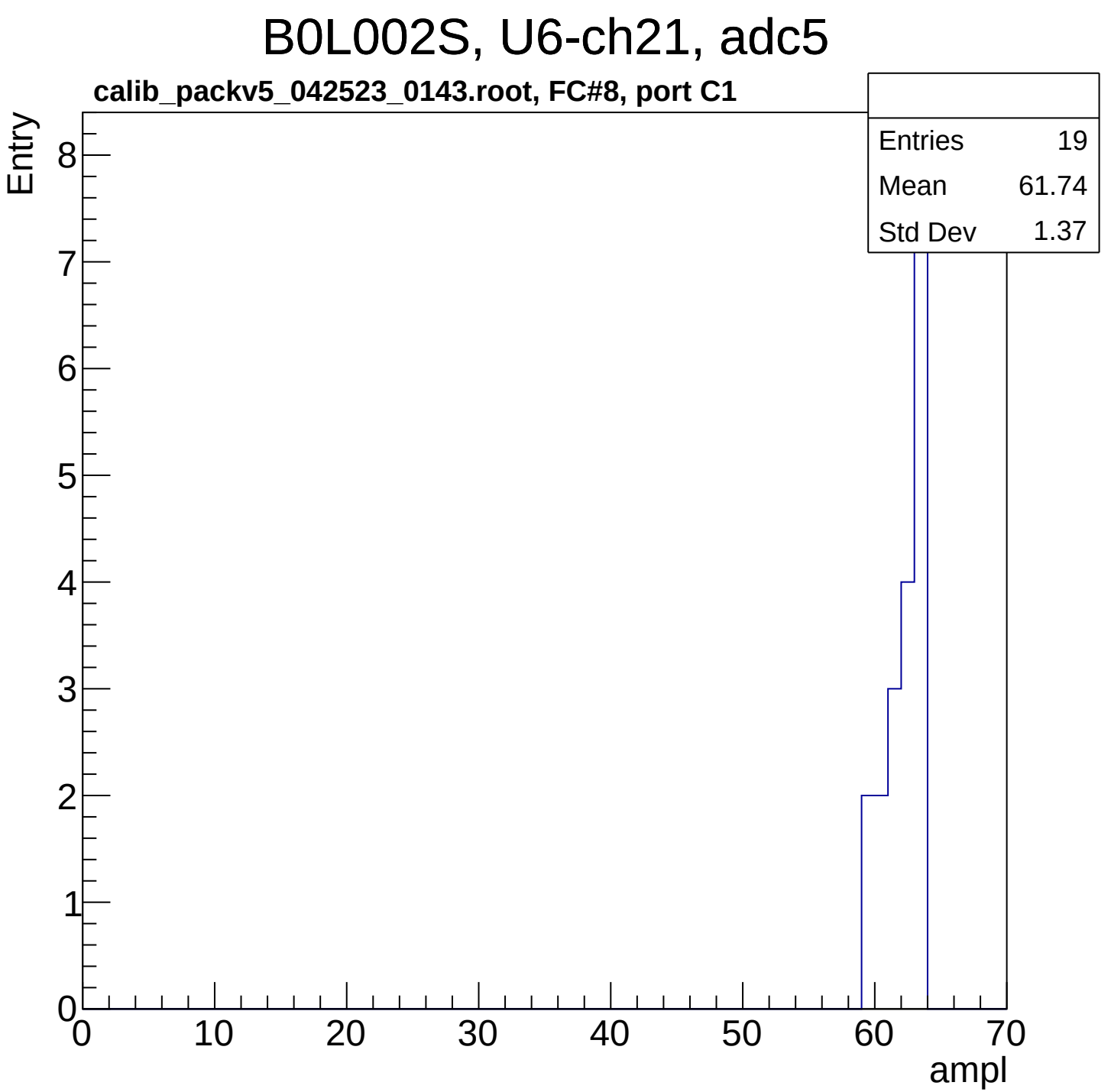
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.74
Std Dev 1.37
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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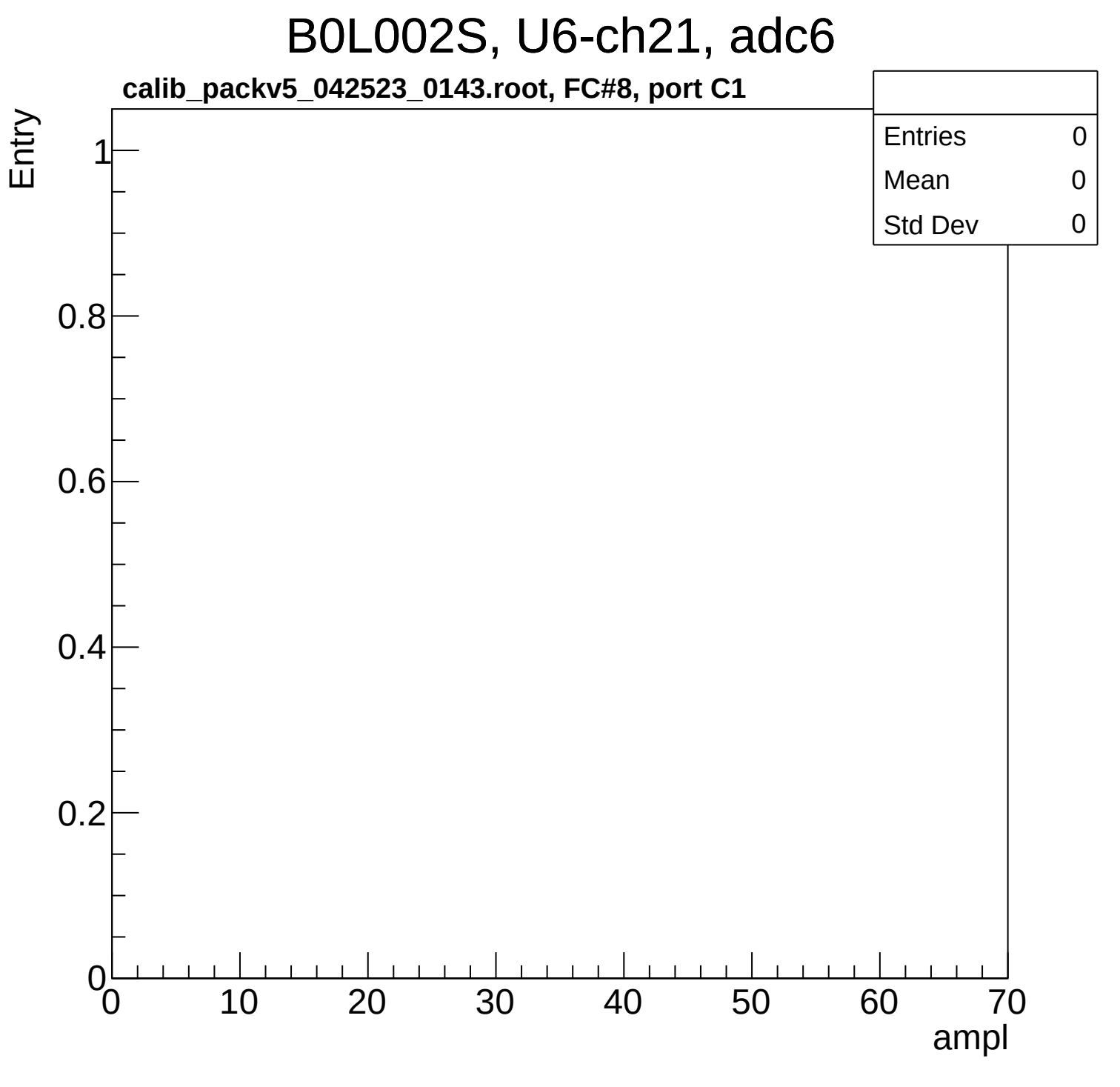
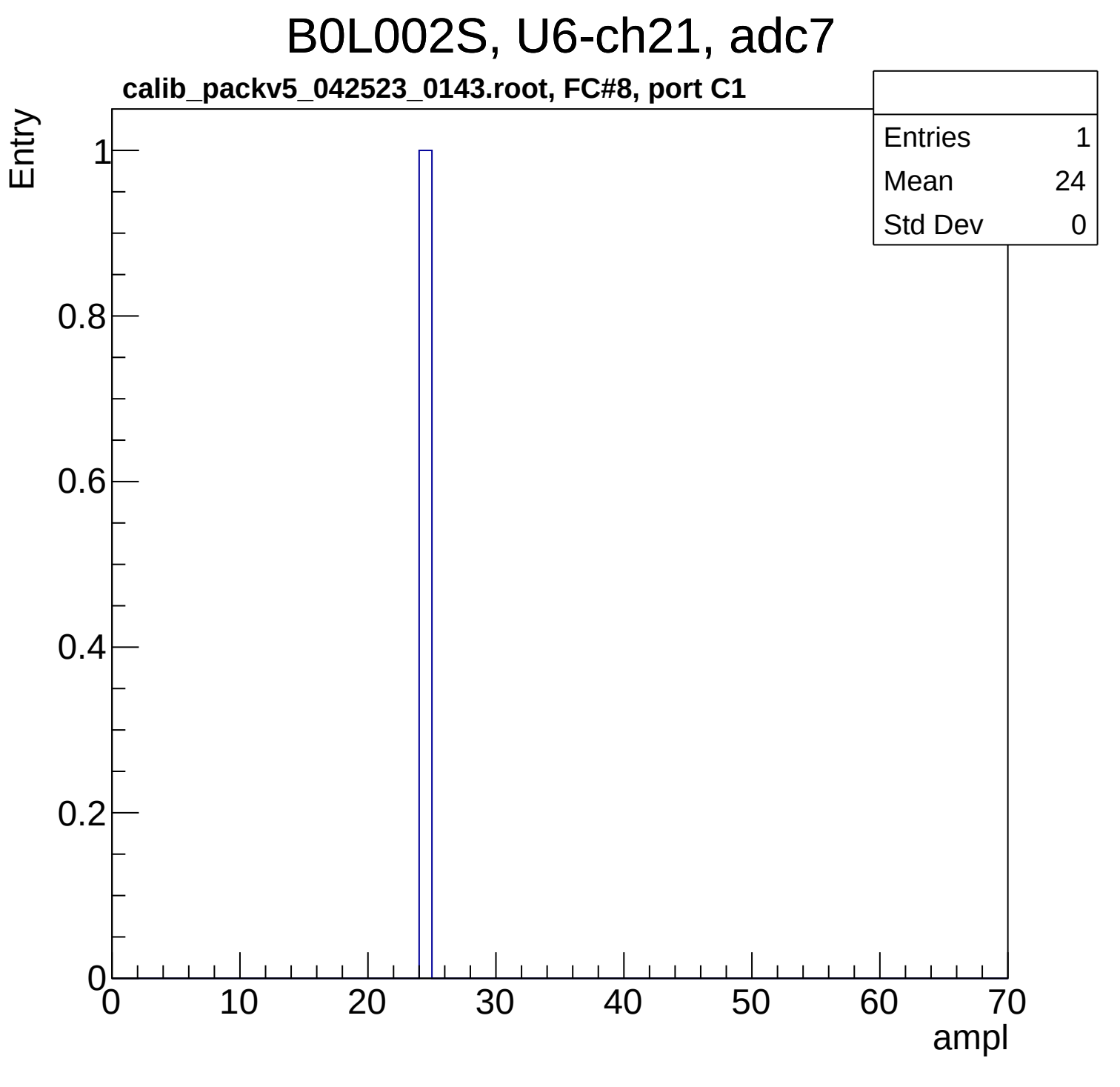

B0L002S, U6-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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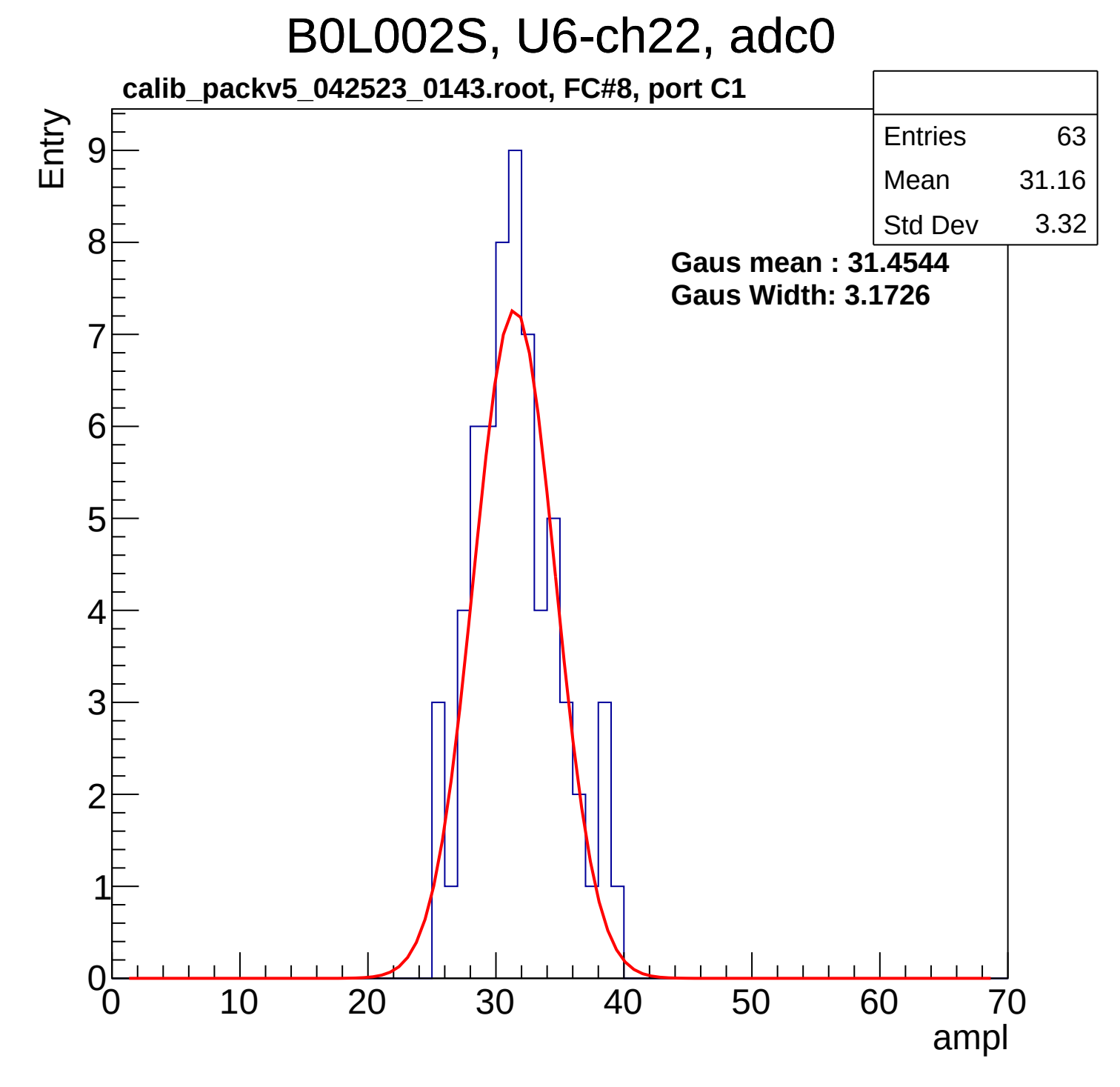
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.16
Std Dev 3.32
Gaus mean : 31.4544
Gaus Width: 3.1726
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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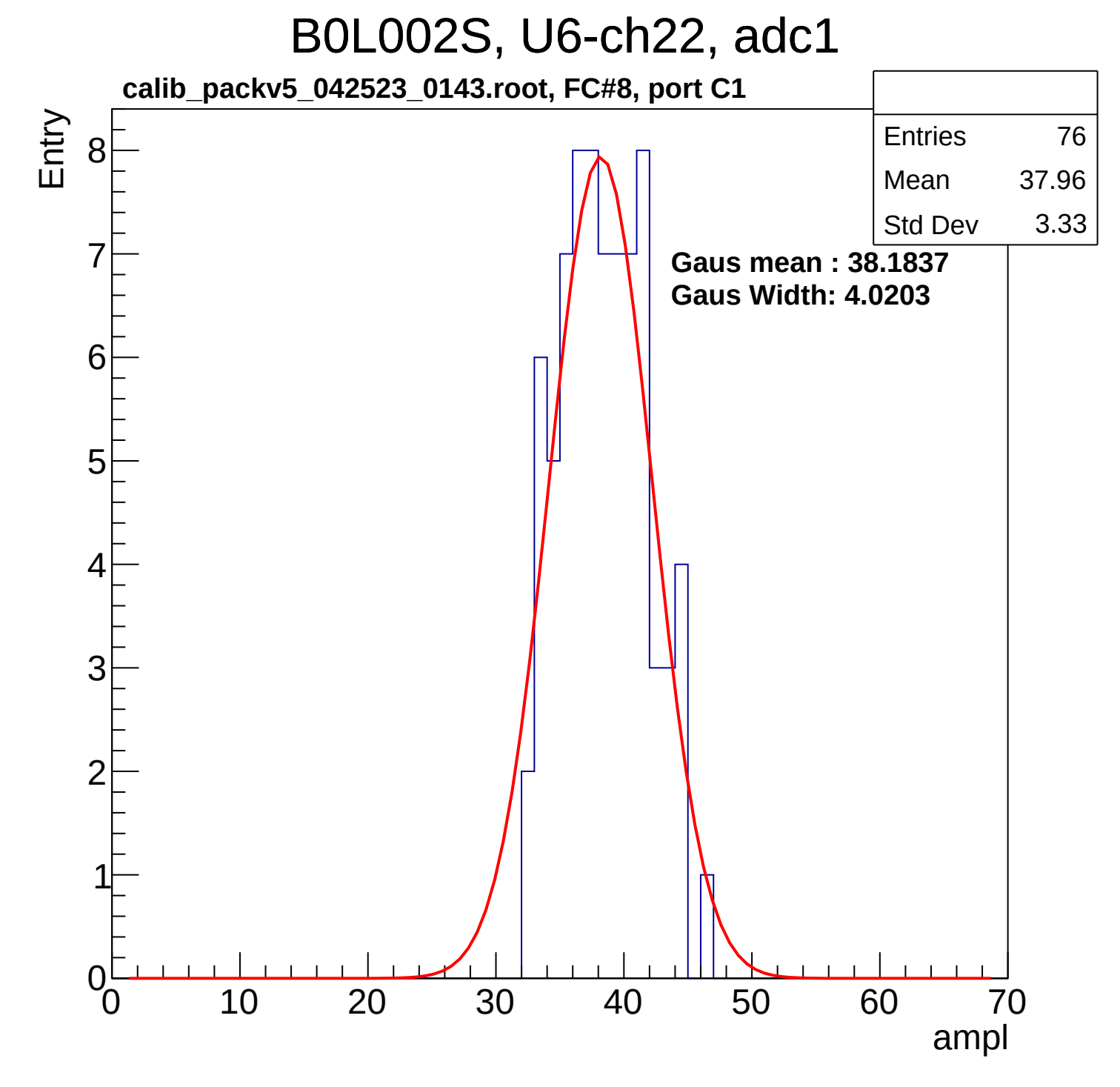
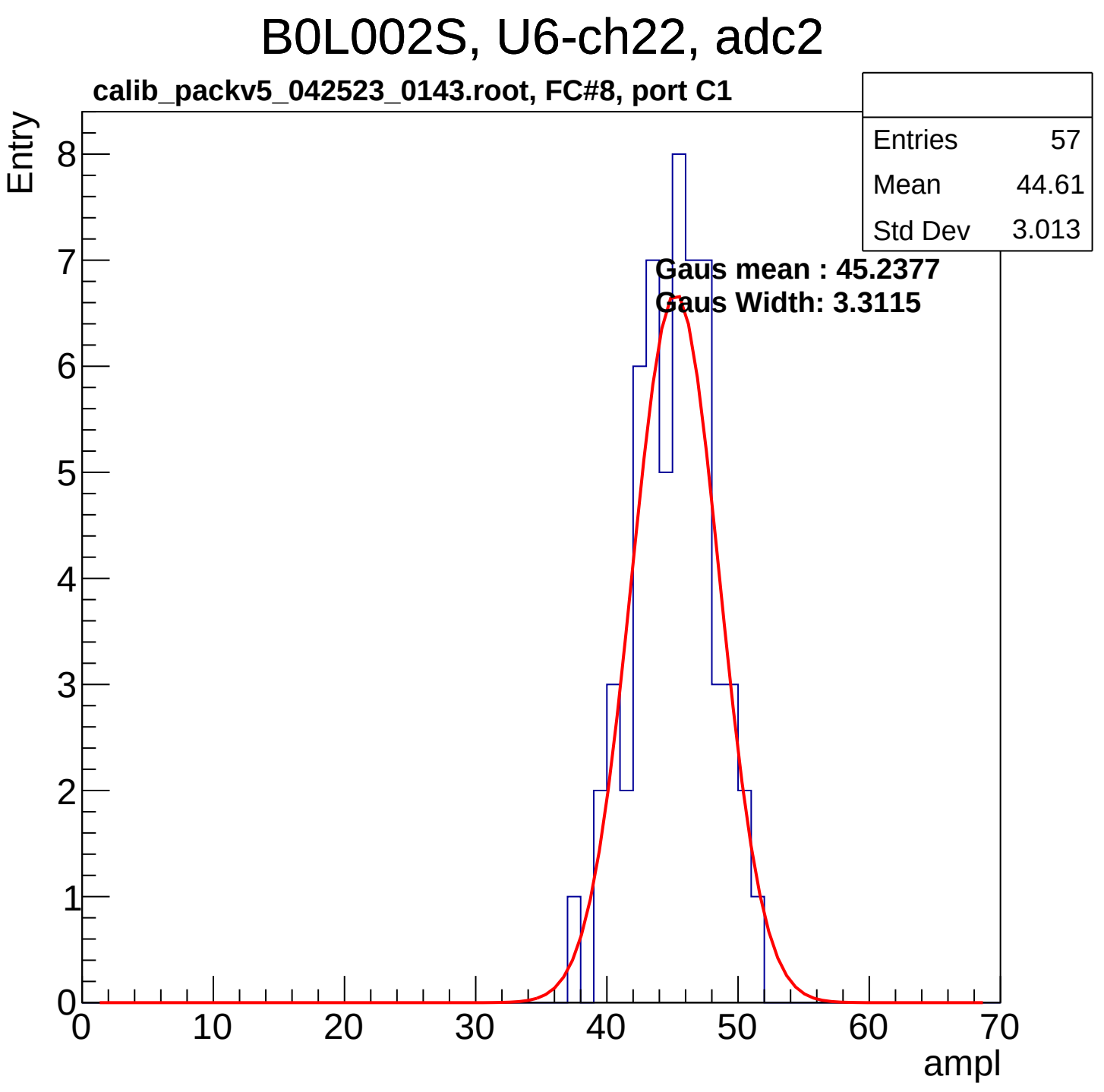
60
70


B0L002S, U6-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.96
Std Dev 3.33
Gaus mean : 38.1837
Gaus Width: 4.0203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 44.61
Std Dev 3.013
Gaus mean : 45.2377
Gaus Width: 3.3115
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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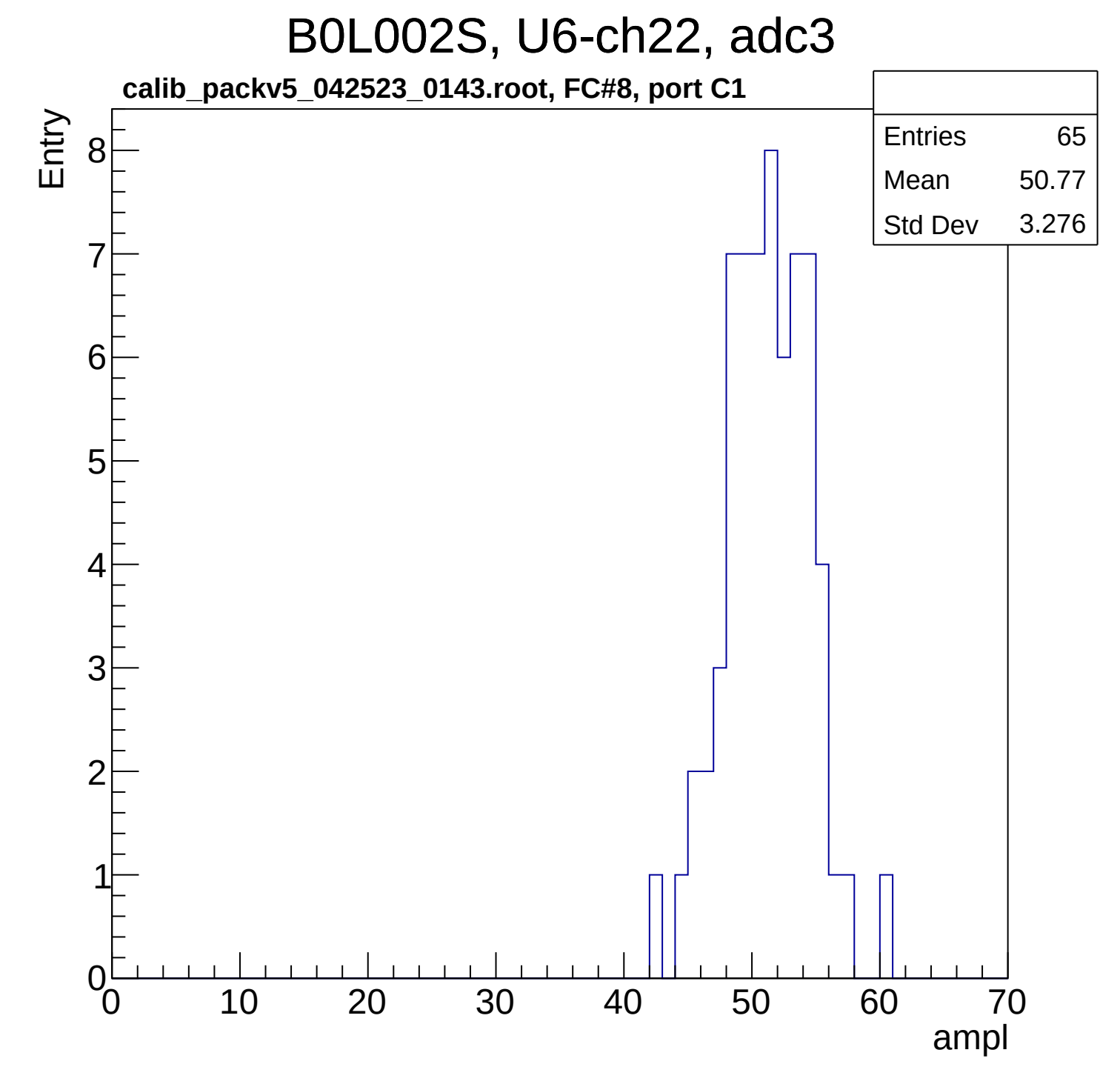
B0L002S, U6-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.77
Std Dev 3.276
Entry
ampl
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2
3
4
5
6
7
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0
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20
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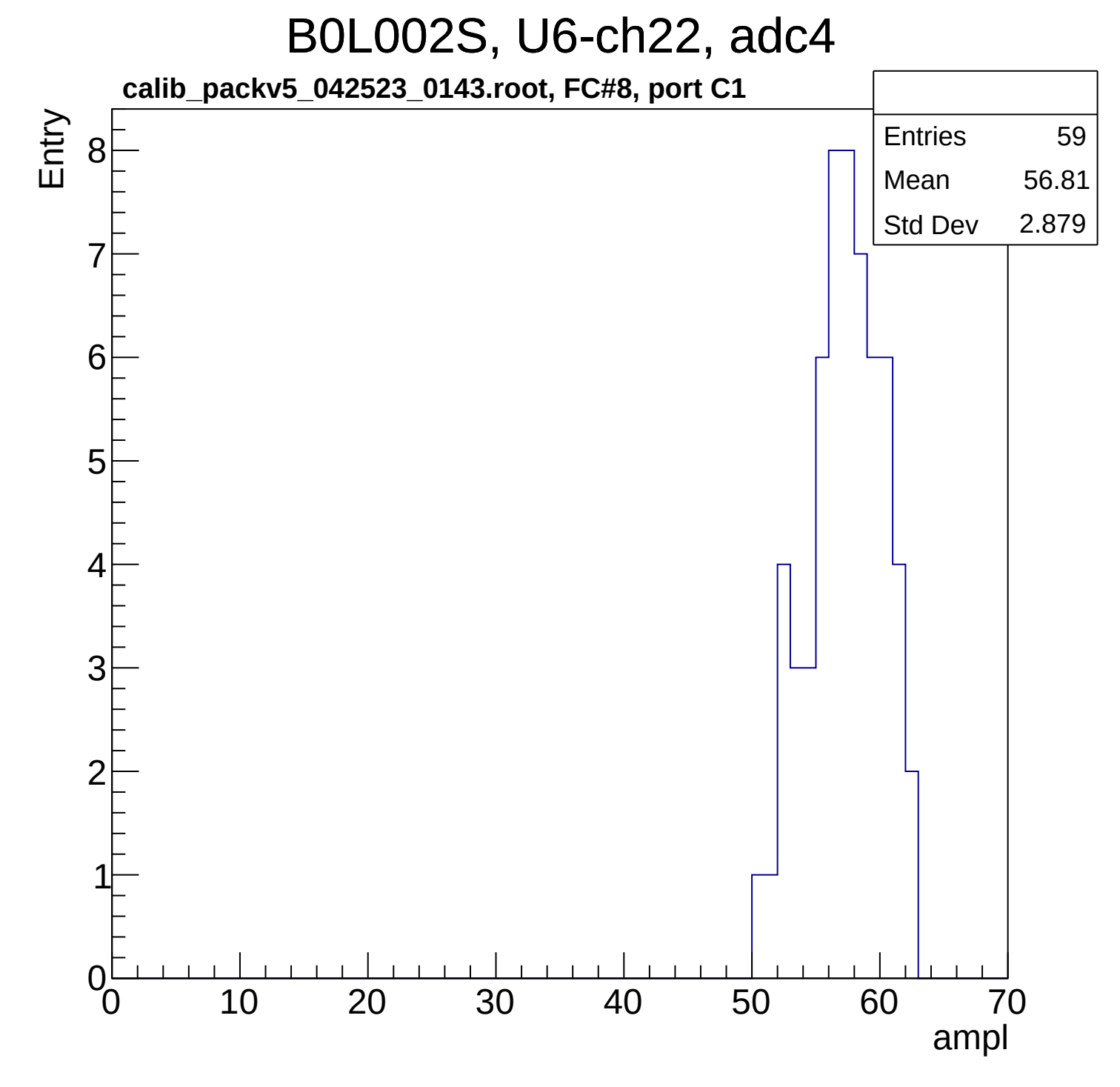
B0L002S, U6-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.81
Std Dev 2.879
Entry
ampl
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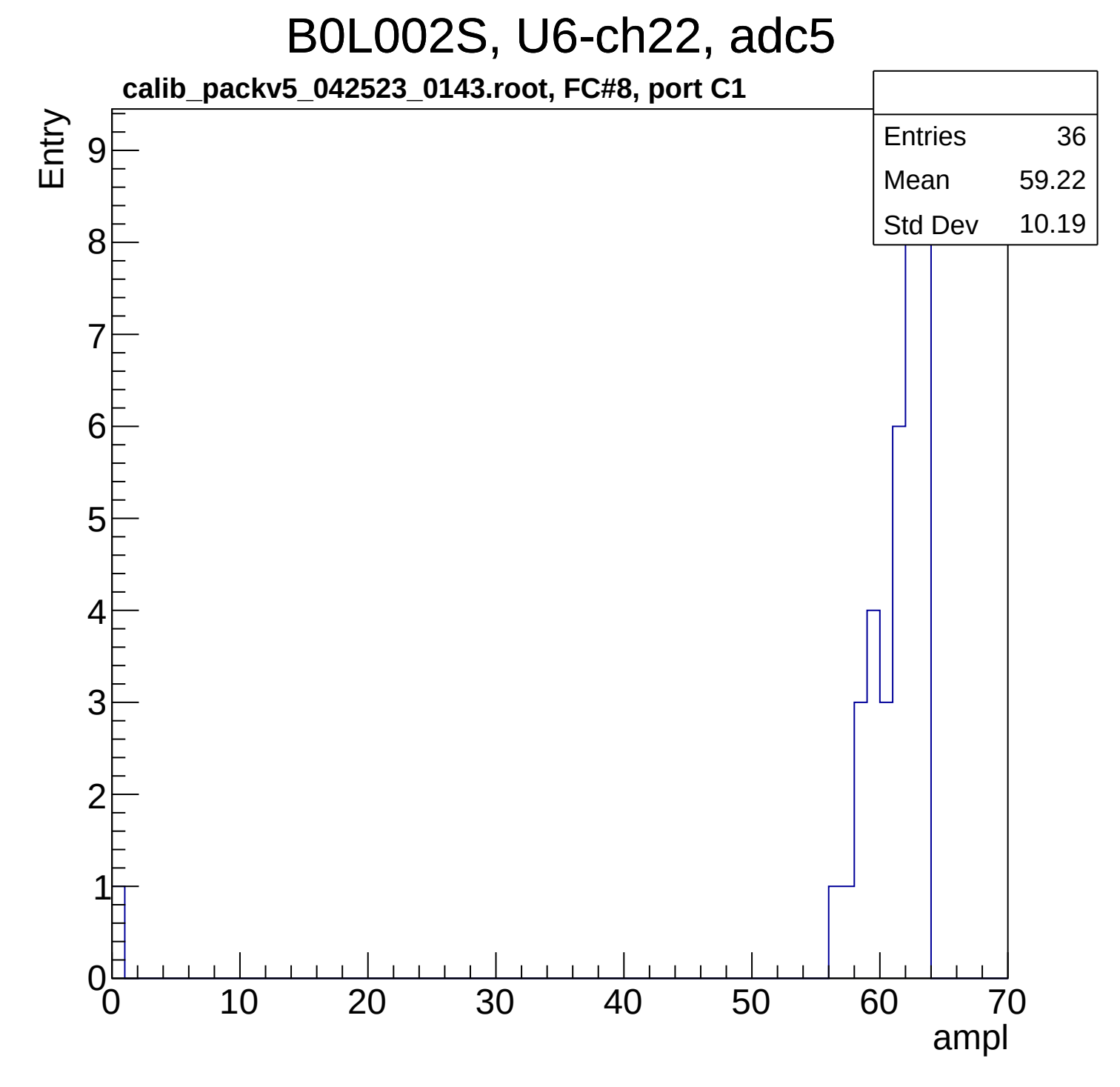
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.22
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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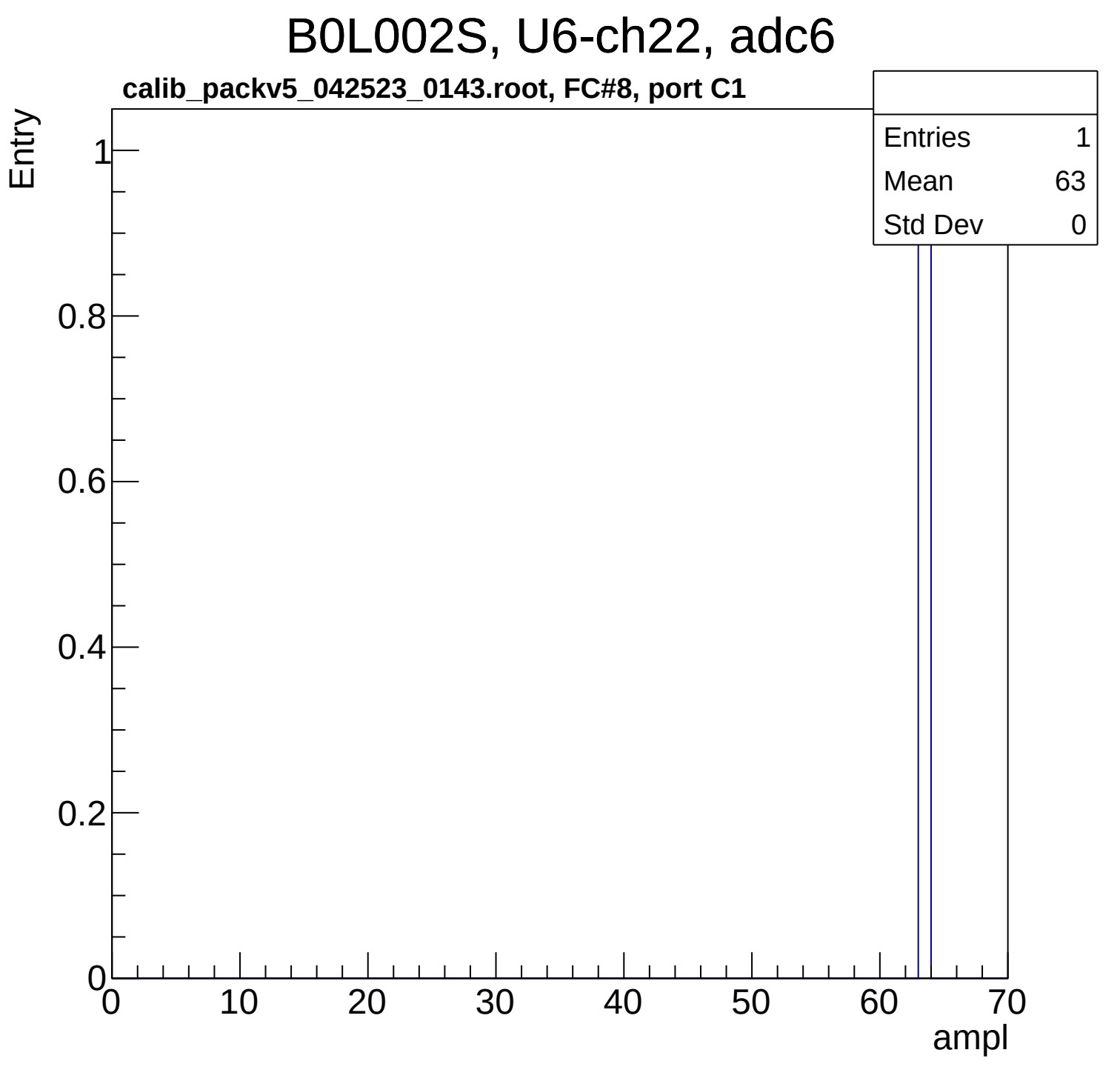
B0L002S, U6-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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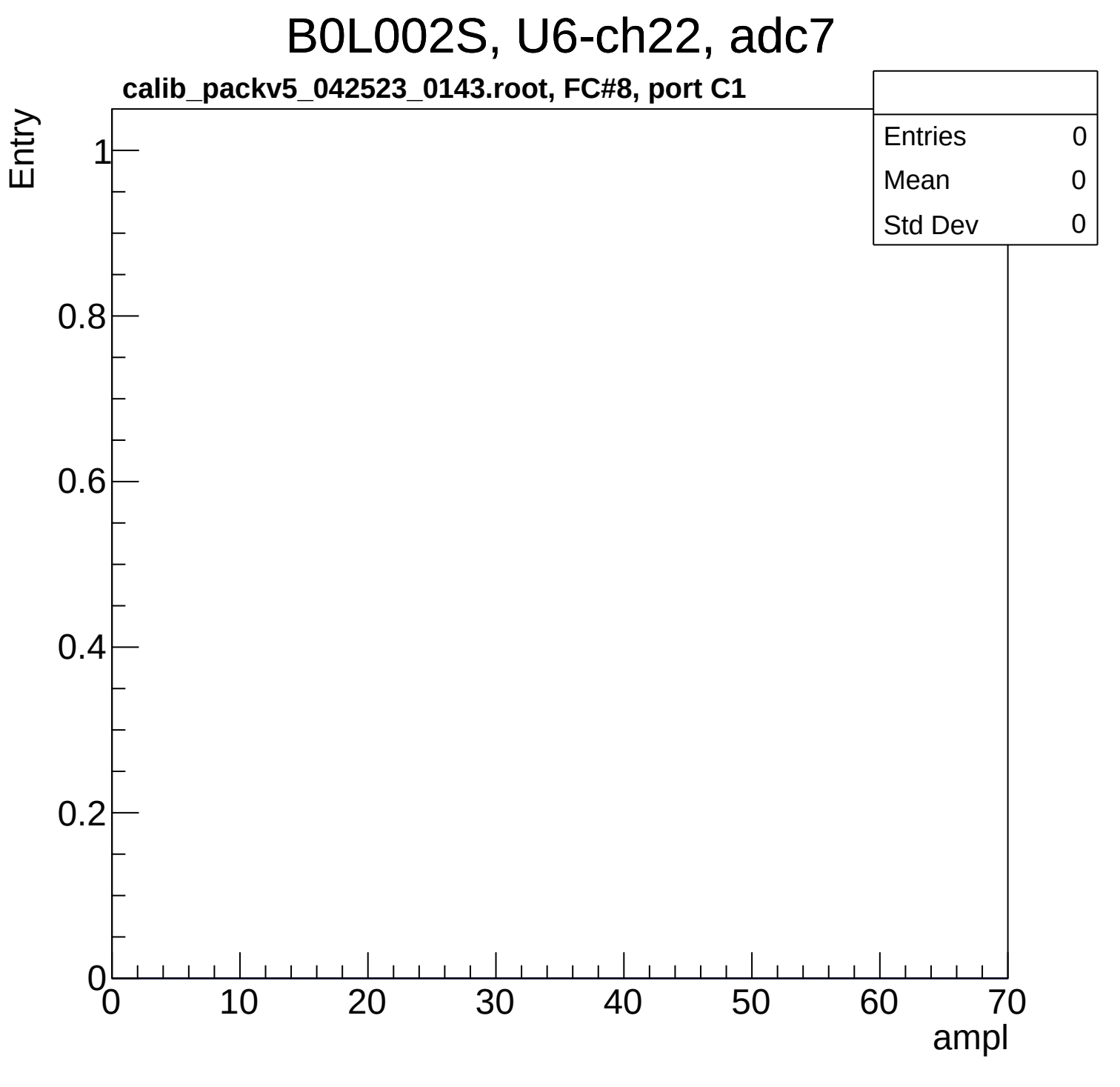
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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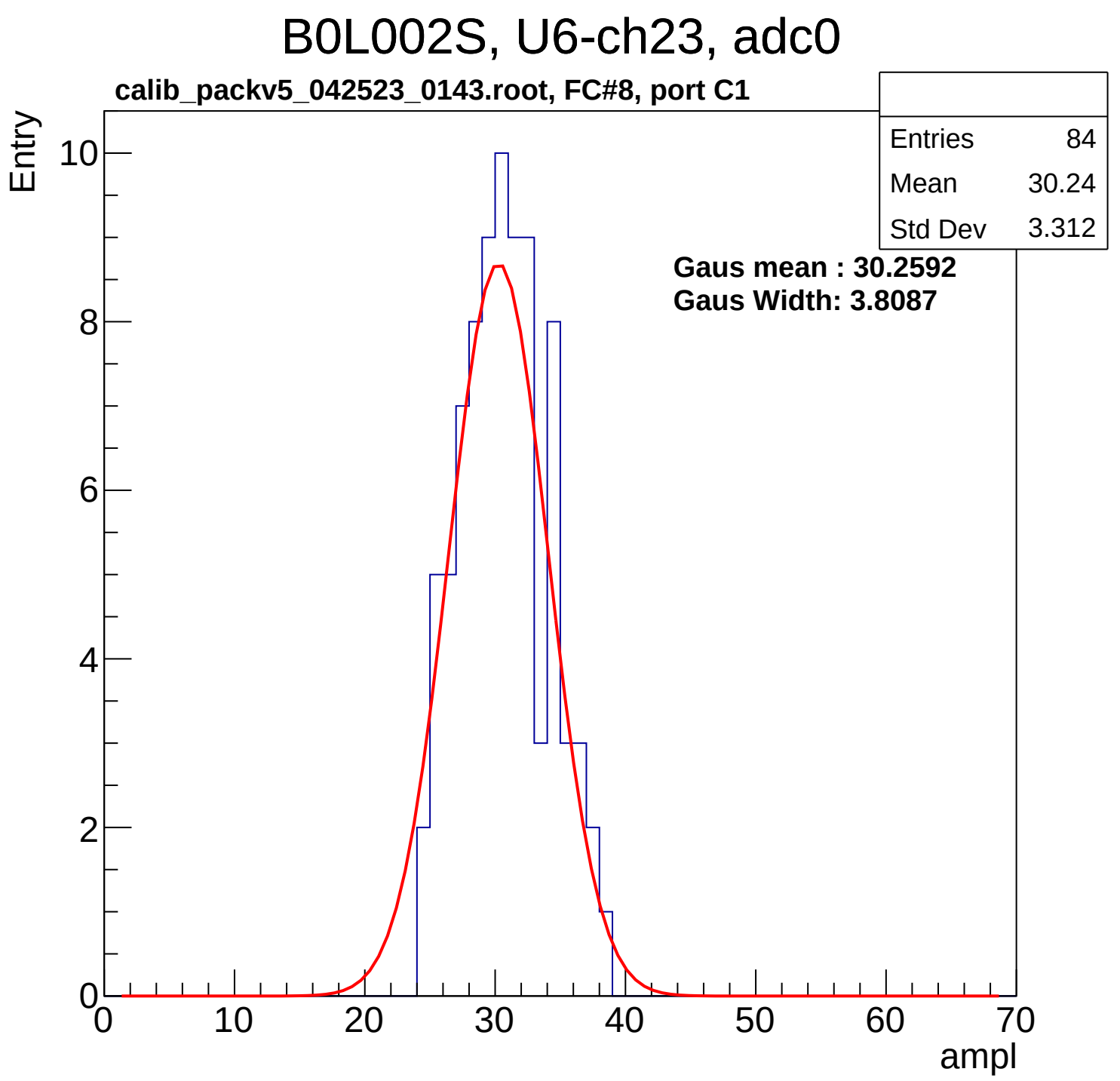
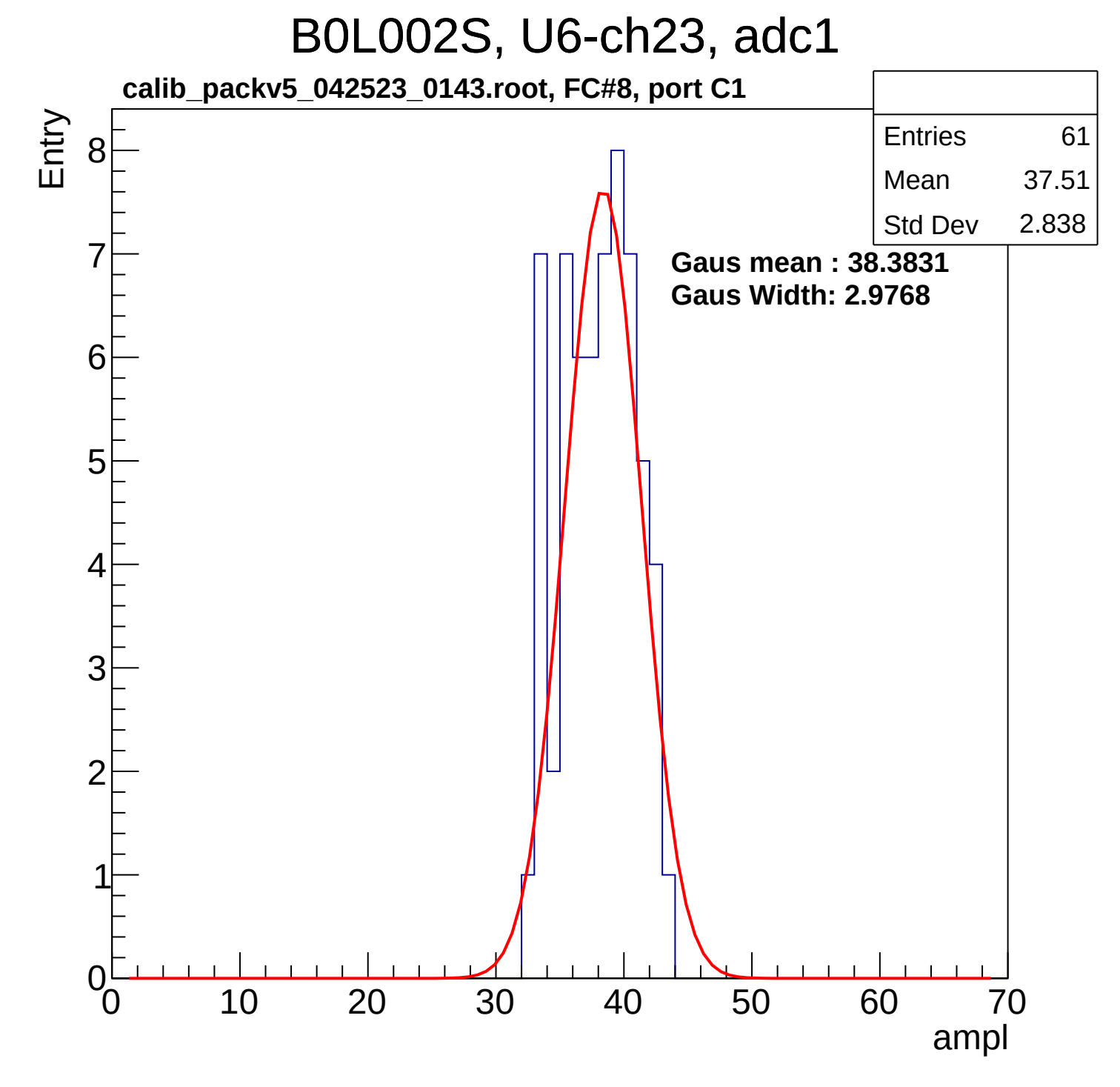
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.24
Std Dev 3.312
Gaus mean : 30.2592
Gaus Width: 3.8087
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.51
Std Dev 2.838
Gaus mean : 38.3831
Gaus Width: 2.9768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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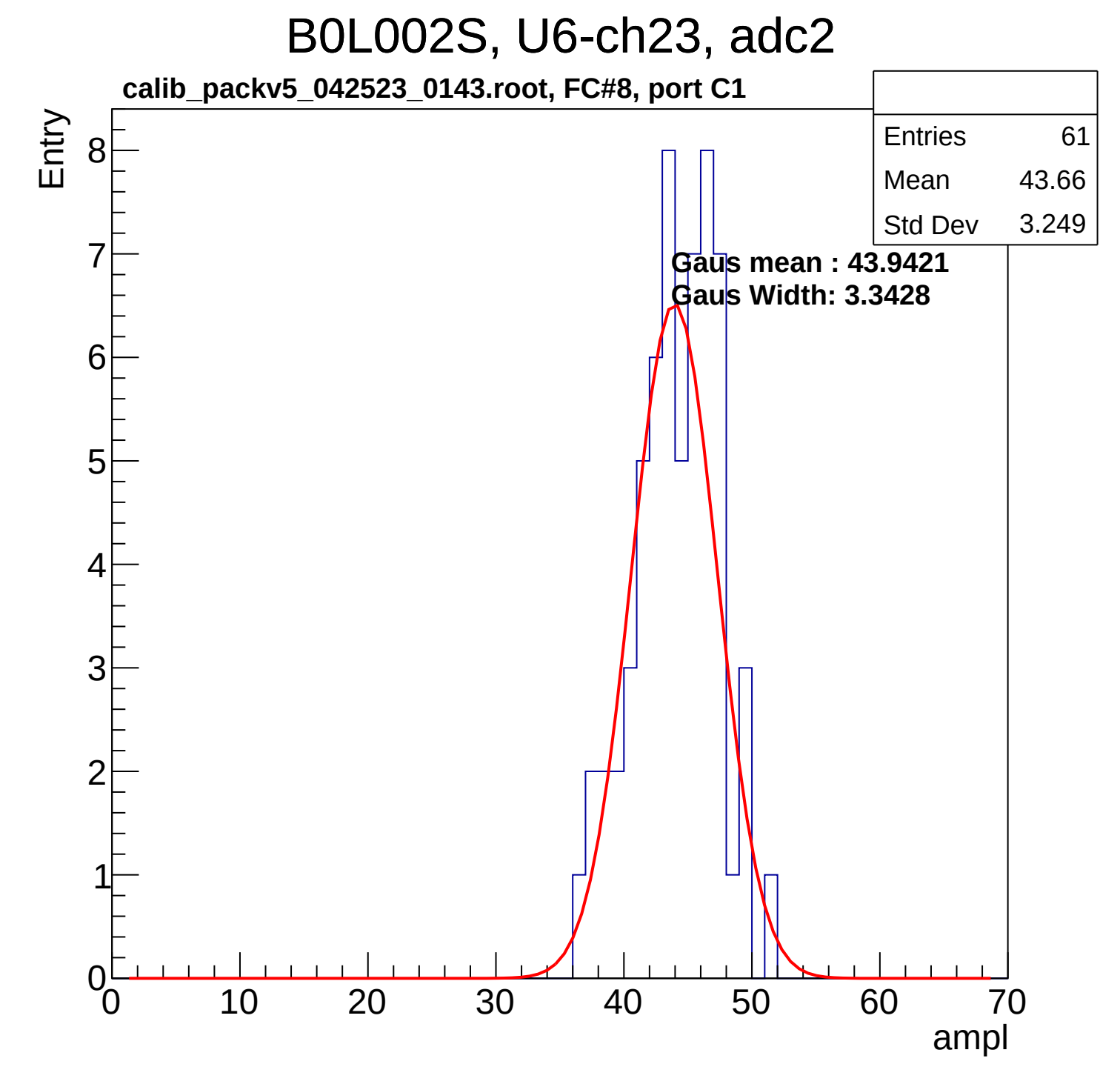
B0L002S, U6-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.66
Std Dev 3.249
Gaus mean : 43.9421
Gaus Width: 3.3428
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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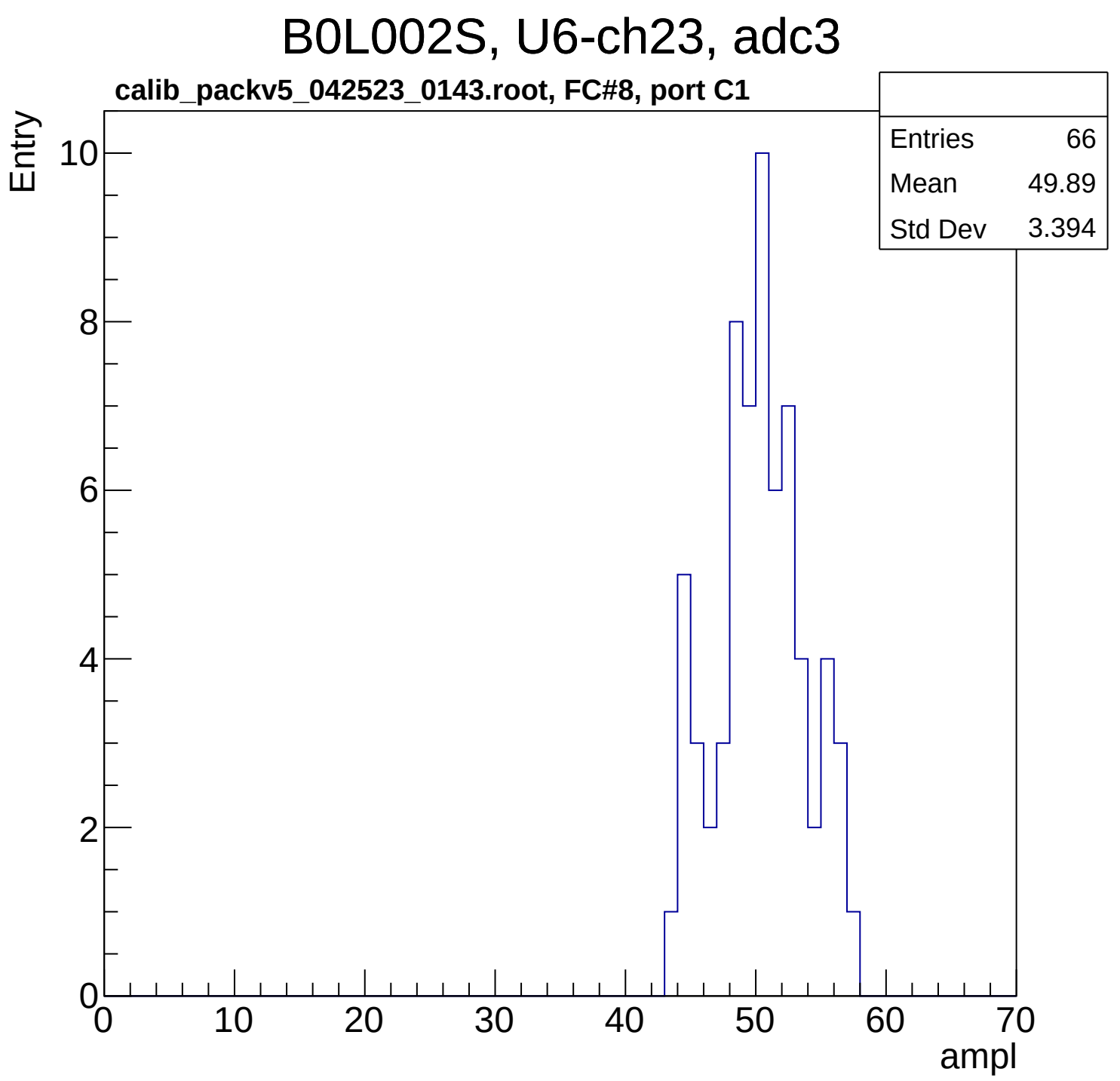
B0L002S, U6-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.89
Std Dev 3.394
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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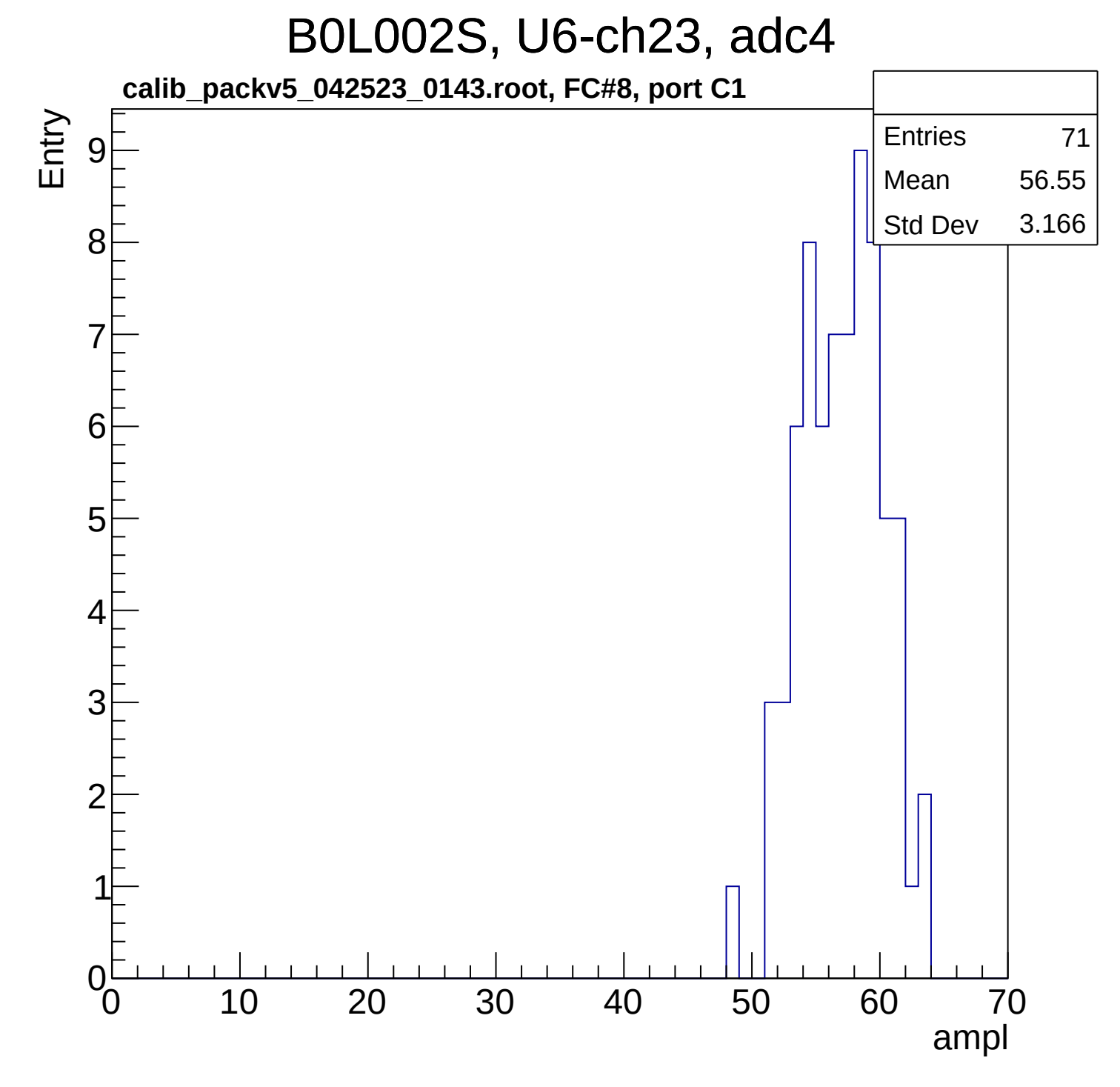
B0L002S, U6-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 56.55
Std Dev 3.166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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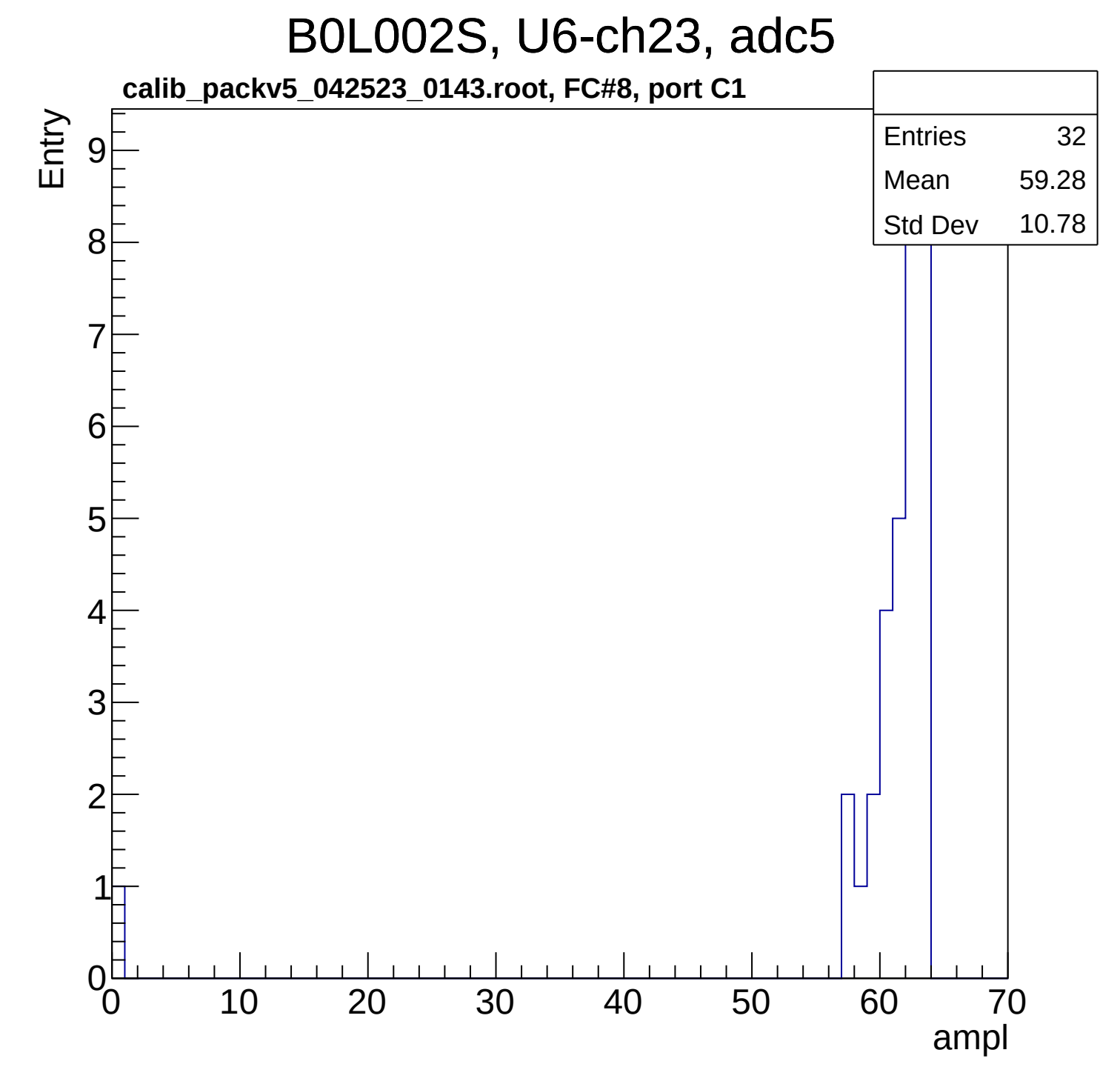
B0L002S, U6-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.28
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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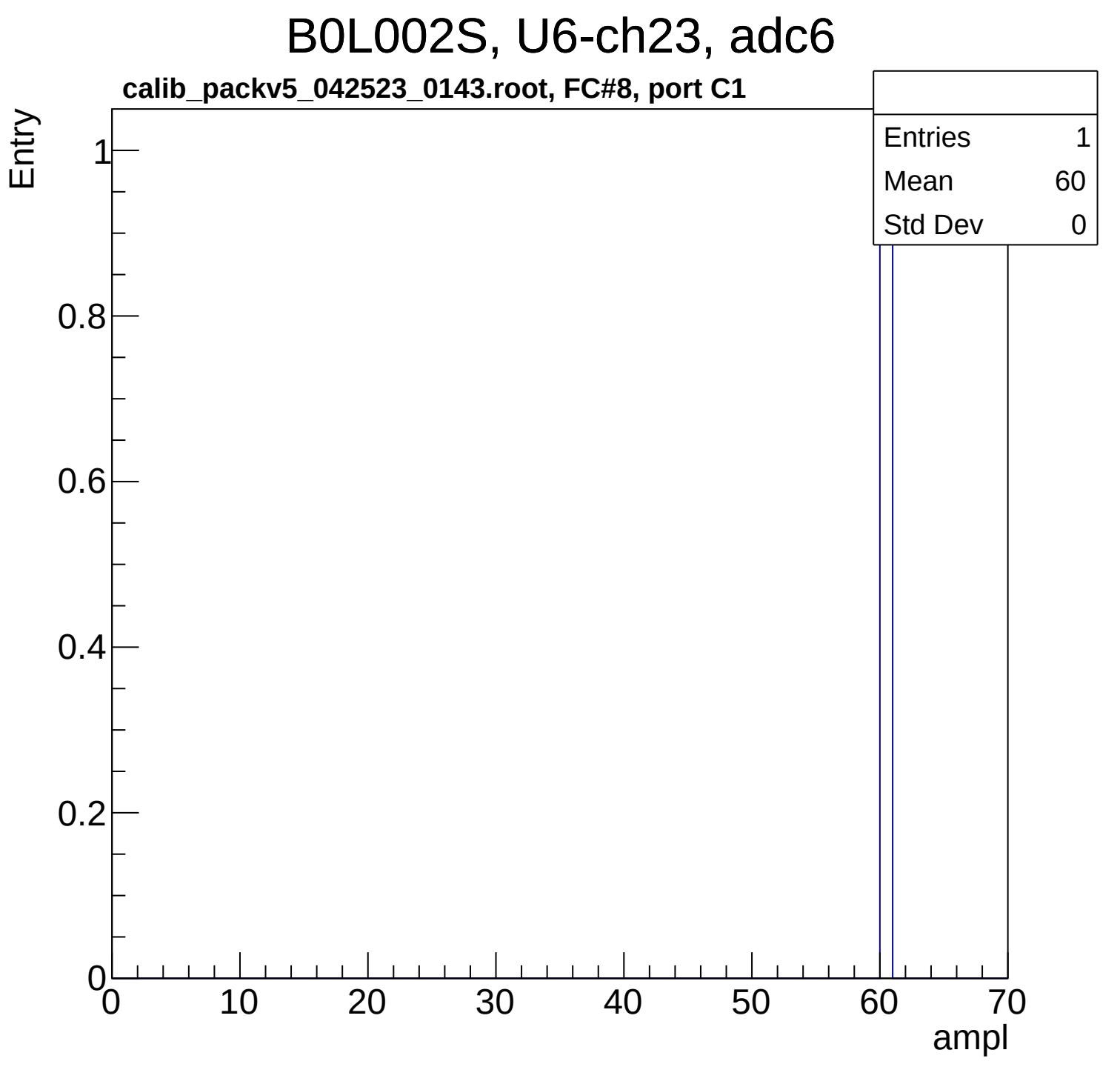
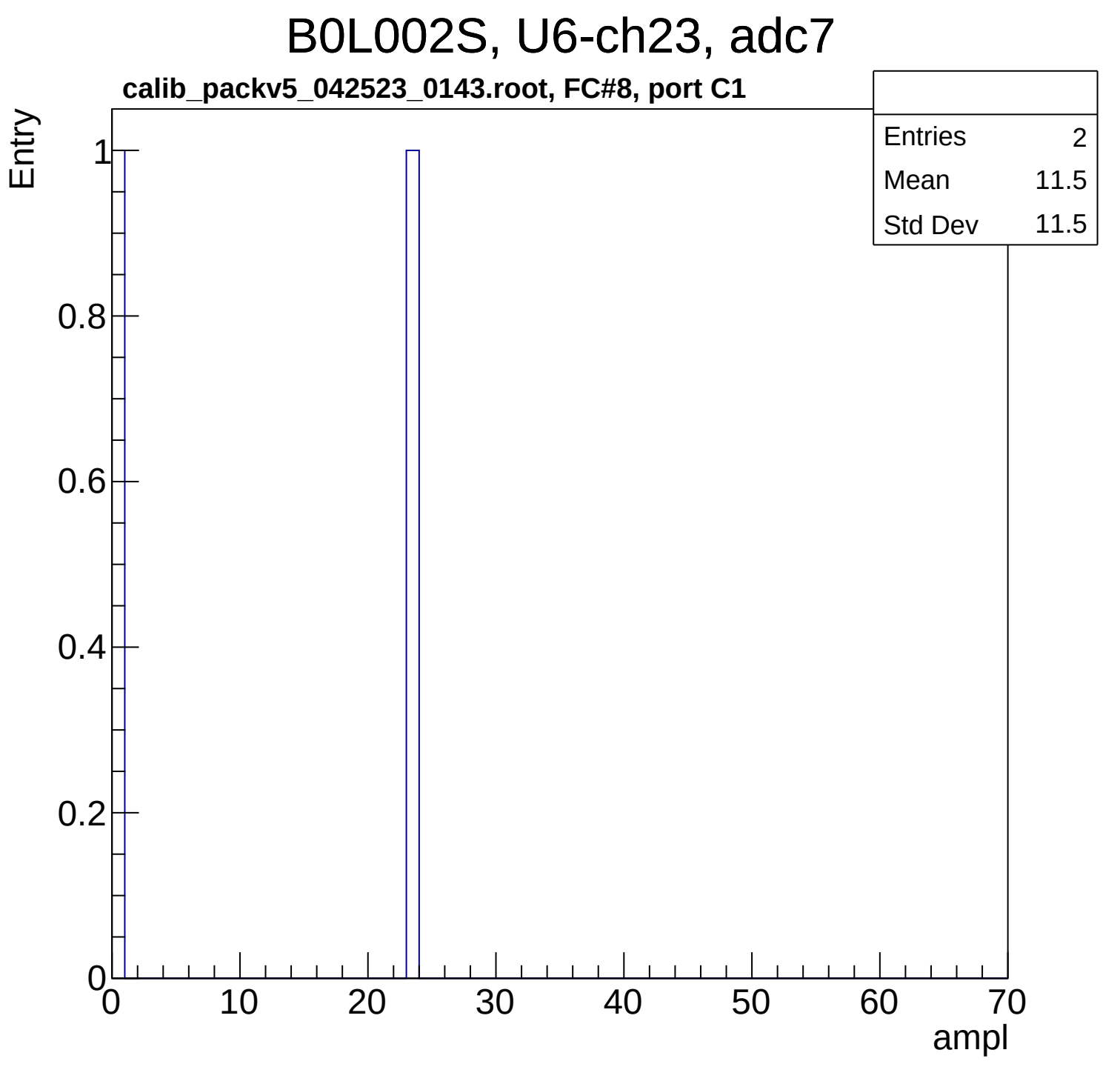
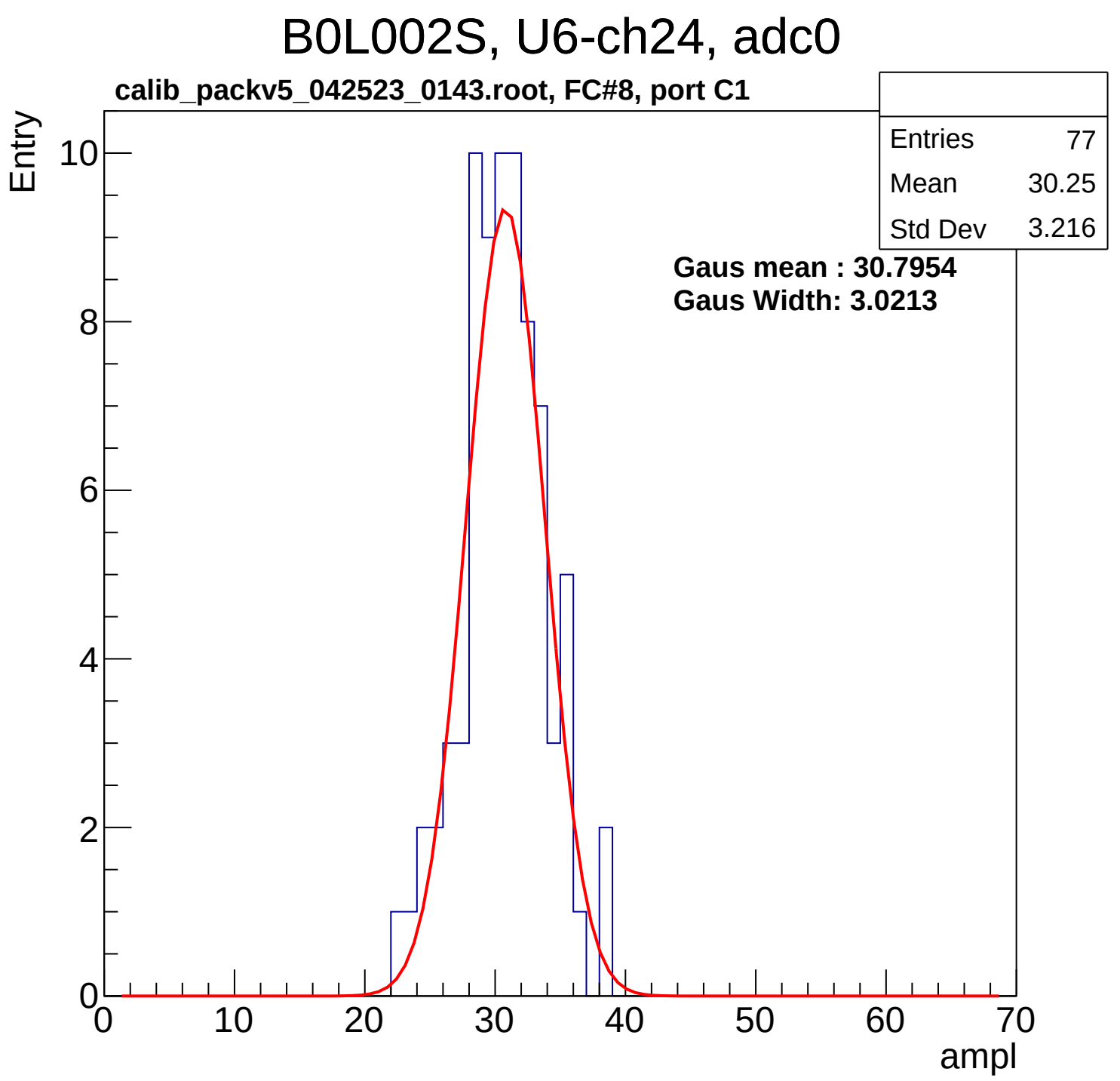
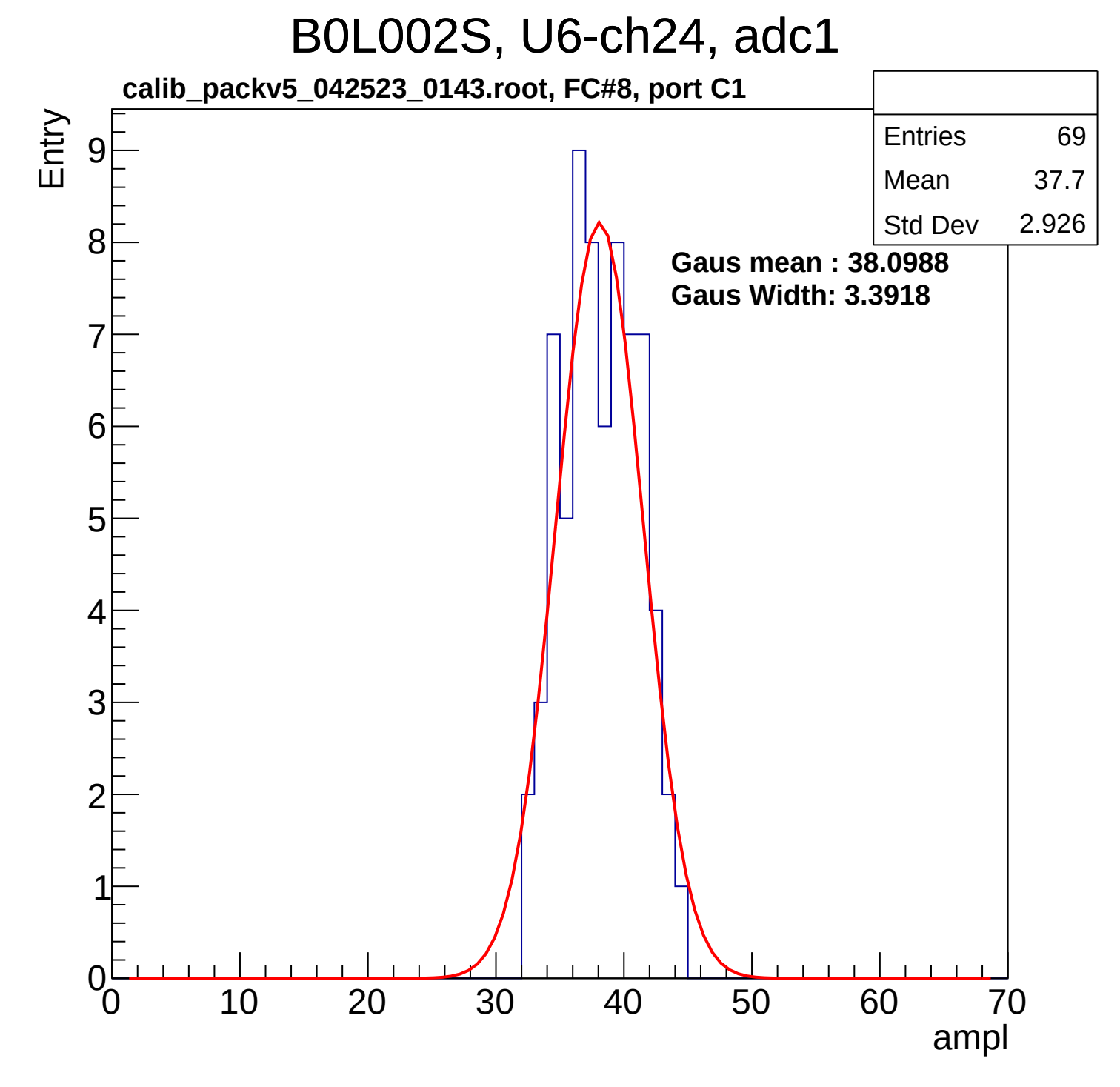
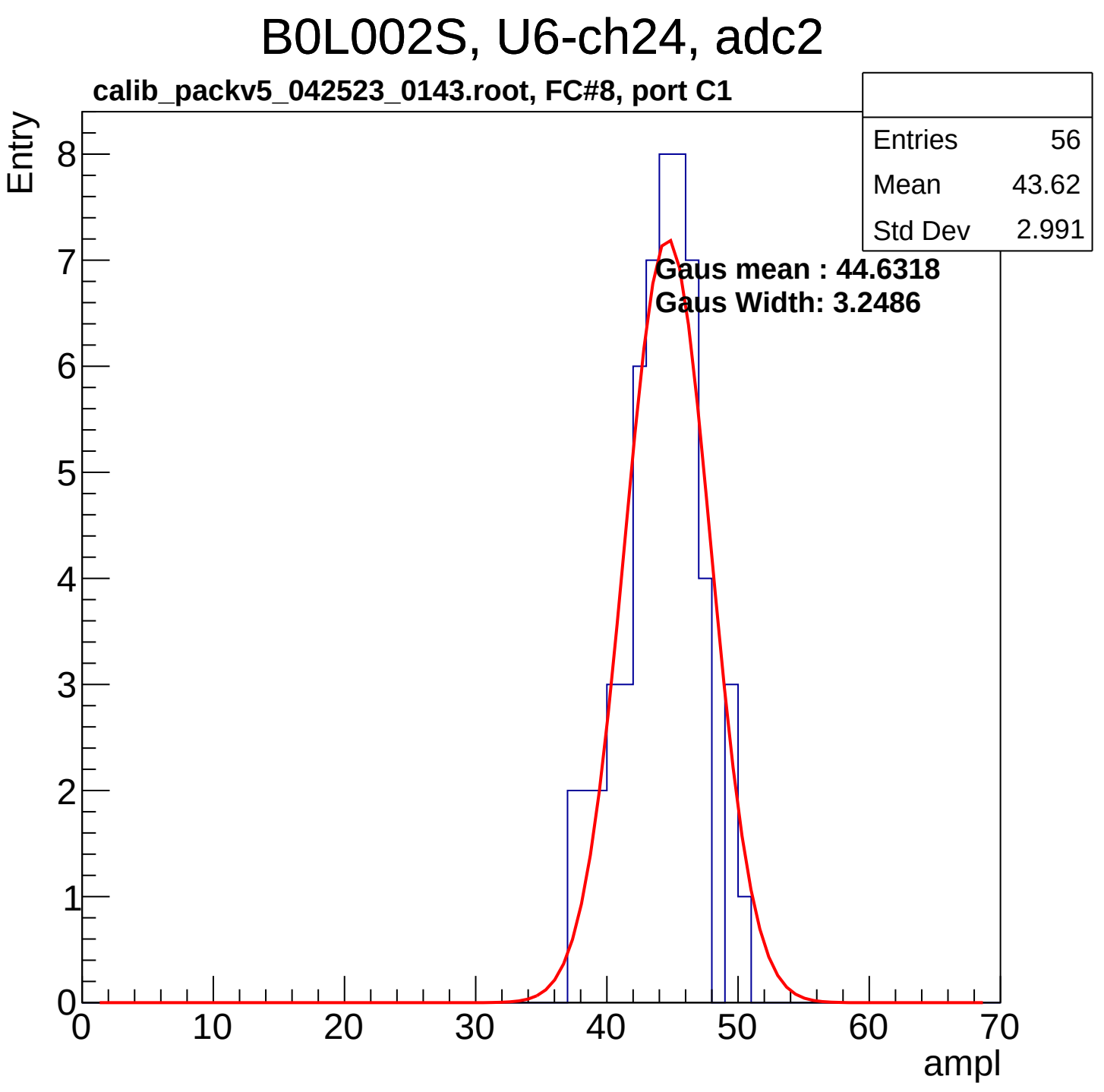
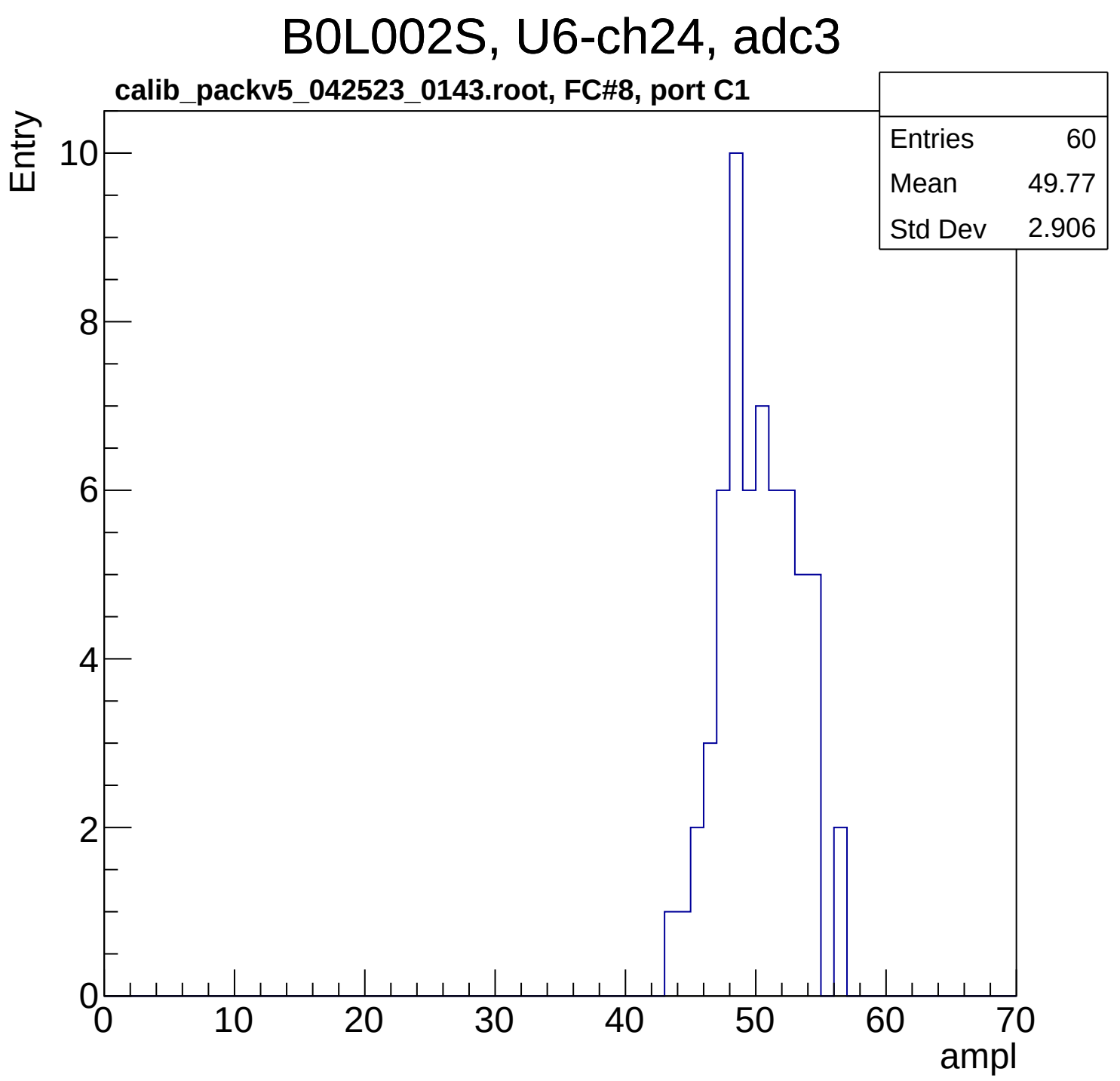

B0L002S, U6-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

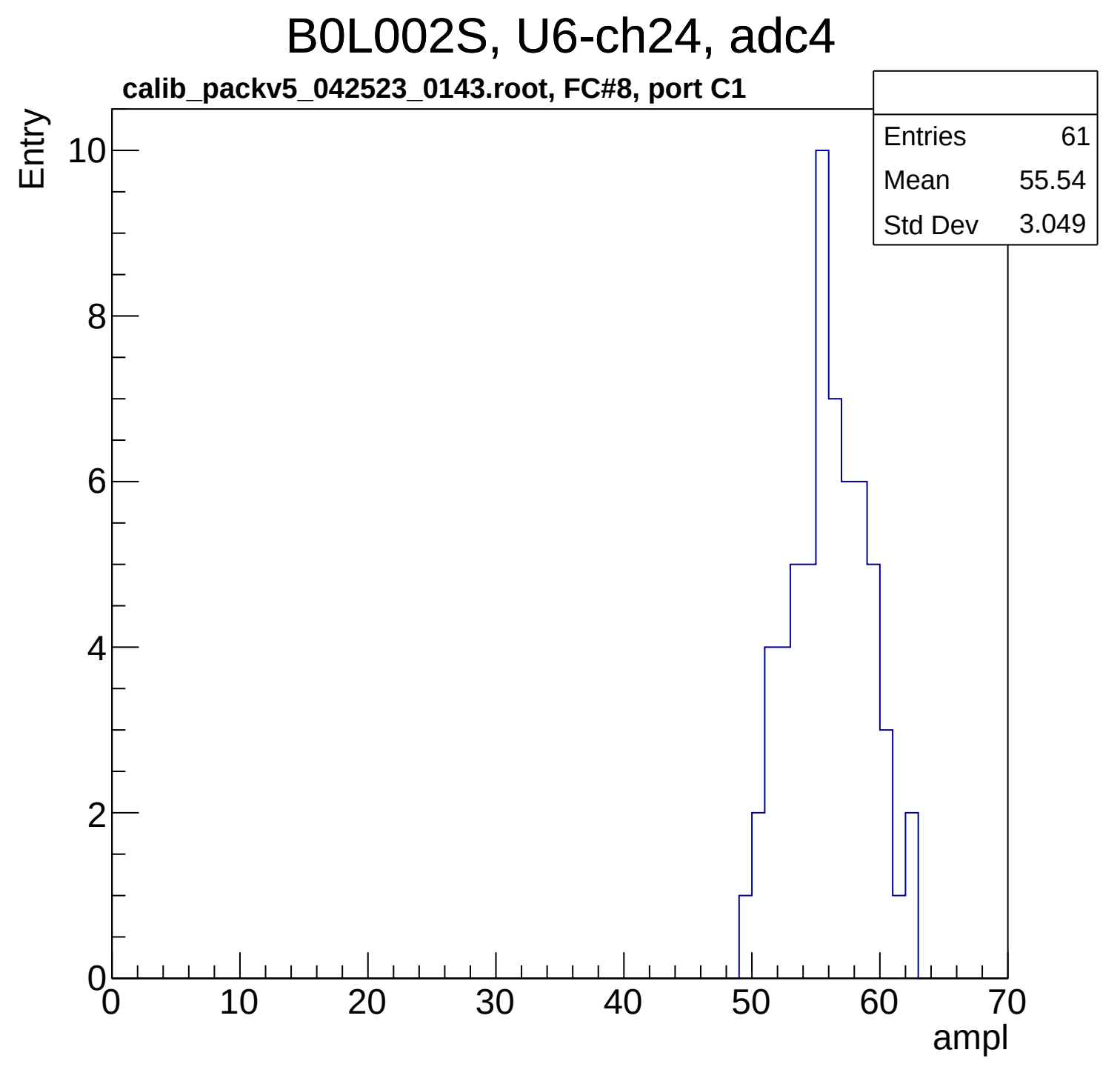

B0L002S, U6-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

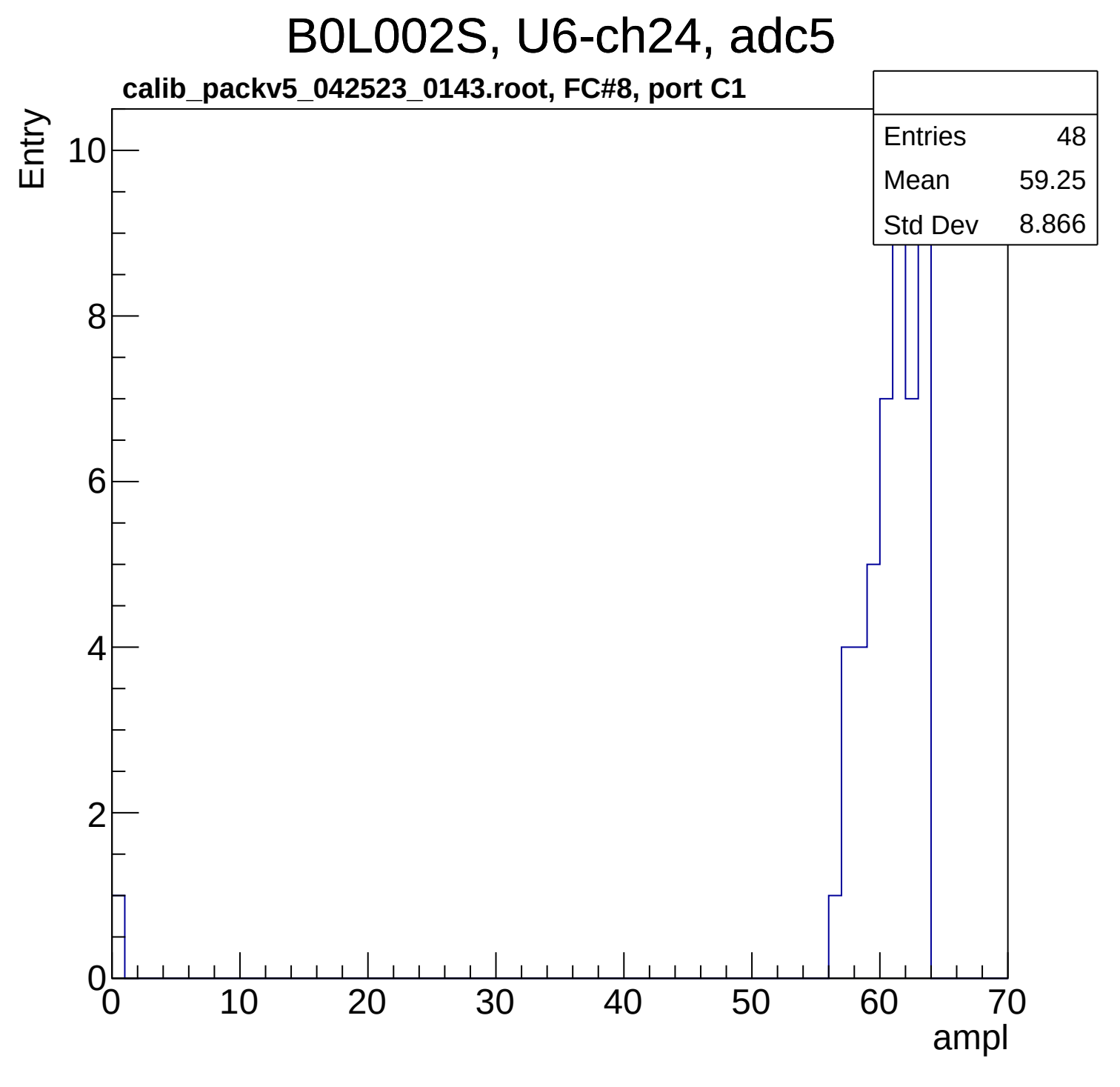

B0L002S, U6-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.25
Std Dev 3.216
Gaus mean : 30.7954
Gaus Width: 3.0213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

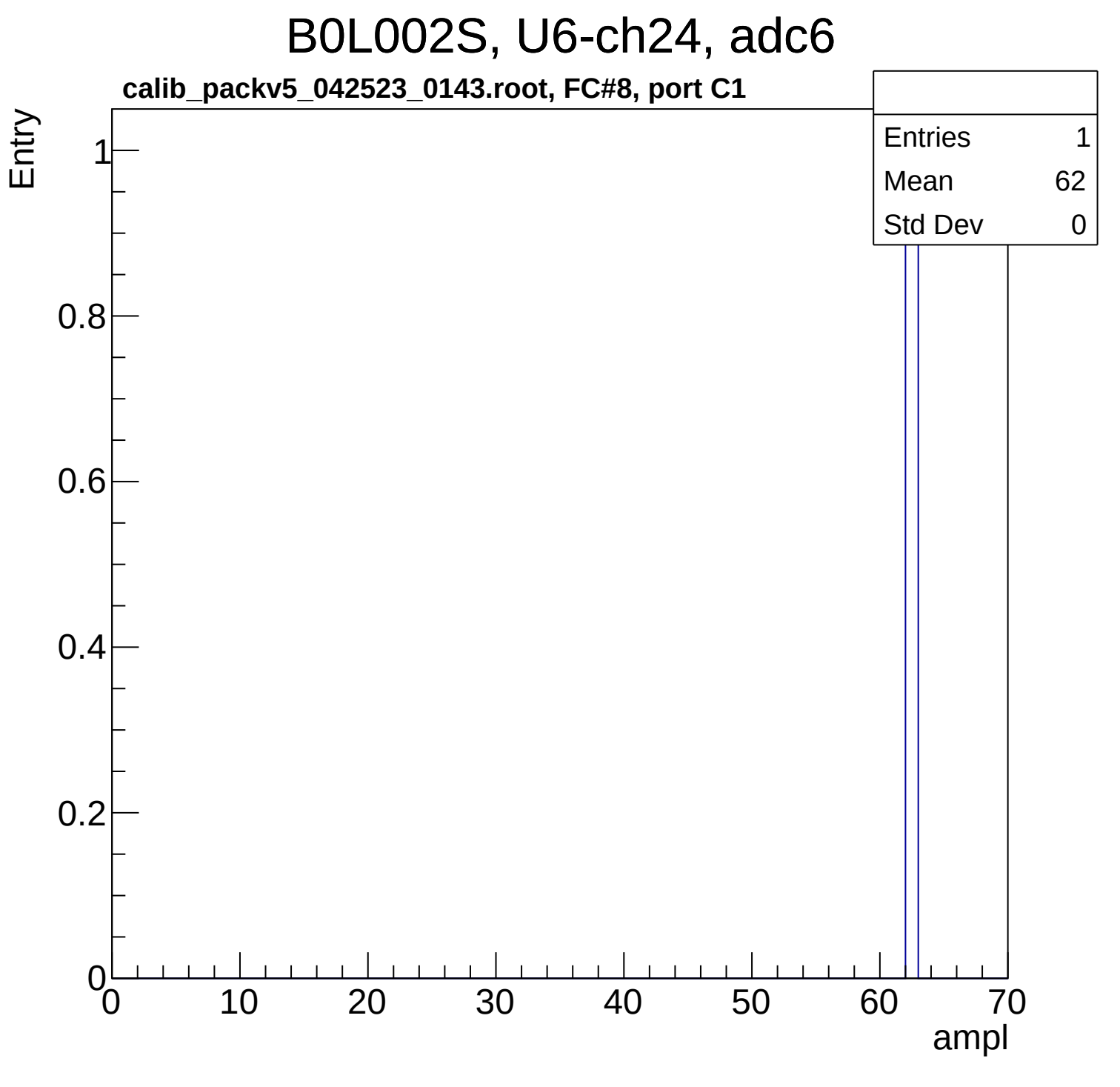

B0L002S, U6-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.7
Std Dev 2.926
Gaus mean : 38.0988
Gaus Width: 3.3918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

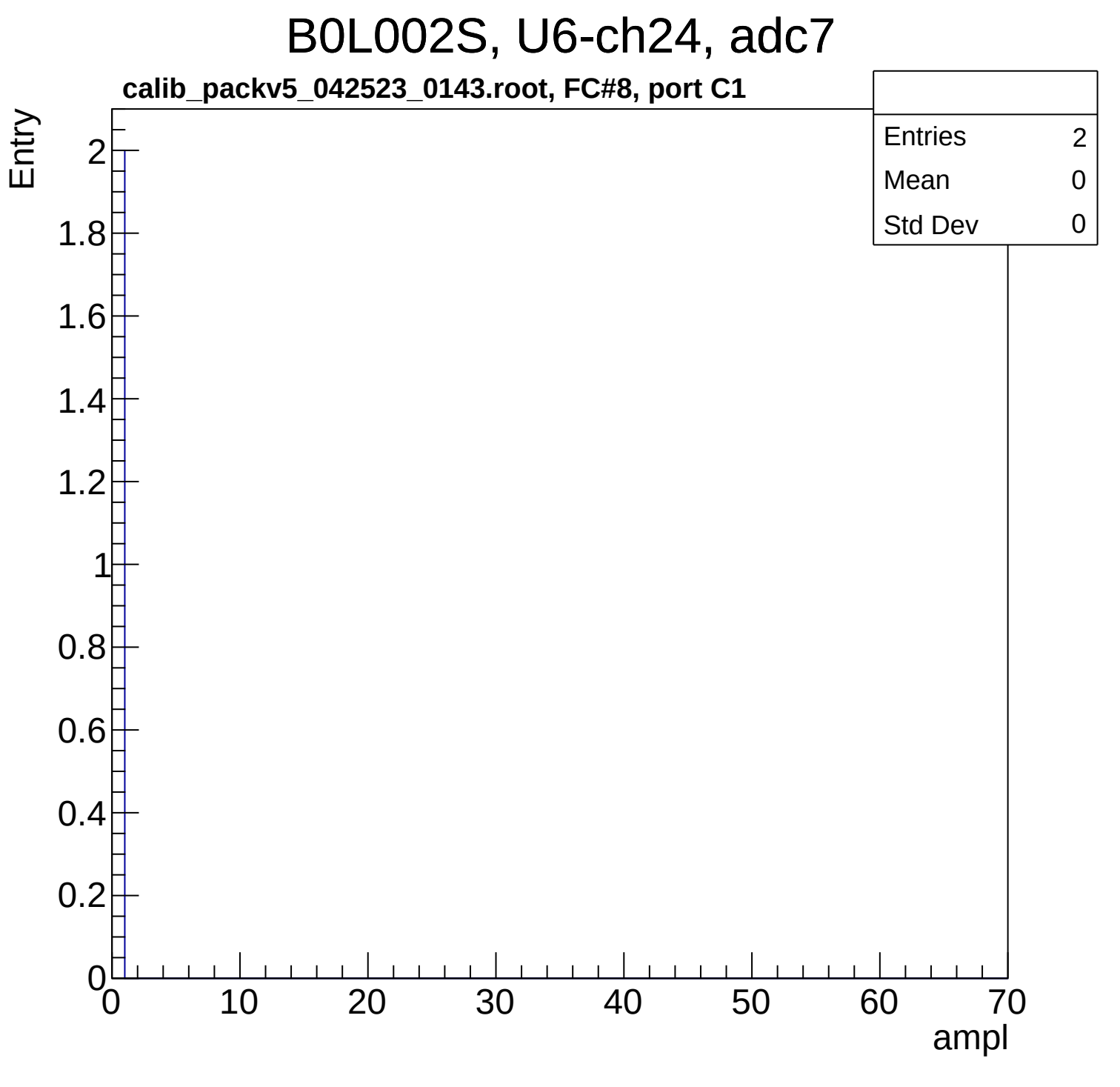

B0L002S, U6-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 43.62
Std Dev 2.991
Gaus mean : 44.6318
Gaus Width: 3.2486
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

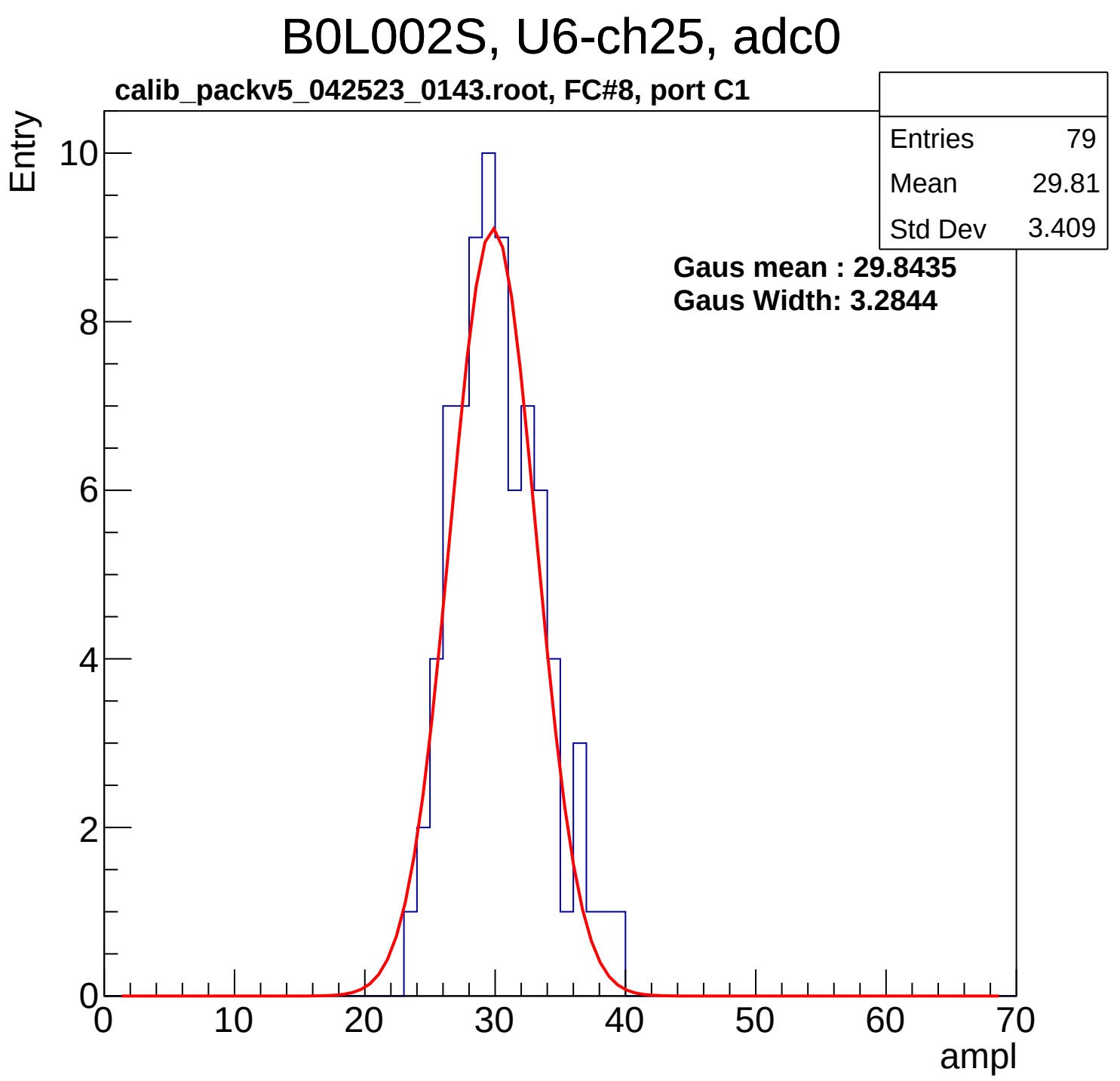

B0L002S, U6-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 49.77
Std Dev 2.906
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

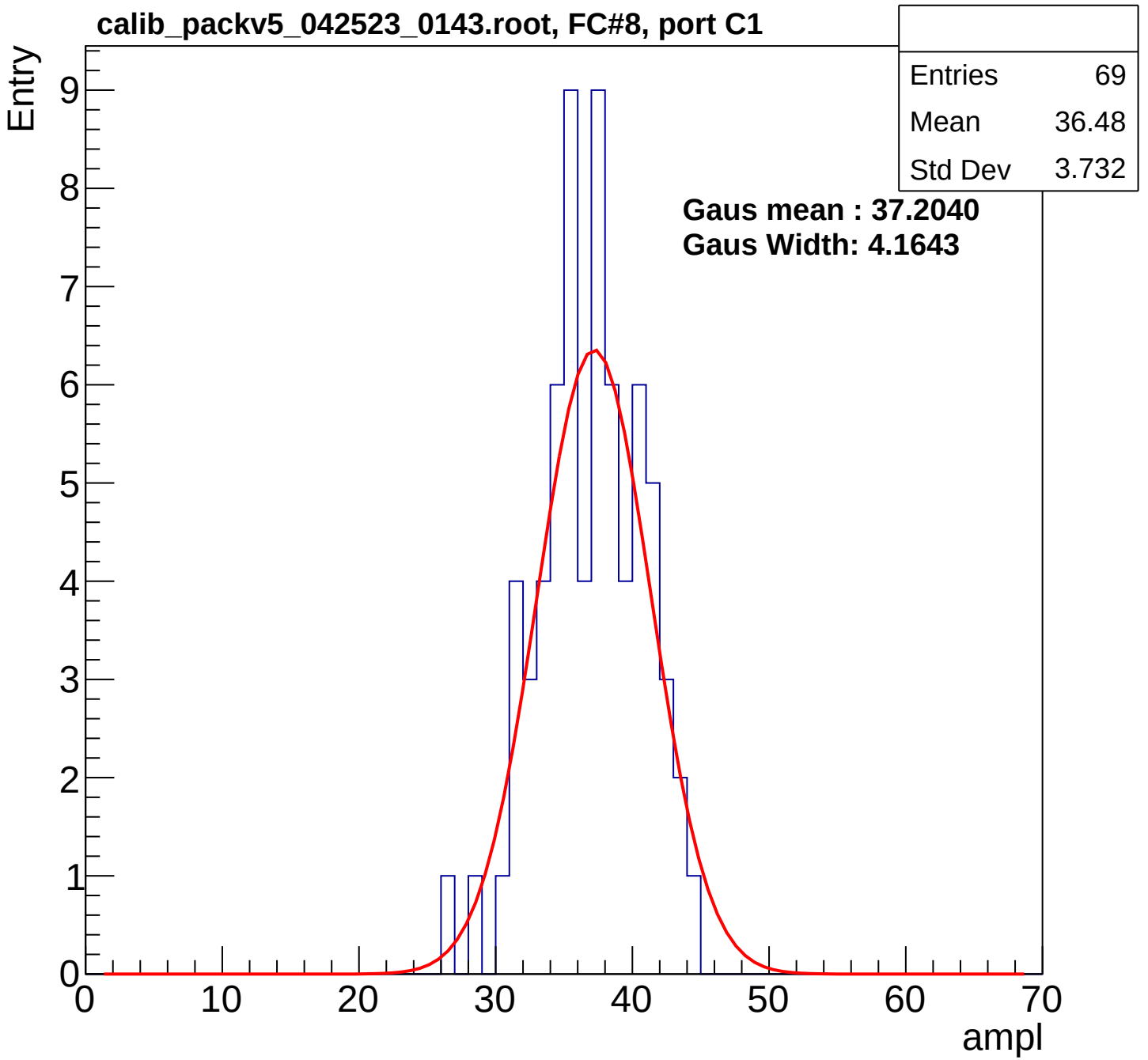

B0L002S, U6-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.54
Std Dev 3.049
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

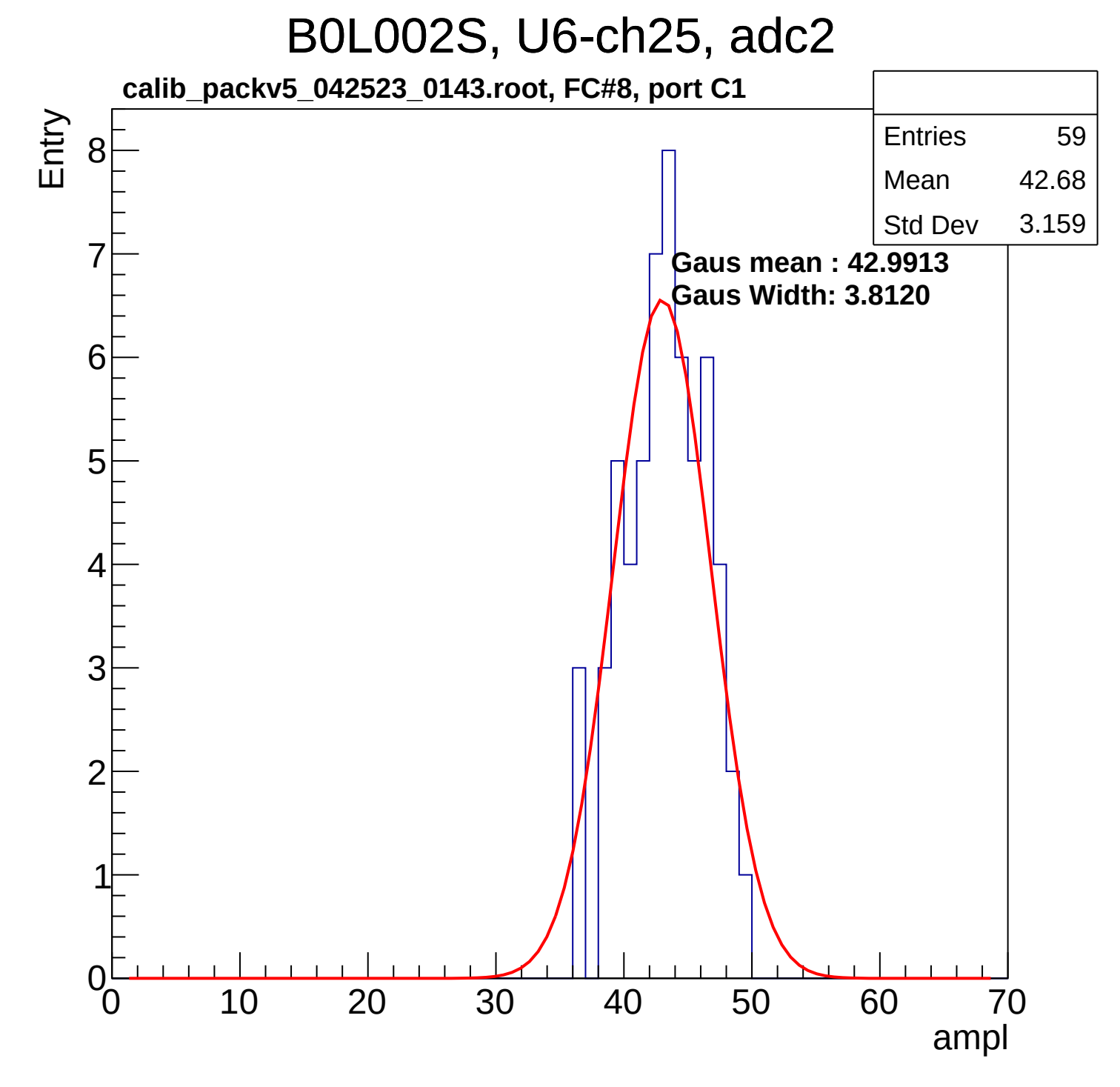

B0L002S, U6-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 59.25
Std Dev 8.866
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

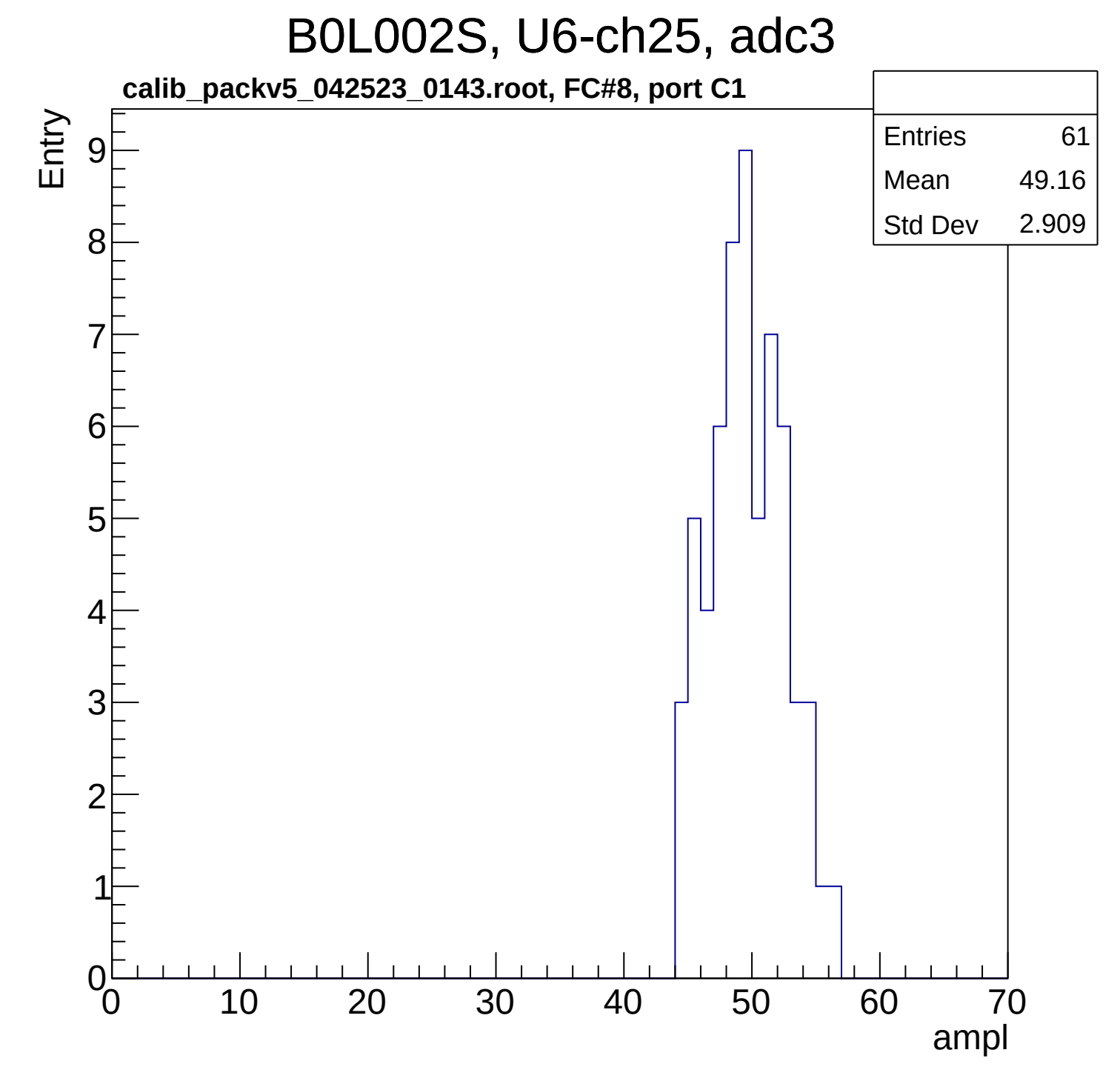

B0L002S, U6-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.81
Std Dev 3.409
Gaus mean : 29.8435
Gaus Width: 3.2844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.48
Std Dev 3.732
Gaus mean : 37.2040
Gaus Width: 4.1643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 42.68
Std Dev 3.159
Gaus mean : 42.9913
Gaus Width: 3.8120
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 49.16
Std Dev 2.909
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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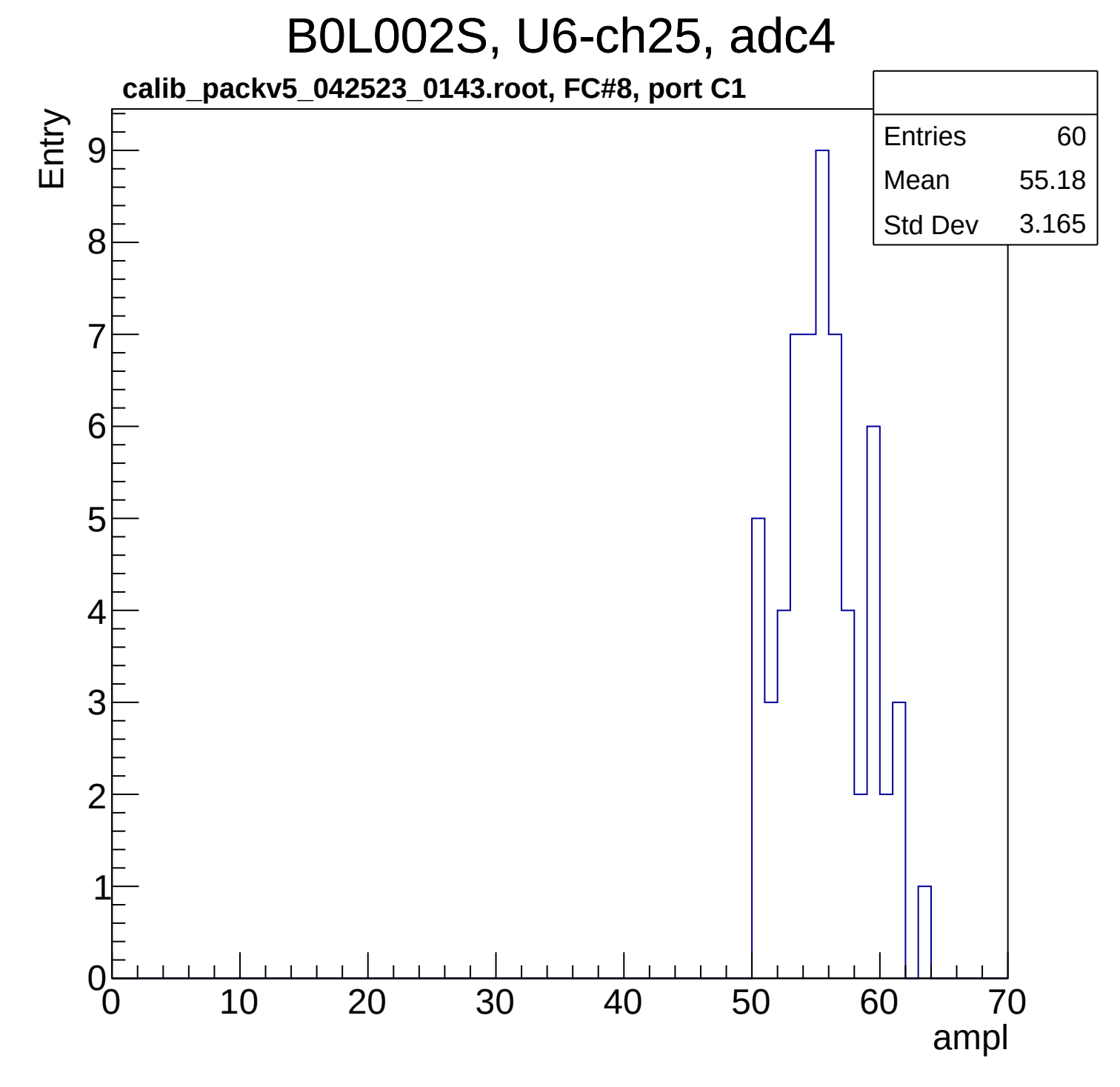
B0L002S, U6-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.18
Std Dev 3.165
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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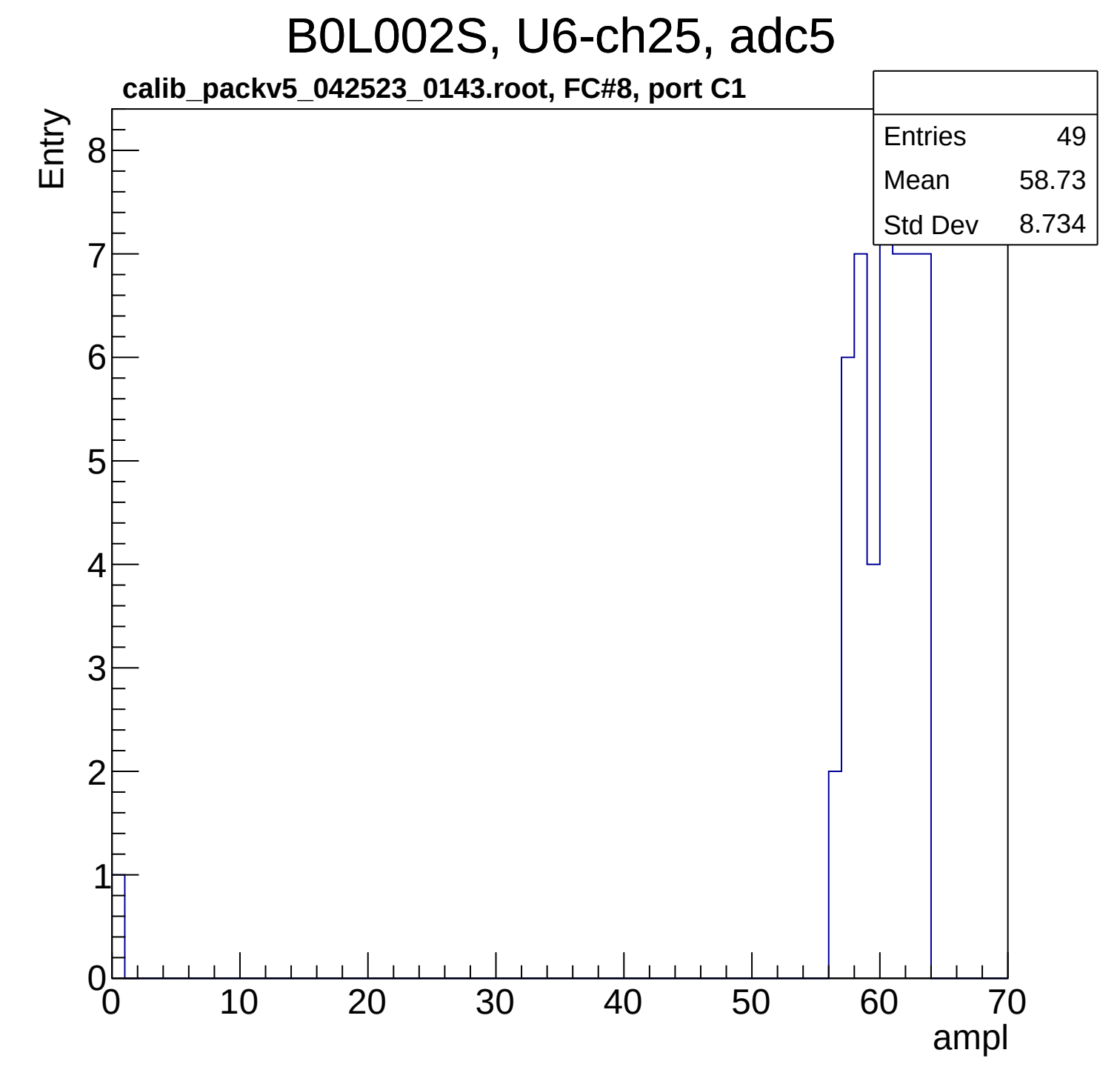
40
50
60
70


B0L002S, U6-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.73
Std Dev 8.734
Entry
ampl
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1
2
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4
5
6
7
8
0
10
20
30
40
50
60
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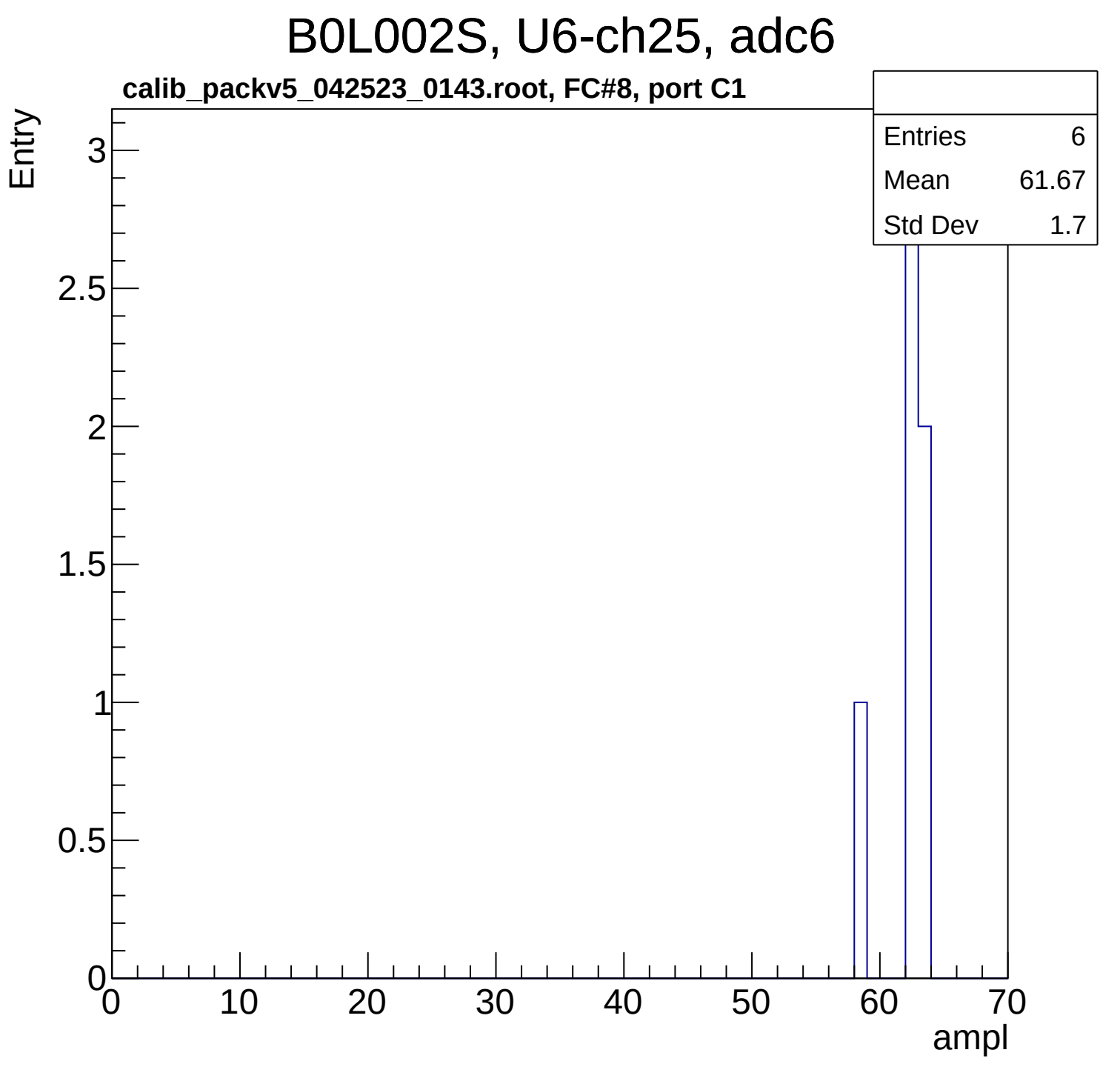
B0L002S, U6-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.67
Std Dev 1.7
Entry
ampl
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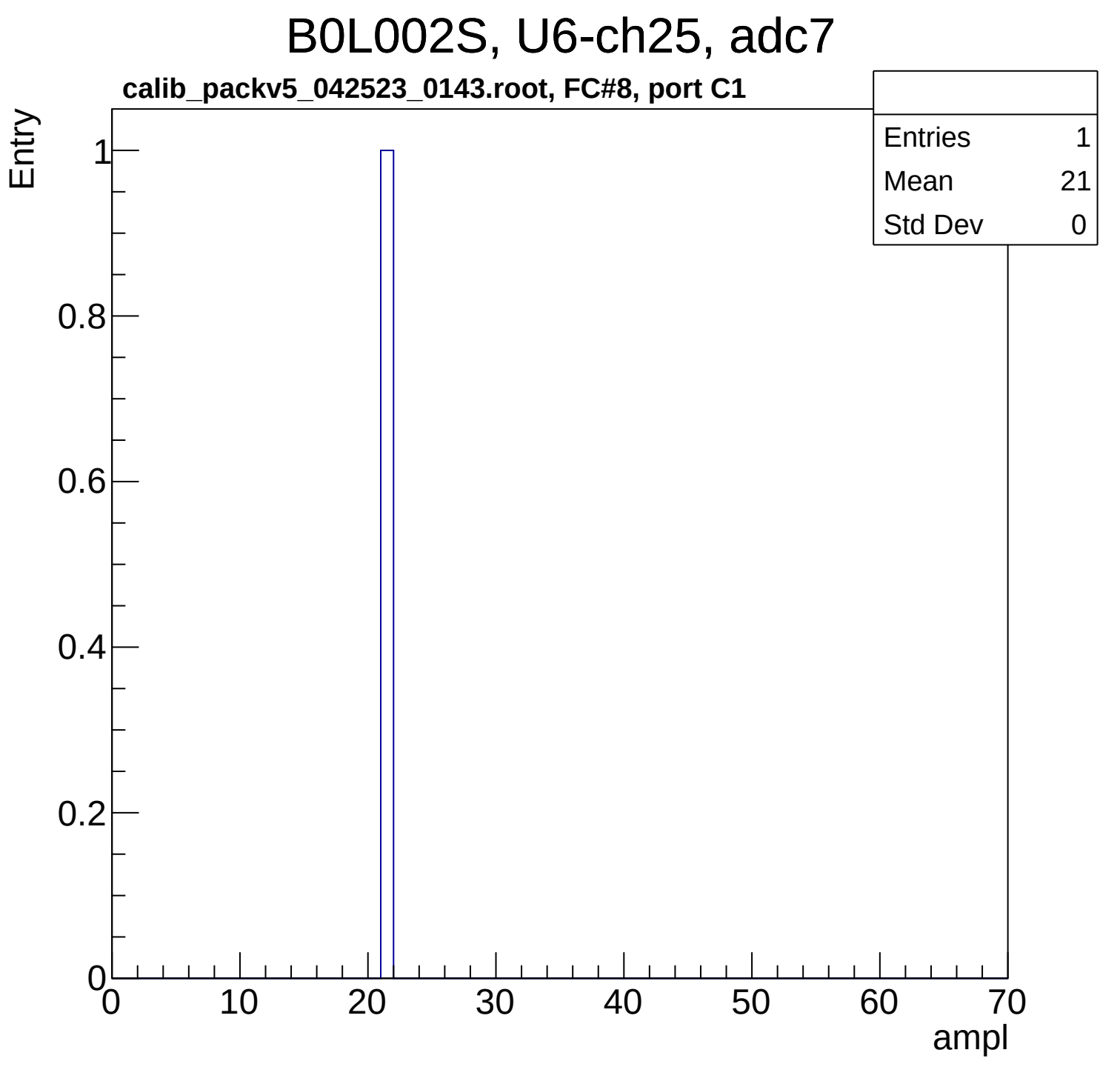
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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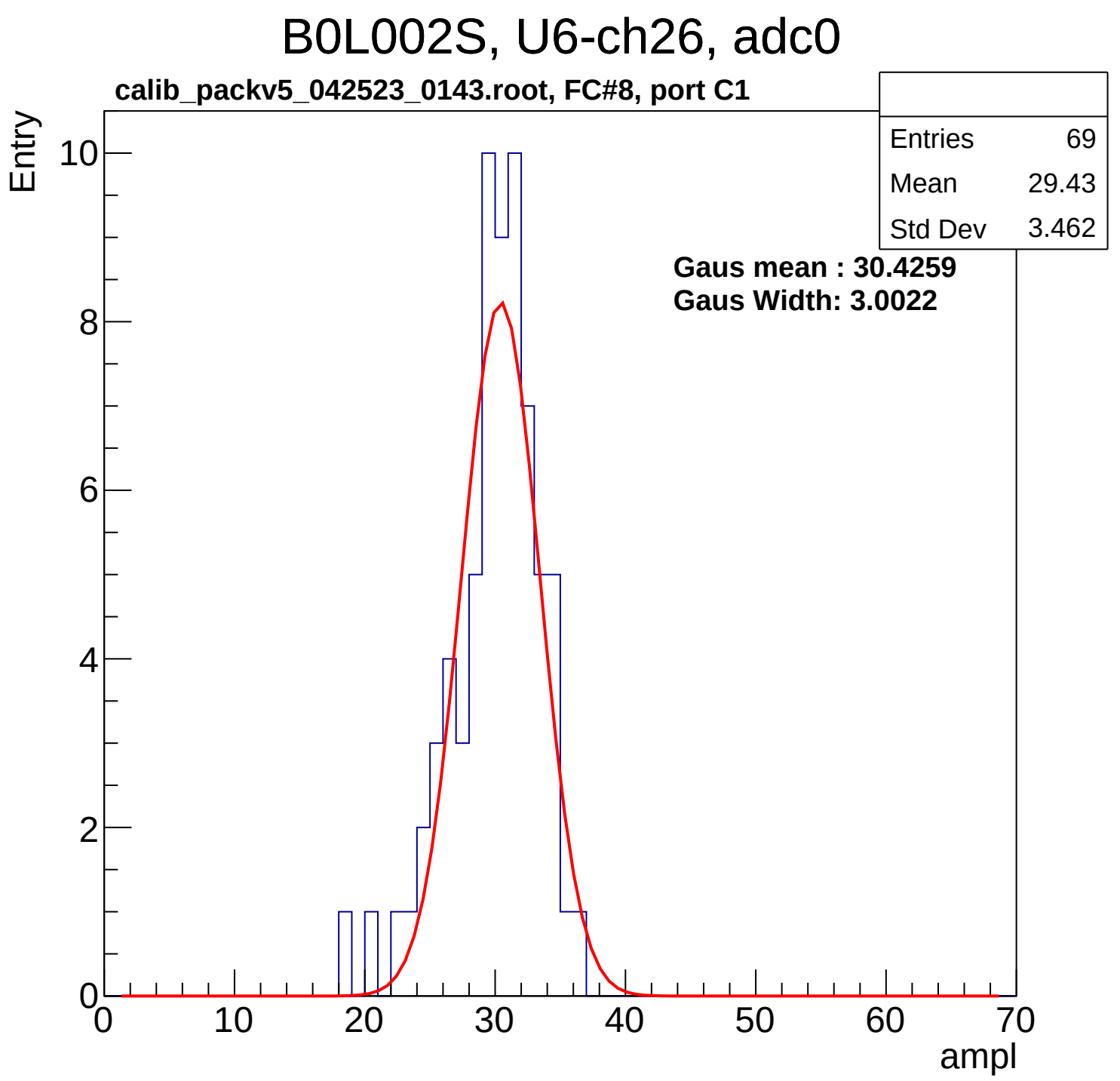
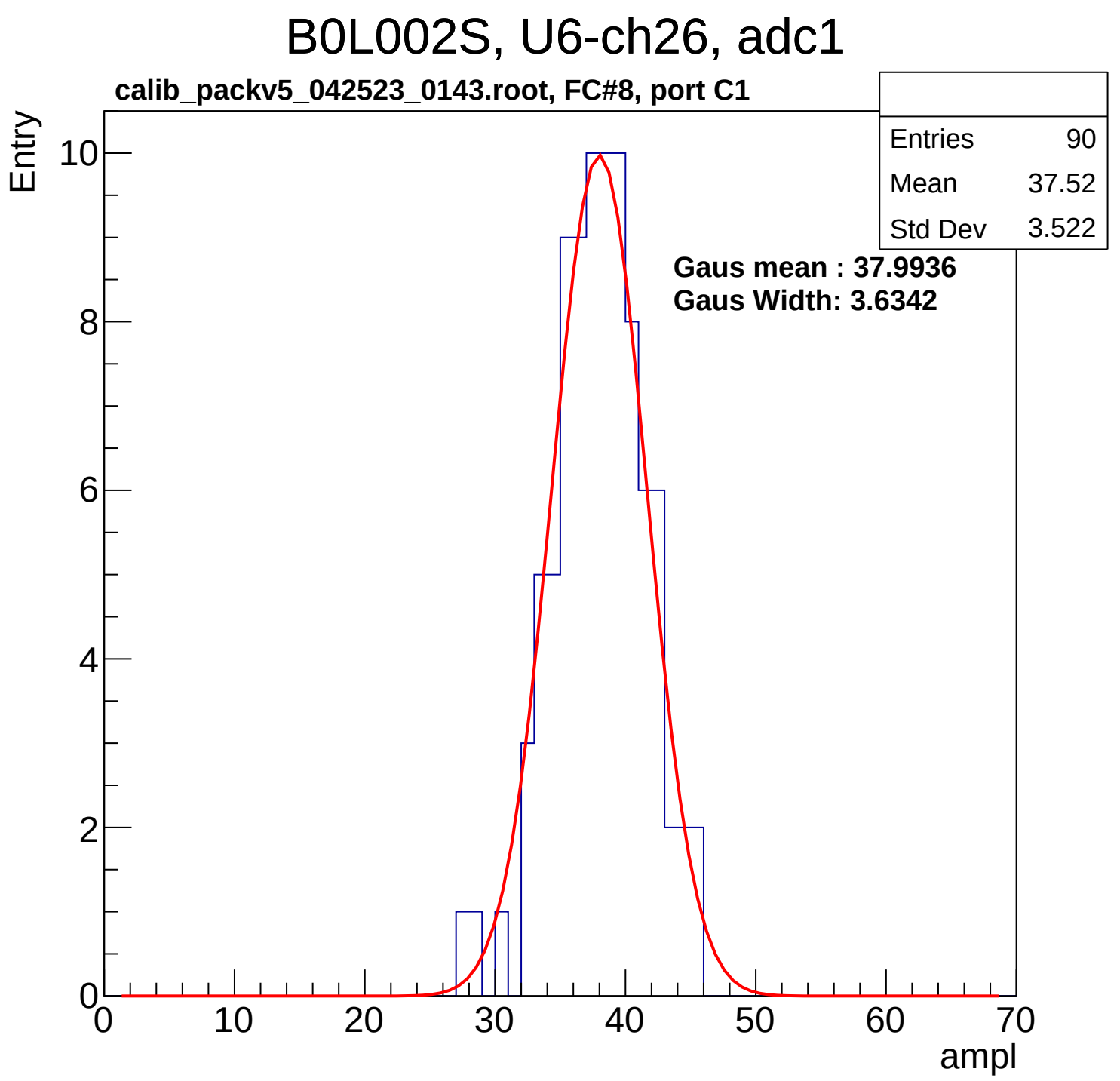
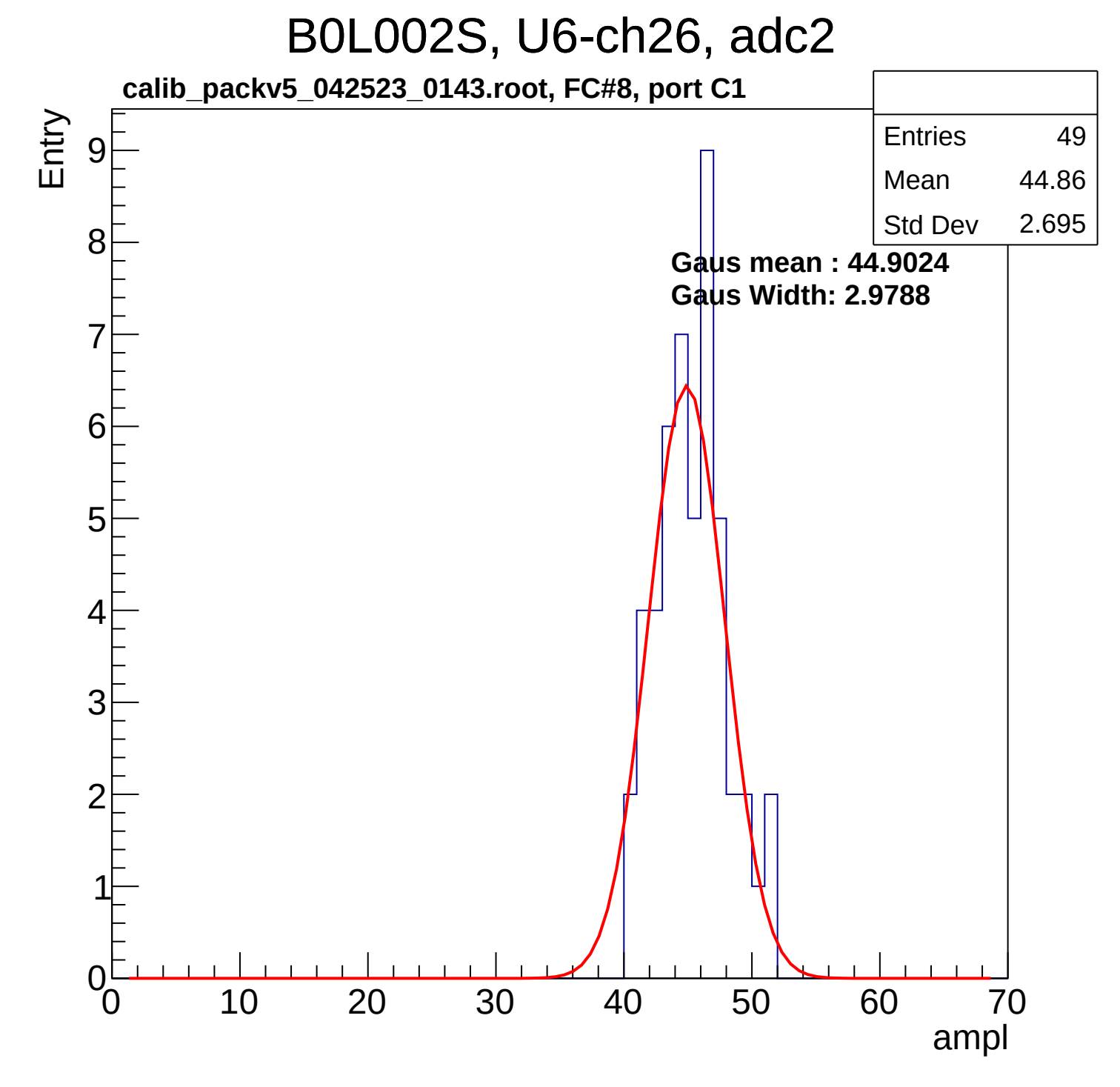
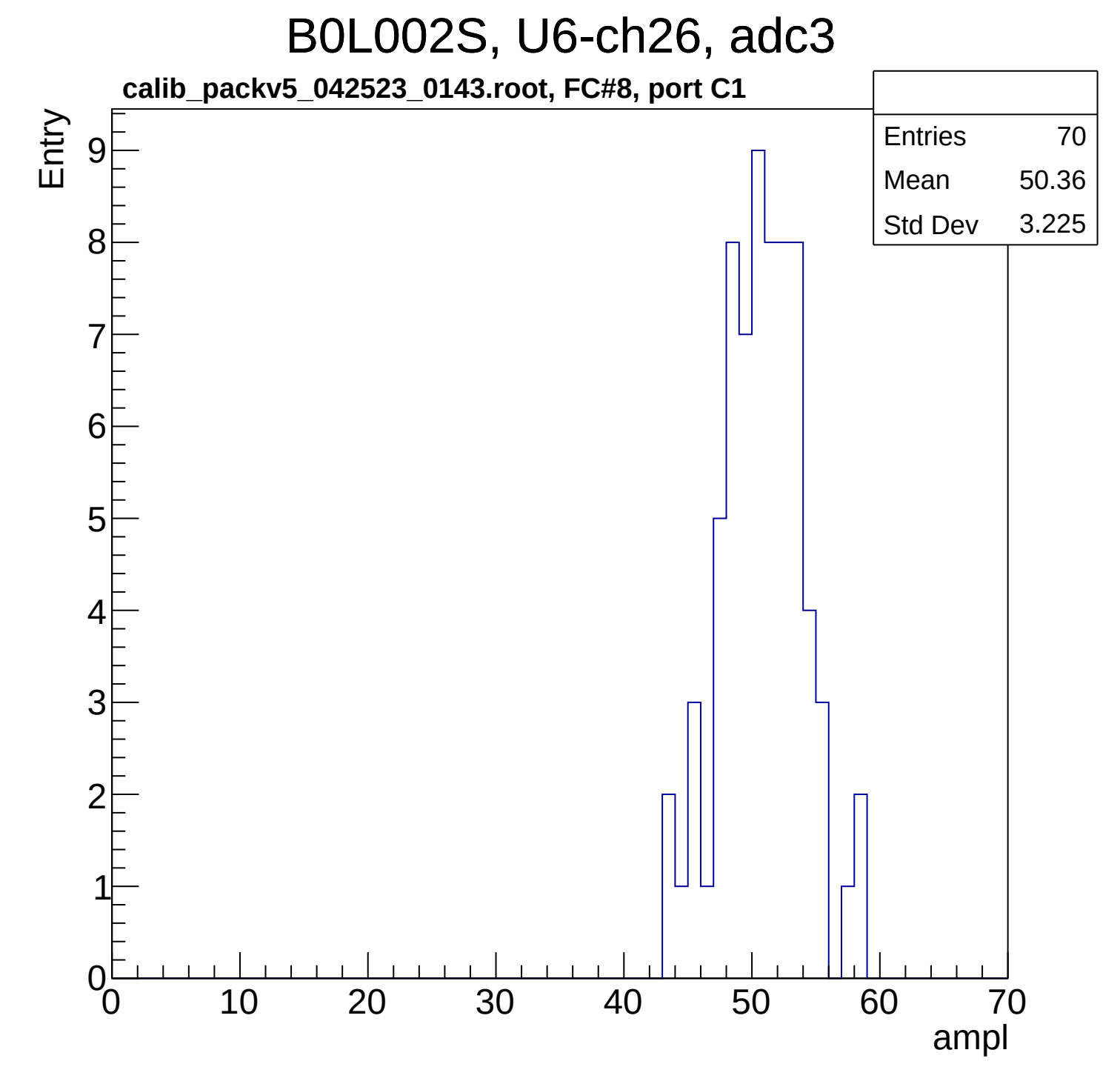
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.43
Std Dev 3.462
Gaus mean : 30.4259
Gaus Width: 3.0022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 37.52
Std Dev 3.522
Gaus mean : 37.9936
Gaus Width: 3.6342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 44.86
Std Dev 2.695
Gaus mean : 44.9024
Gaus Width: 2.9788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 50.36
Std Dev 3.225
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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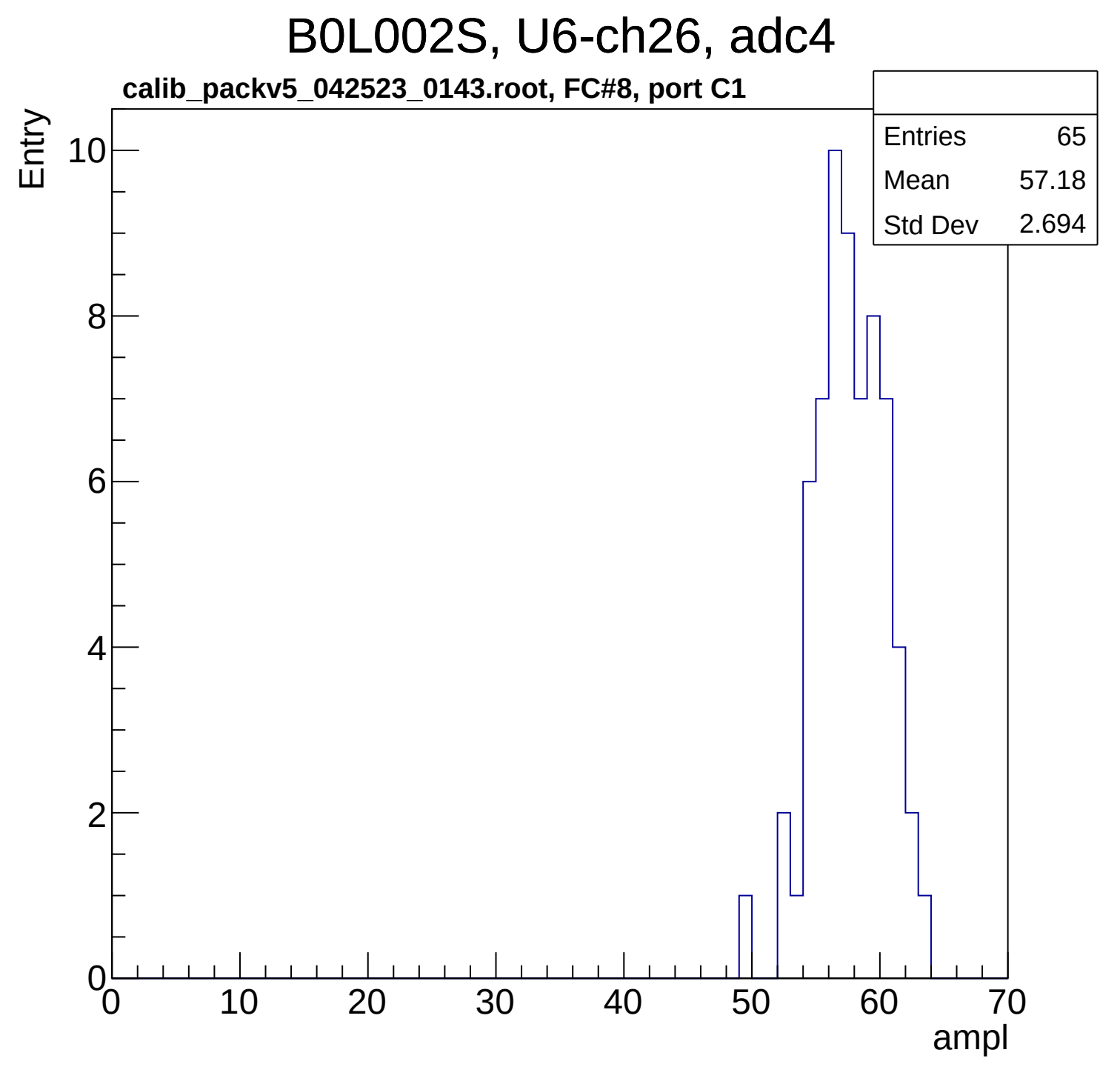
B0L002S, U6-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.18
Std Dev 2.694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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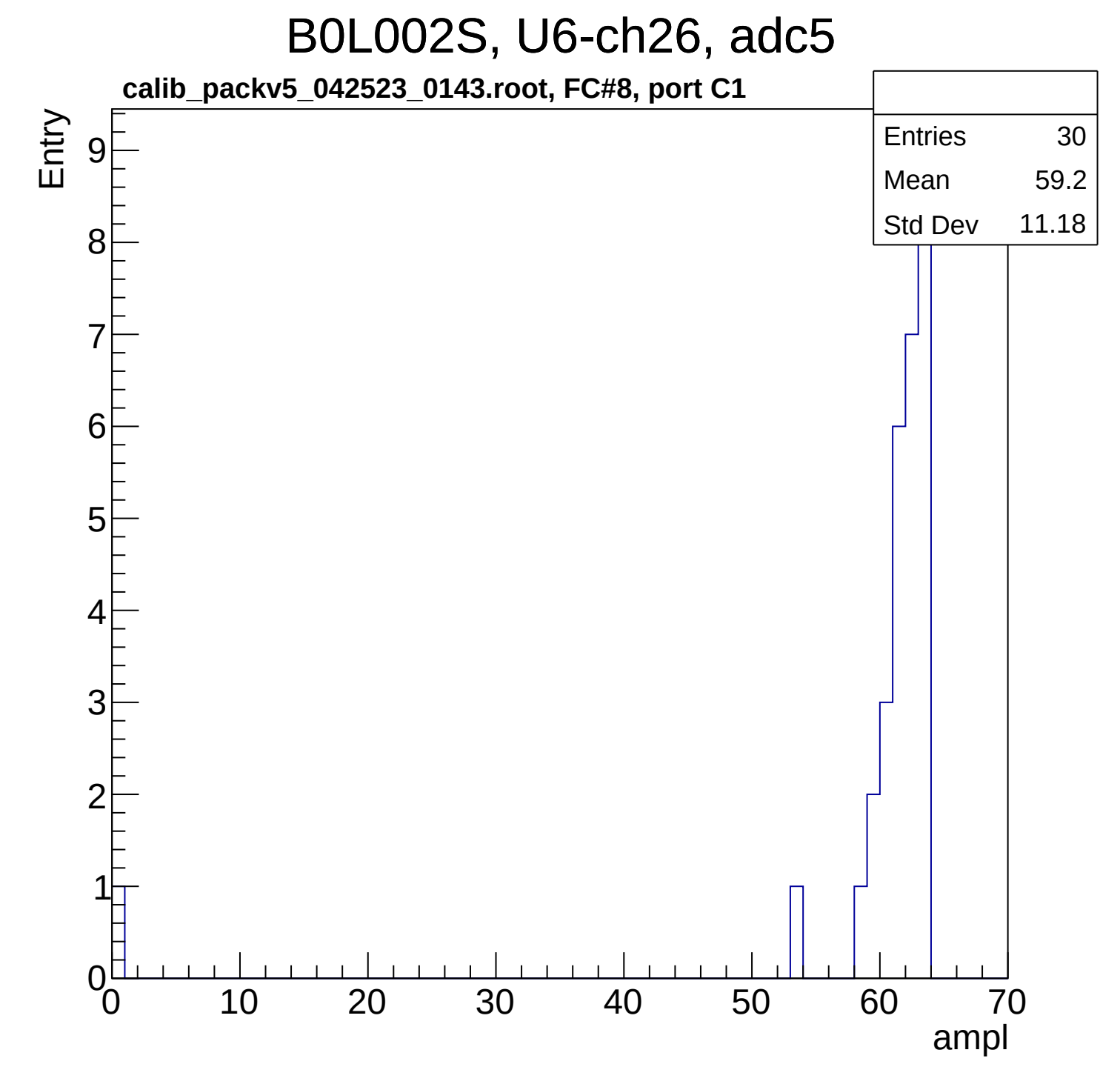
B0L002S, U6-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.2
Std Dev 11.18
Entry
ampl
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2
3
4
5
6
7
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0
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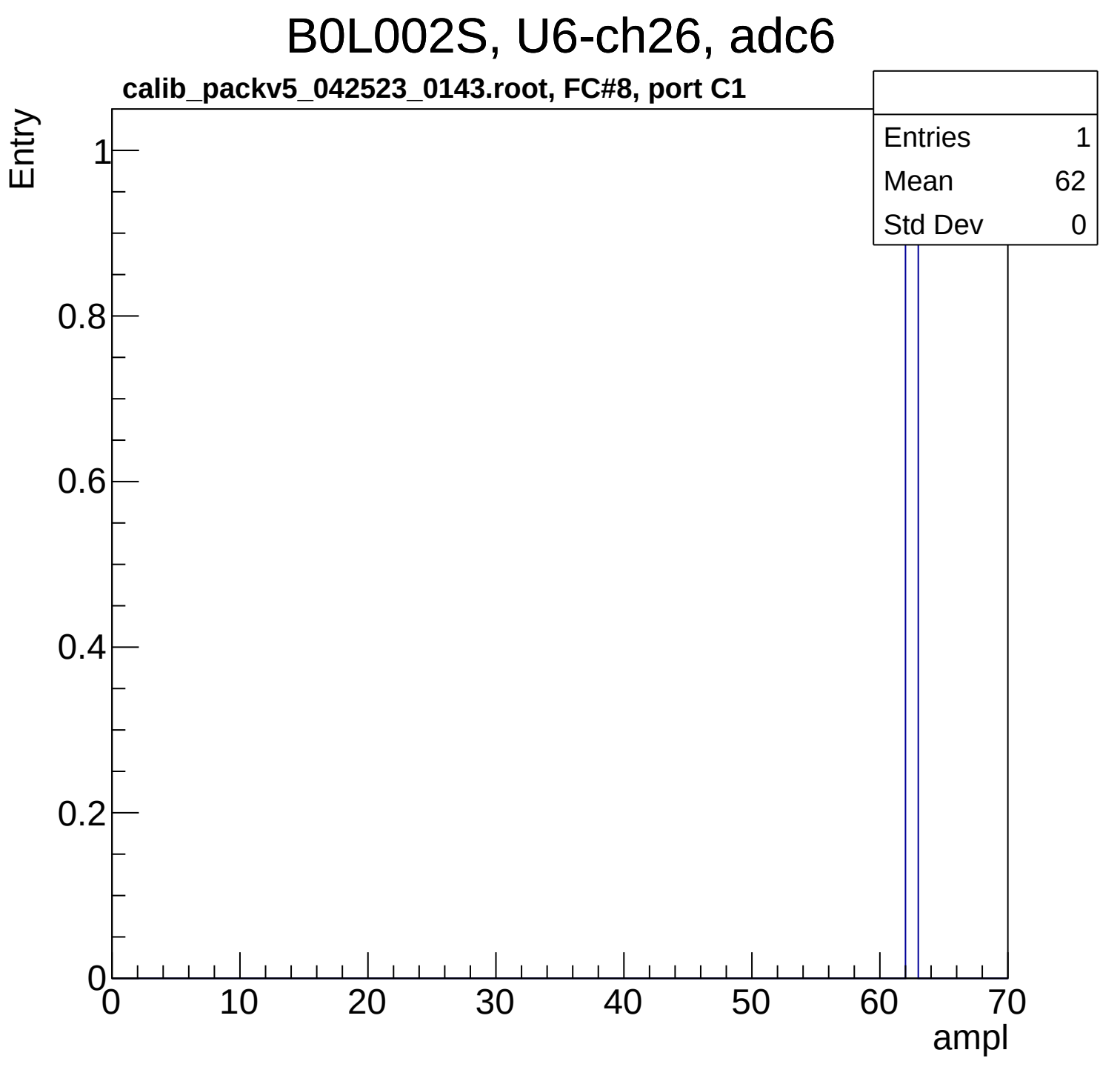
B0L002S, U6-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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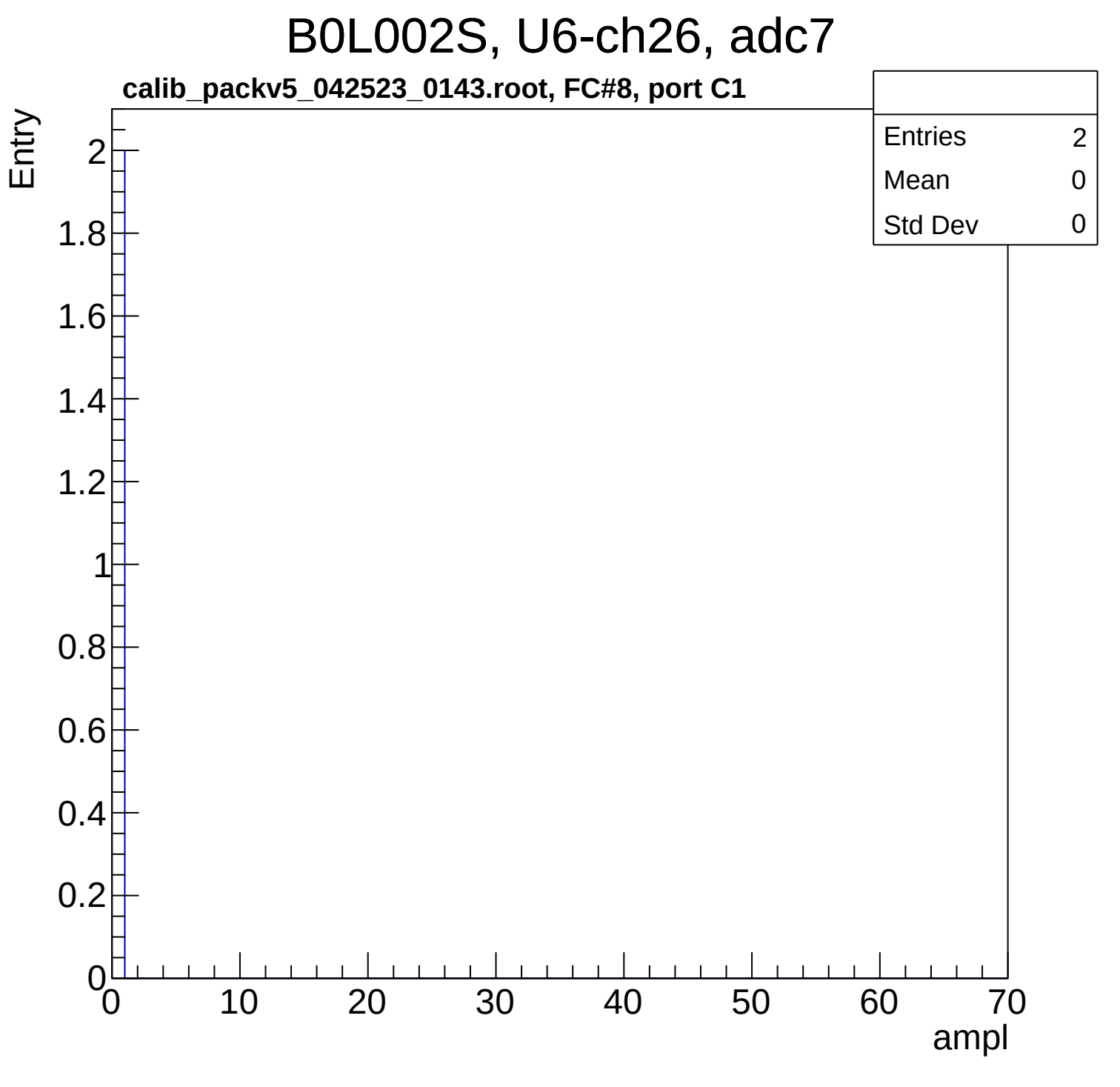
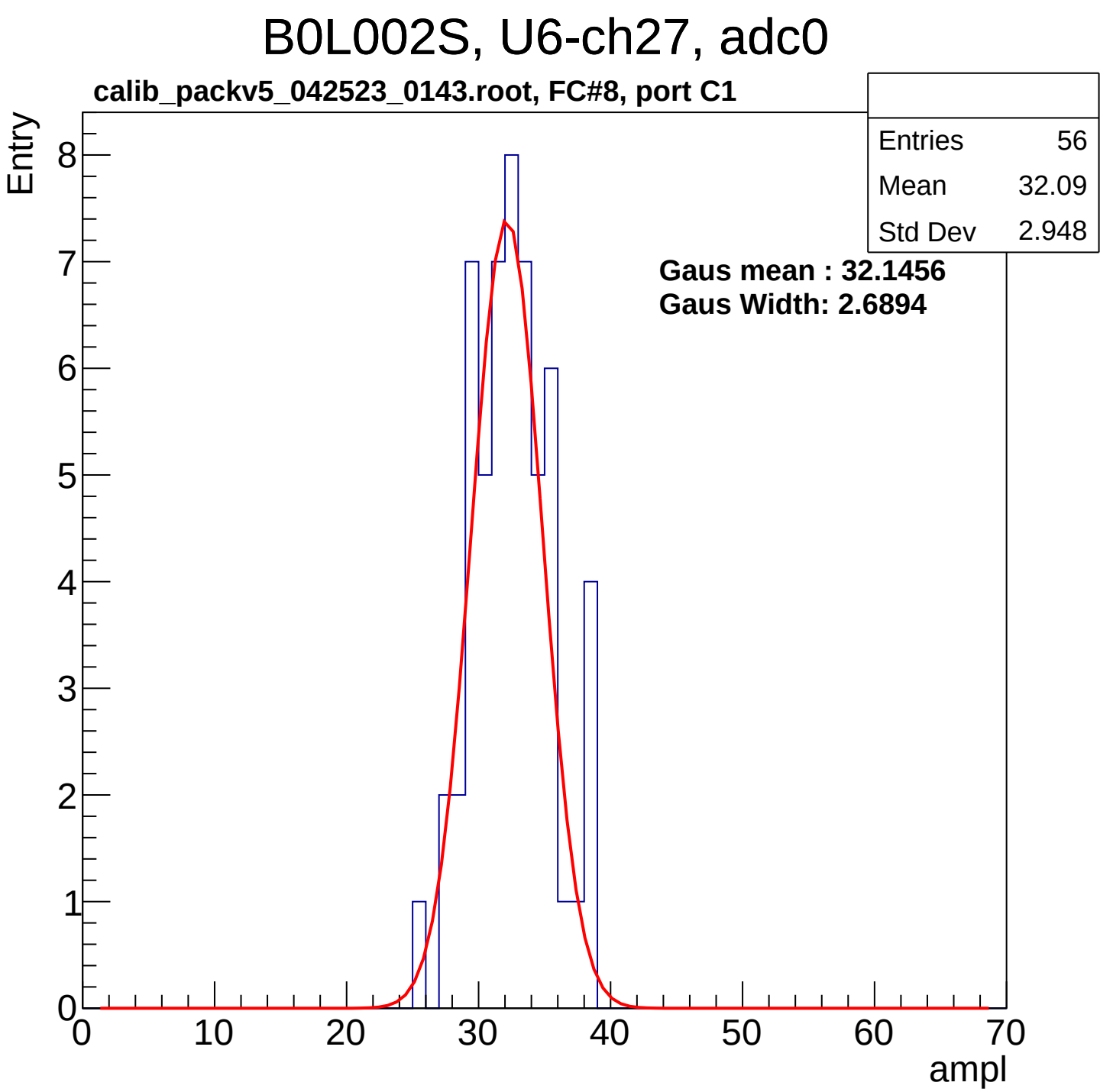
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 32.09
Std Dev 2.948
Gaus mean : 32.1456
Gaus Width: 2.6894
Entry
ampl
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1
2
3
4
5
6
7
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0
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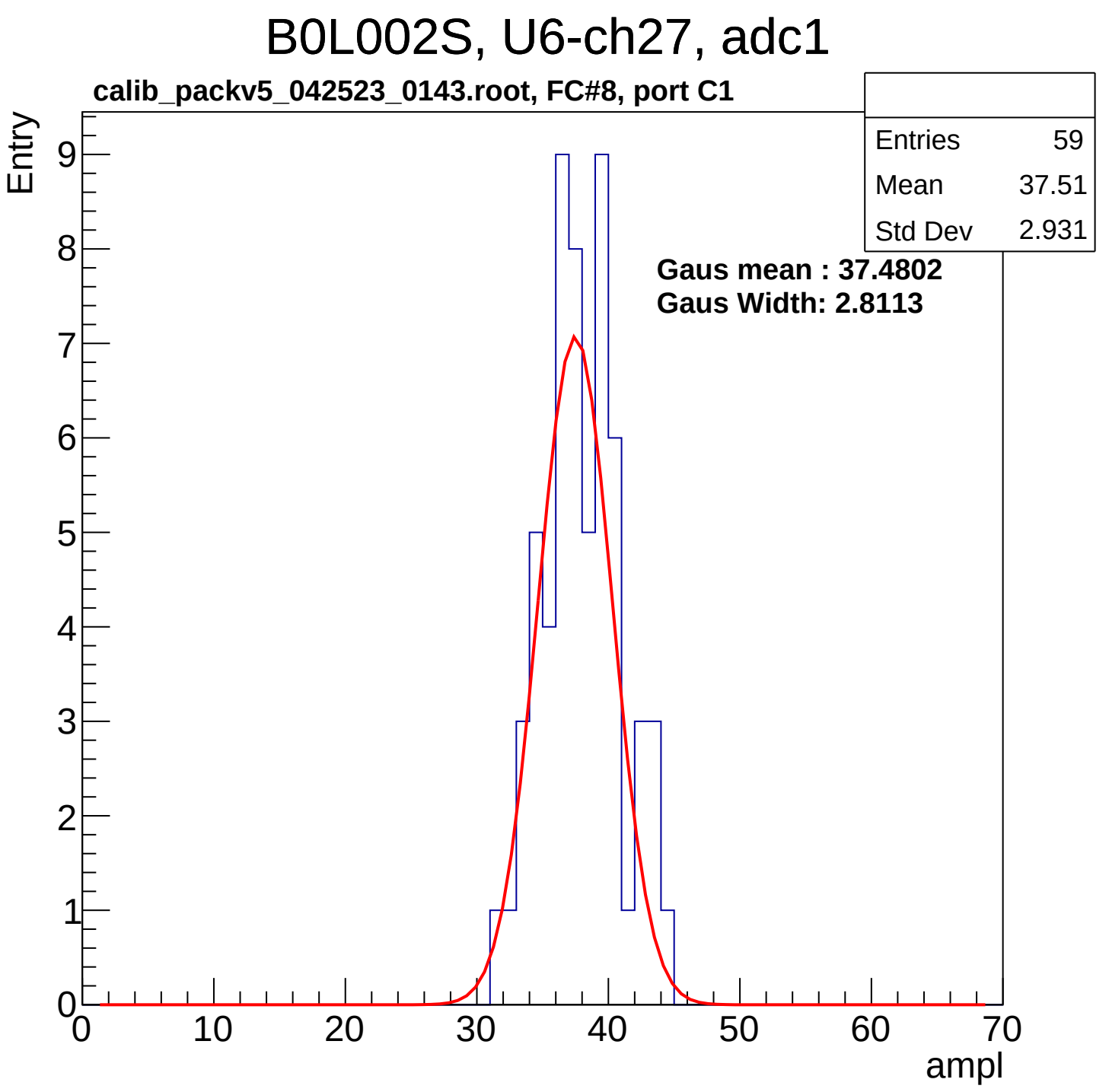
B0L002S, U6-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.51
Std Dev 2.931
Gaus mean : 37.4802
Gaus Width: 2.8113
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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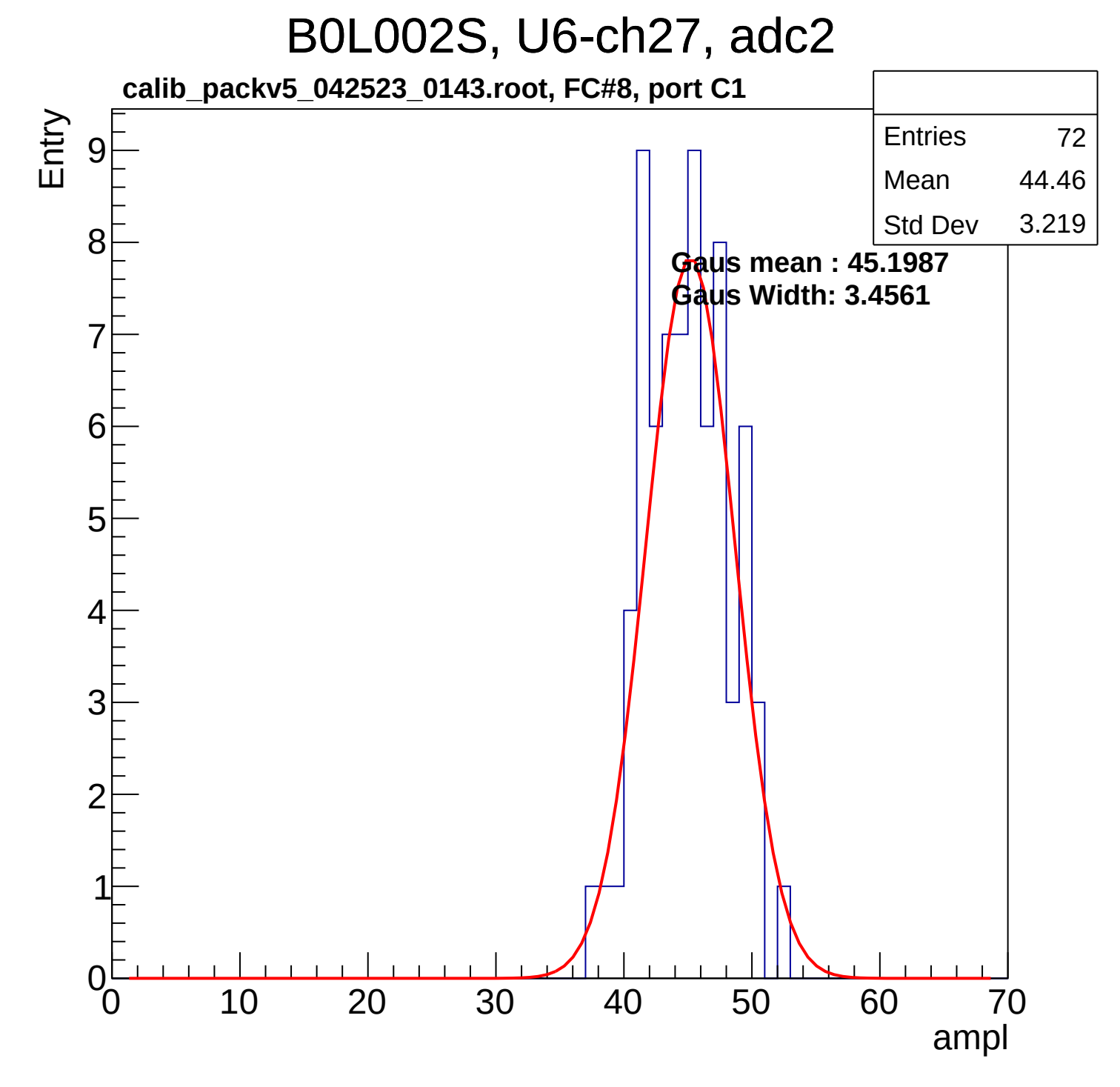
B0L002S, U6-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.46
Std Dev 3.219
Gaus mean : 45.1987
Gaus Width: 3.4561
Entry
ampl
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3
4
5
6
7
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0
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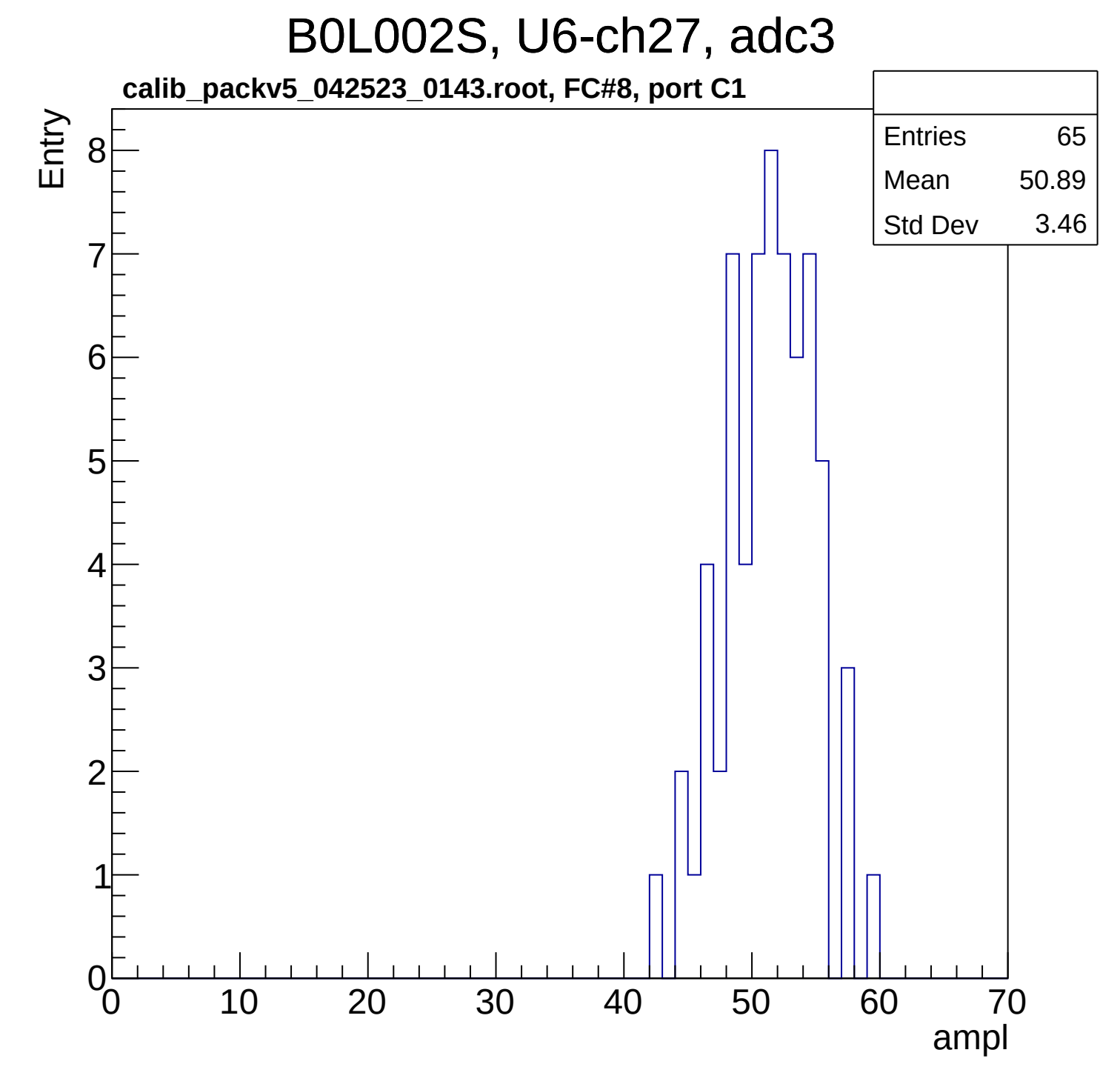
B0L002S, U6-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.89
Std Dev 3.46
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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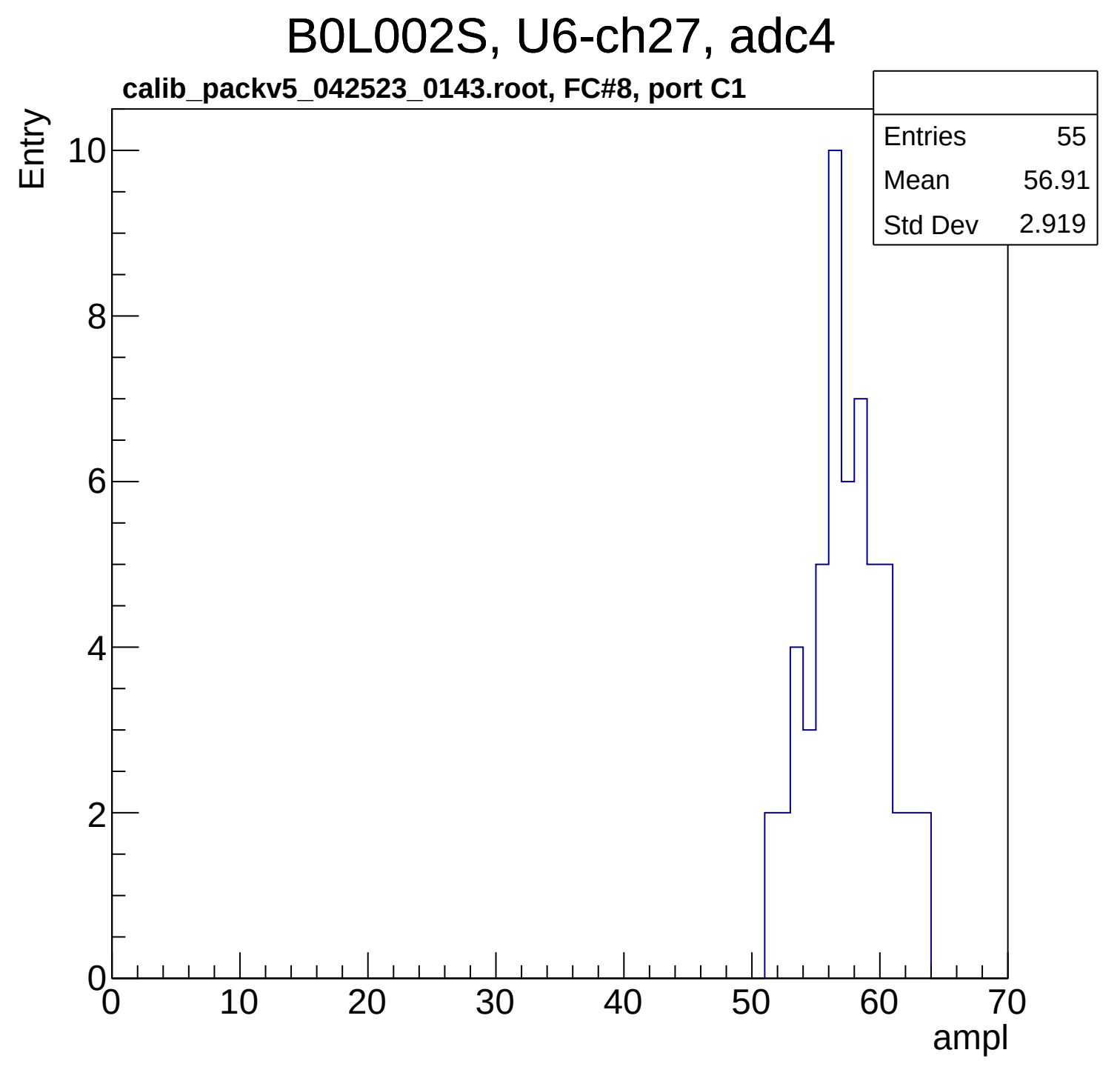
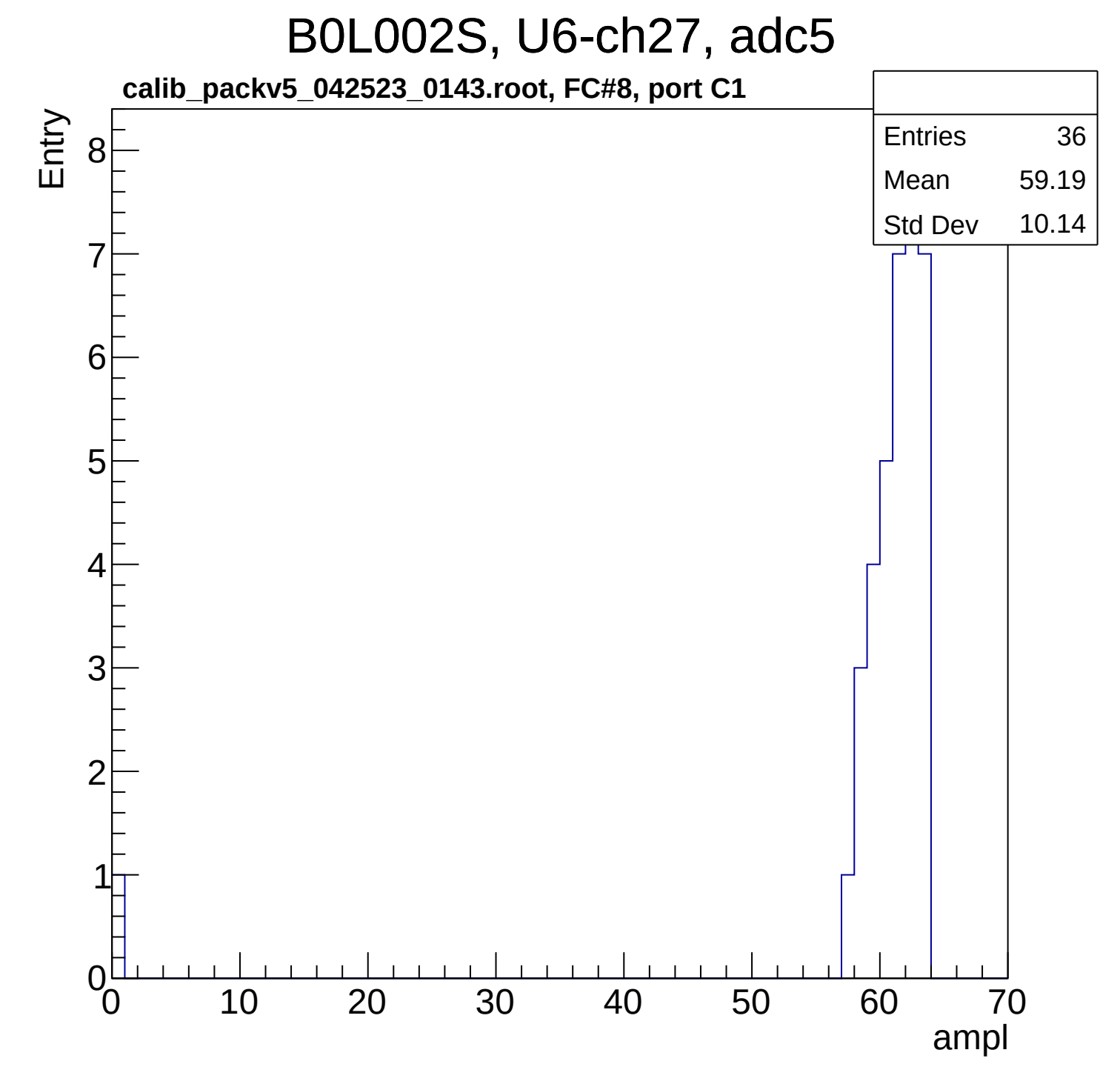

B0L002S, U6-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.91
Std Dev 2.919
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.19
Std Dev 10.14
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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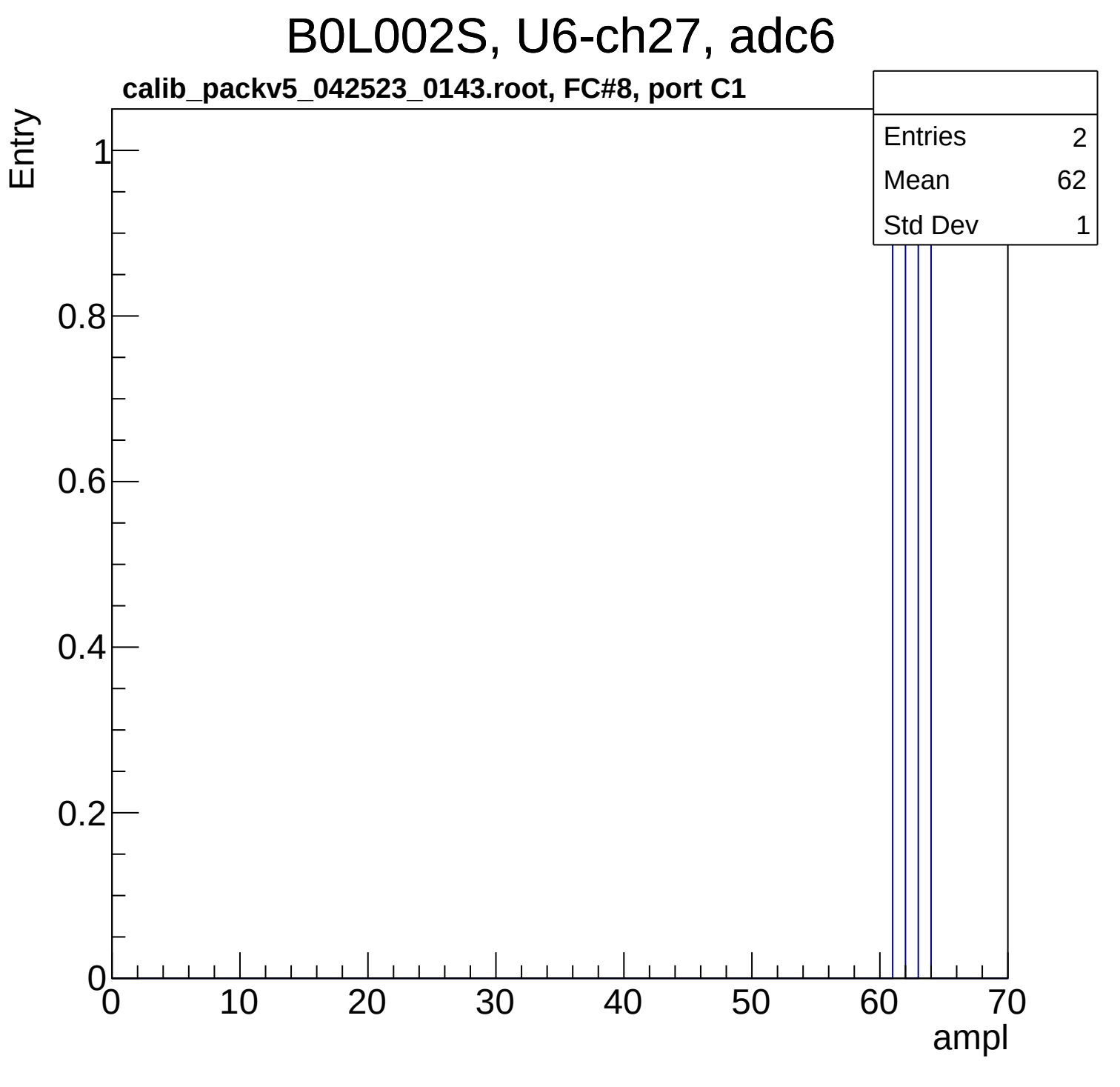
B0L002S, U6-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
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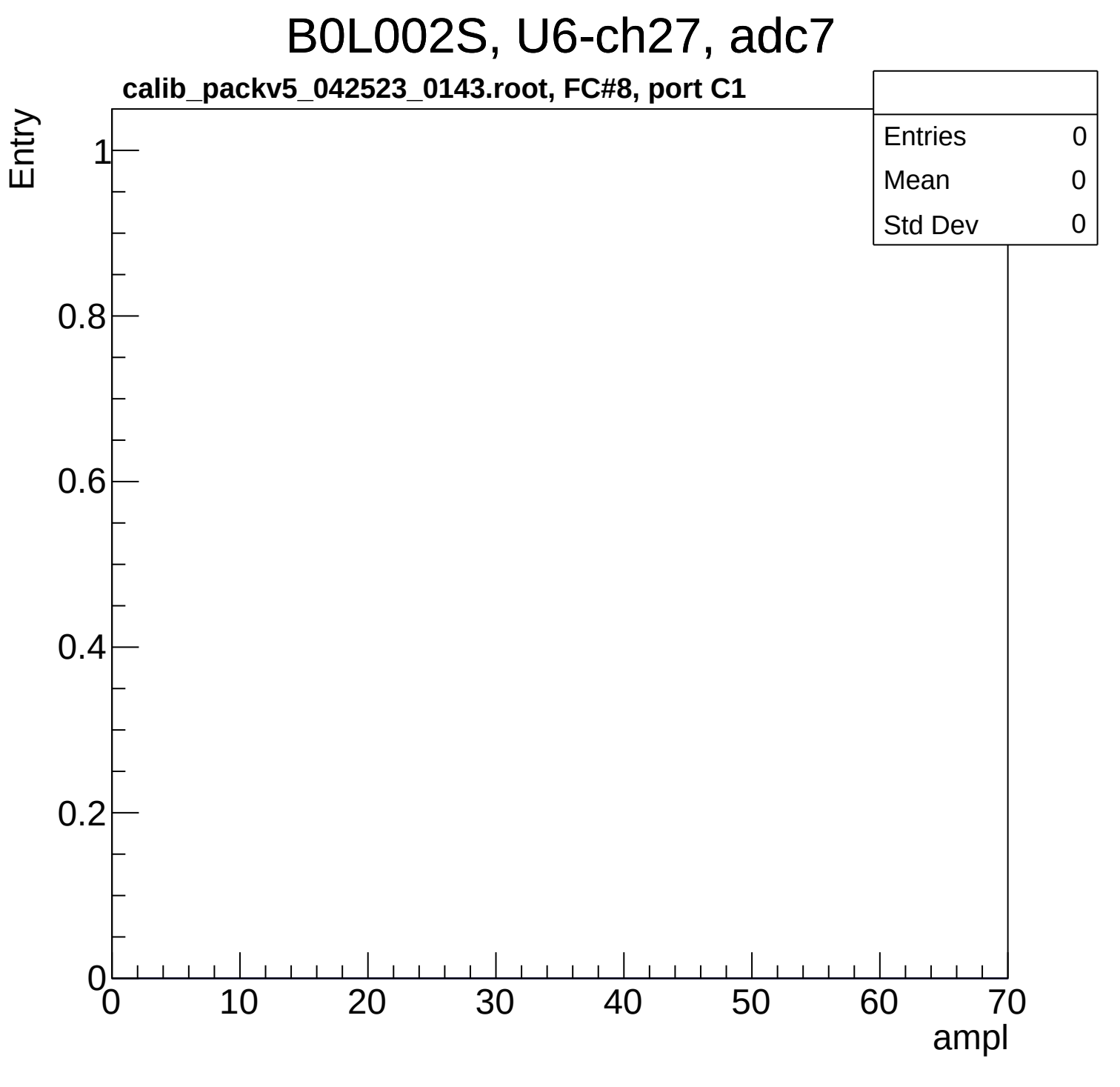
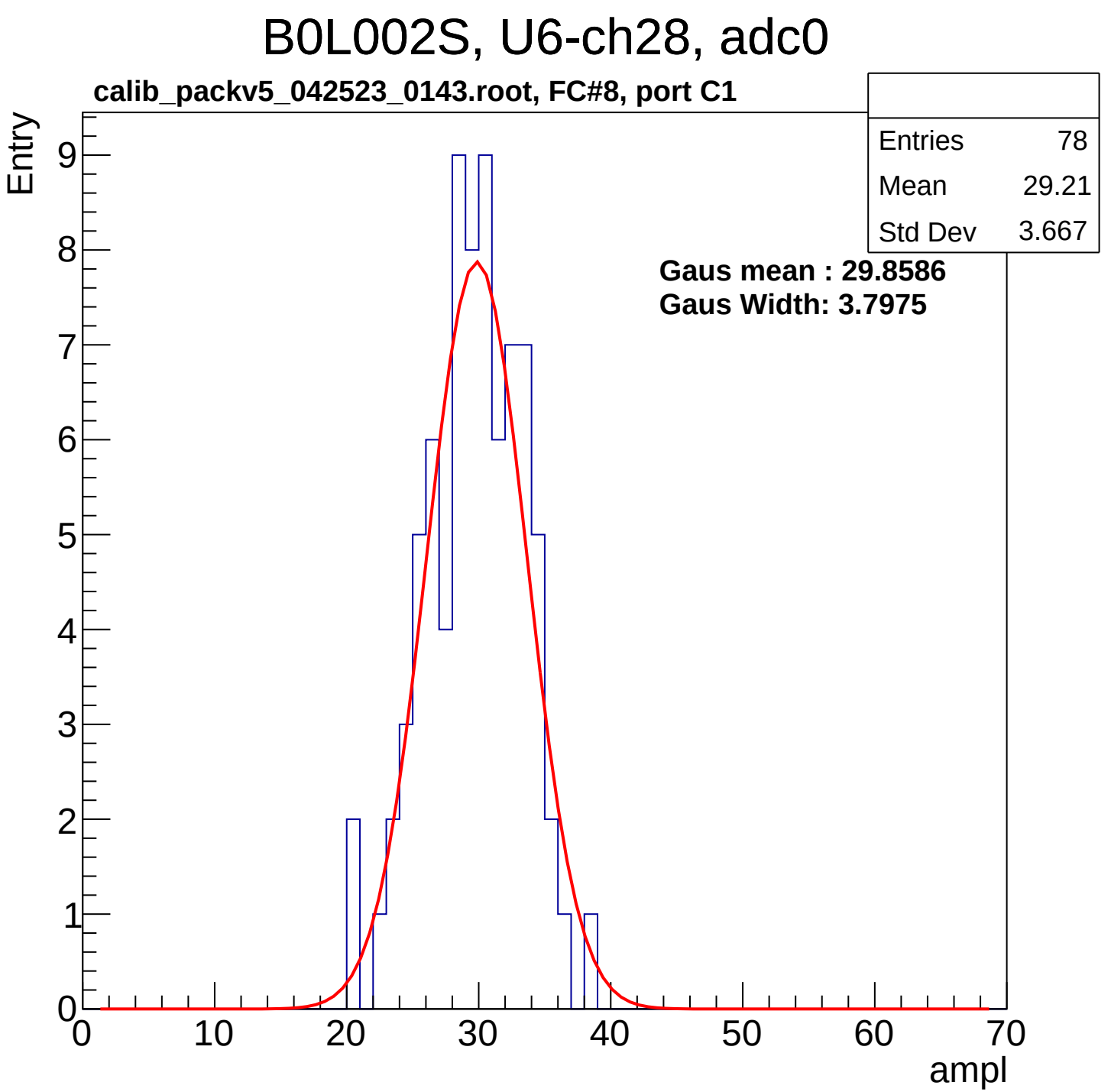
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.21
Std Dev 3.667
Gaus mean : 29.8586
Gaus Width: 3.7975
Entry
ampl
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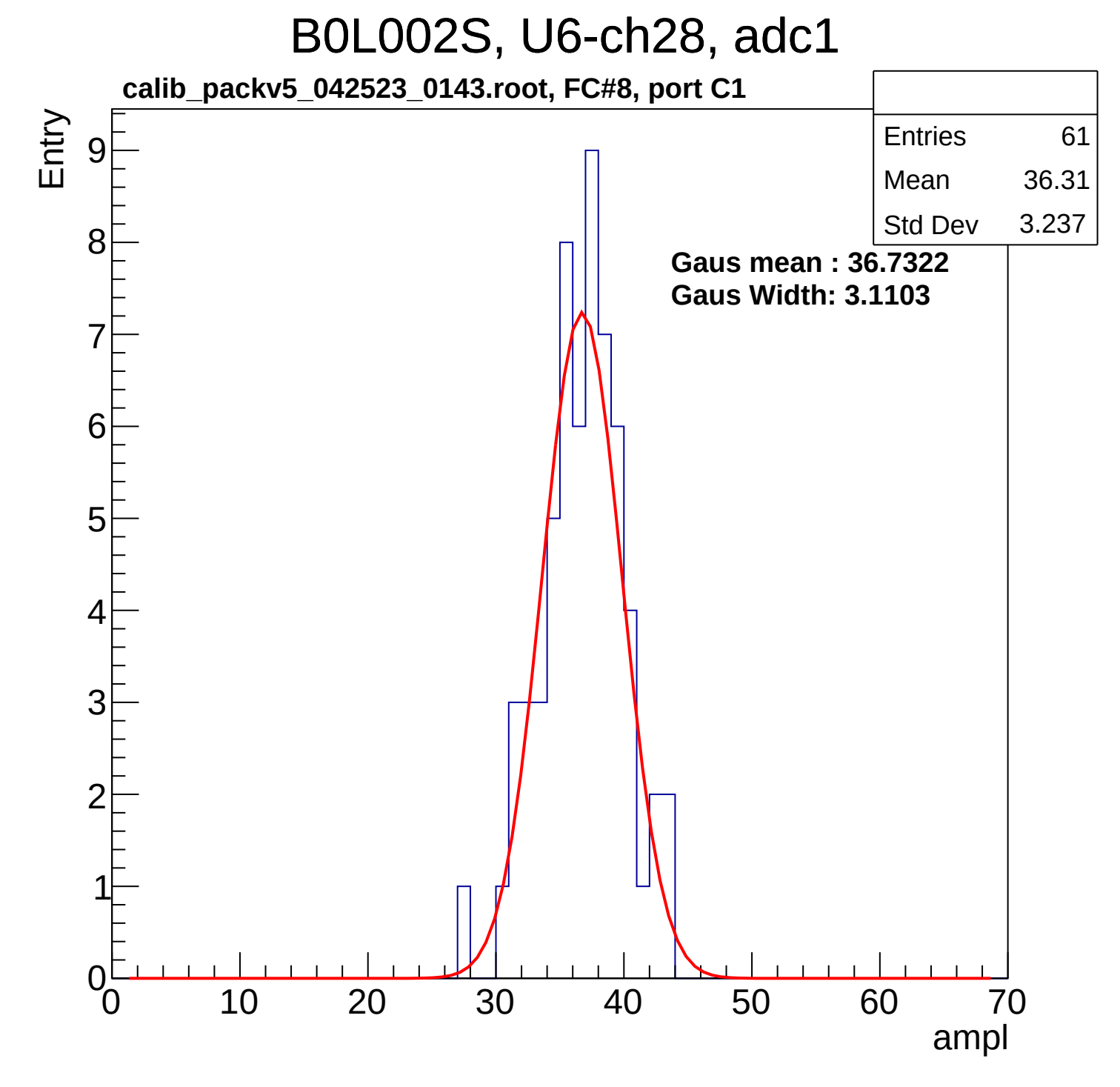
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.31
Std Dev 3.237
Gaus mean : 36.7322
Gaus Width: 3.1103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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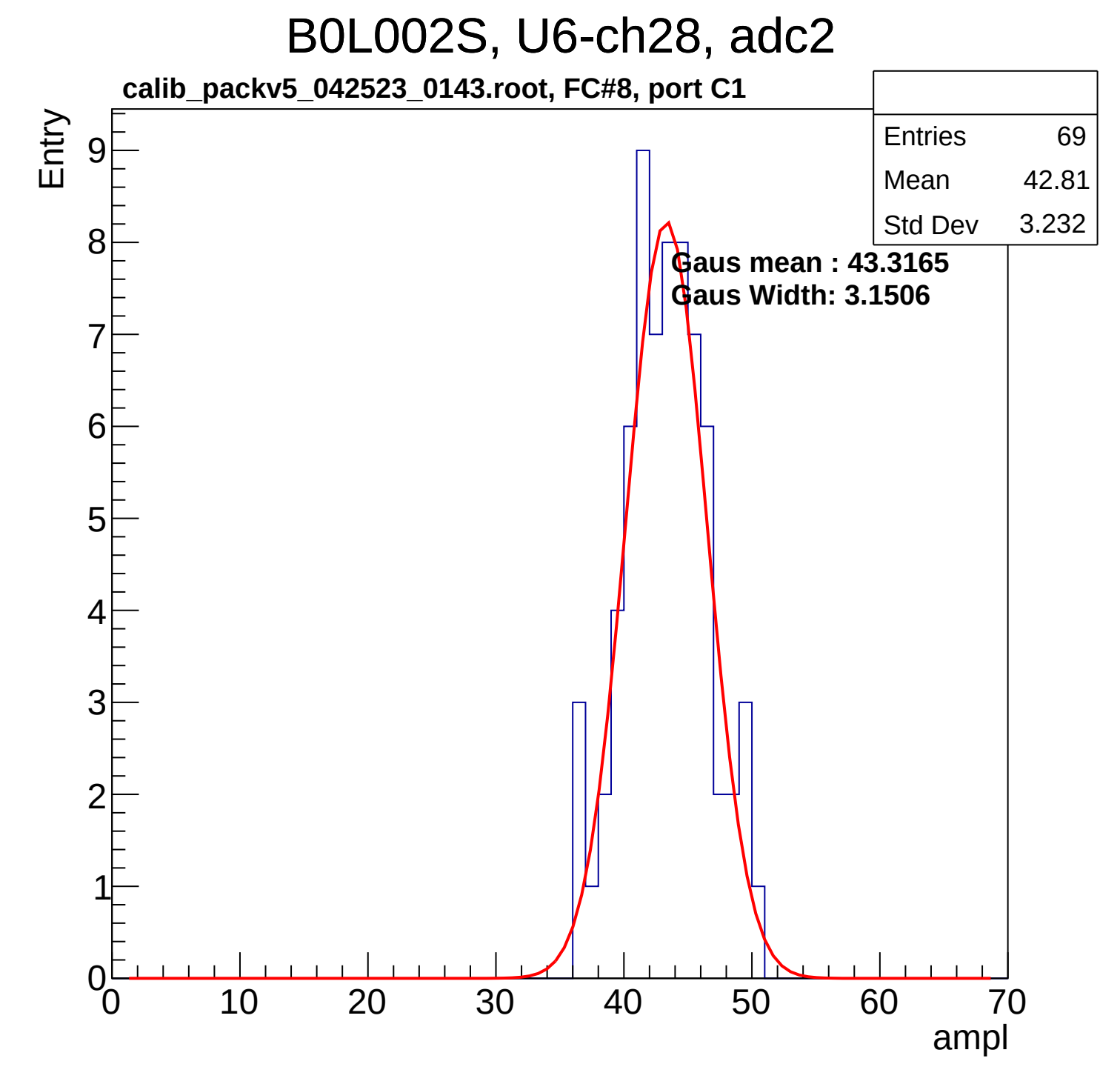
B0L002S, U6-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.81
Std Dev 3.232
Gaus mean : 43.3165
Gaus Width: 3.1506
Entry
ampl
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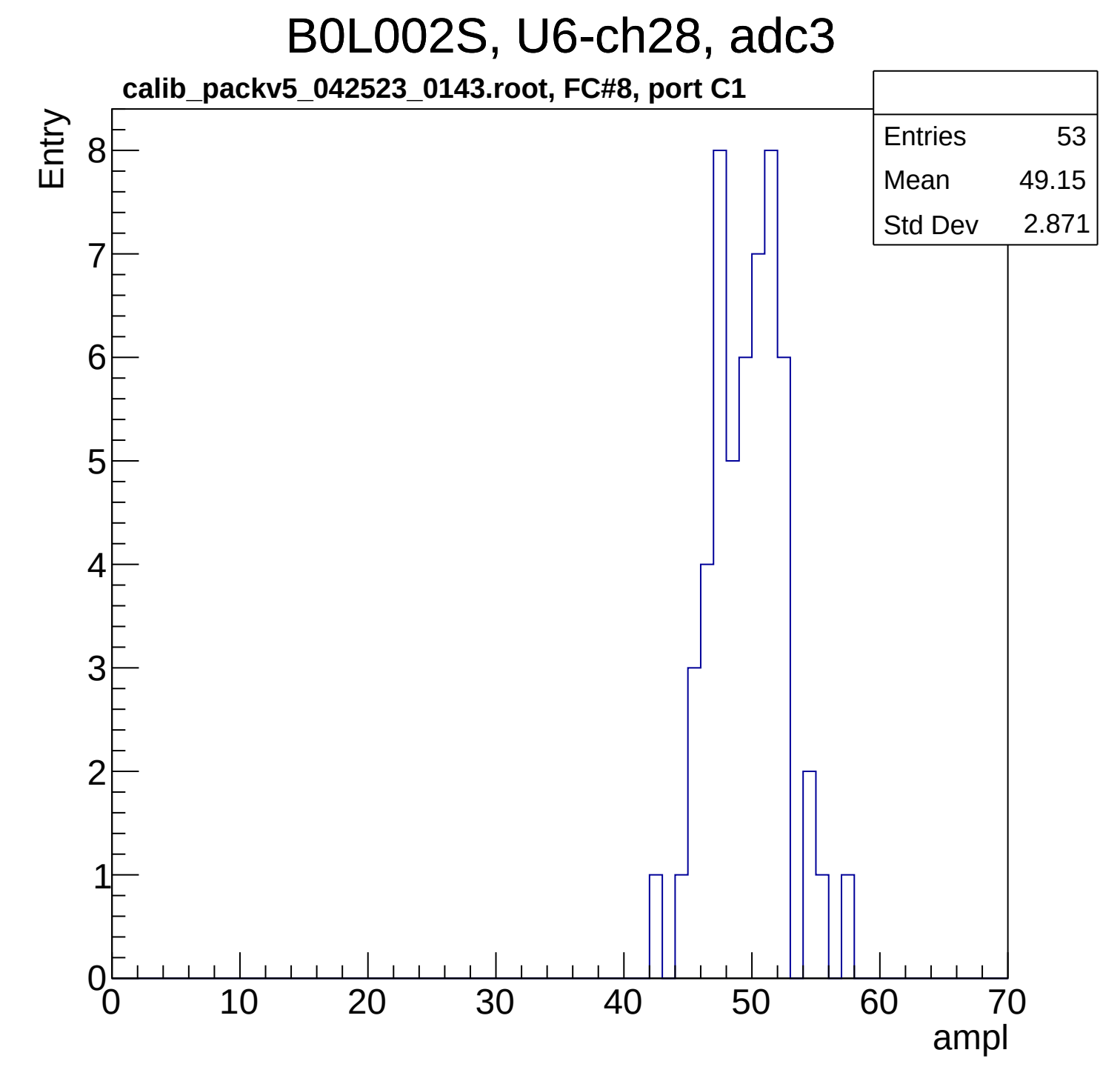
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U6-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 49.15
Std Dev 2.871
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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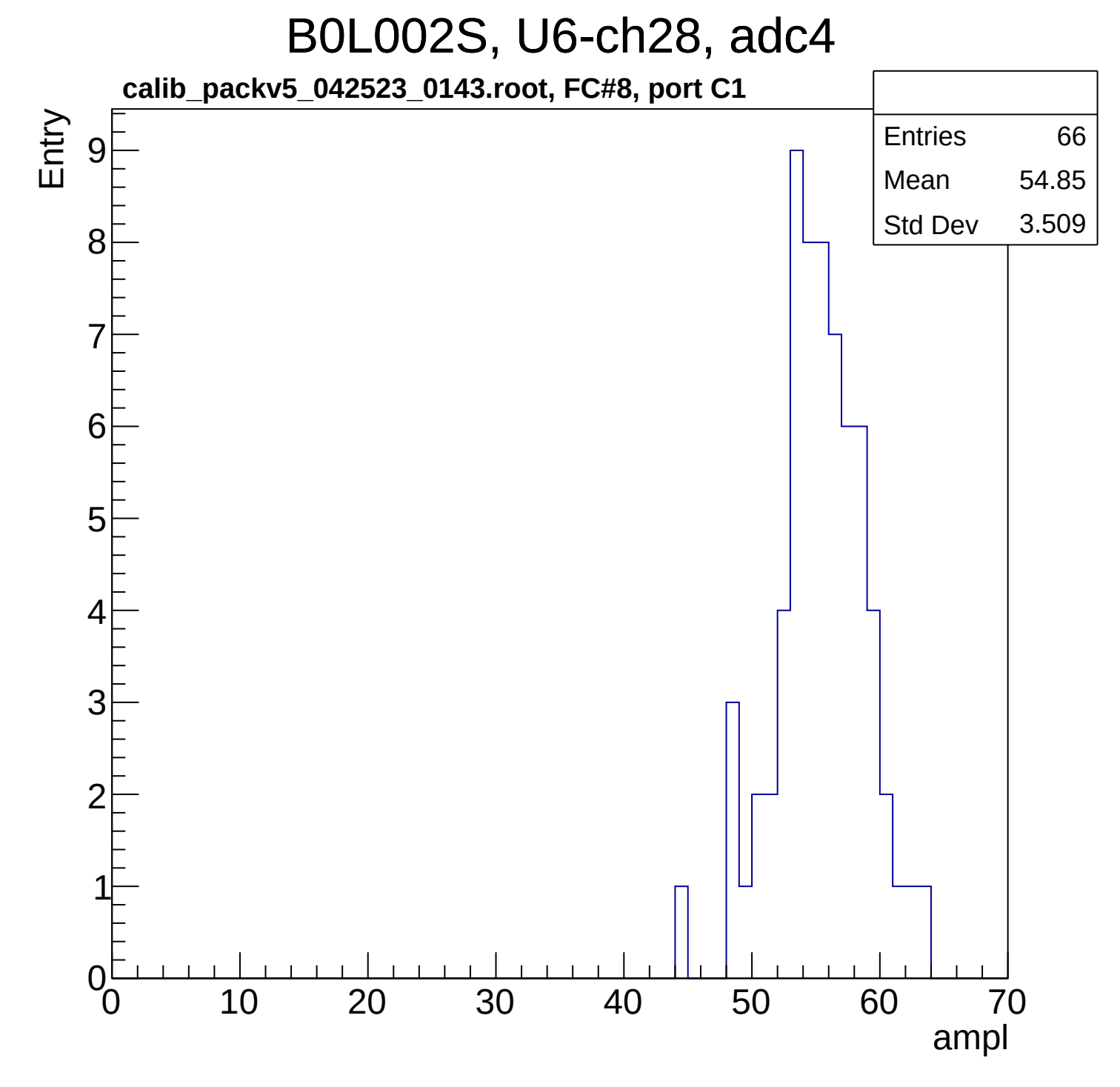
B0L002S, U6-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 54.85
Std Dev 3.509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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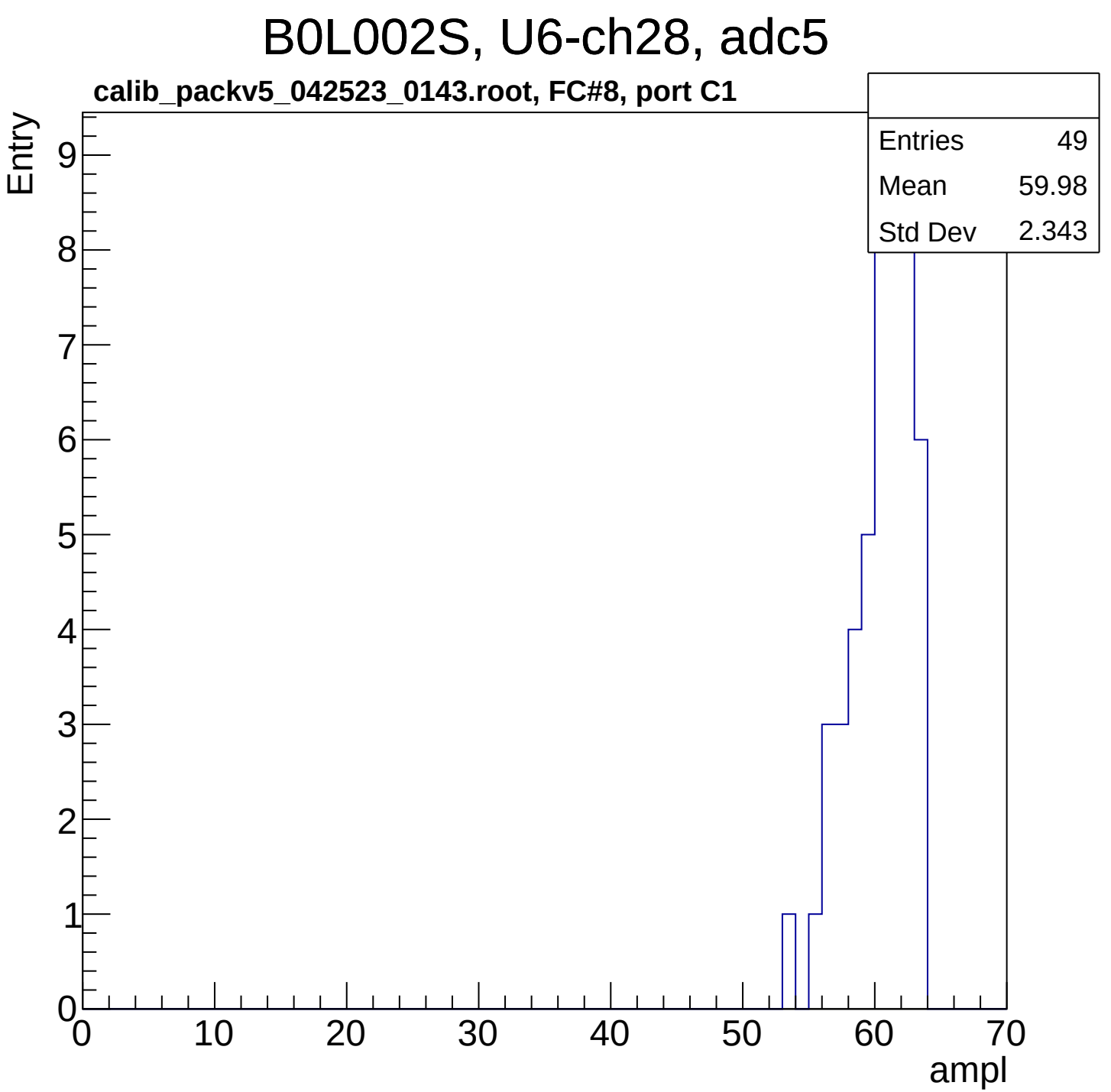
B0L002S, U6-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.98
Std Dev 2.343
Entry
ampl
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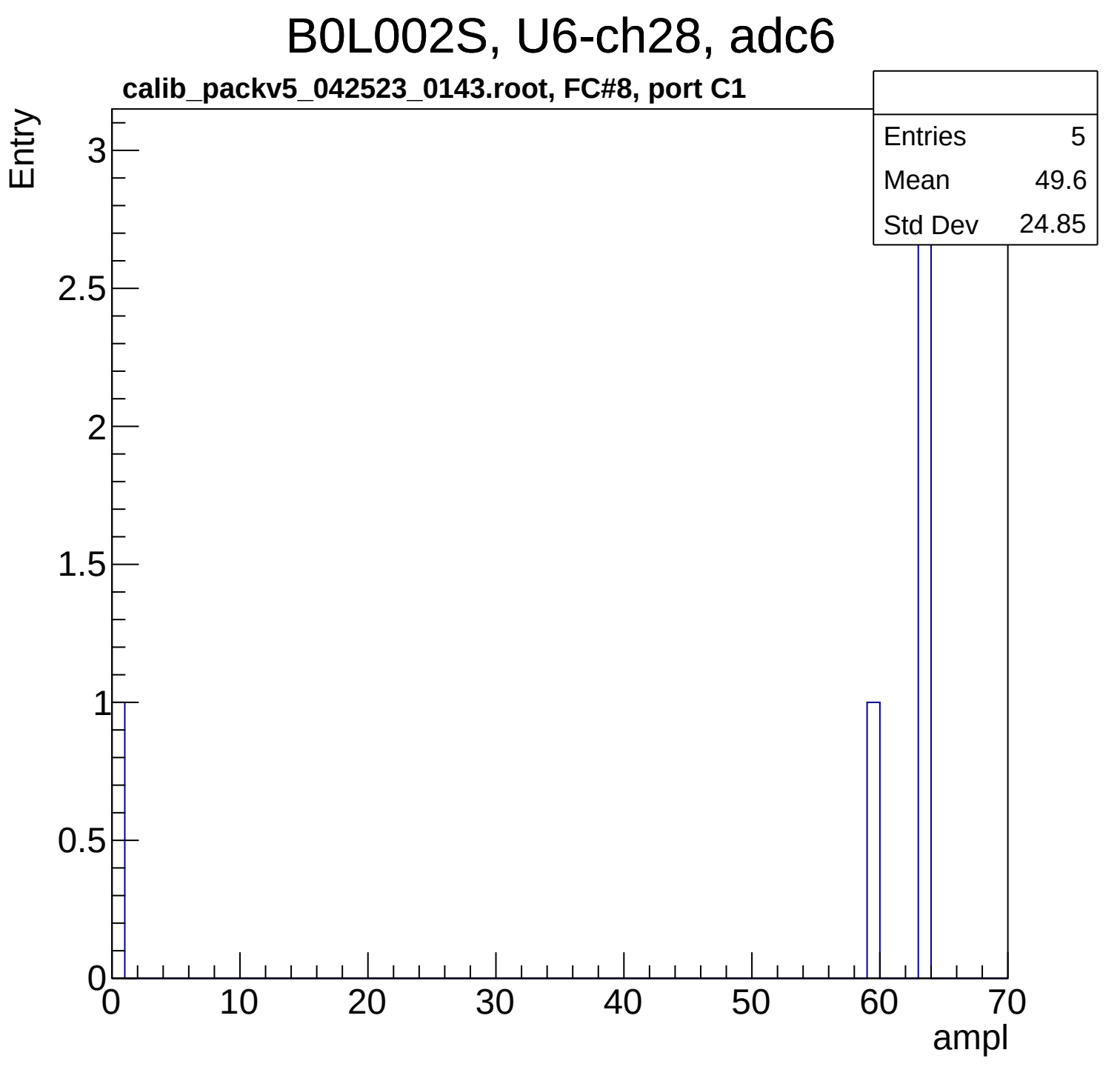
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49.6
Std Dev 24.85
Entry
ampl
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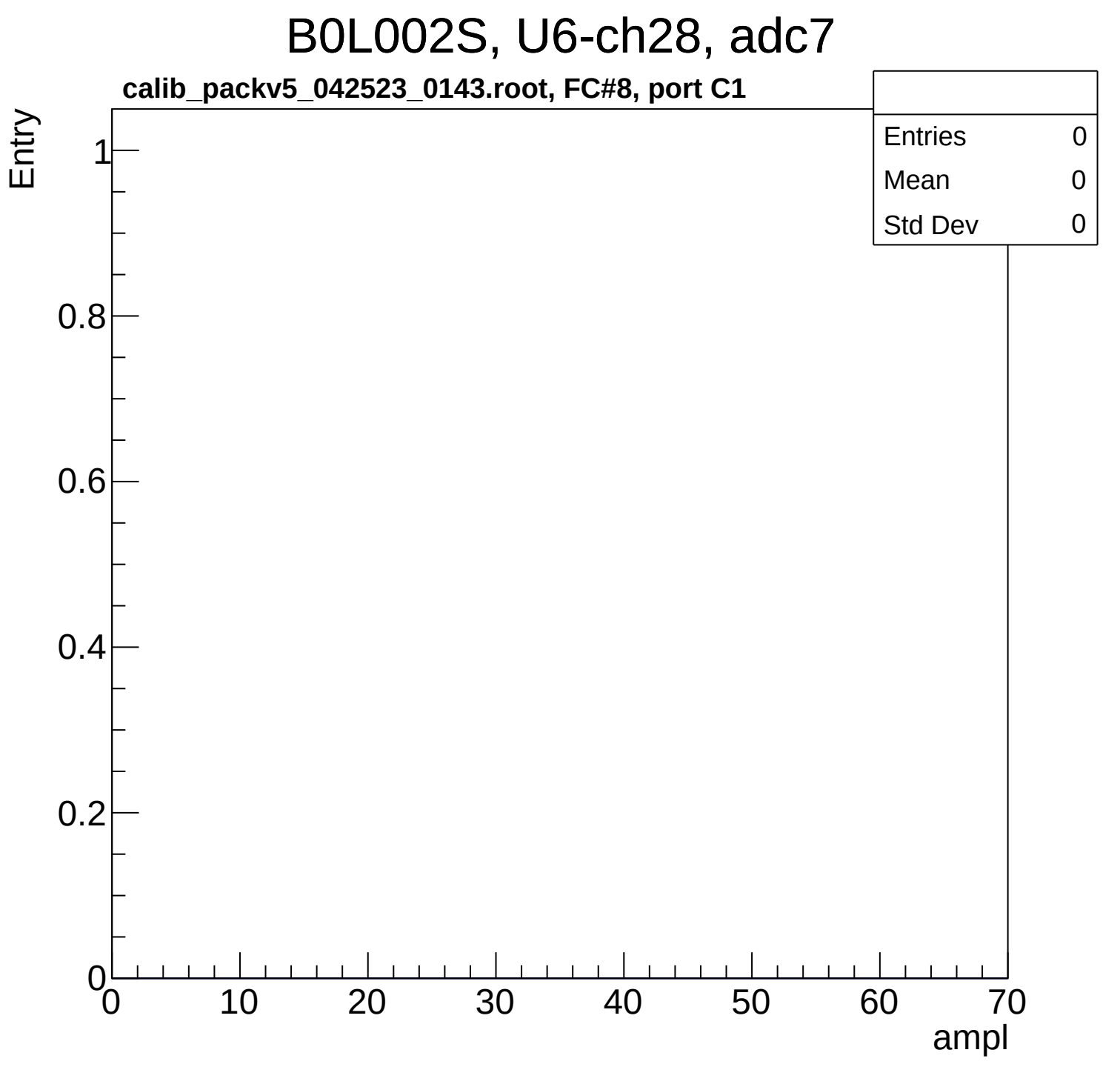
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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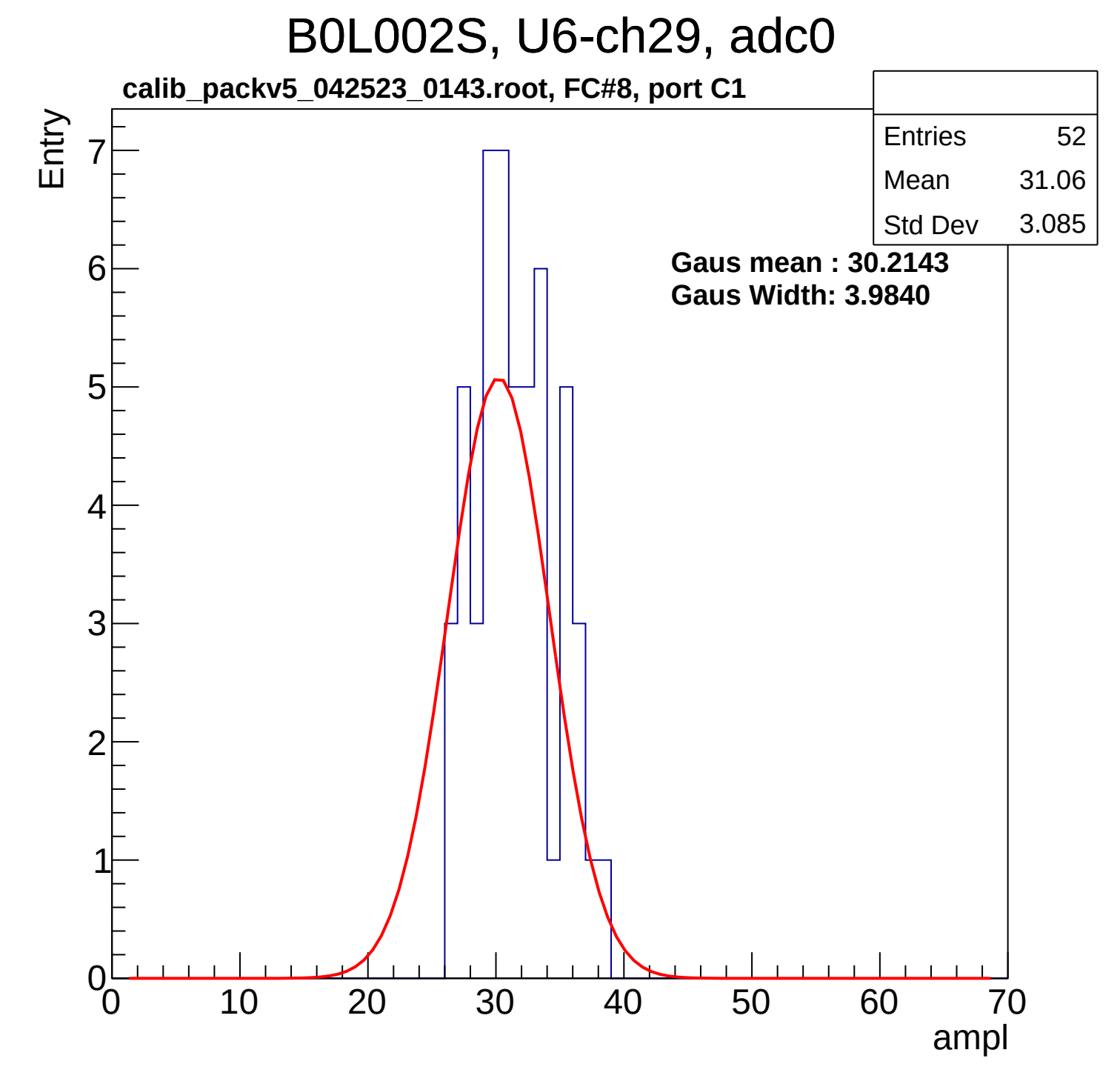
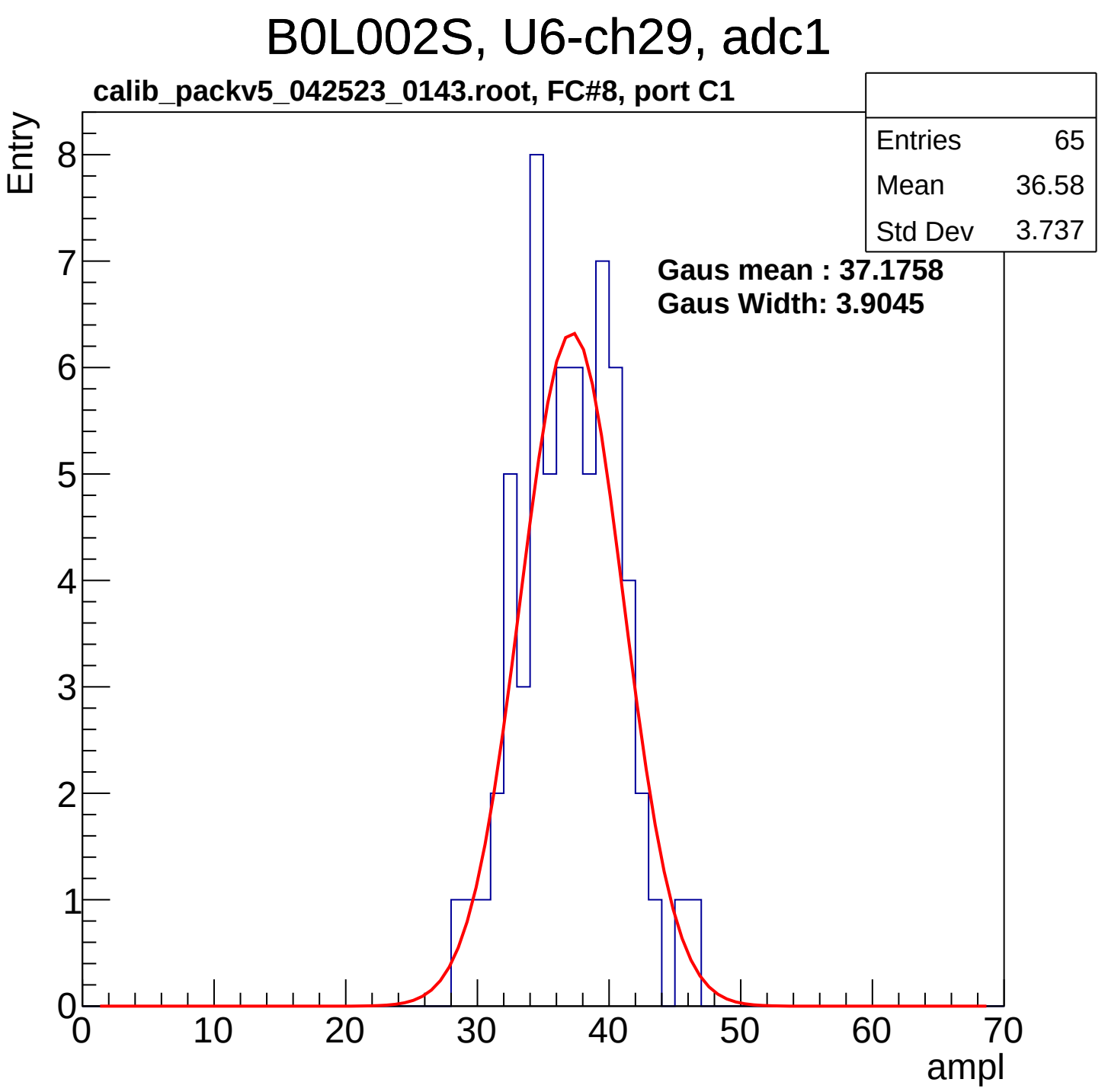
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 31.06
Std Dev 3.085
Gaus mean : 30.2143
Gaus Width: 3.9840
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.58
Std Dev 3.737
Gaus mean : 37.1758
Gaus Width: 3.9045
Entry
ampl
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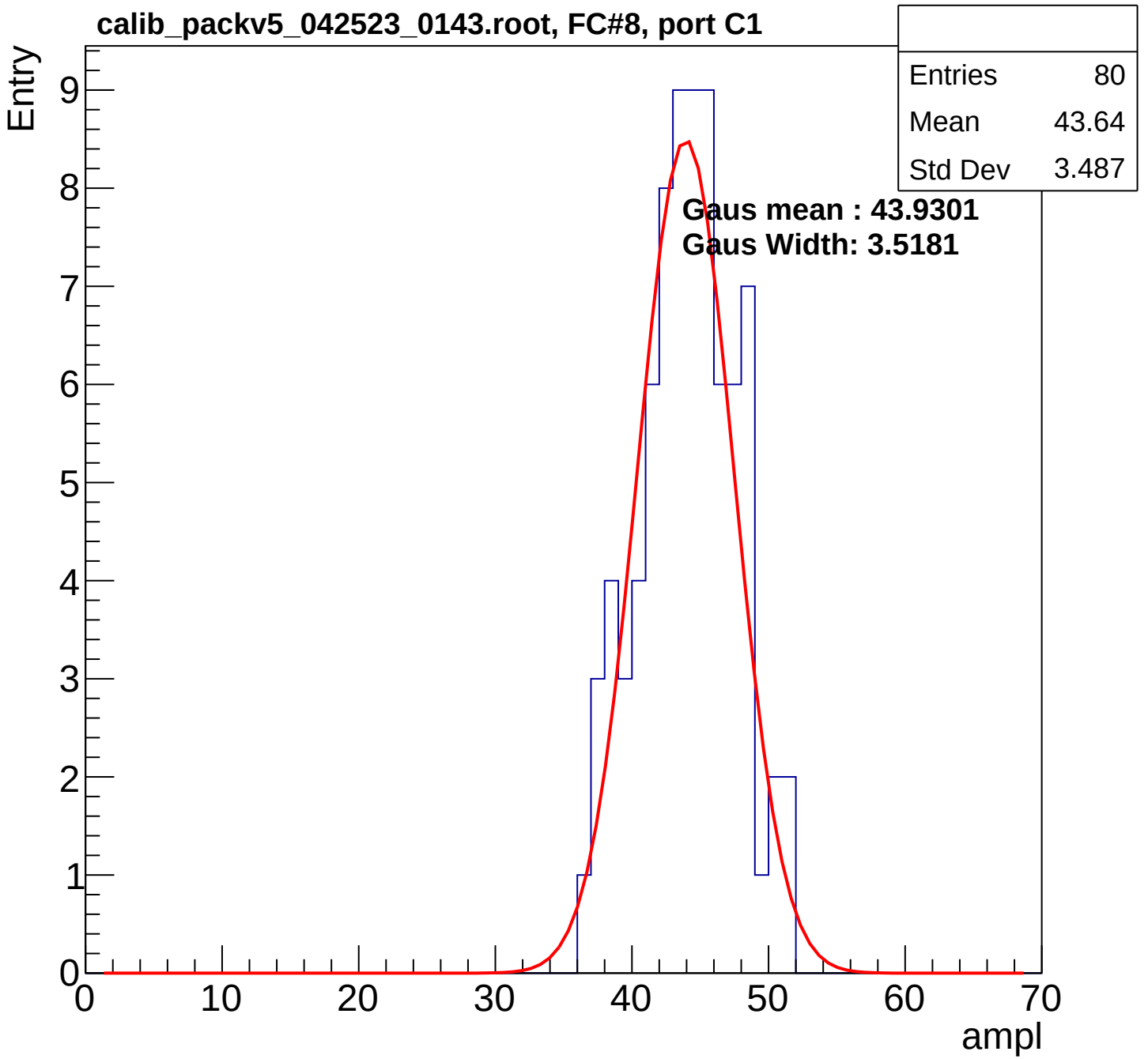
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.64
Std Dev 3.487
Gaus mean : 43.9301
Gaus Width: 3.5181
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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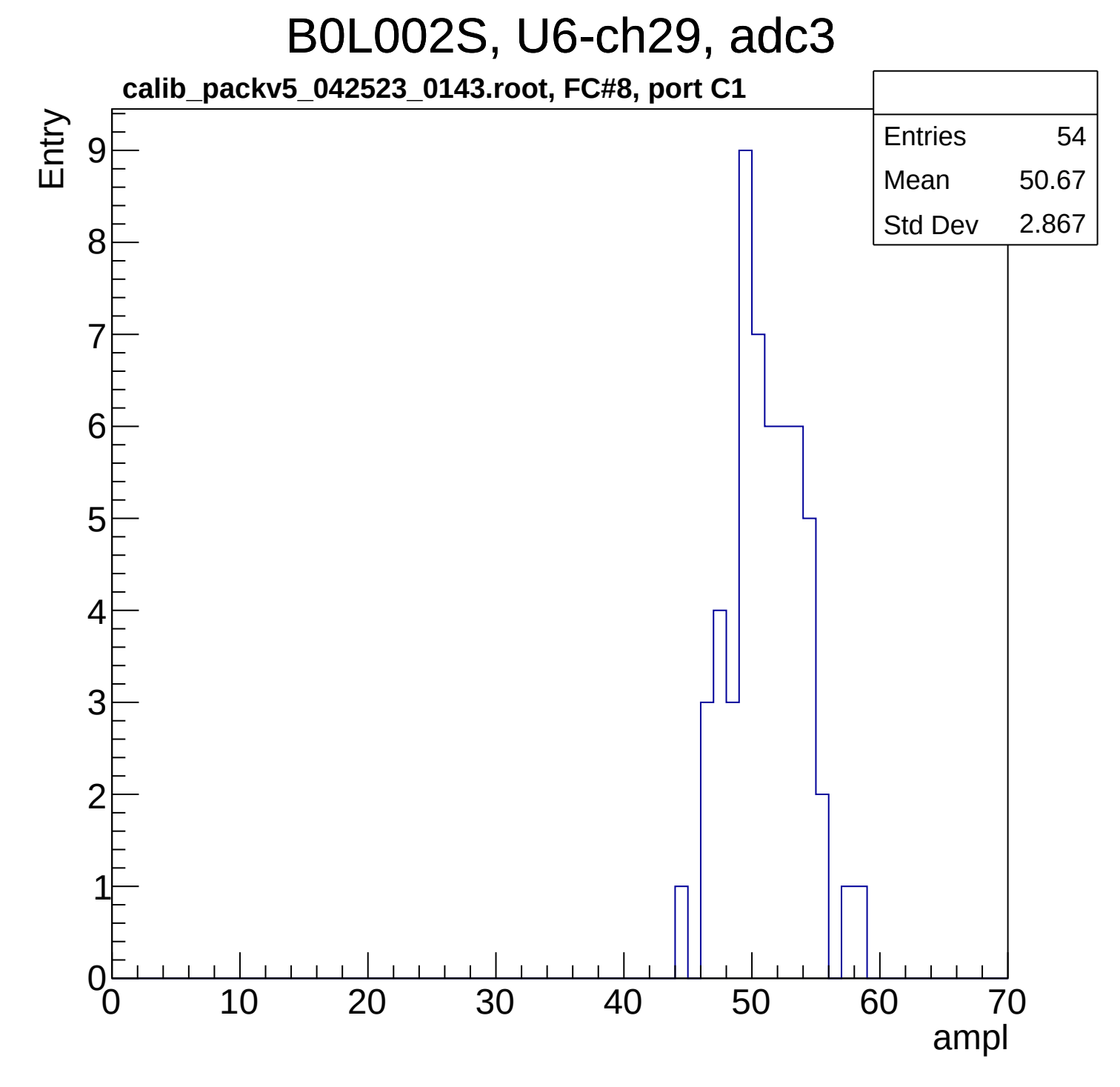
B0L002S, U6-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.67
Std Dev 2.867
Entry
ampl
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6
7
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0
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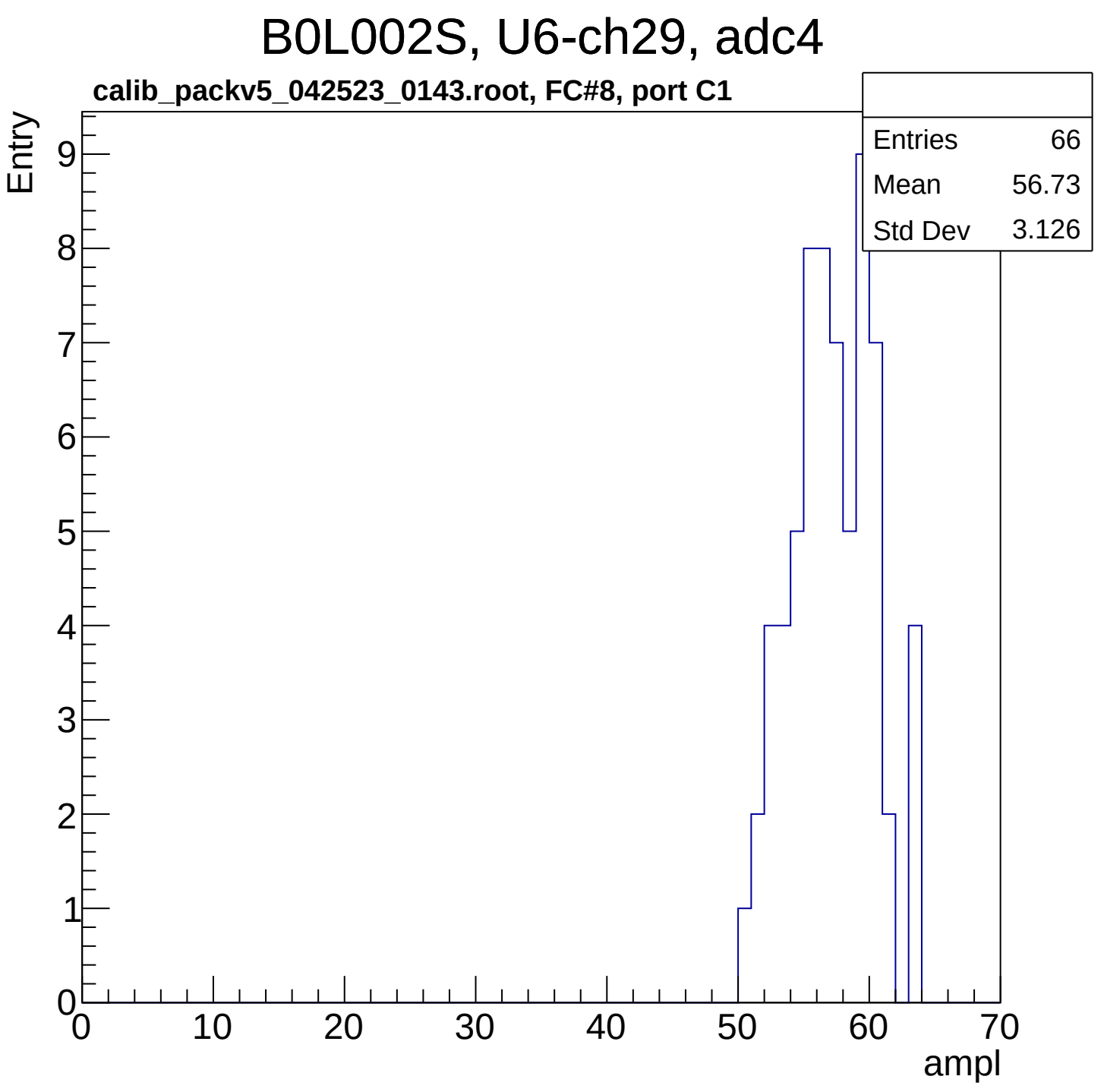
B0L002S, U6-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 56.73
Std Dev 3.126
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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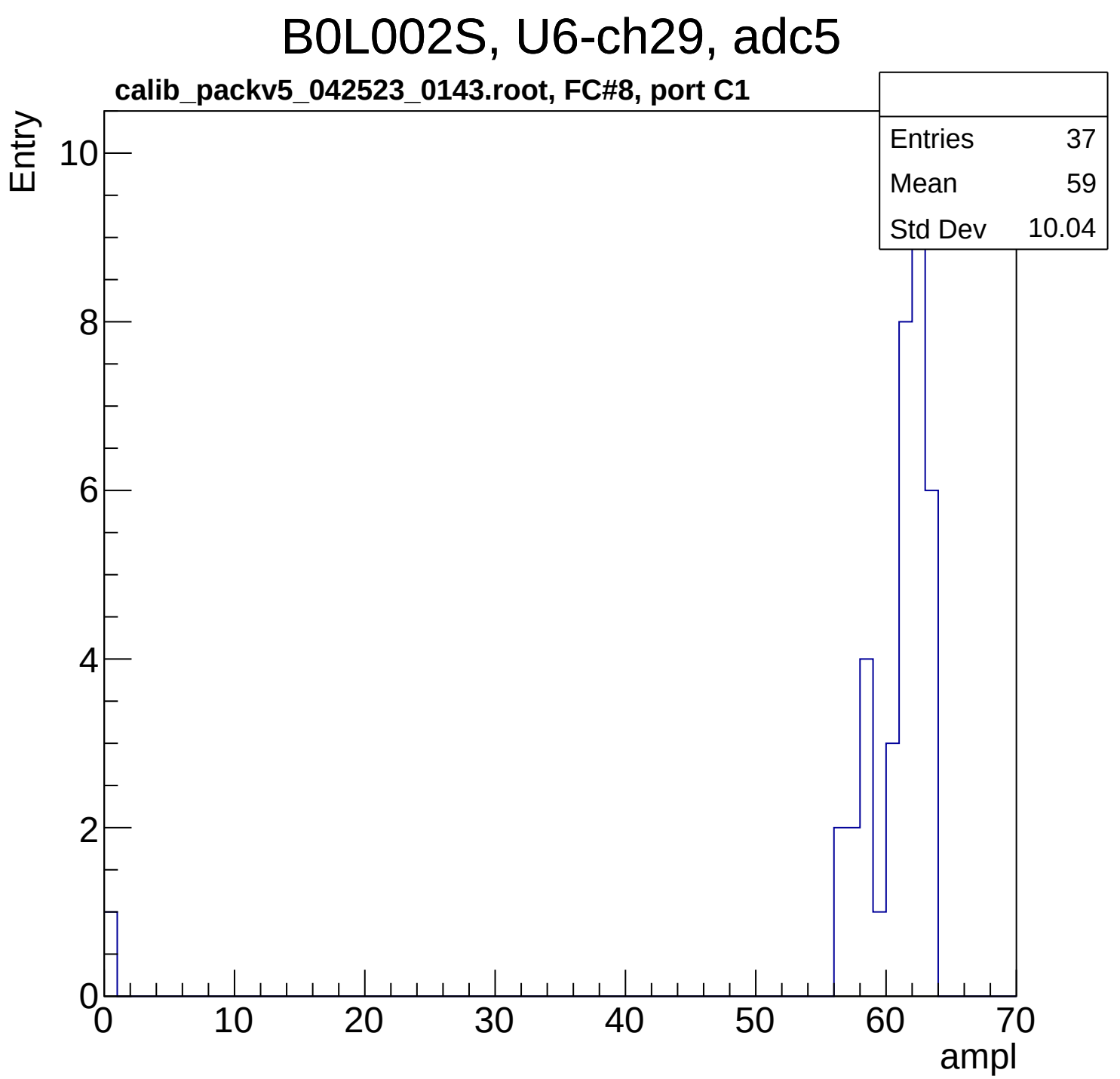
B0L002S, U6-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59
Std Dev 10.04
Entry
ampl
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2
4
6
8
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0
10
20
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40
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60
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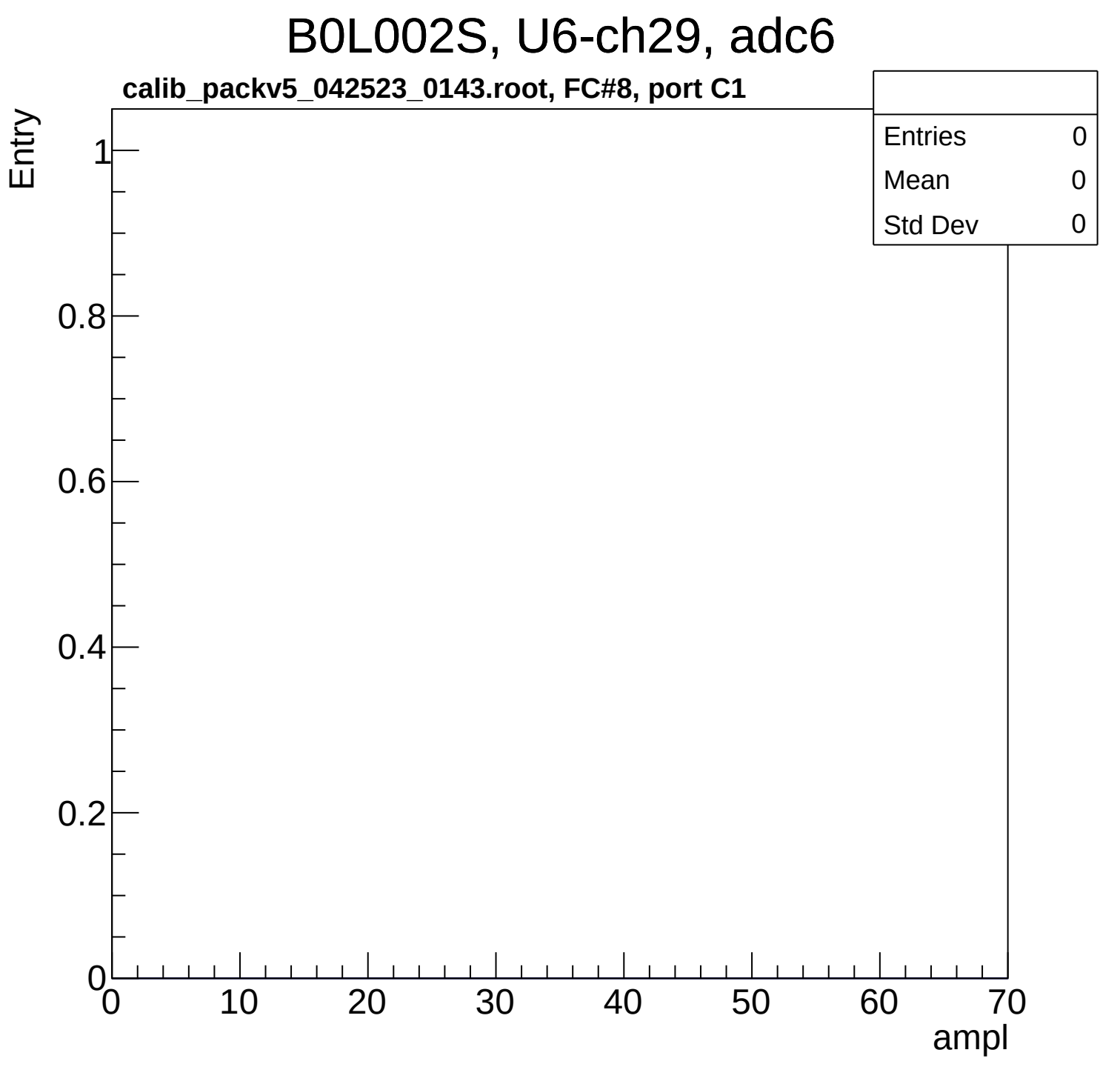
B0L002S, U6-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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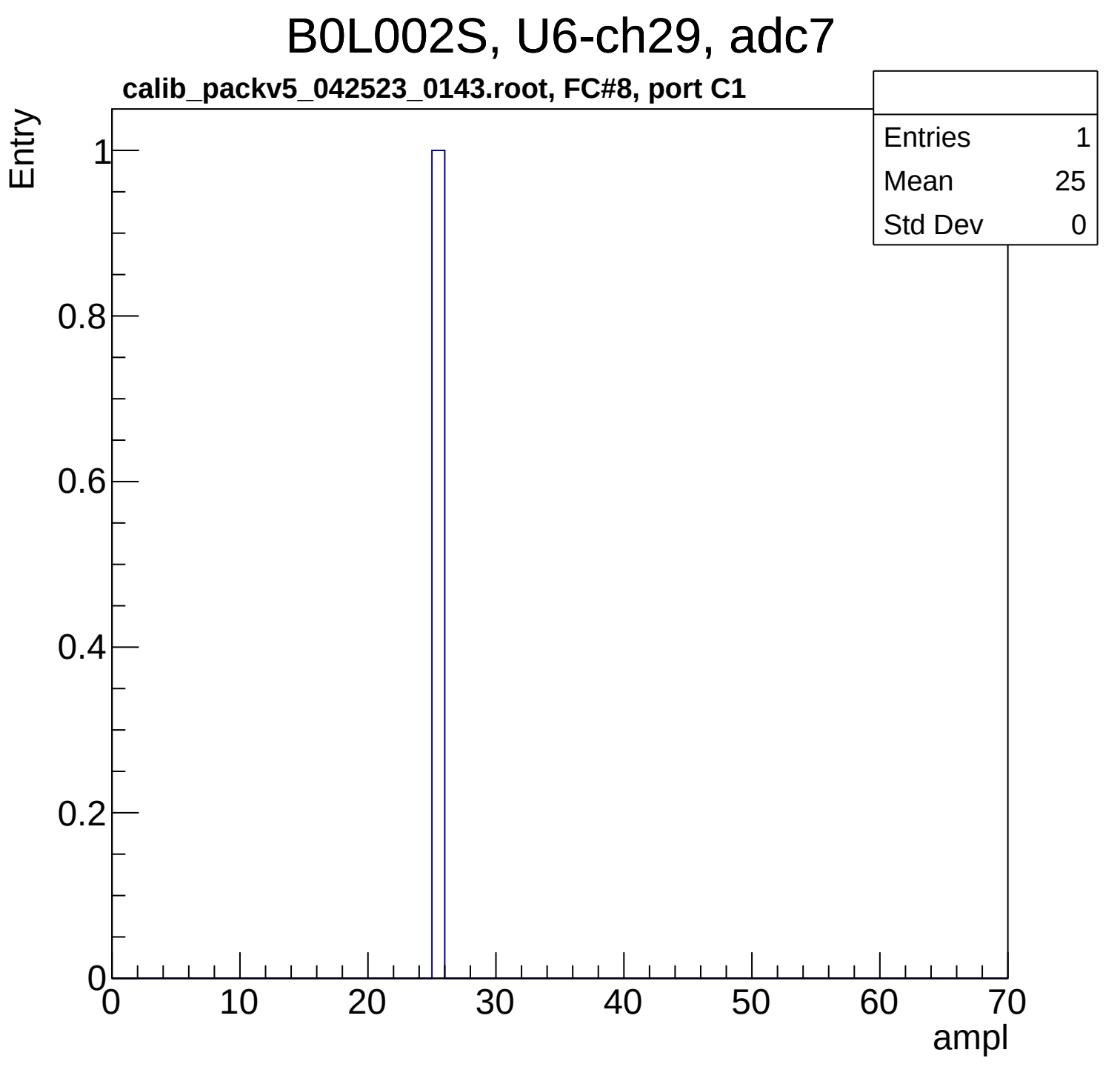
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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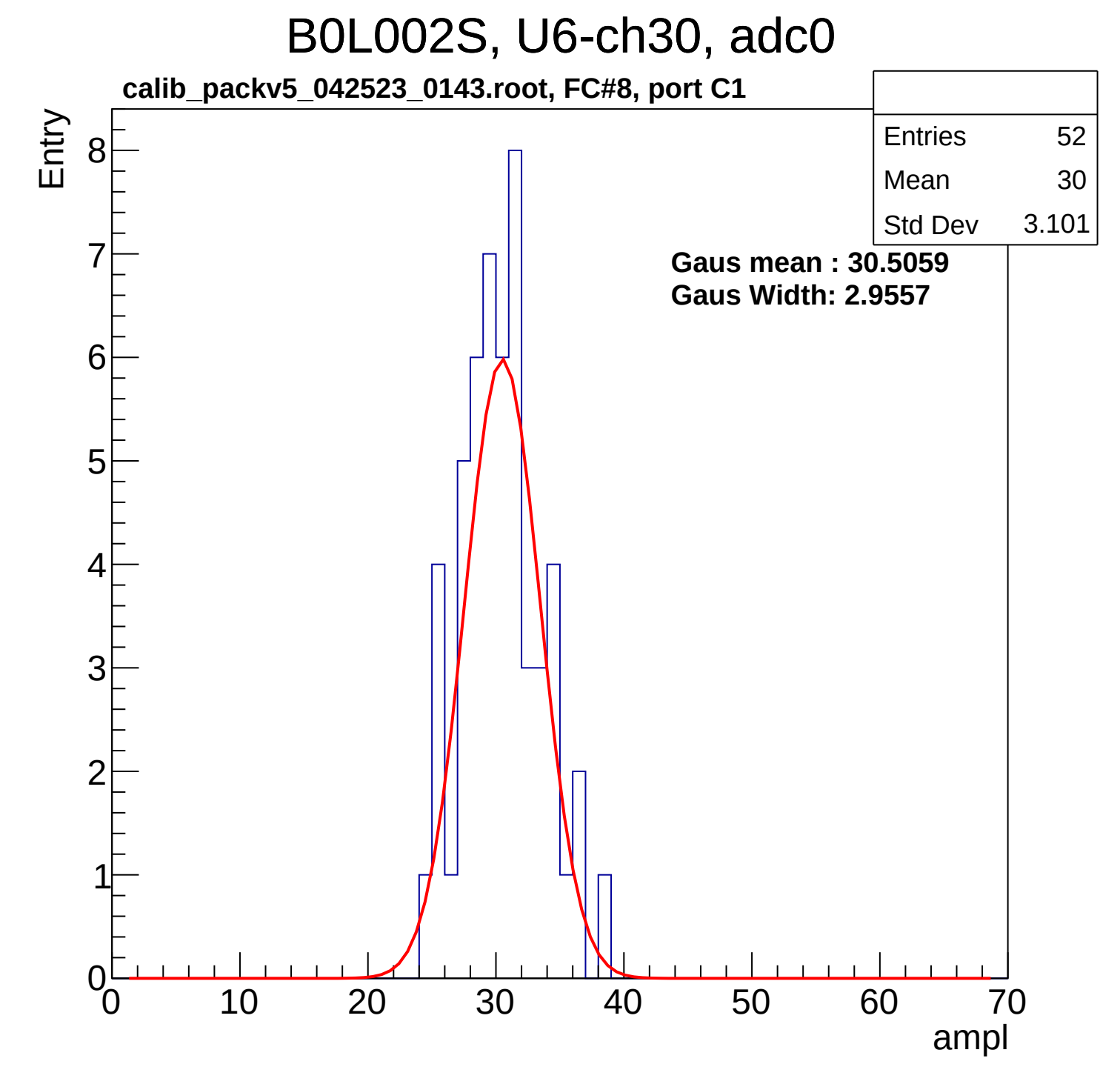
50
60
70
ampl


B0L002S, U6-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 30
Std Dev 3.101
Gaus mean : 30.5059
Gaus Width: 2.9557
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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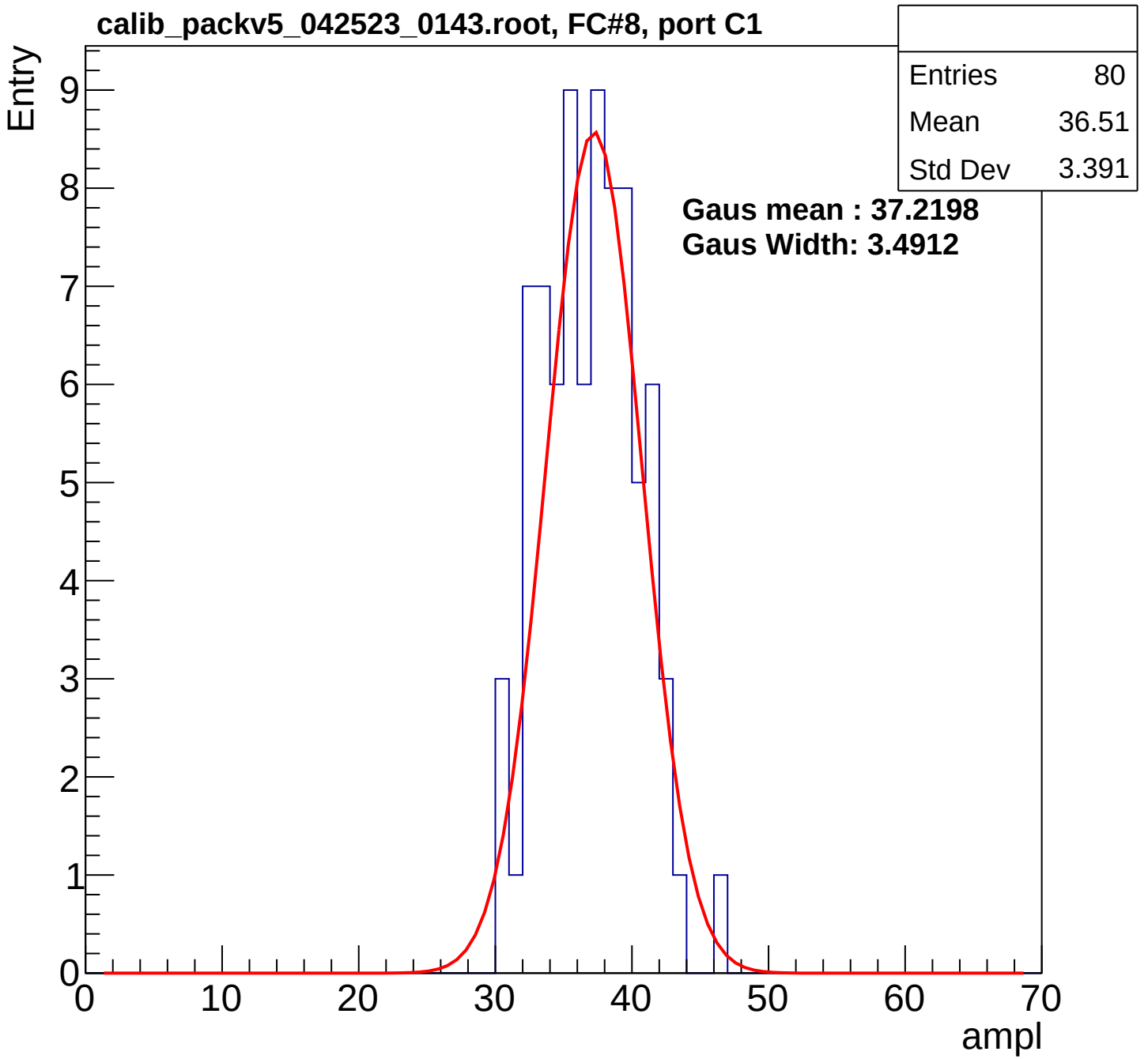
B0L002S, U6-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 36.51
Std Dev 3.391
Gaus mean : 37.2198
Gaus Width: 3.4912
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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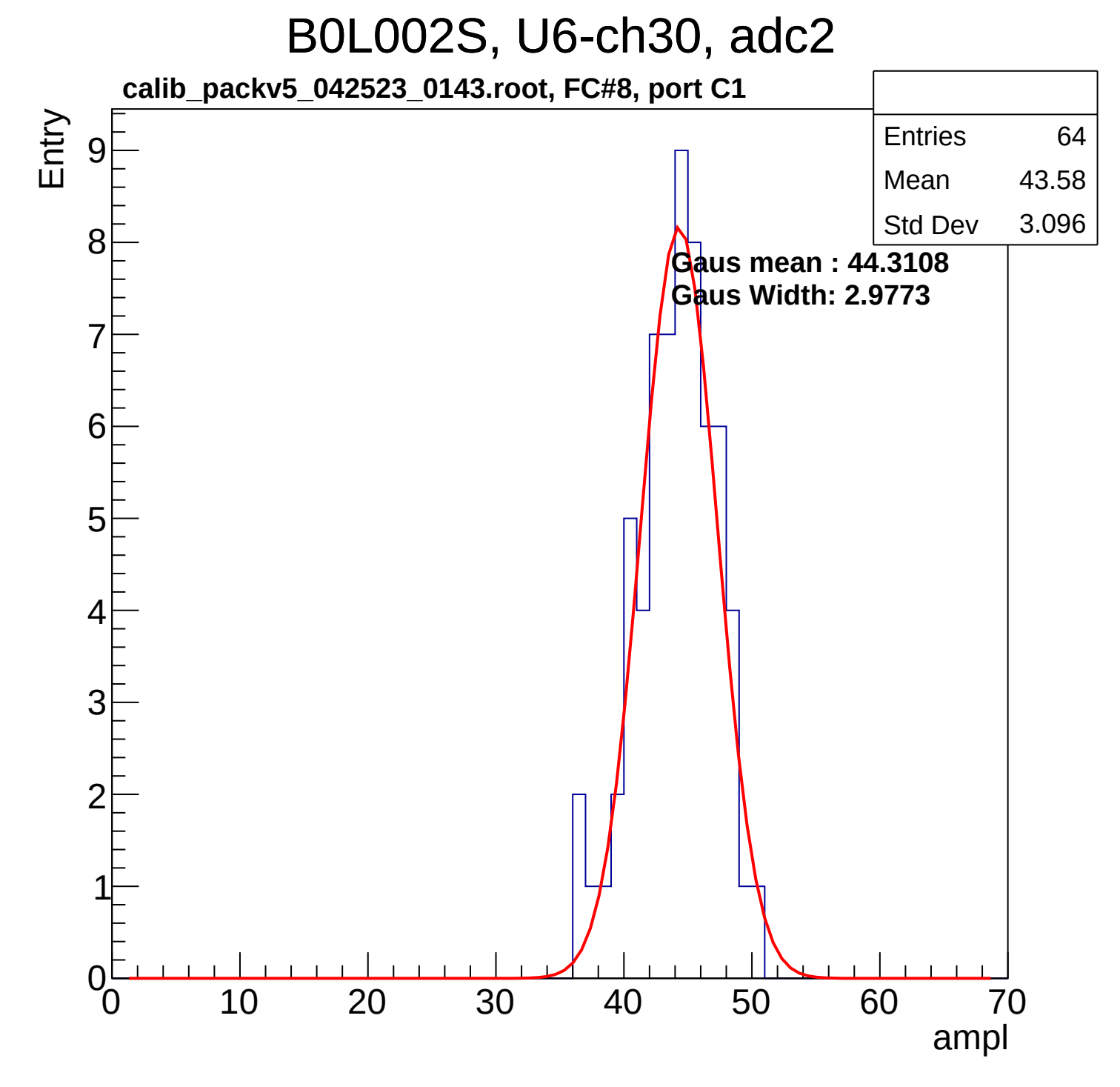
B0L002S, U6-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.58
Std Dev 3.096
Gaus mean : 44.3108
Gaus Width: 2.9773
Entry
ampl
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2
3
4
5
6
7
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0
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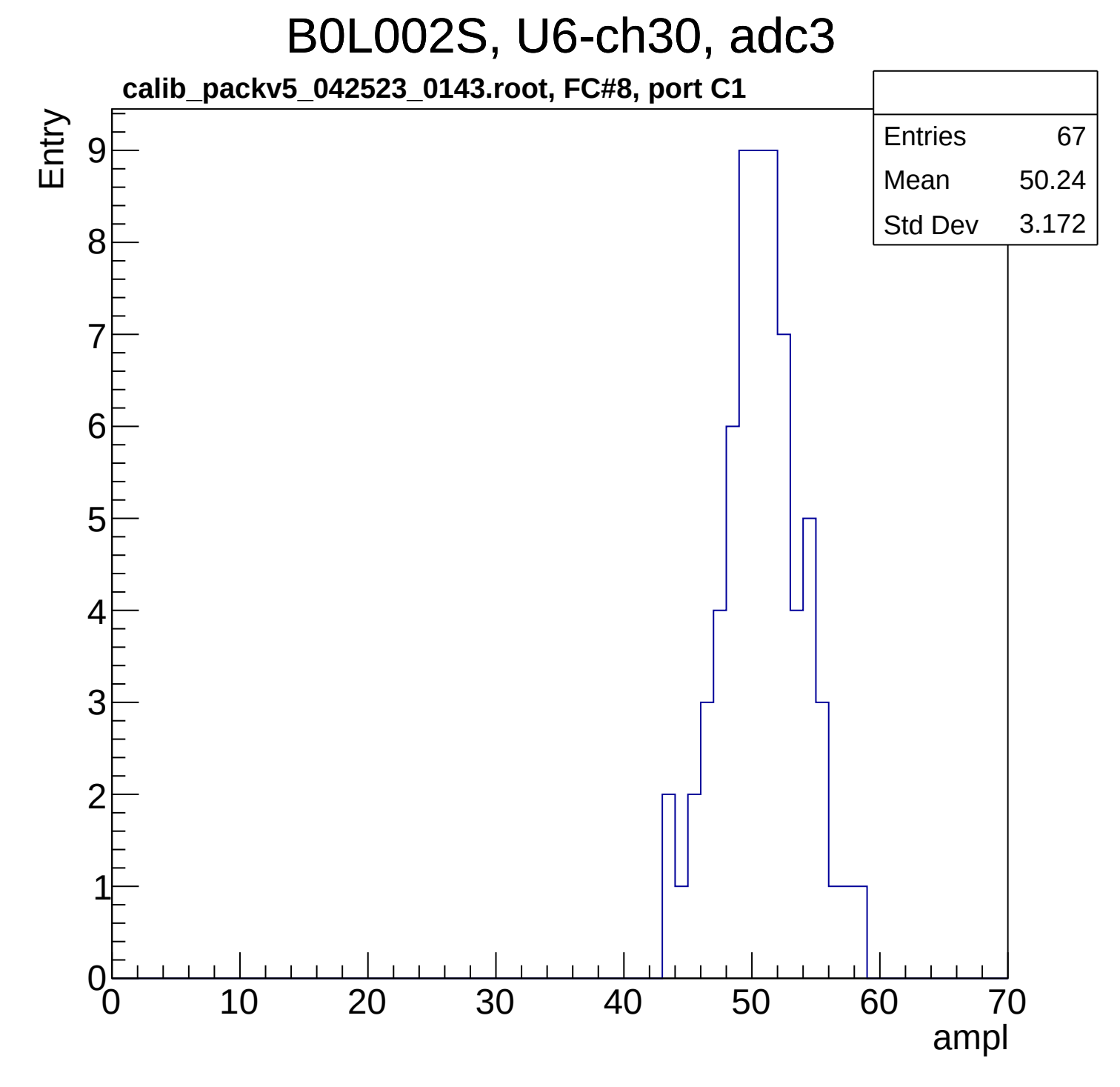
B0L002S, U6-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.24
Std Dev 3.172
Entry
ampl
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7
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9
0
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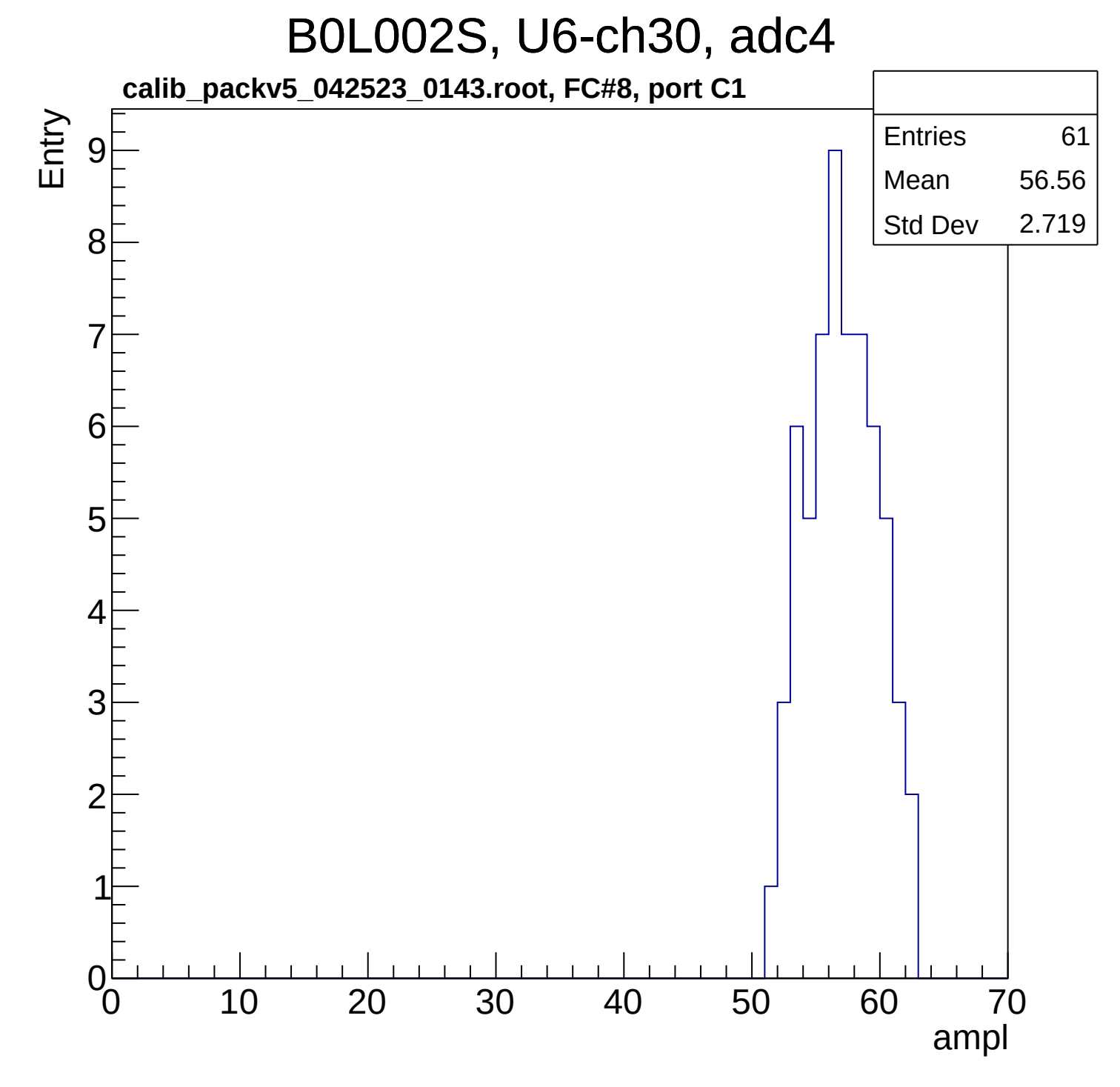
B0L002S, U6-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.56
Std Dev 2.719
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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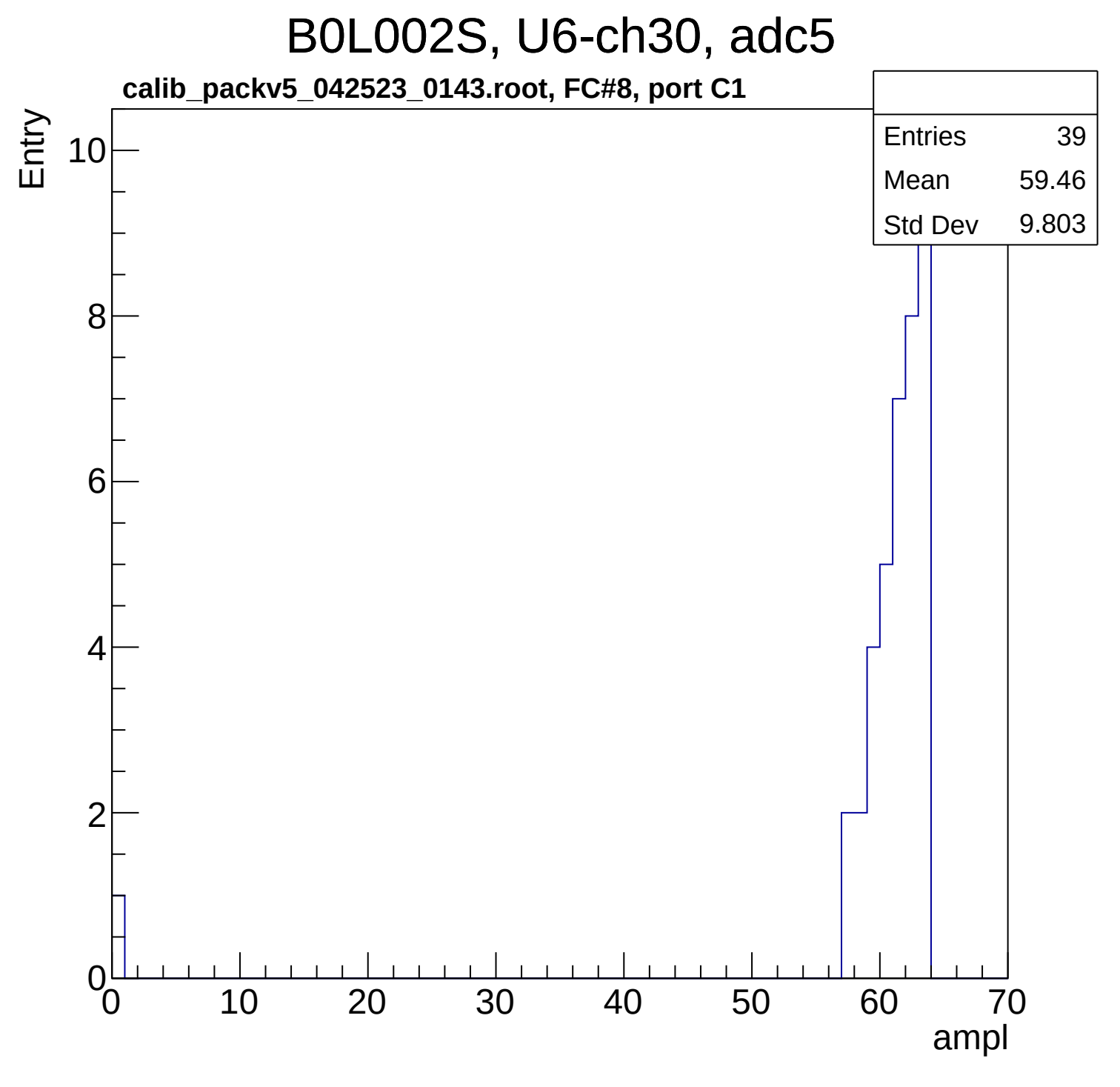
B0L002S, U6-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.46
Std Dev 9.803
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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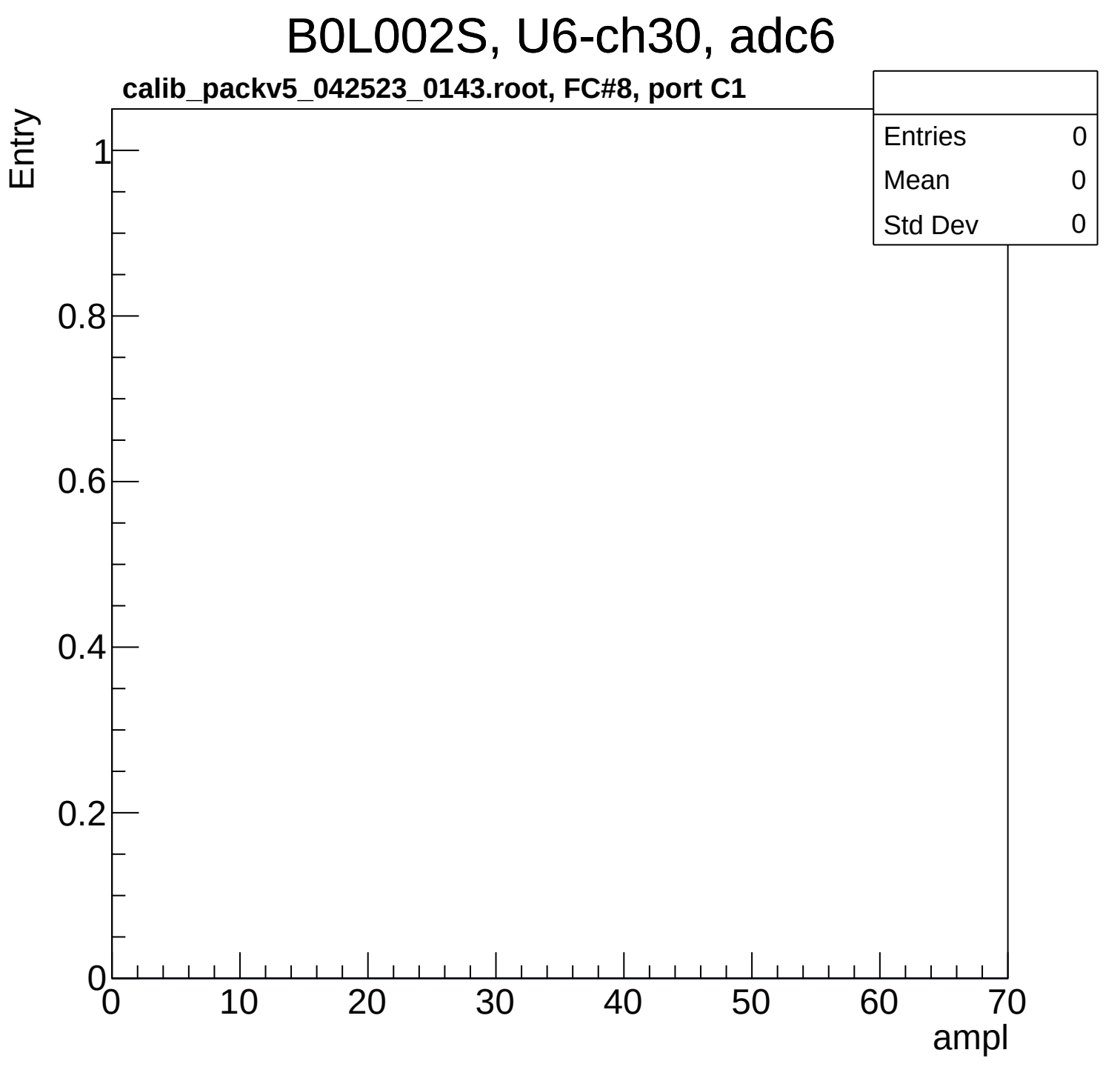
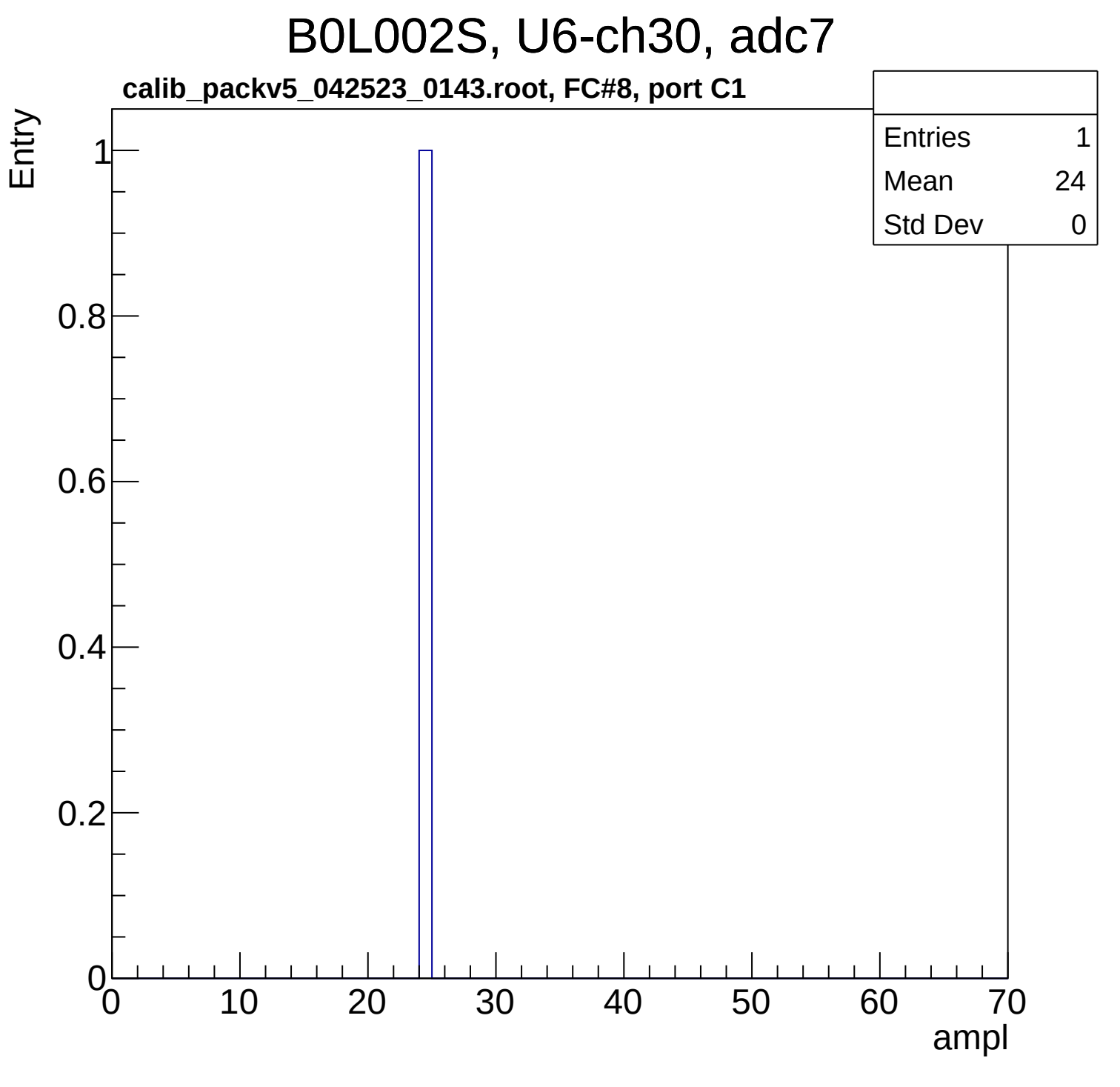

B0L002S, U6-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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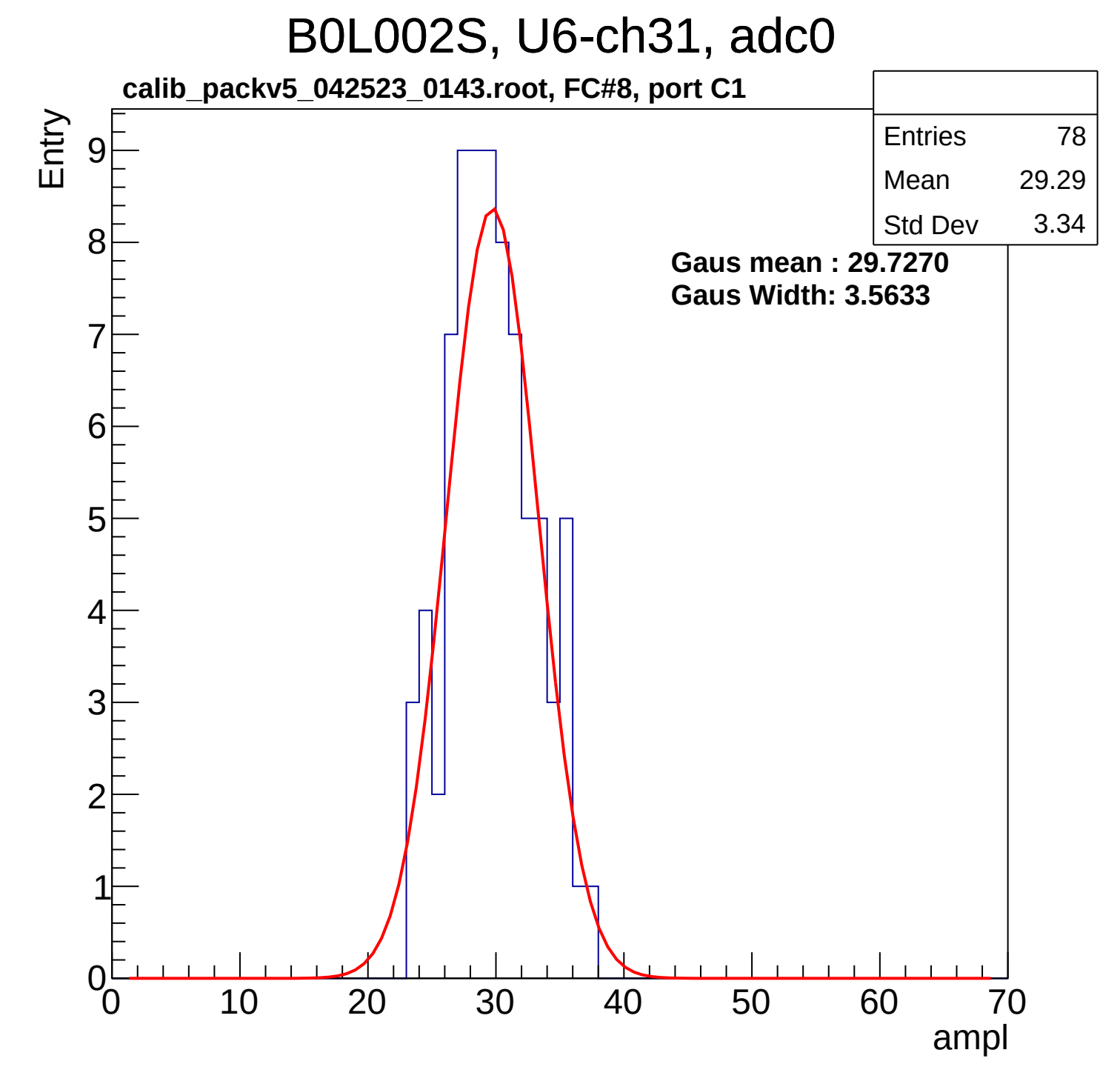
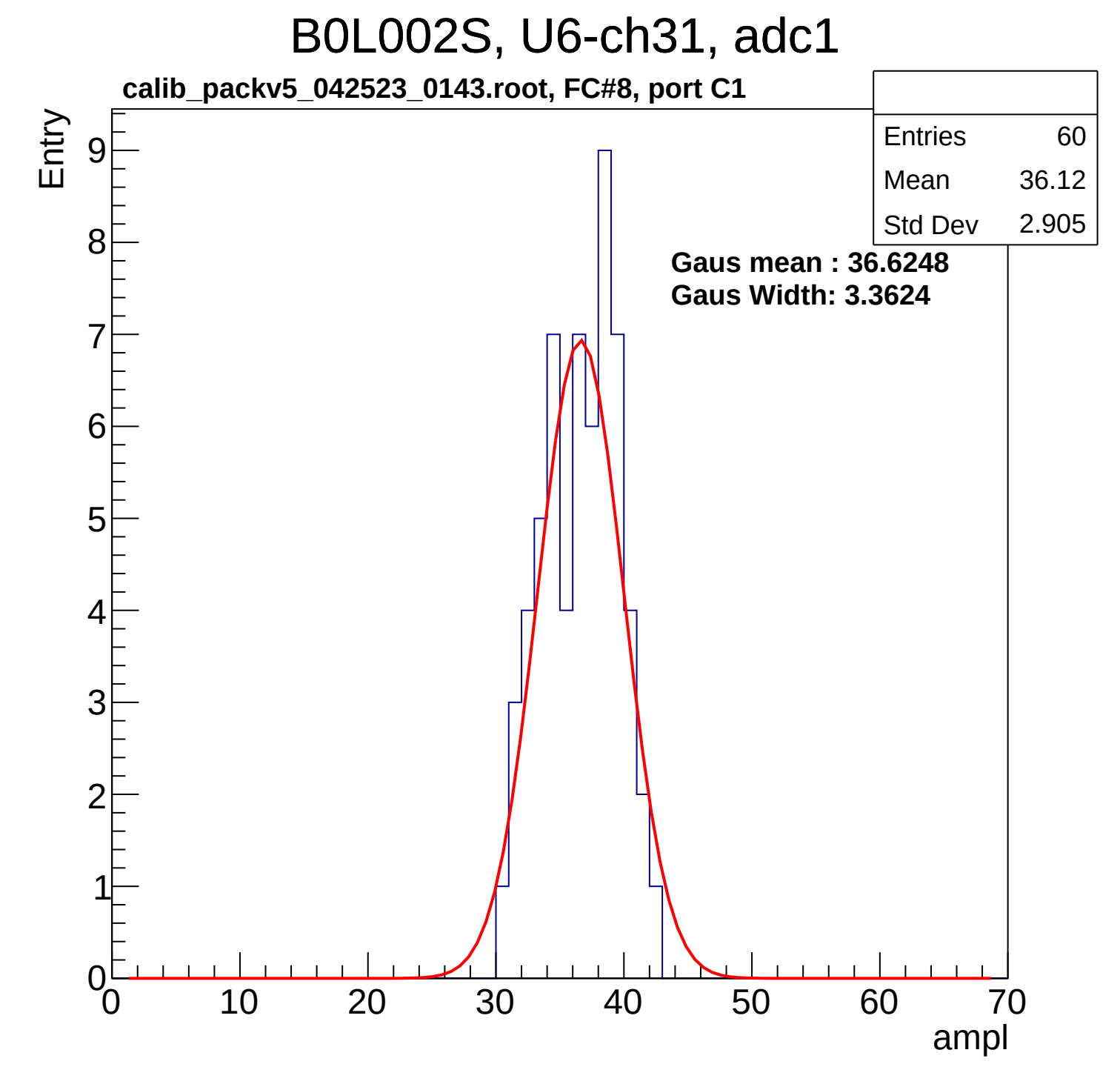
40
50
60
70
ampl


B0L002S, U6-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.29
Std Dev 3.34
Gaus mean : 29.7270
Gaus Width: 3.5633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.12
Std Dev 2.905
Gaus mean : 36.6248
Gaus Width: 3.3624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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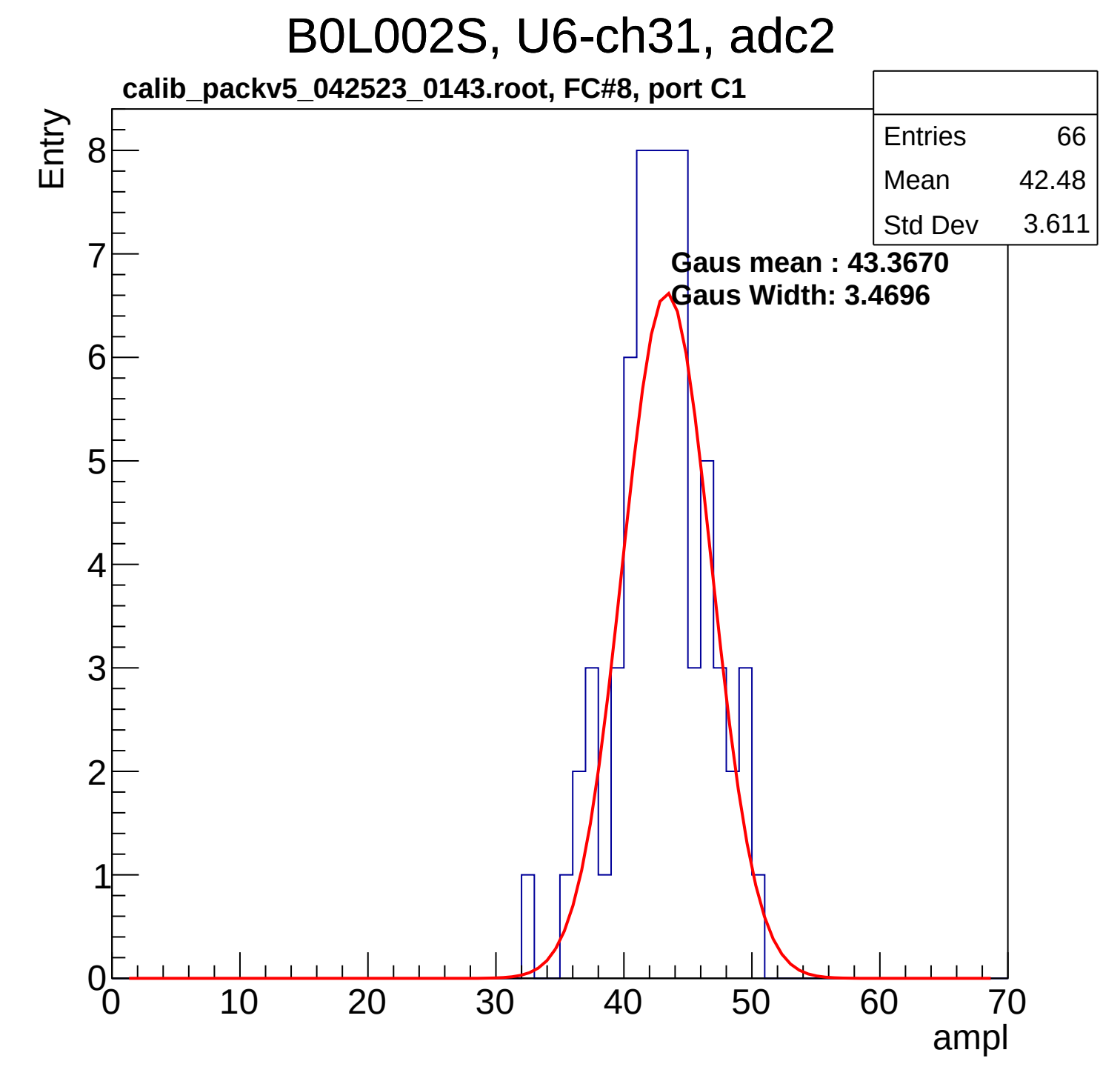
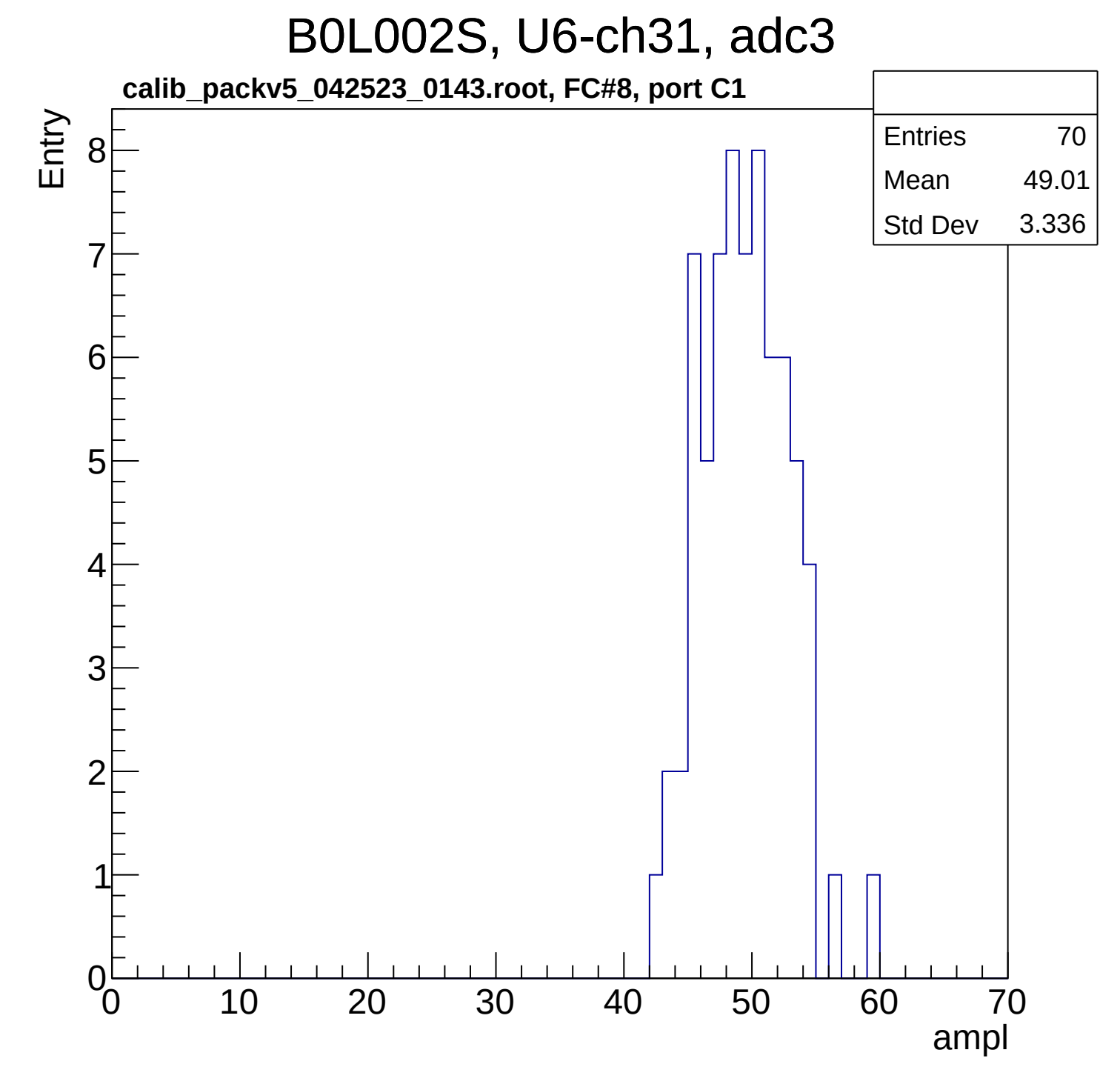
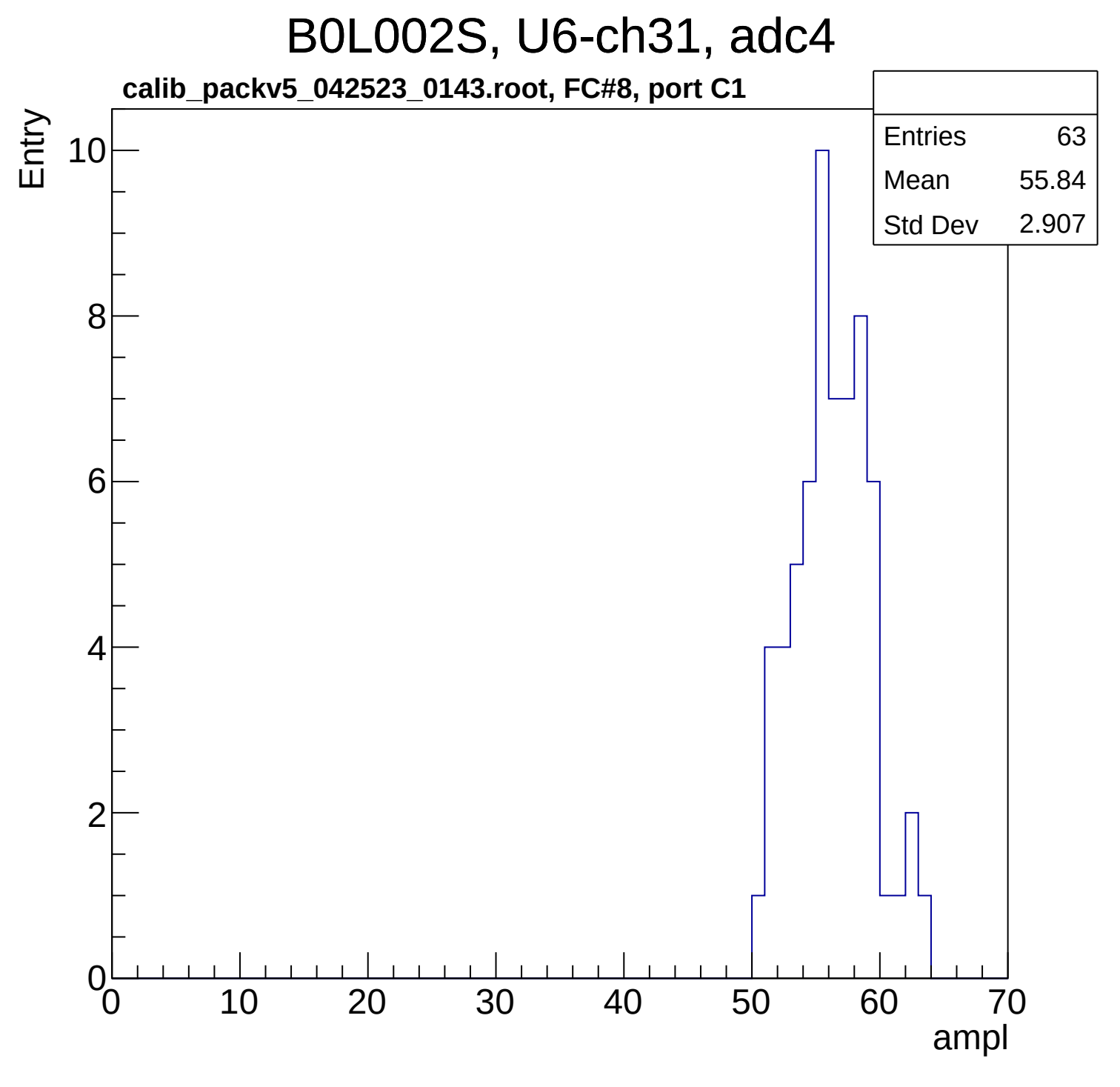
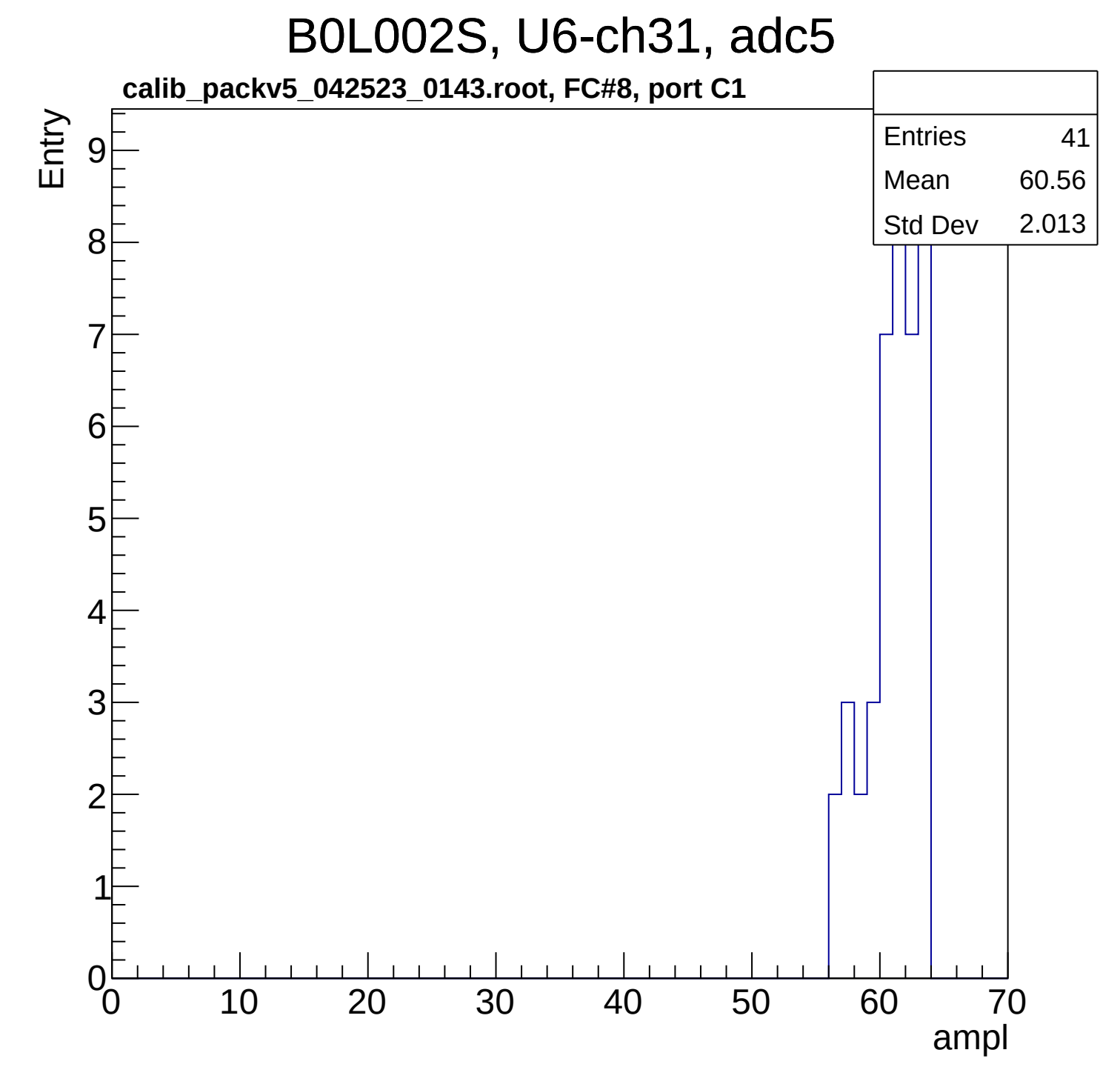

B0L002S, U6-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.48
Std Dev 3.611
Gaus mean : 43.3670
Gaus Width: 3.4696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 49.01
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.84
Std Dev 2.907
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 60.56
Std Dev 2.013
Entry
ampl
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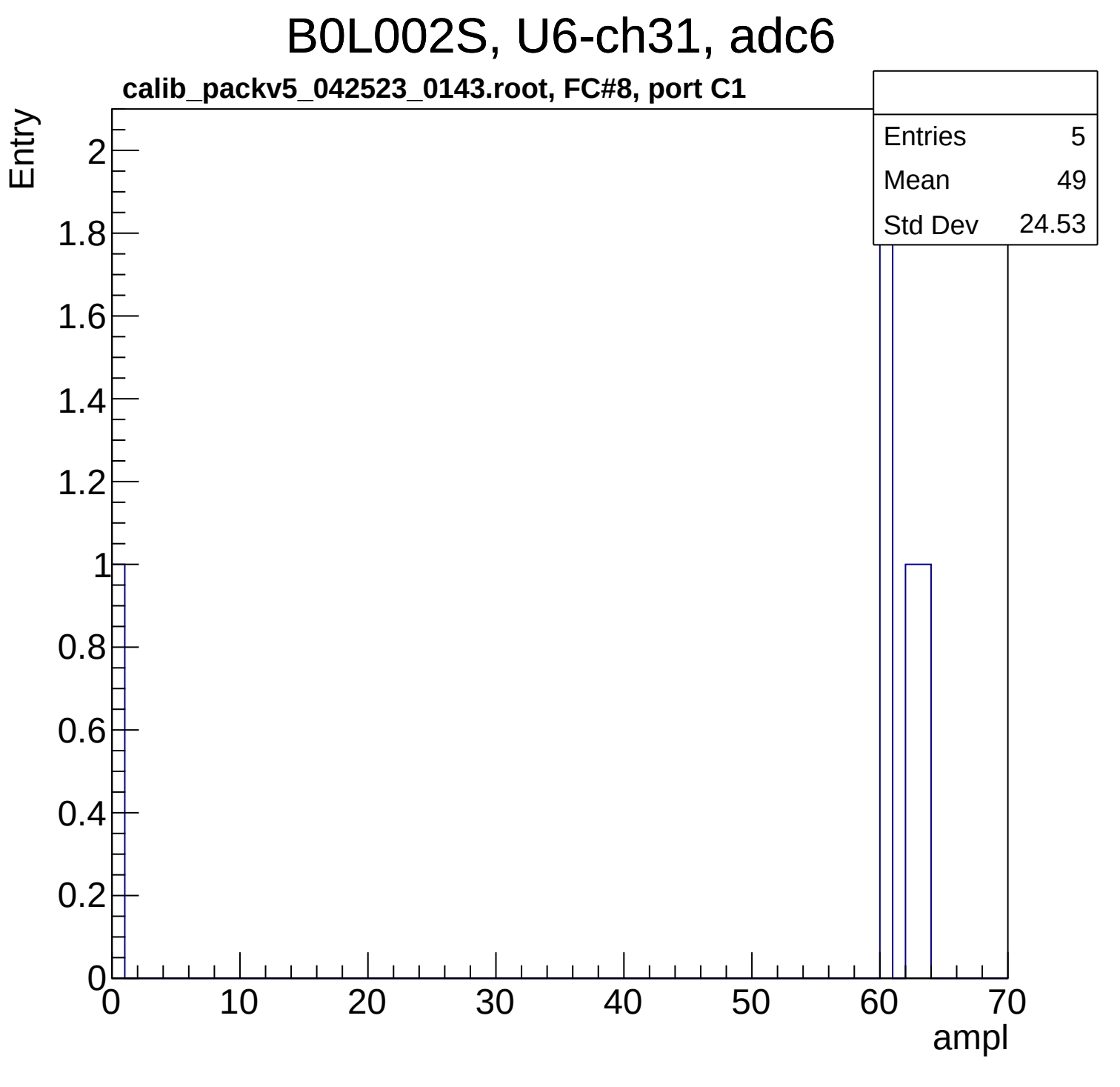
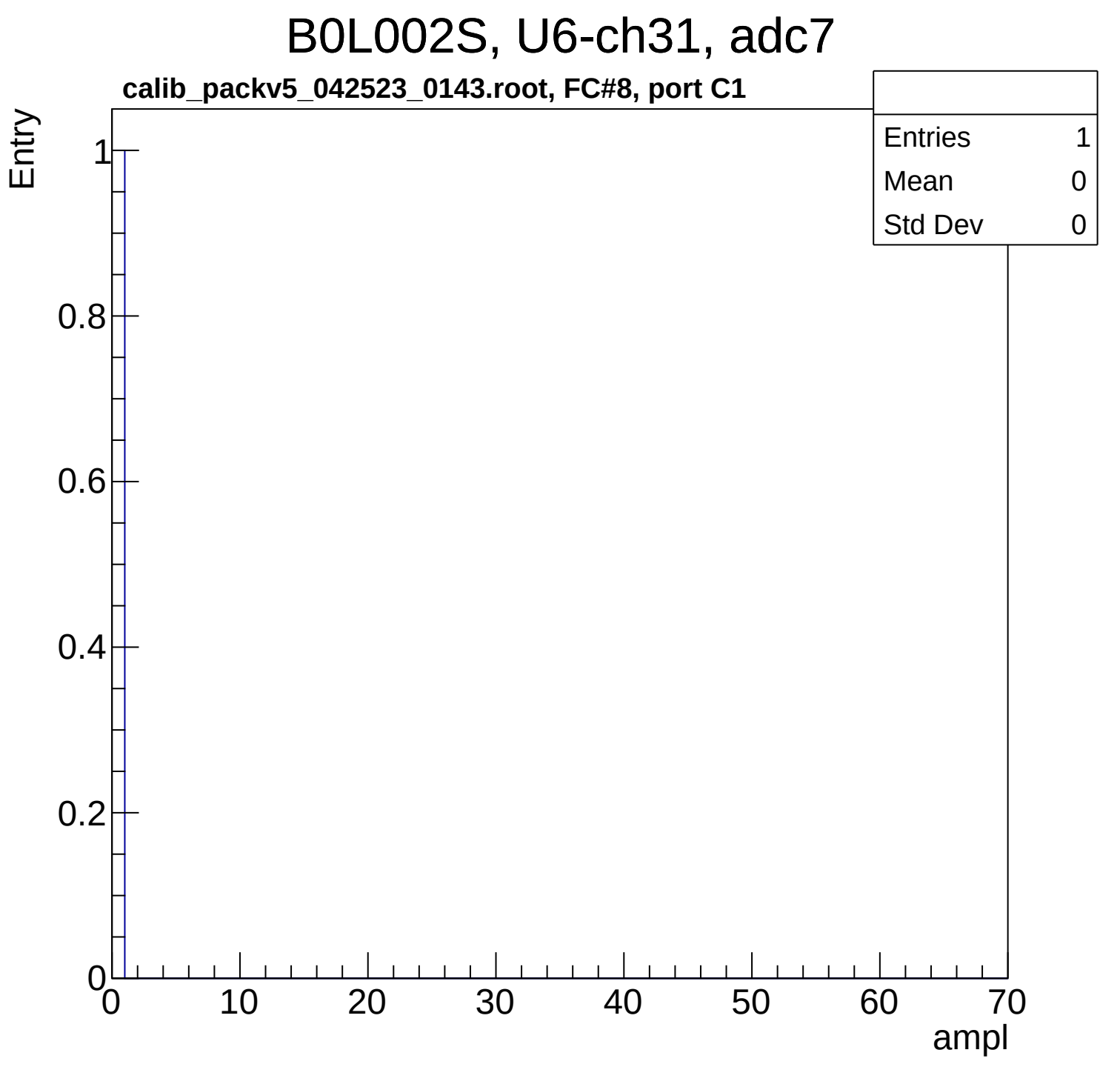
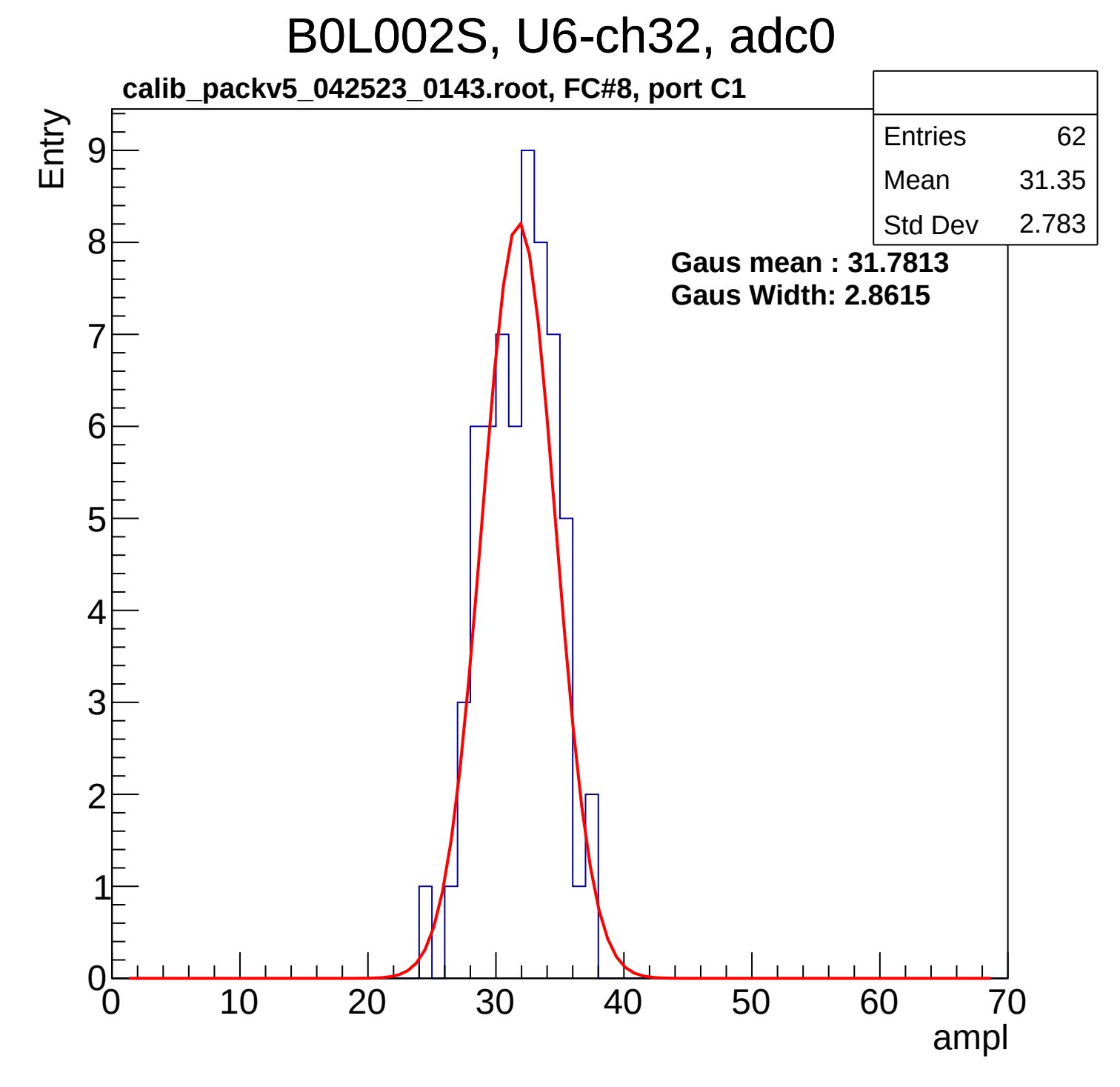
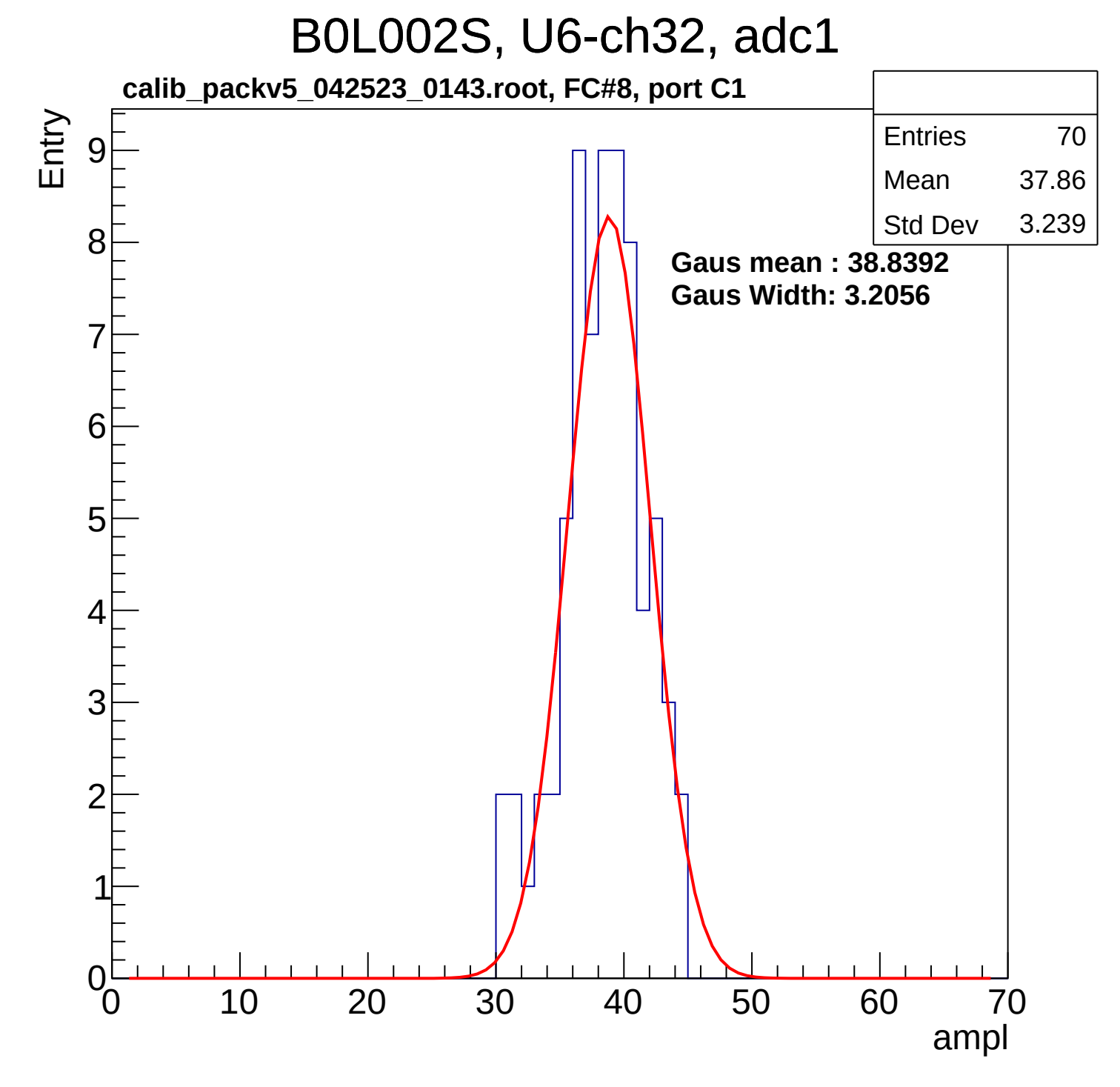
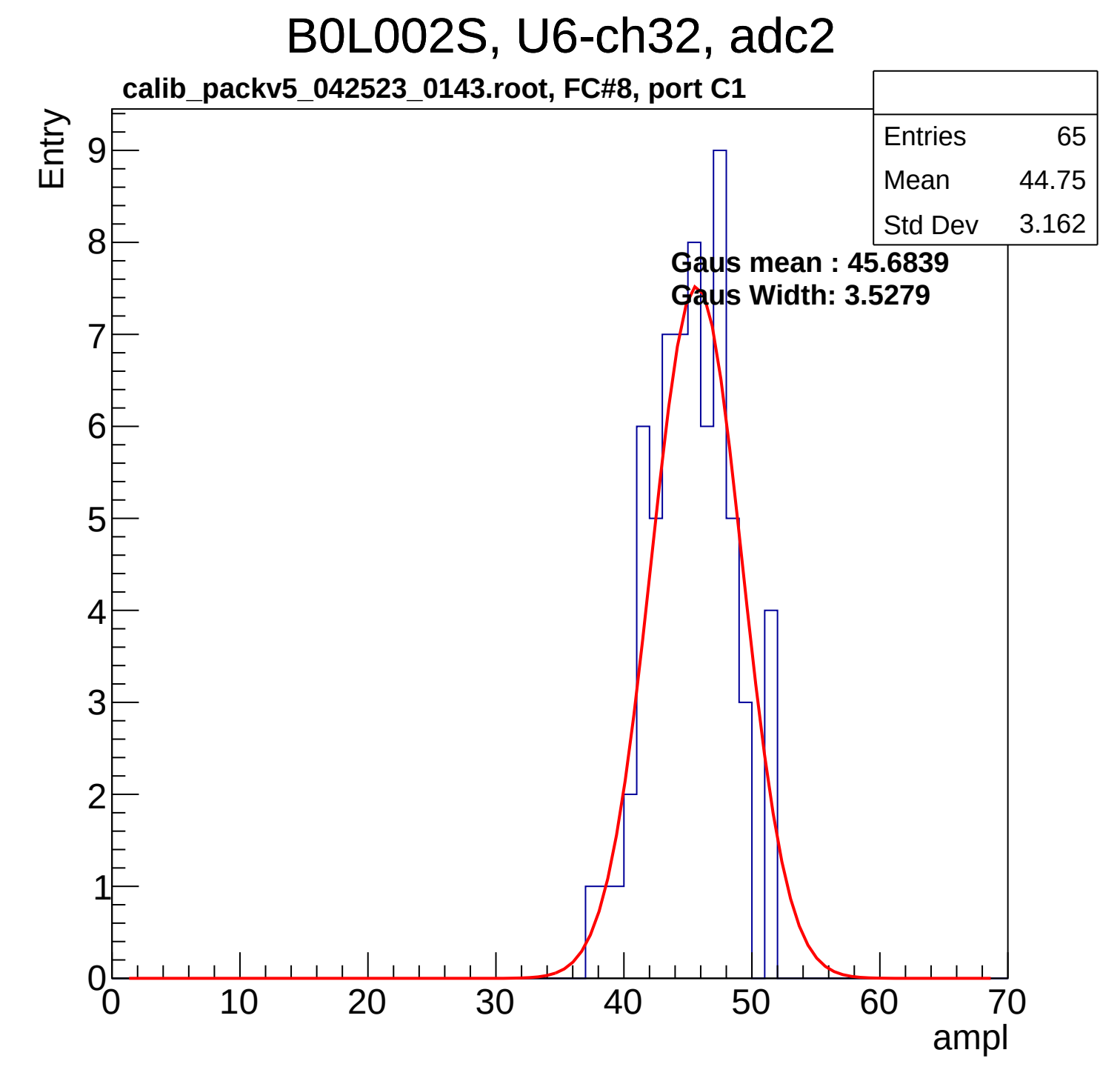
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49
Std Dev 24.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.35
Std Dev 2.783
Gaus mean : 31.7813
Gaus Width: 2.8615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.86
Std Dev 3.239
Gaus mean : 38.8392
Gaus Width: 3.2056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.75
Std Dev 3.162
Gaus mean : 45.6839
Gaus Width: 3.5279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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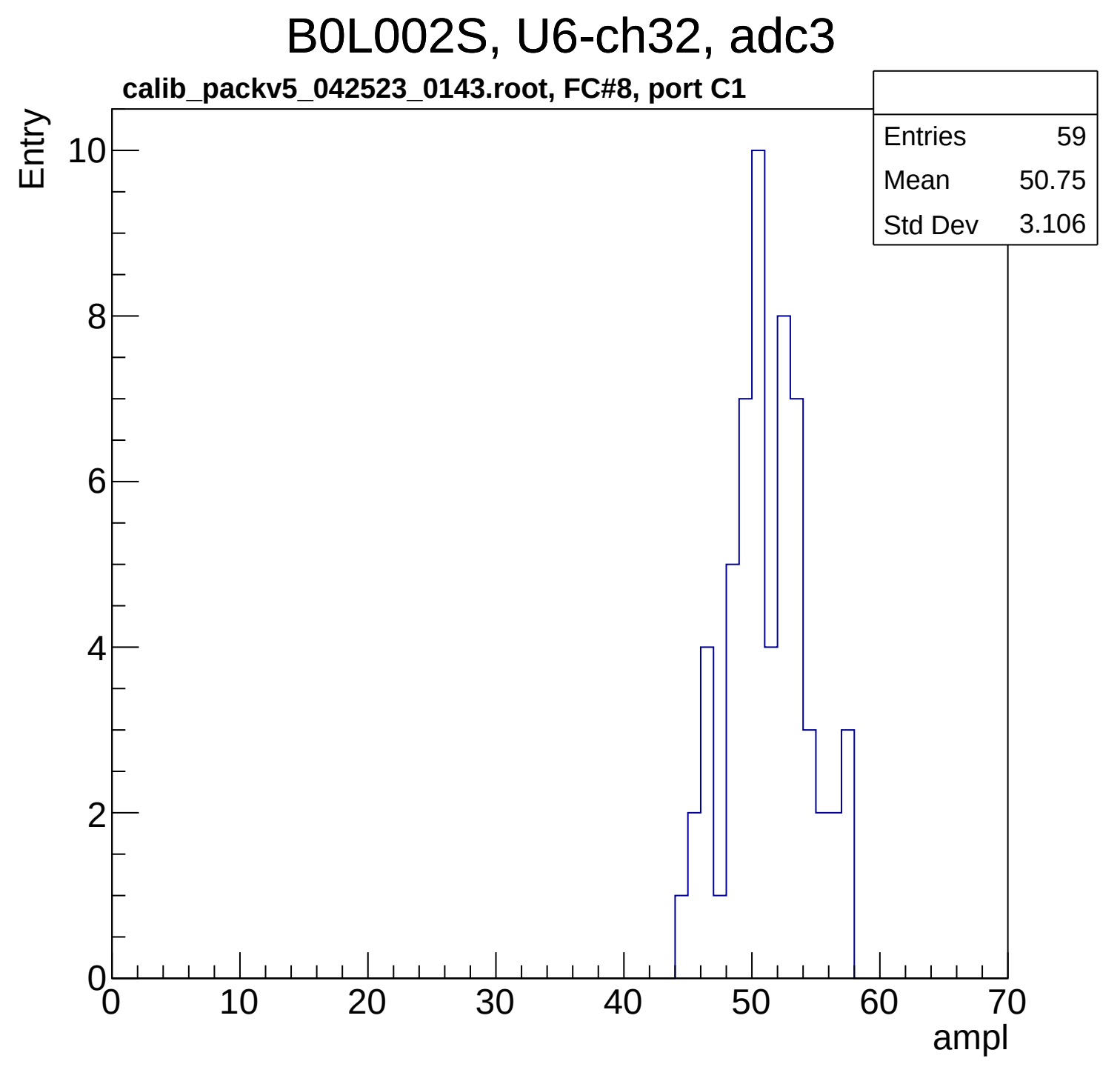
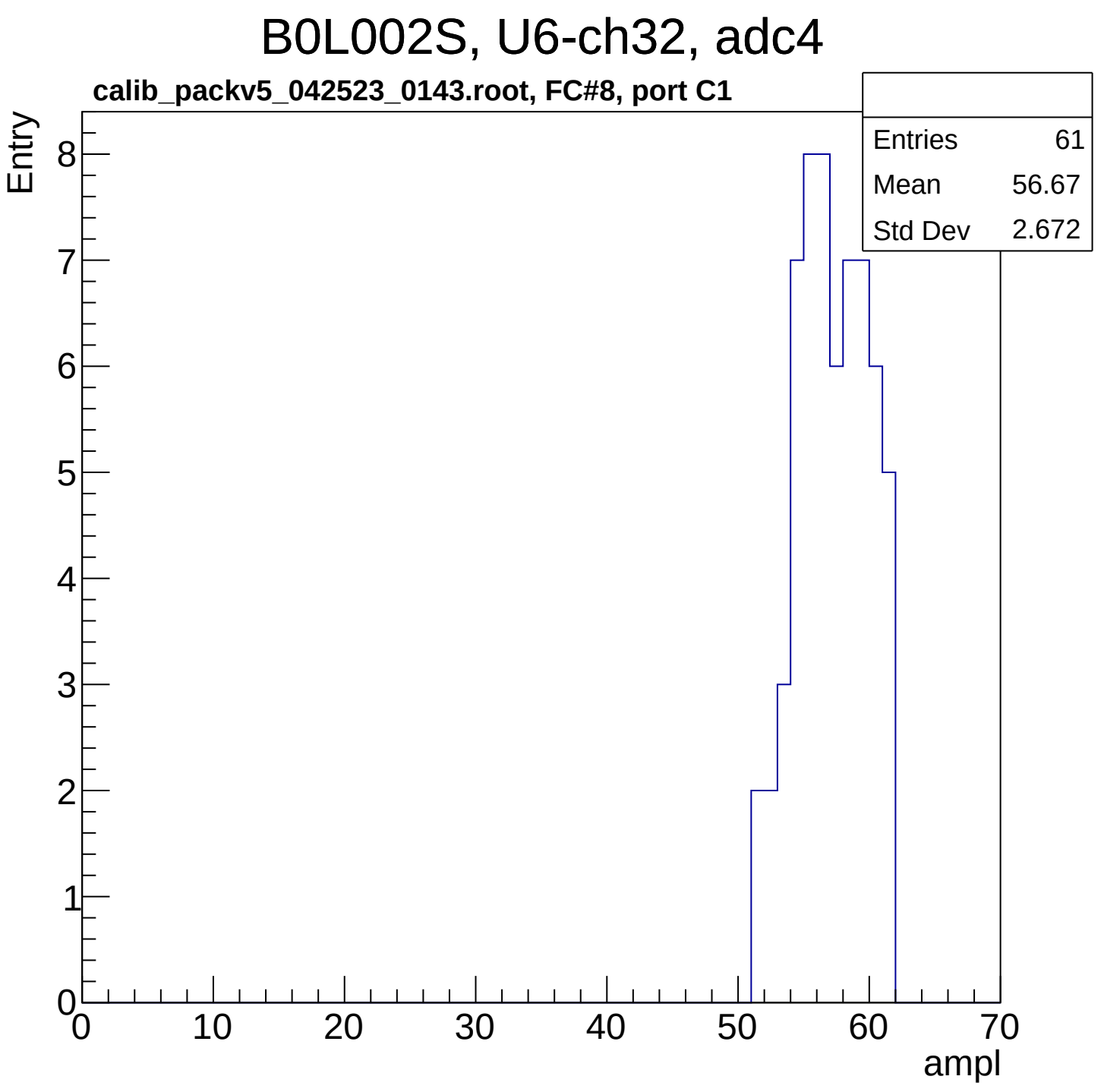

B0L002S, U6-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.75
Std Dev 3.106
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.67
Std Dev 2.672
Entry
0
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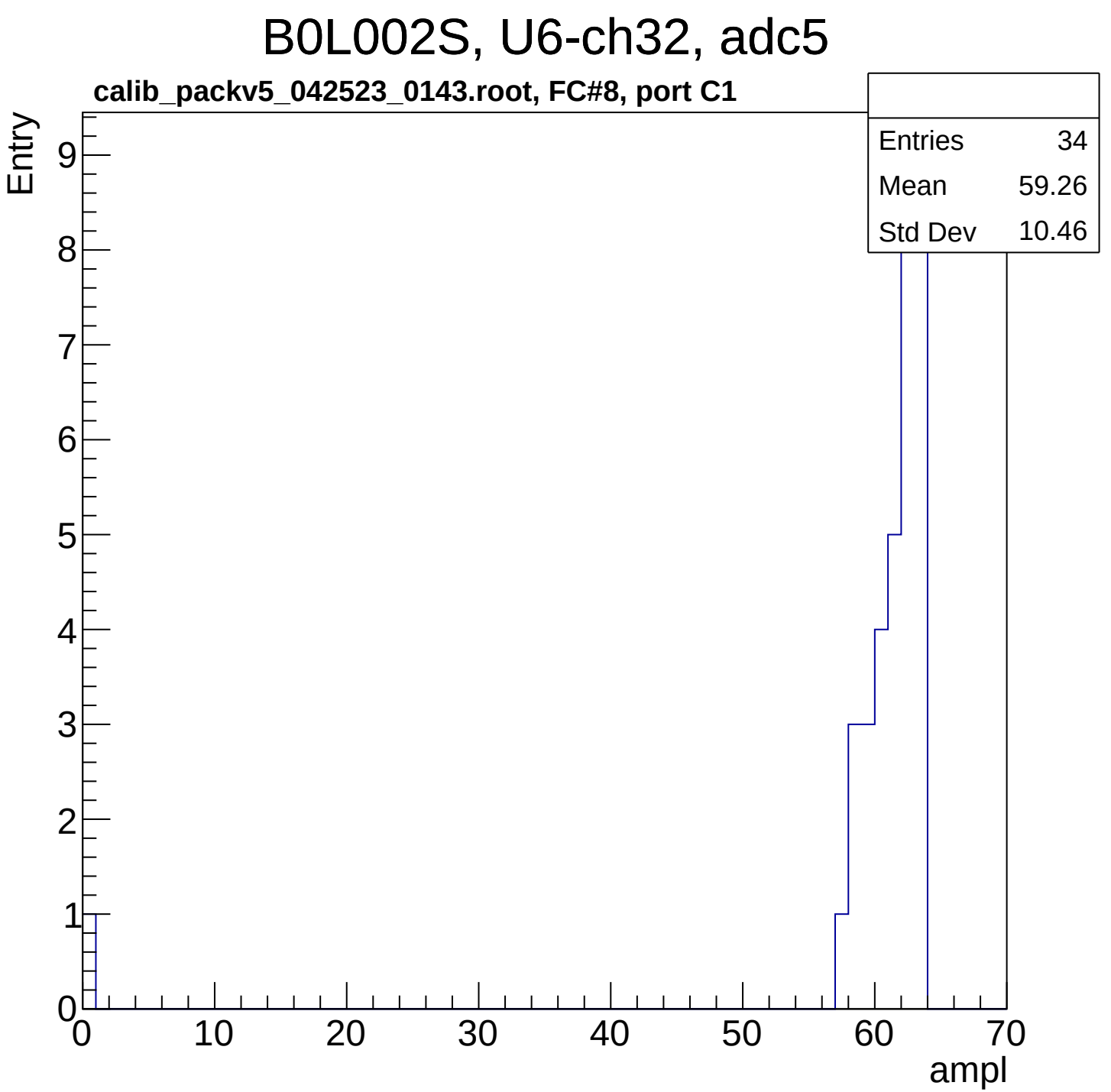
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.26
Std Dev 10.46
Entry
ampl
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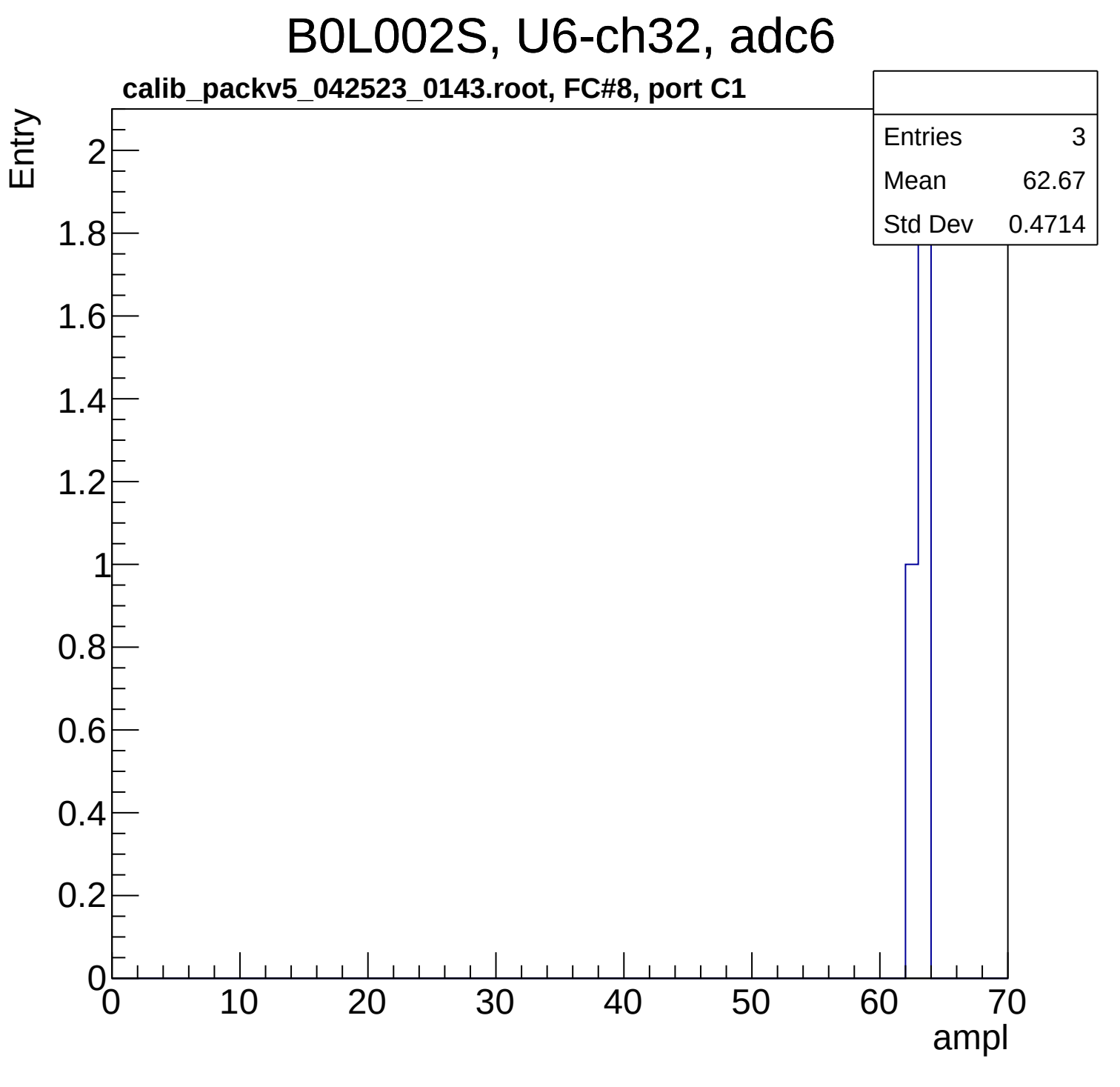
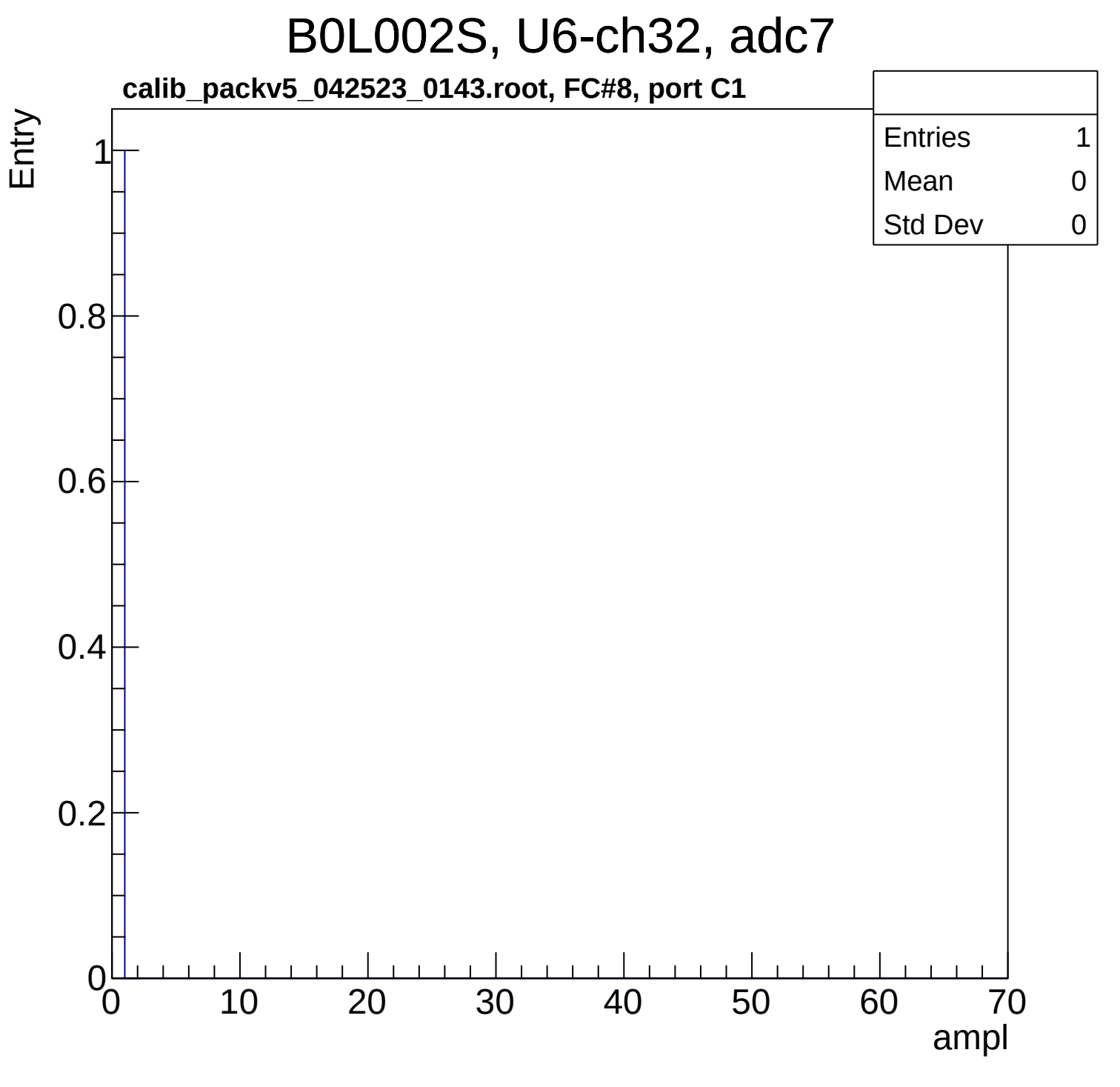
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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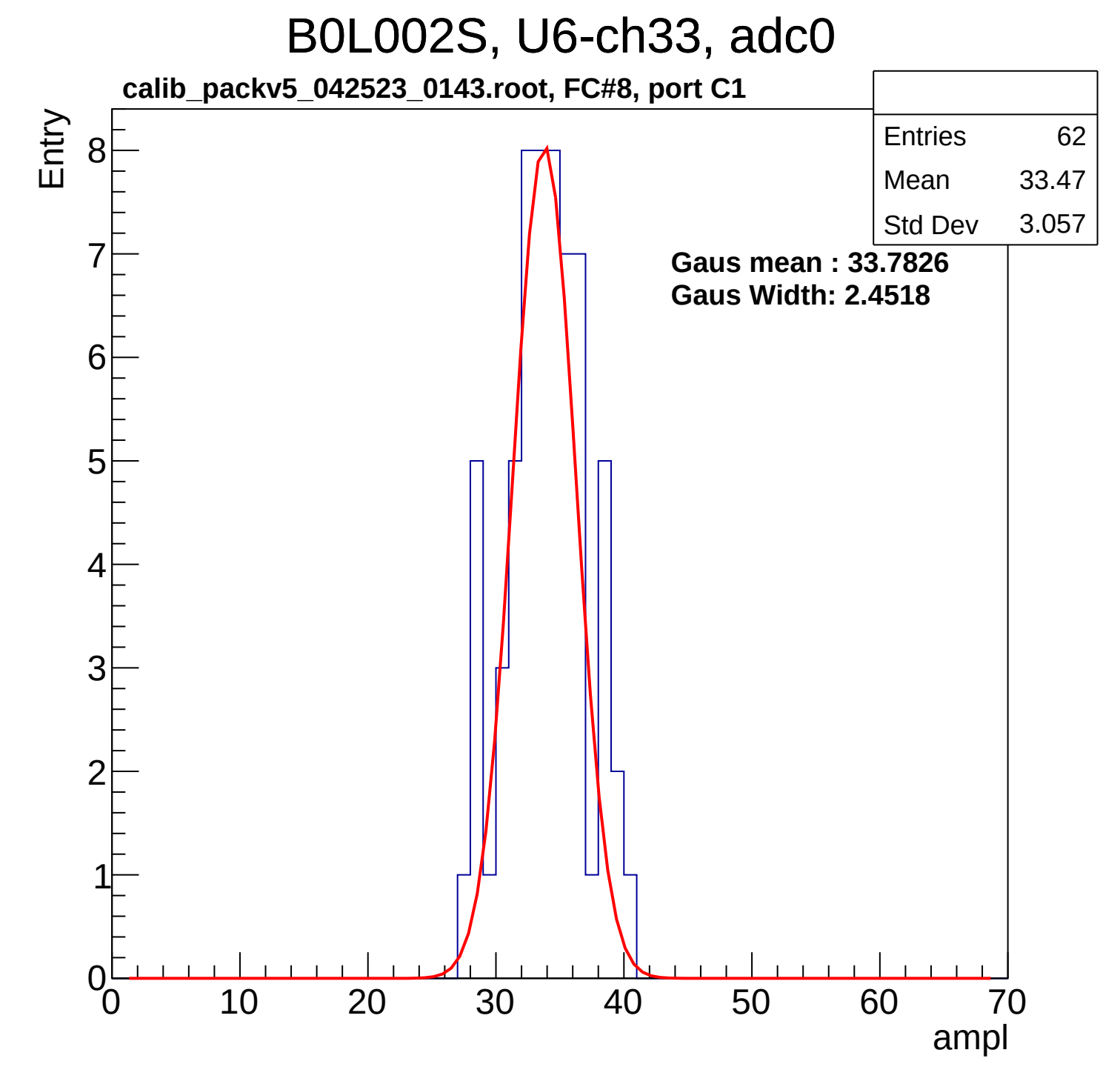
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 33.47
Std Dev 3.057
Gaus mean : 33.7826
Gaus Width: 2.4518
Entry
ampl
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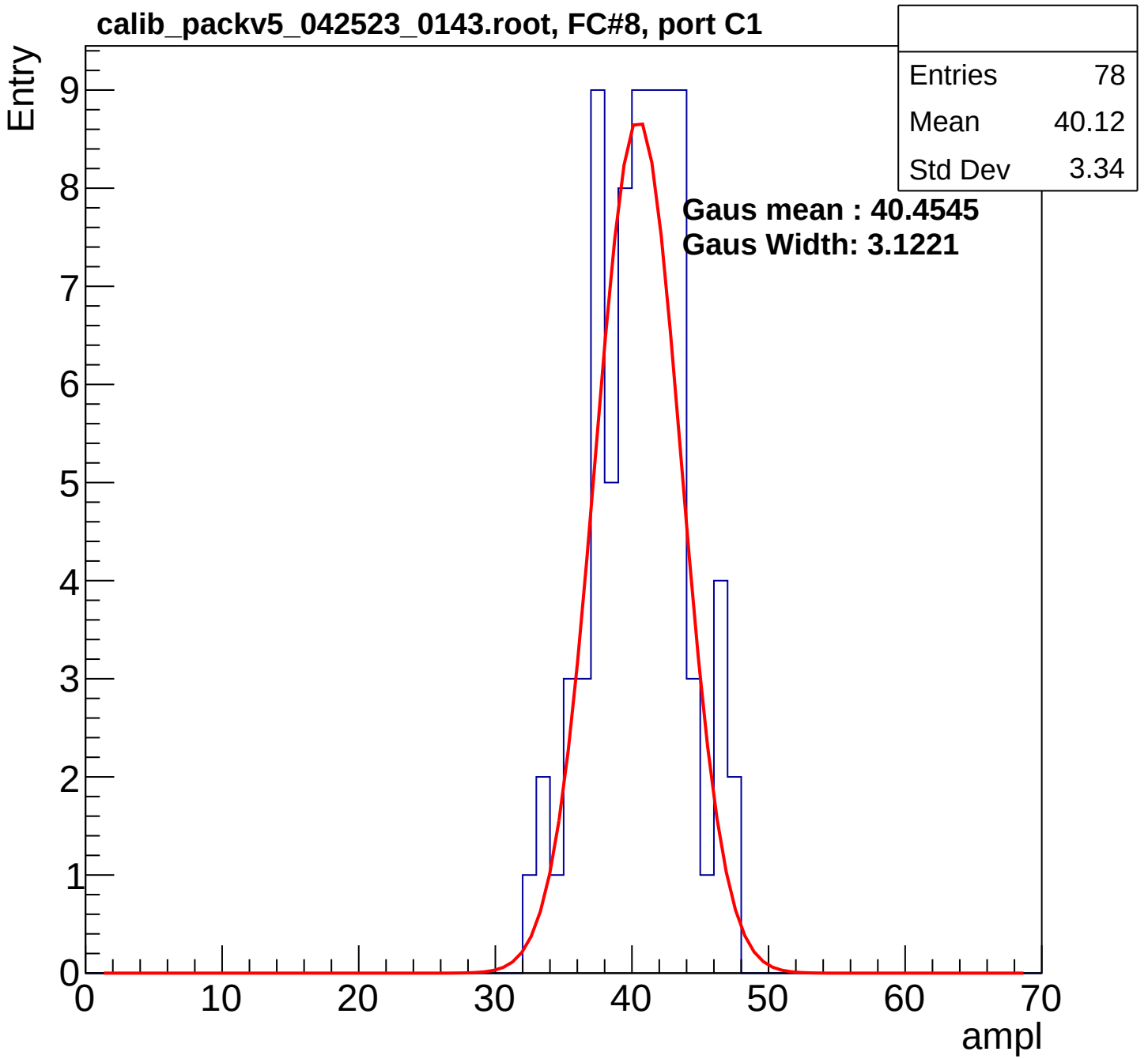
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 40.12
Std Dev 3.34
Gaus mean : 40.4545
Gaus Width: 3.1221
Entry
ampl
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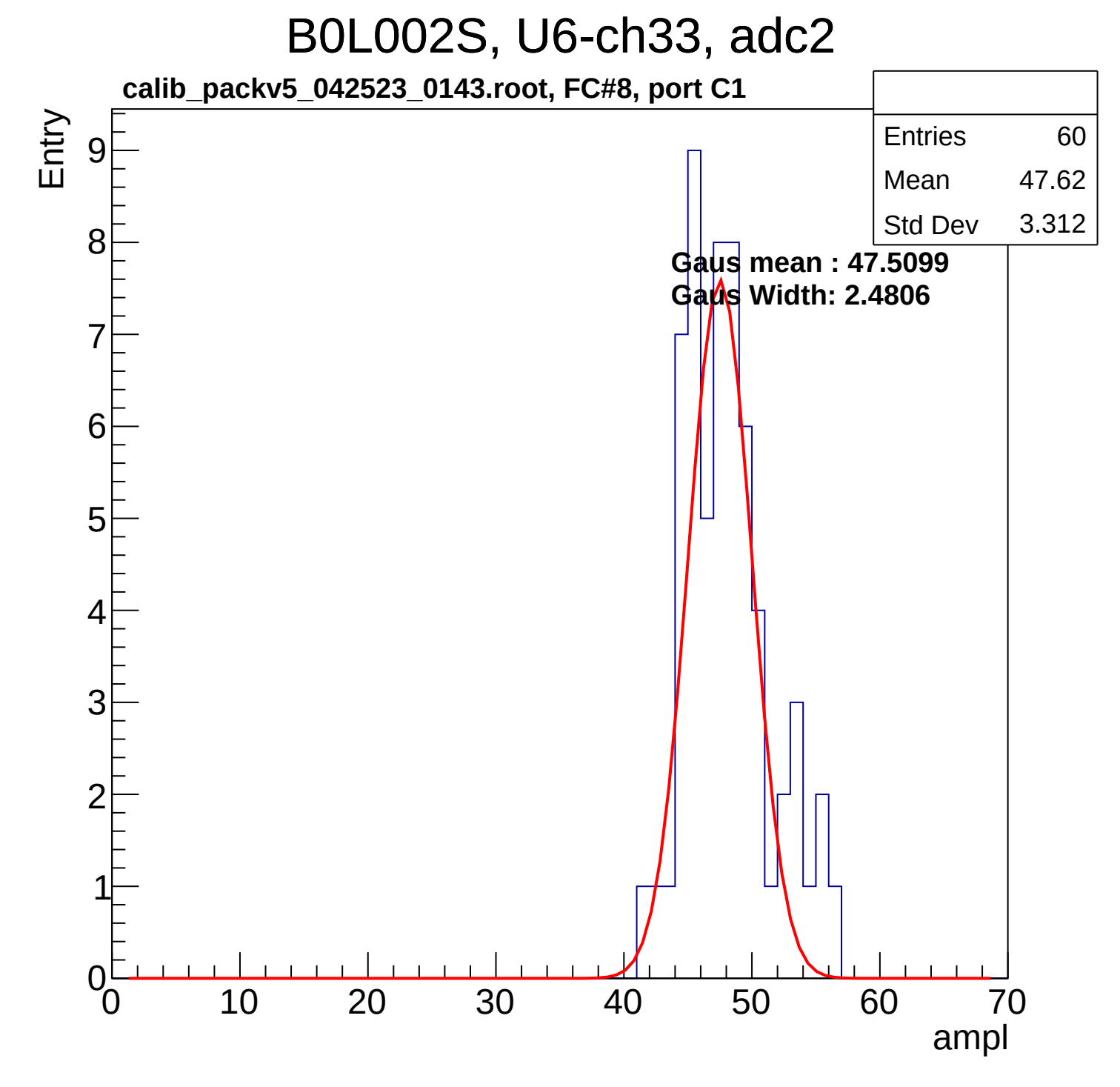
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 47.62
Std Dev 3.312
Gaus mean : 47.5099
Gaus Width: 2.4806
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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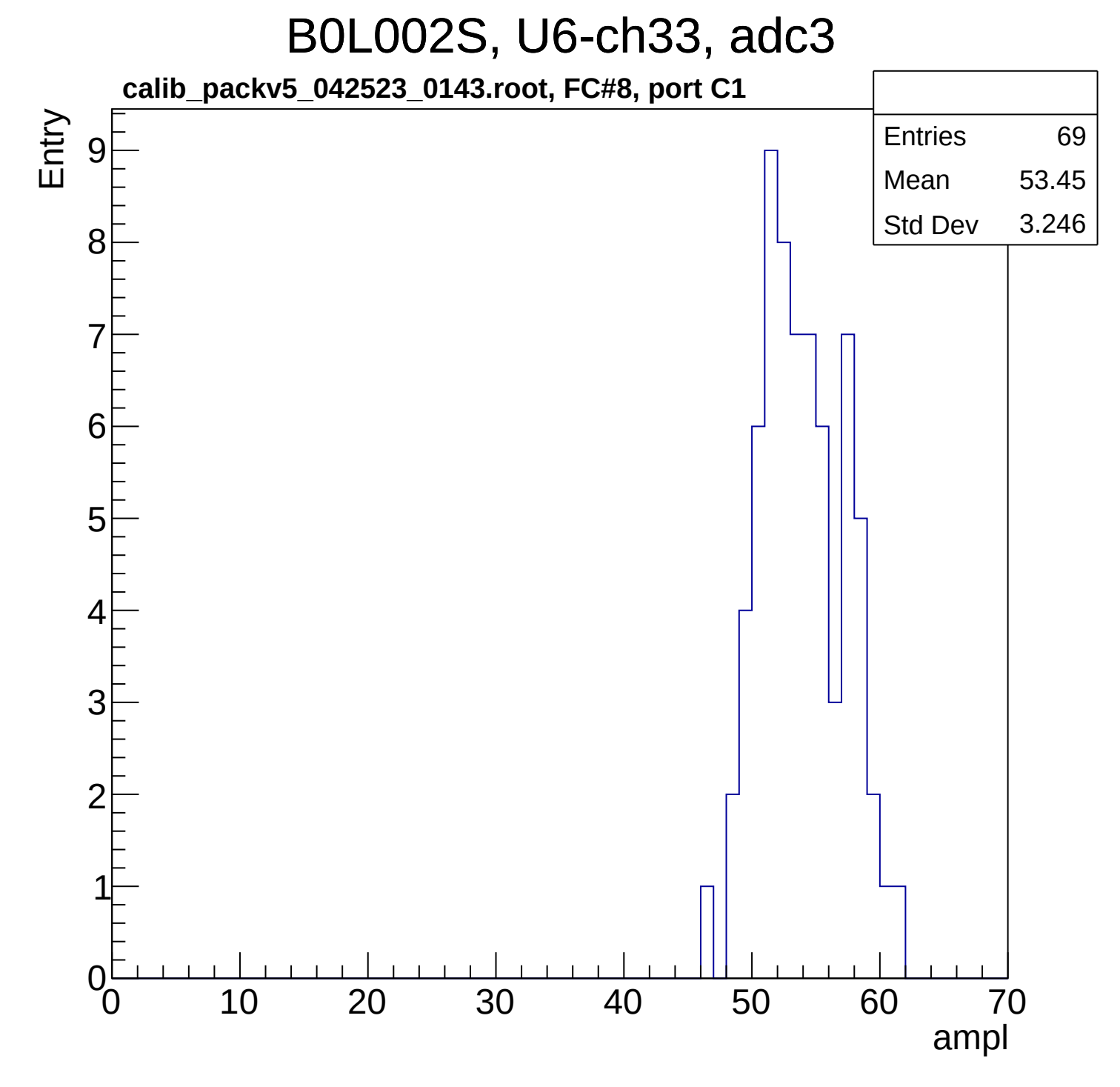
B0L002S, U6-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 53.45
Std Dev 3.246
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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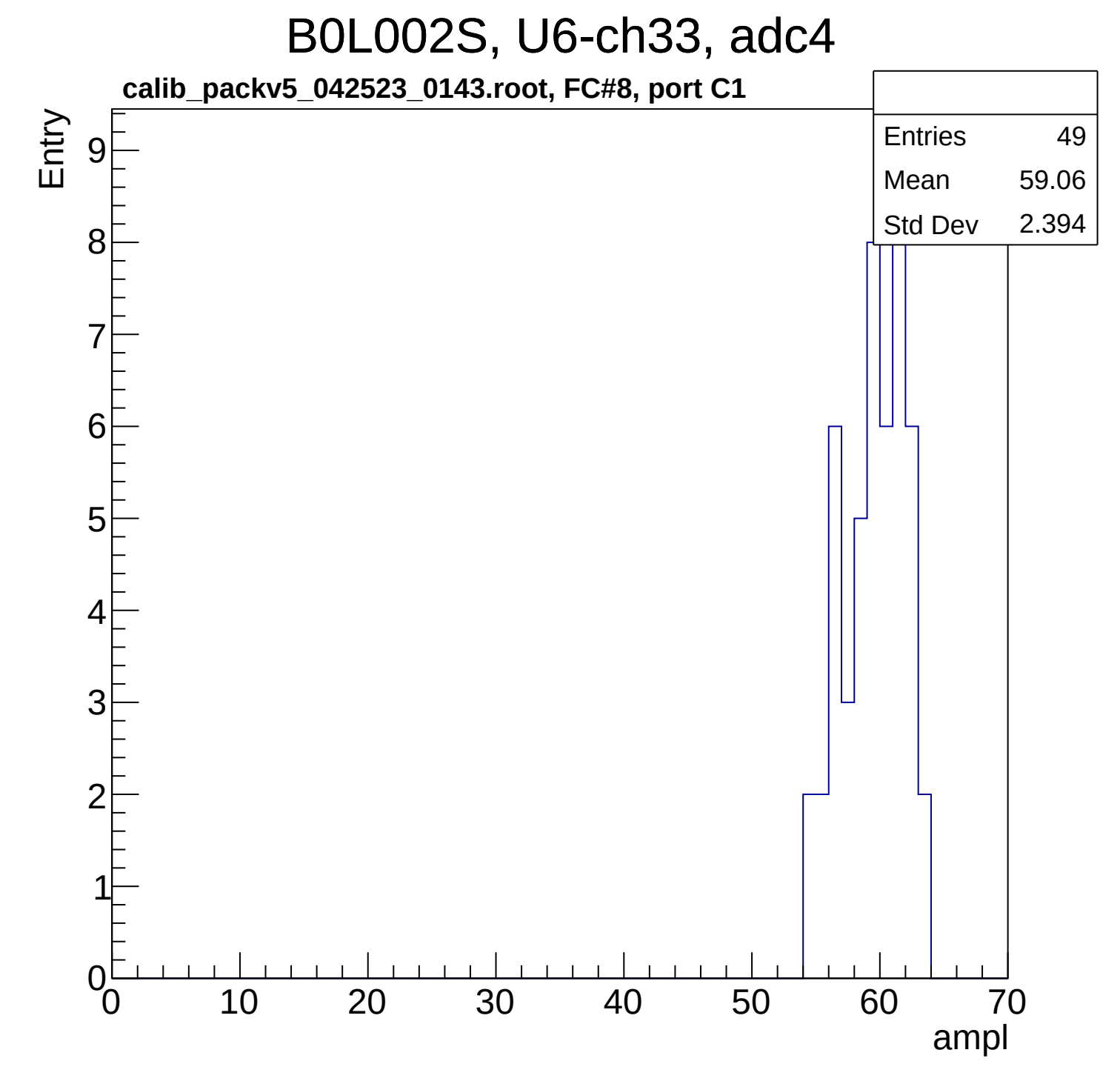
B0L002S, U6-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.06
Std Dev 2.394
Entry
ampl
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2
3
4
5
6
7
8
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0
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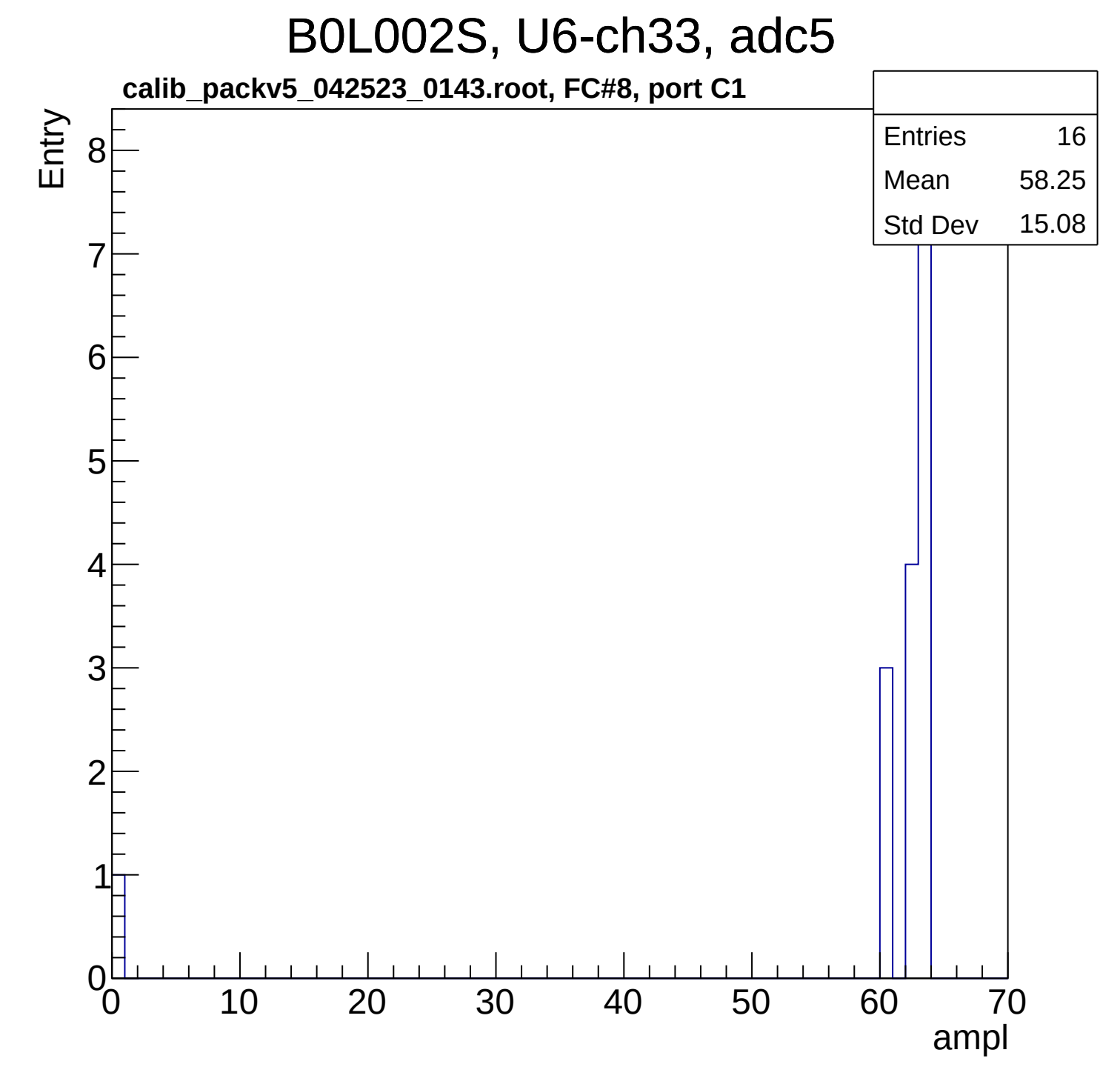
B0L002S, U6-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 58.25
Std Dev 15.08
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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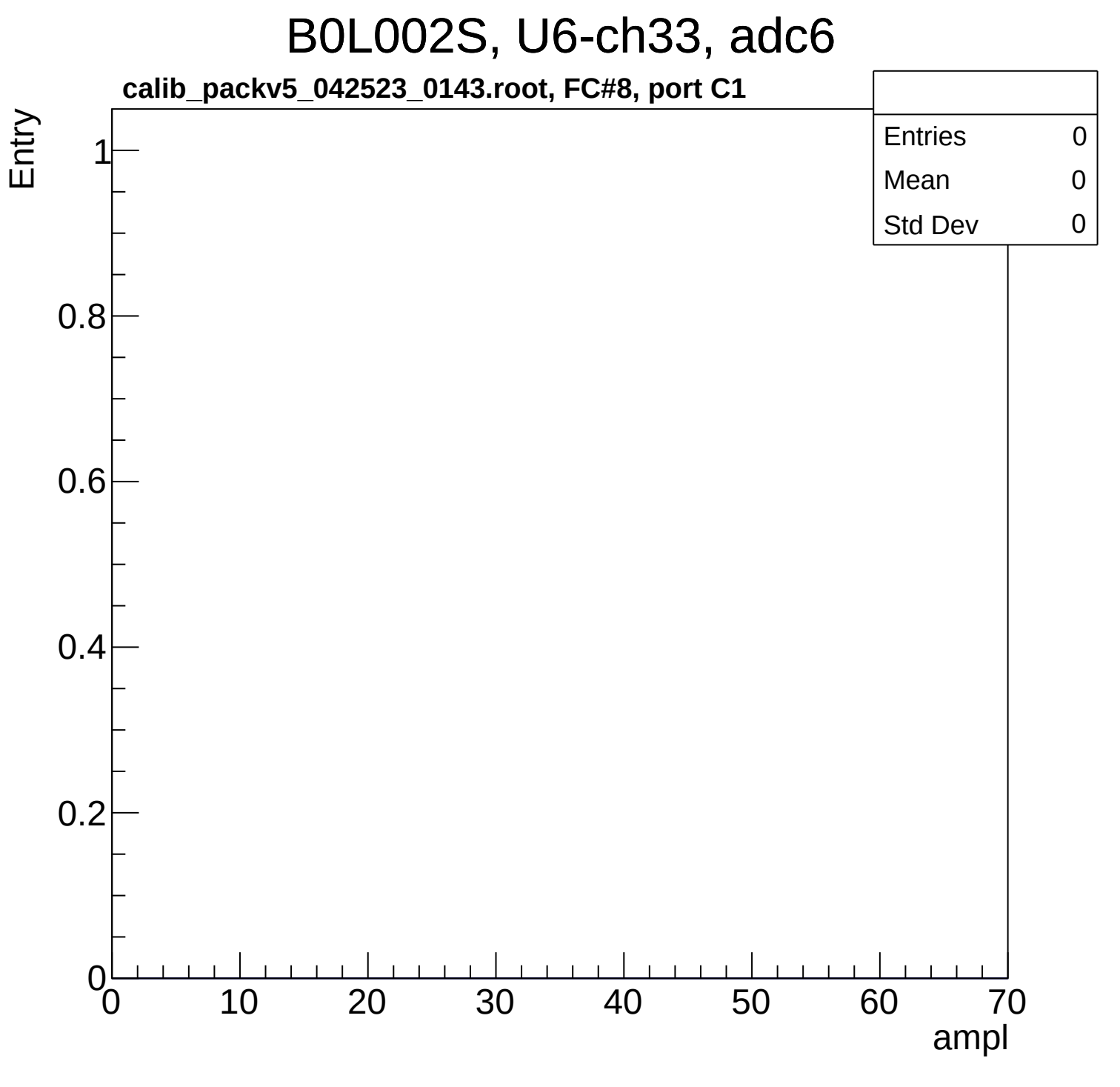
40
50
60
70


B0L002S, U6-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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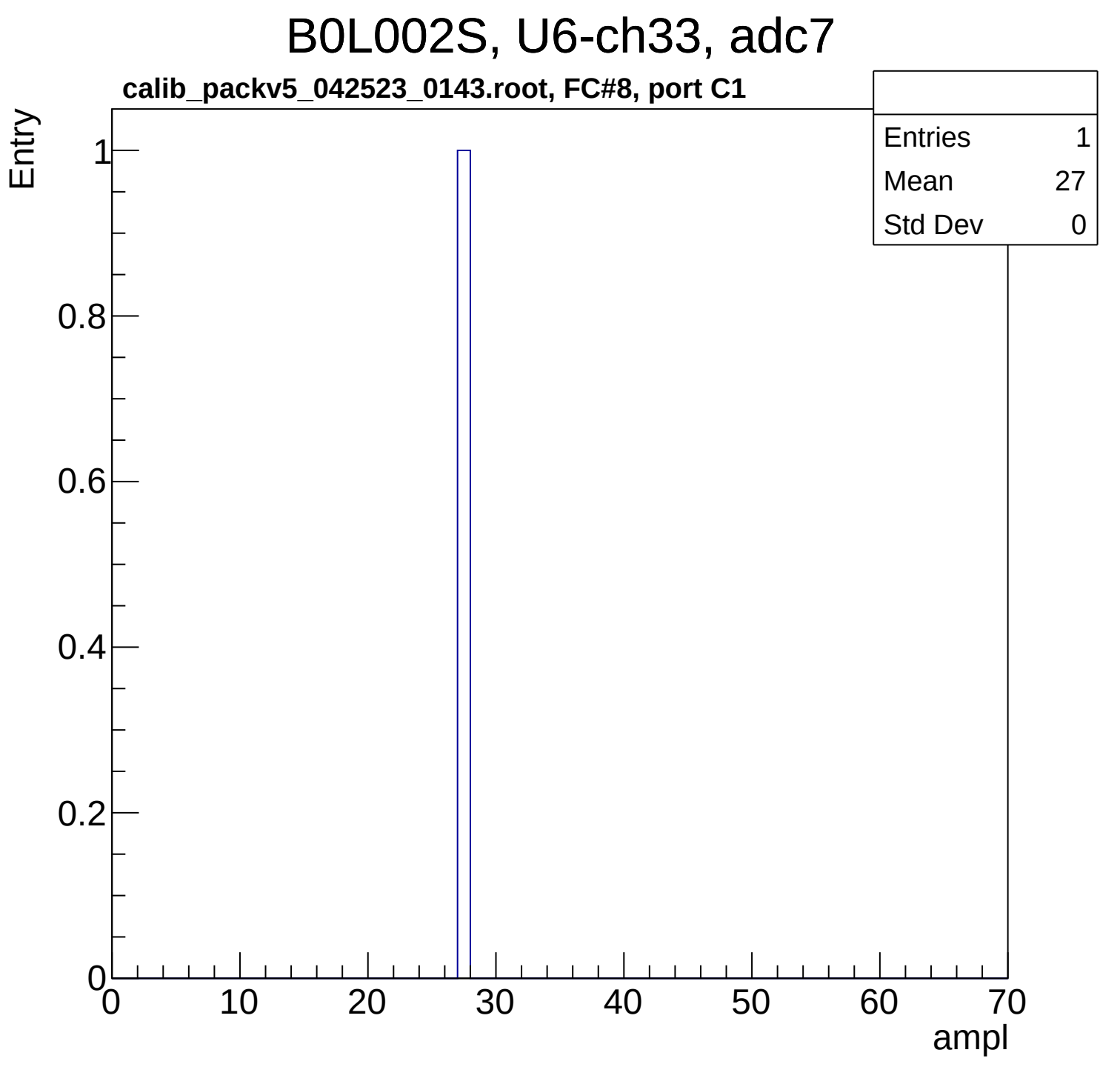
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 27
Std Dev 0
Entry
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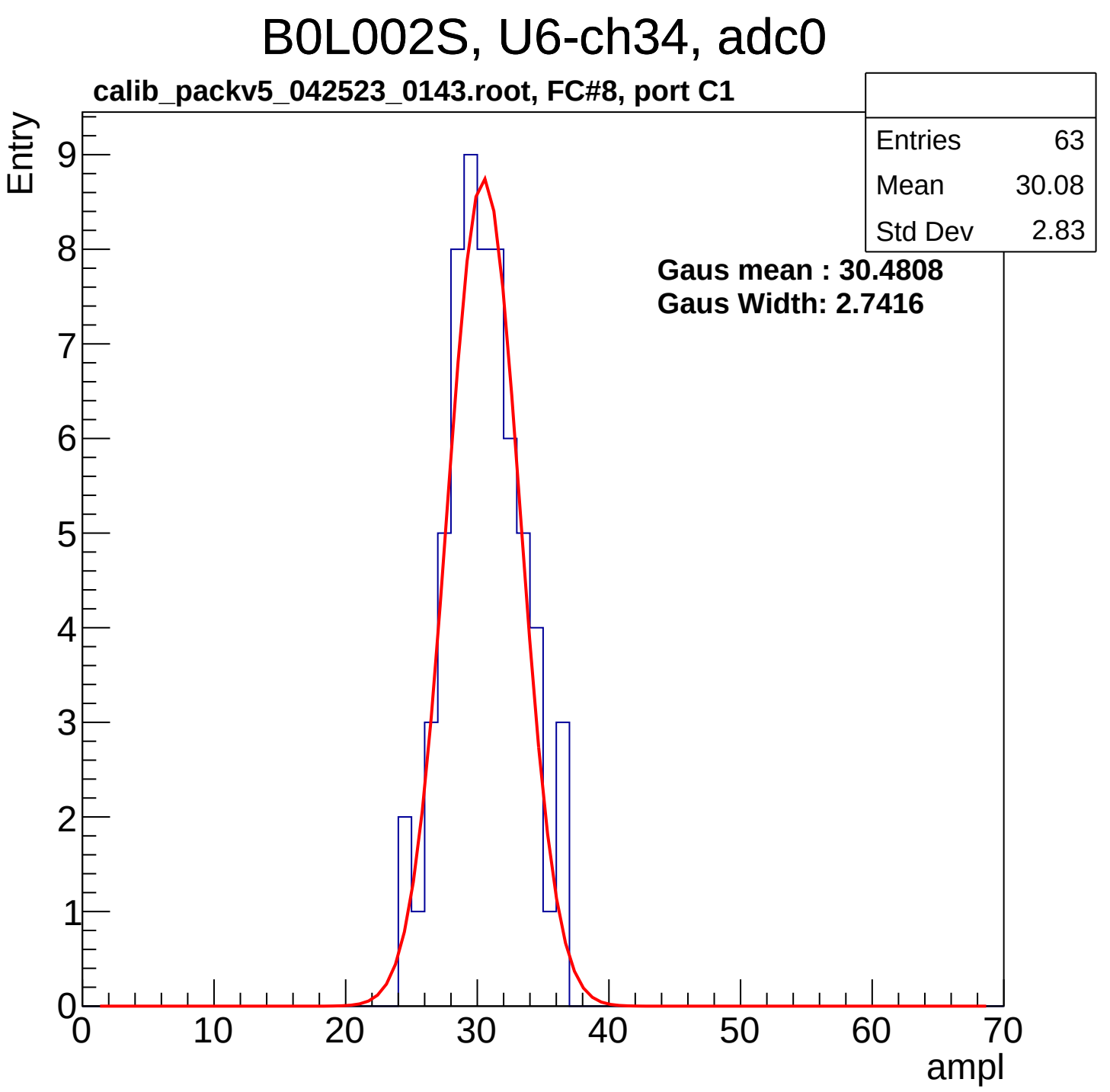
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.08
Std Dev 2.83
Gaus mean : 30.4808
Gaus Width: 2.7416
Entry
ampl
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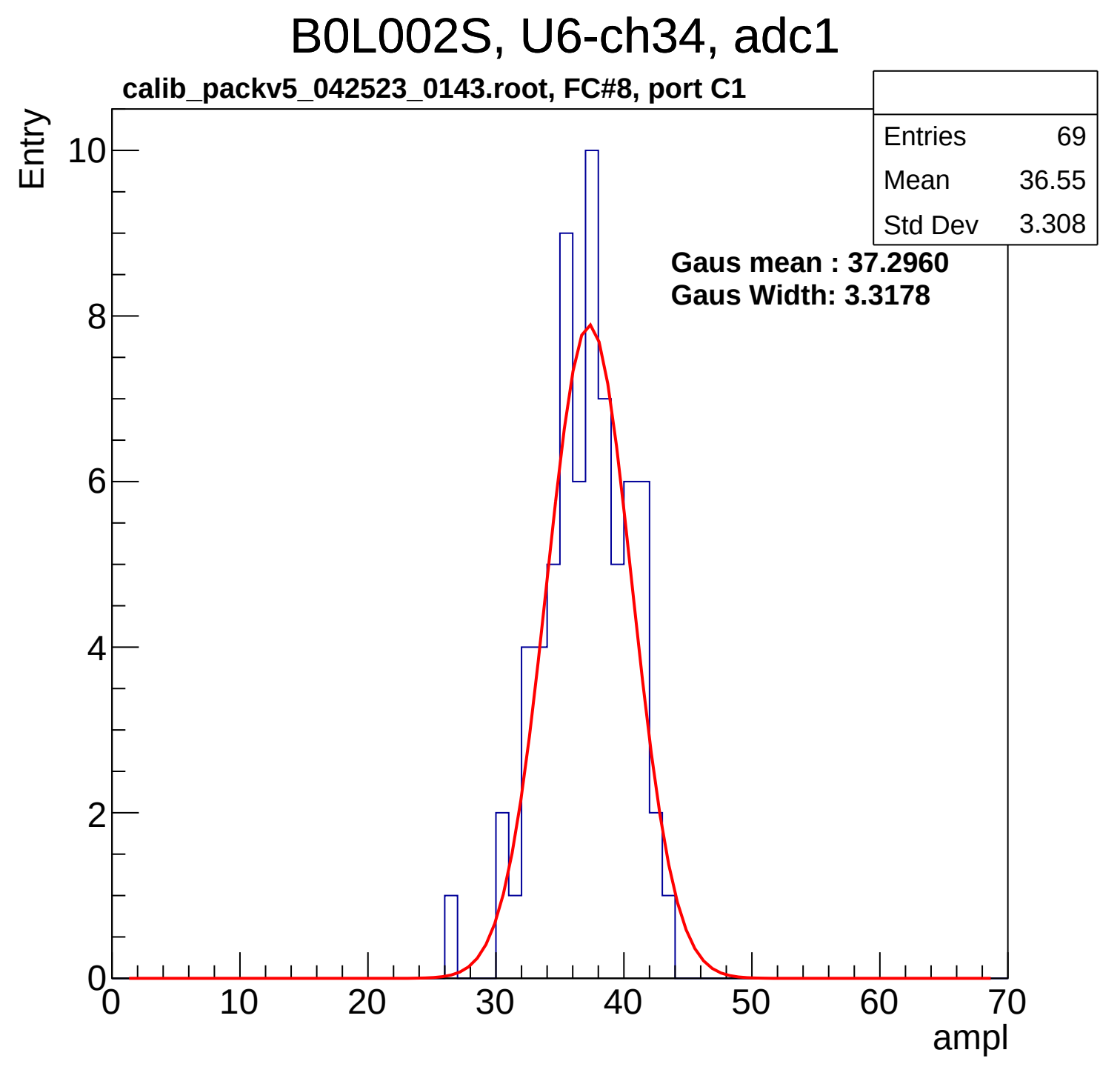
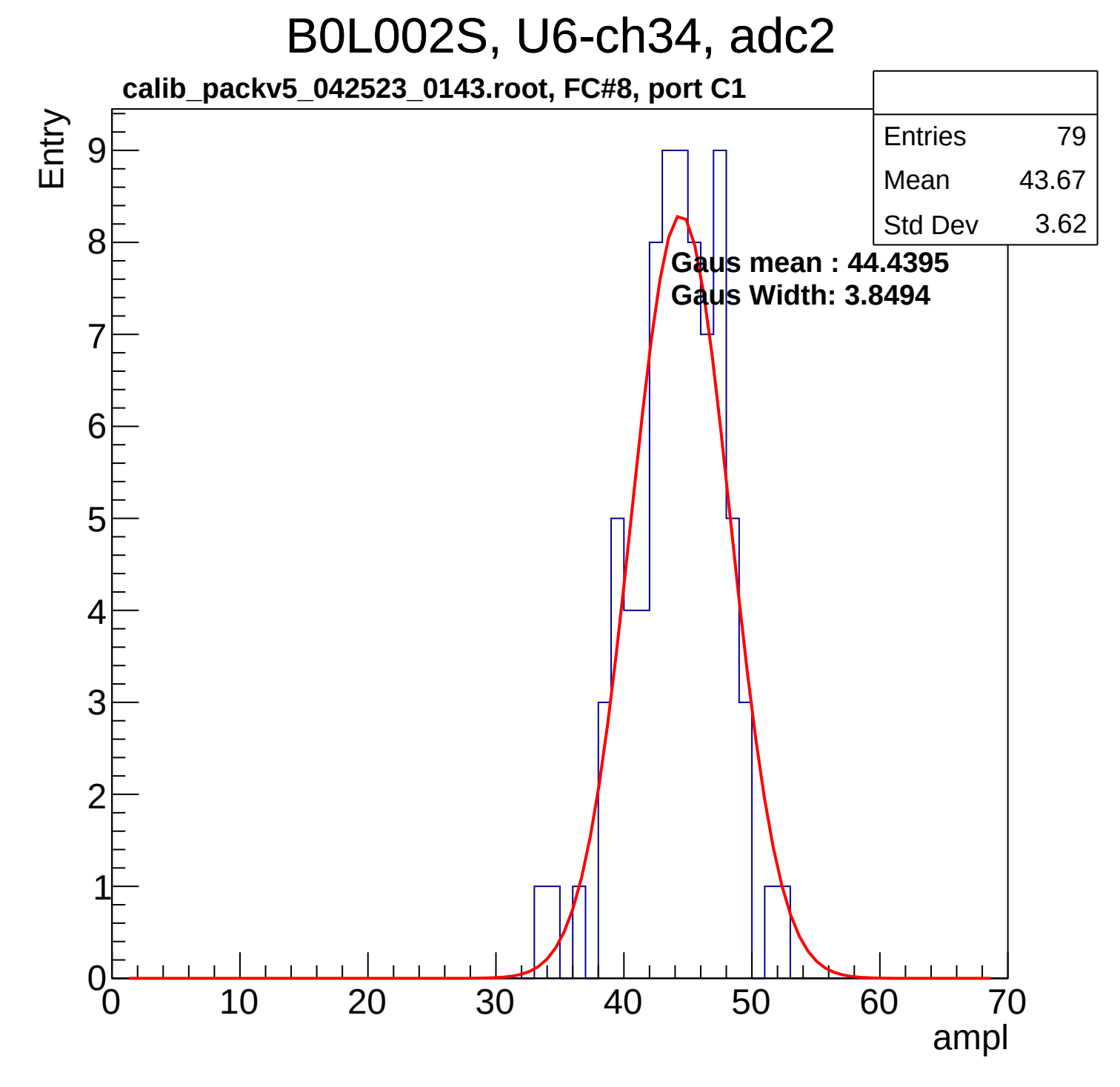
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.55
Std Dev 3.308
Gaus mean : 37.2960
Gaus Width: 3.3178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 43.67
Std Dev 3.62
Gaus mean : 44.4395
Gaus Width: 3.8494
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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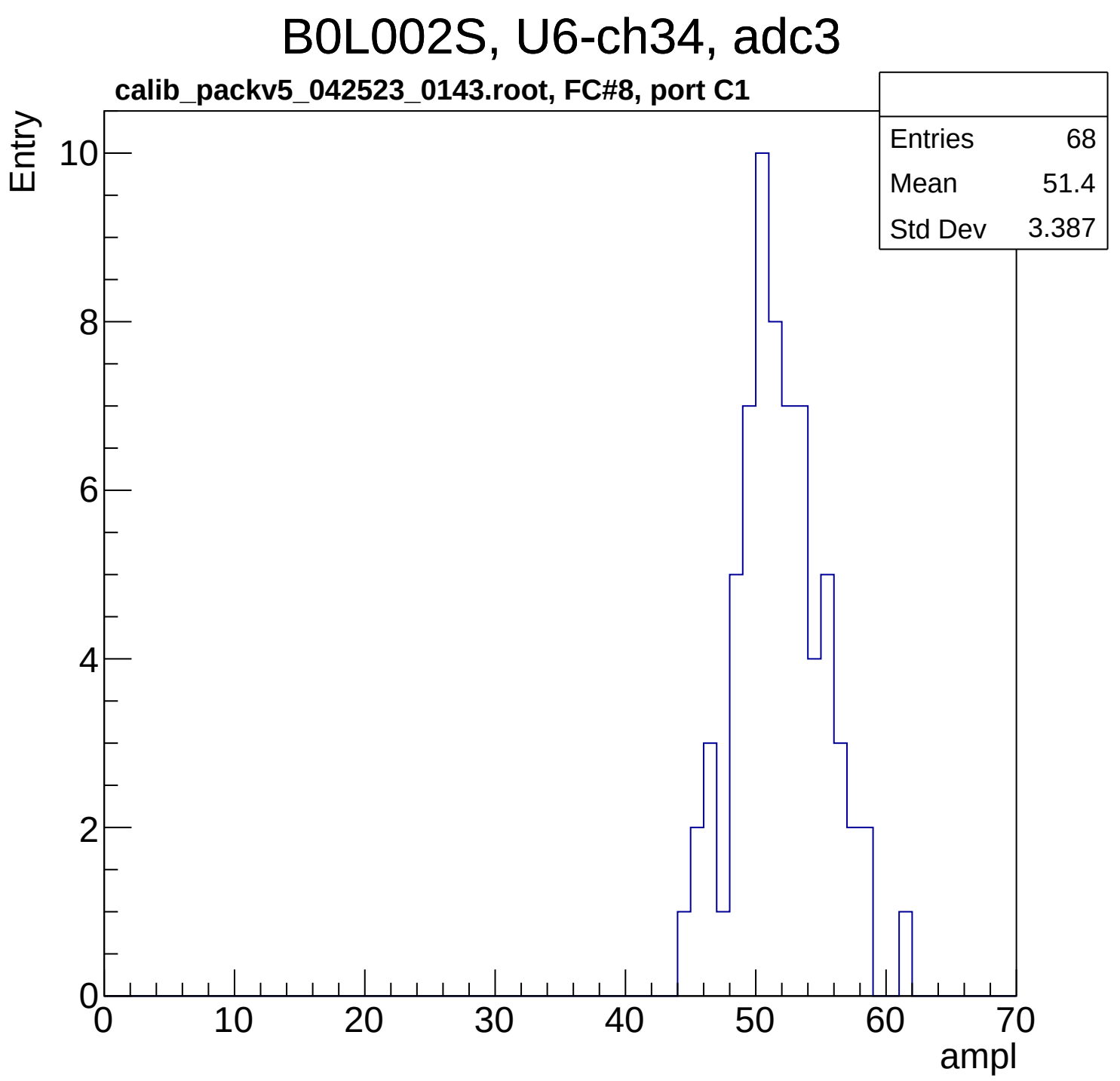
B0L002S, U6-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.4
Std Dev 3.387
Entry
ampl
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2
4
6
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10
0
10
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30
40
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60
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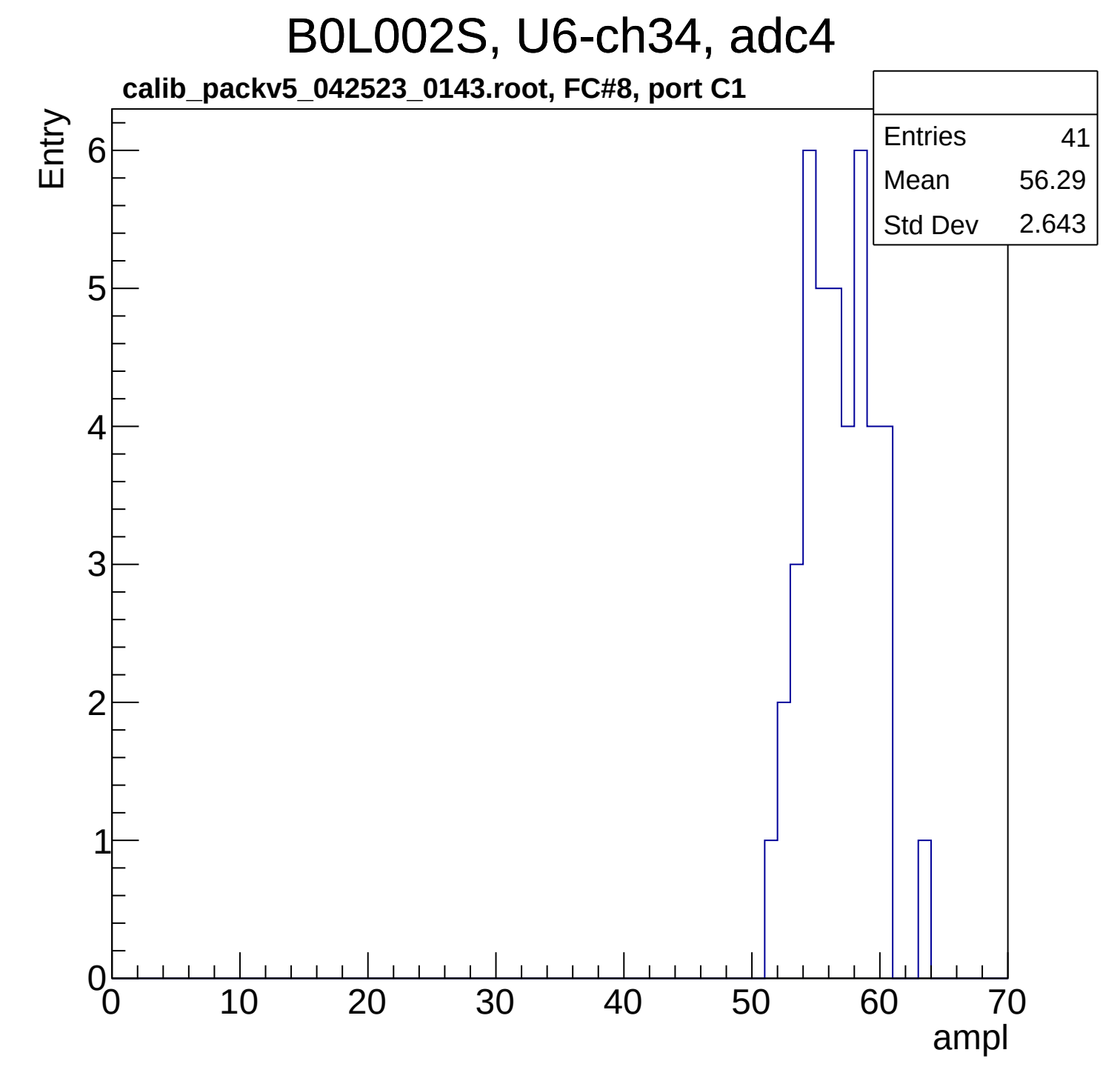
B0L002S, U6-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 56.29
Std Dev 2.643
Entry
ampl
0
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3
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0
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40
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60
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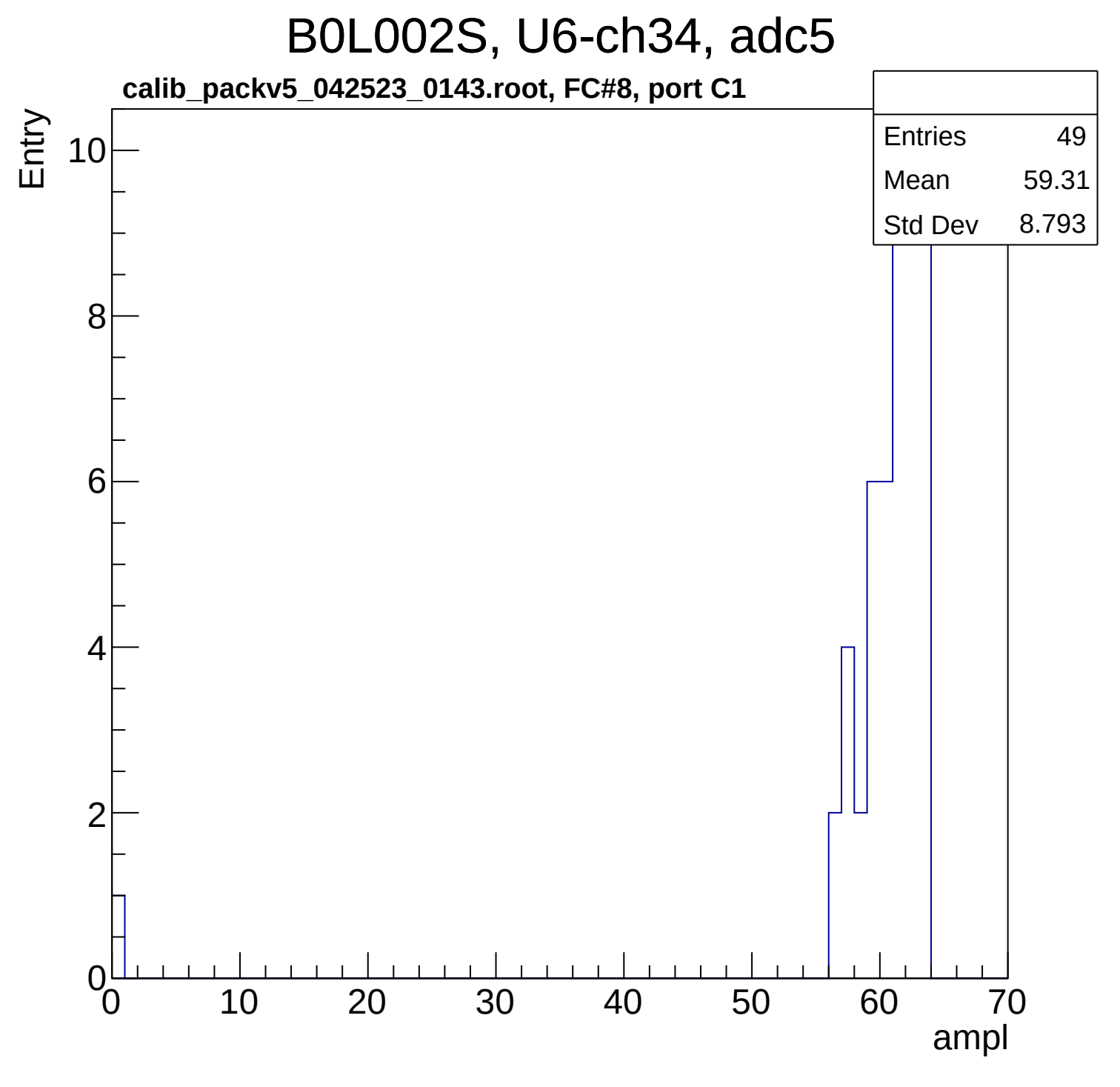
B0L002S, U6-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.31
Std Dev 8.793
Entry
0
2
4
6
8
10
0
10
20
30
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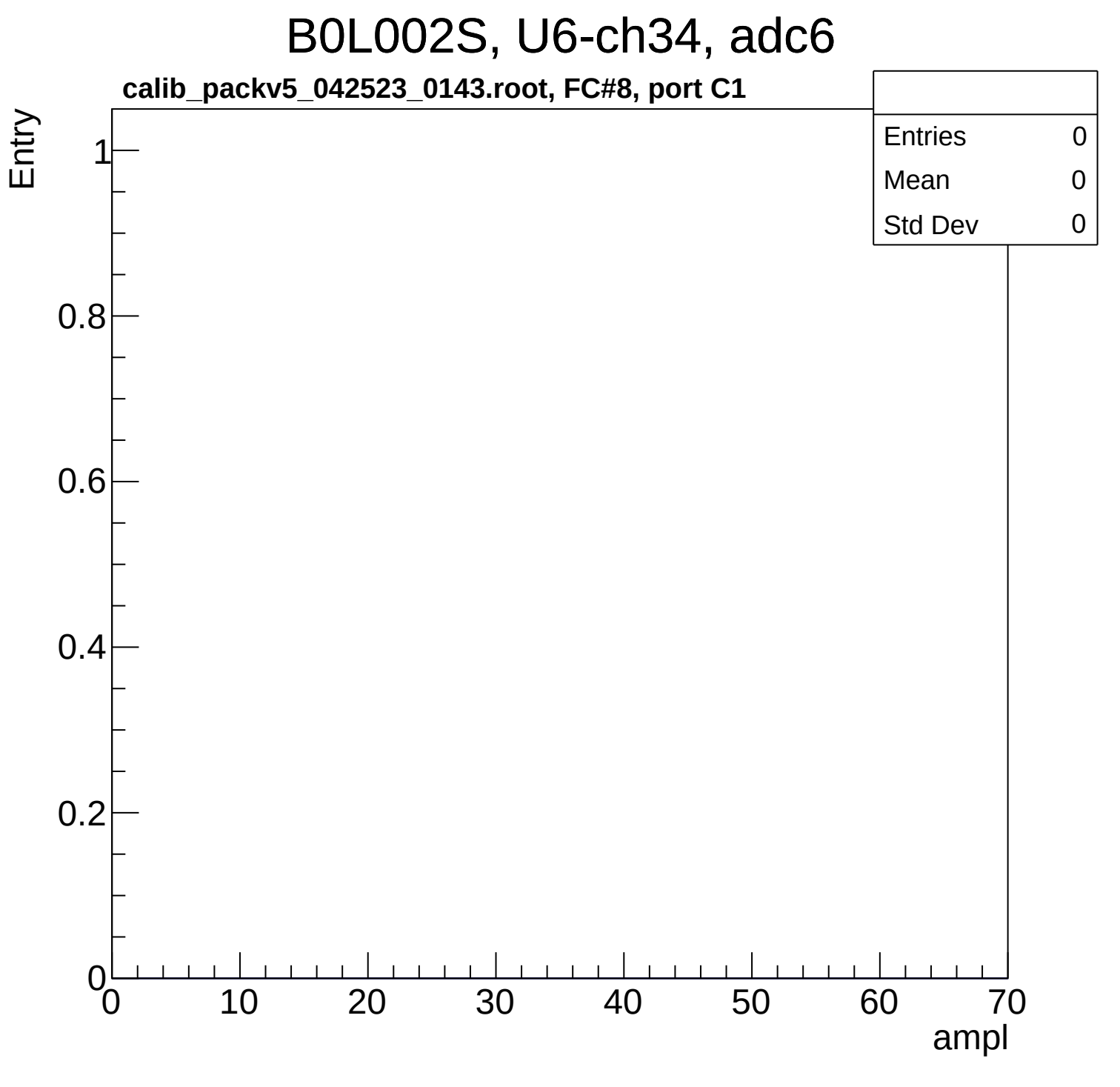
50
60
70
ampl


B0L002S, U6-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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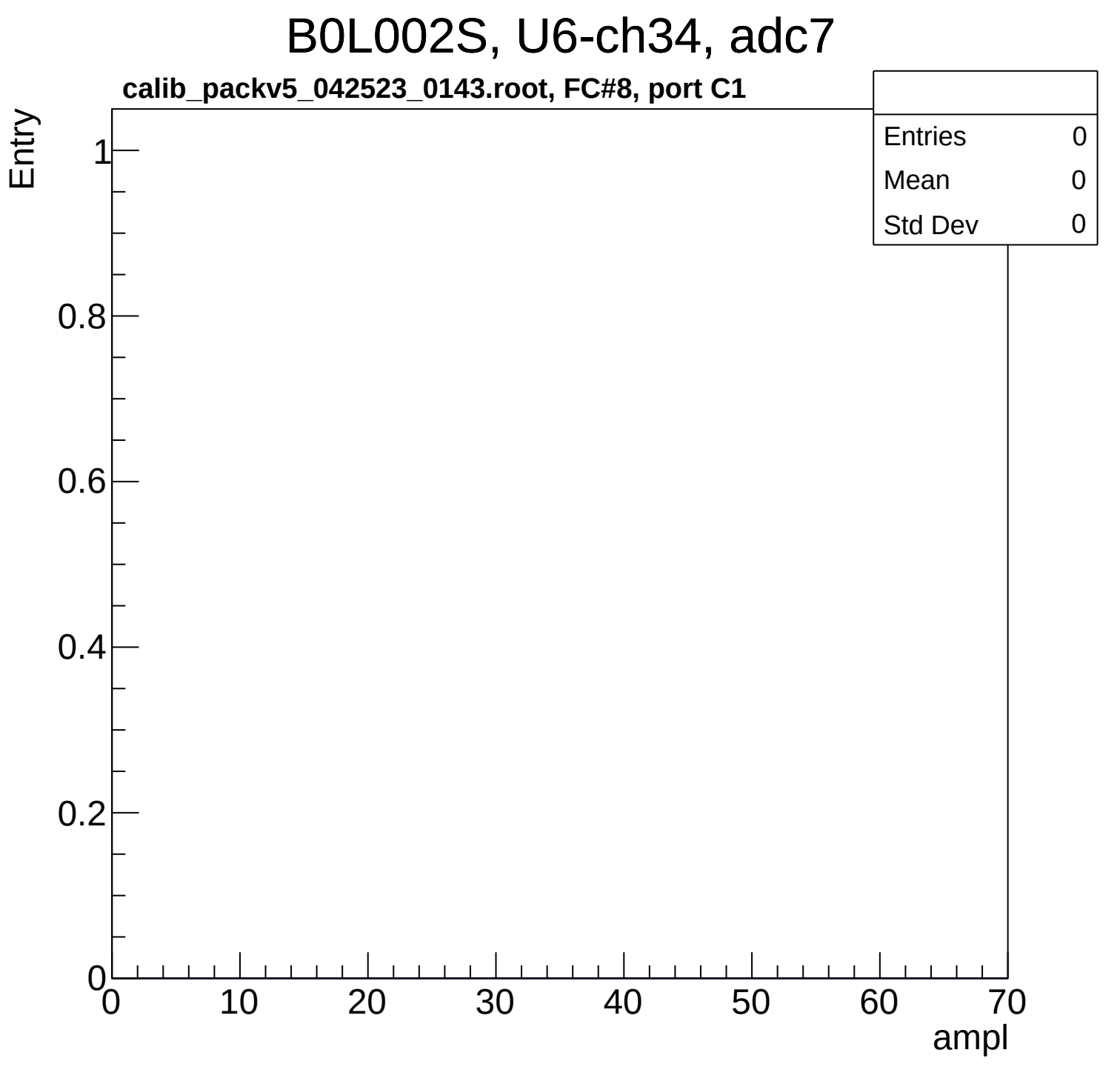
50
60
70
ampl


B0L002S, U6-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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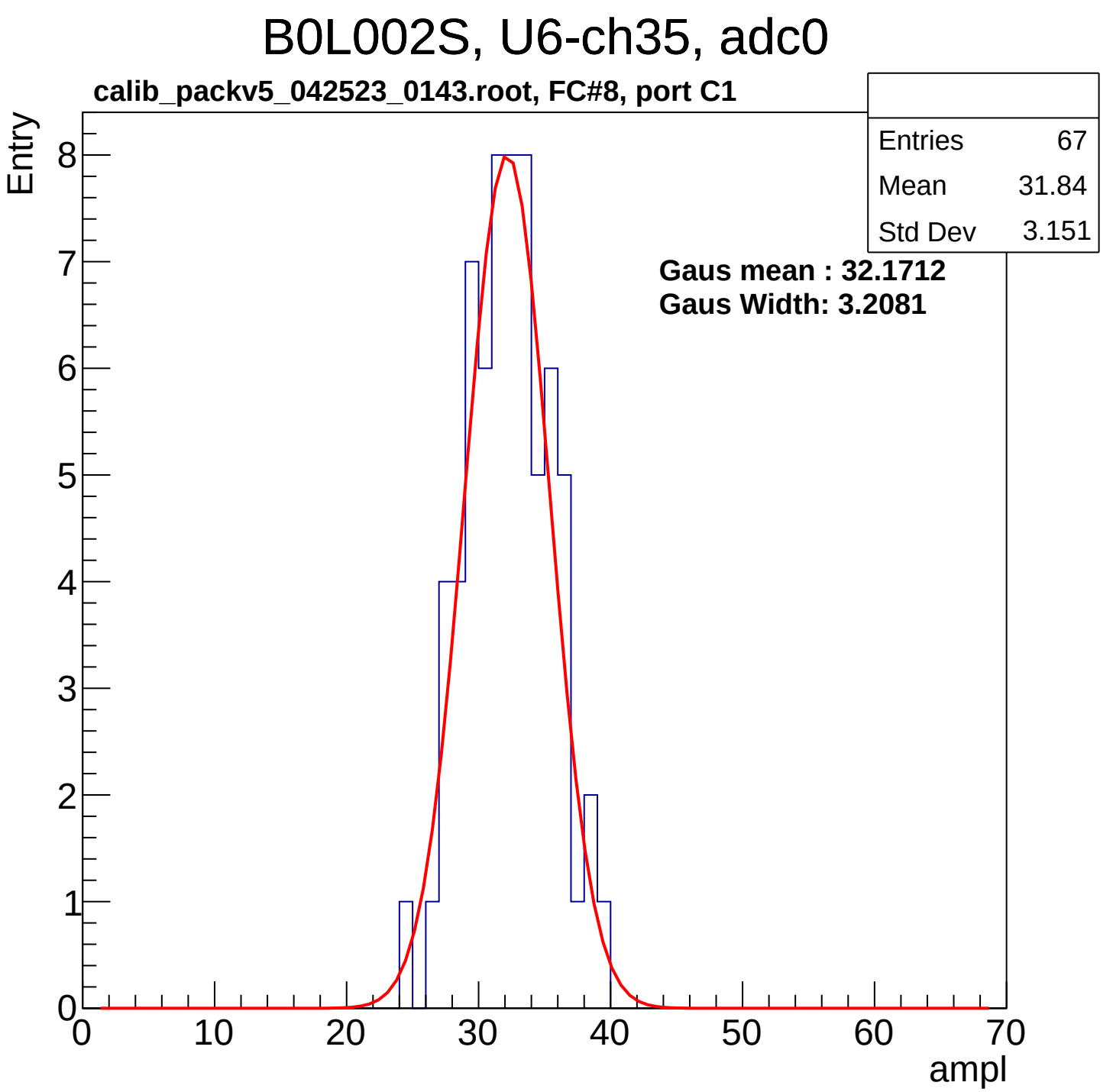
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.84
Std Dev 3.151
Gaus mean : 32.1712
Gaus Width: 3.2081
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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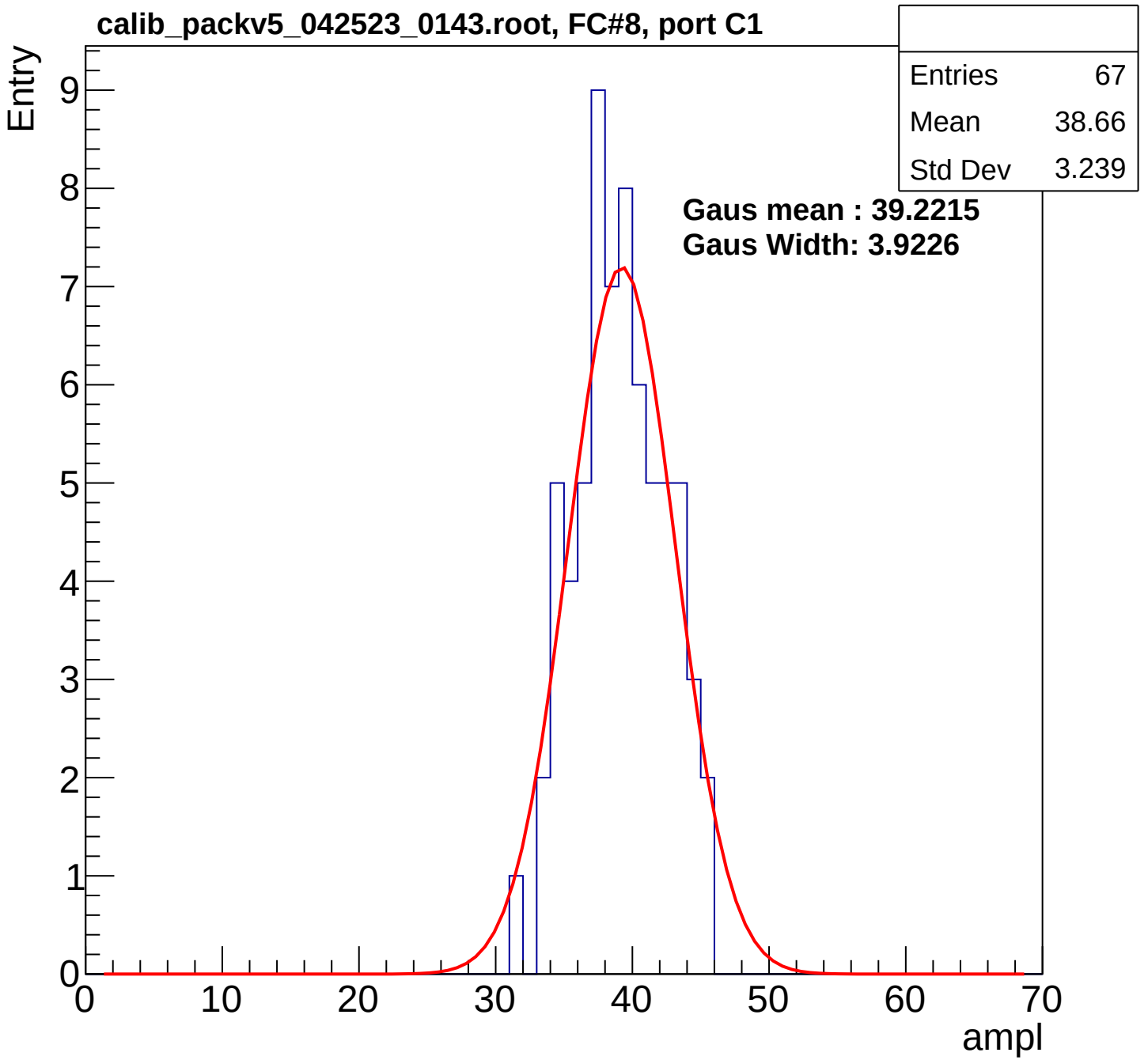
B0L002S, U6-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.66
Std Dev 3.239
Gaus mean : 39.2215
Gaus Width: 3.9226
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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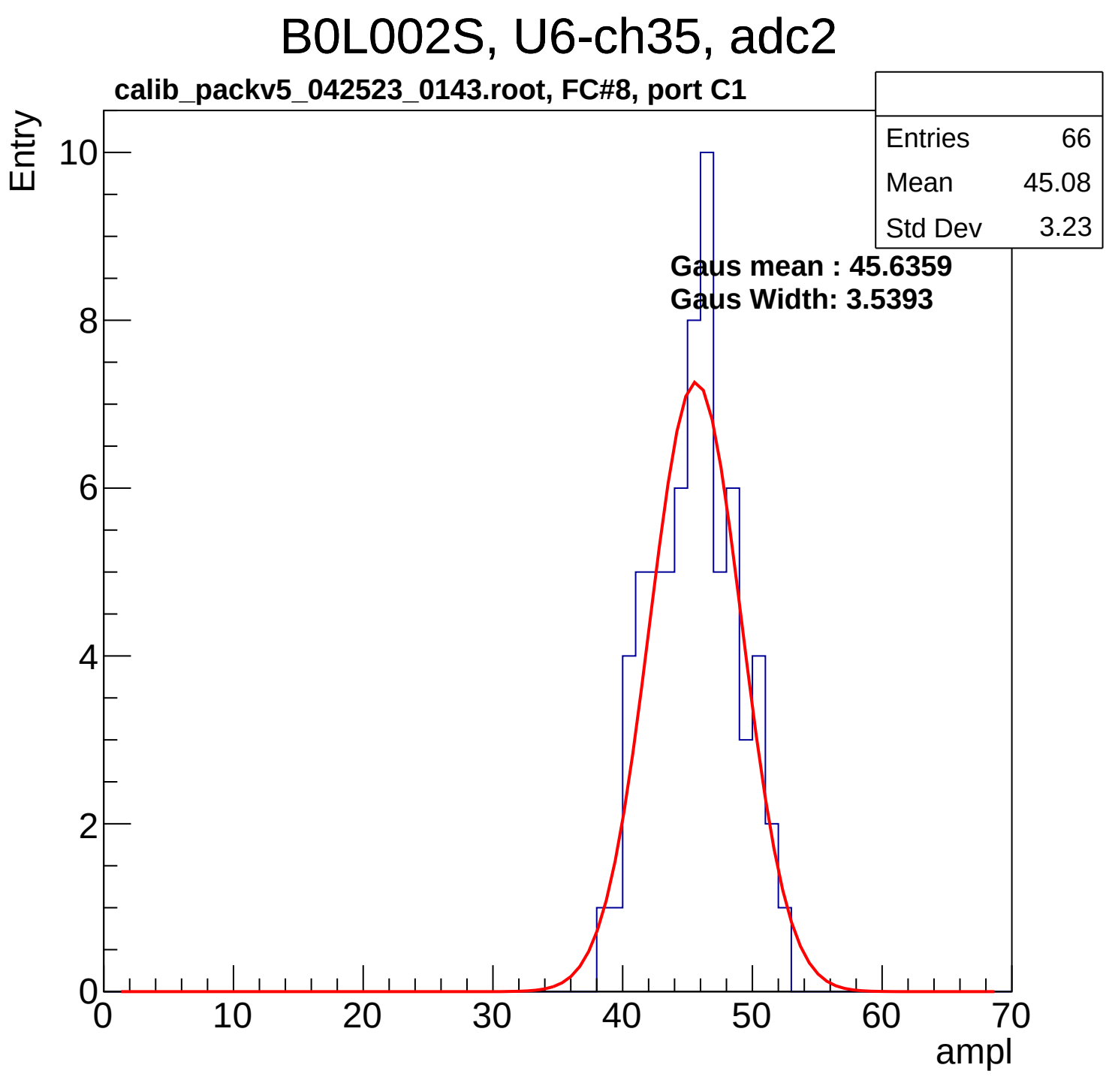
B0L002S, U6-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.08
Std Dev 3.23
Gaus mean : 45.6359
Gaus Width: 3.5393
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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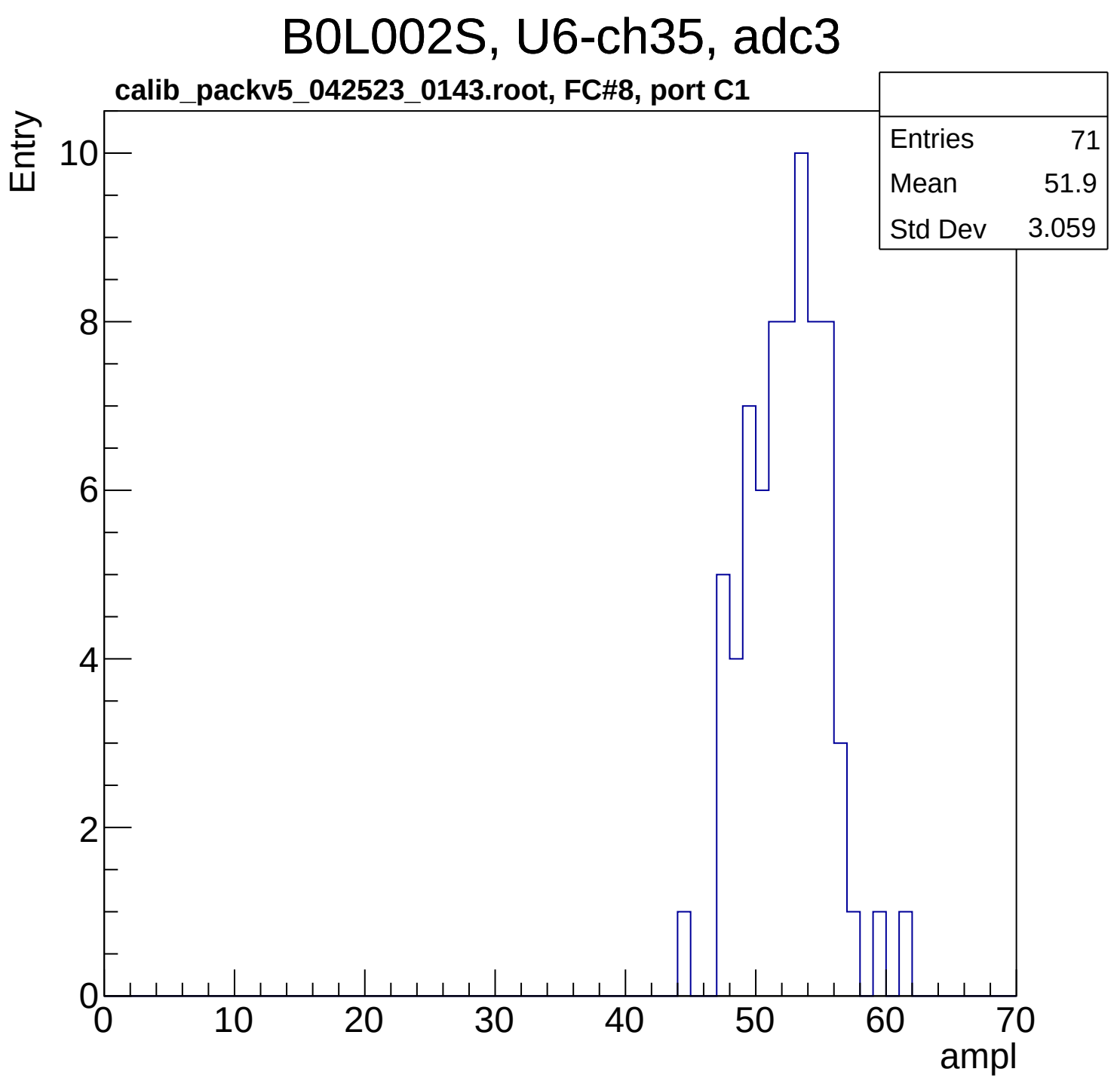
B0L002S, U6-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.9
Std Dev 3.059
Entry
0
2
4
6
8
10
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10
20
30
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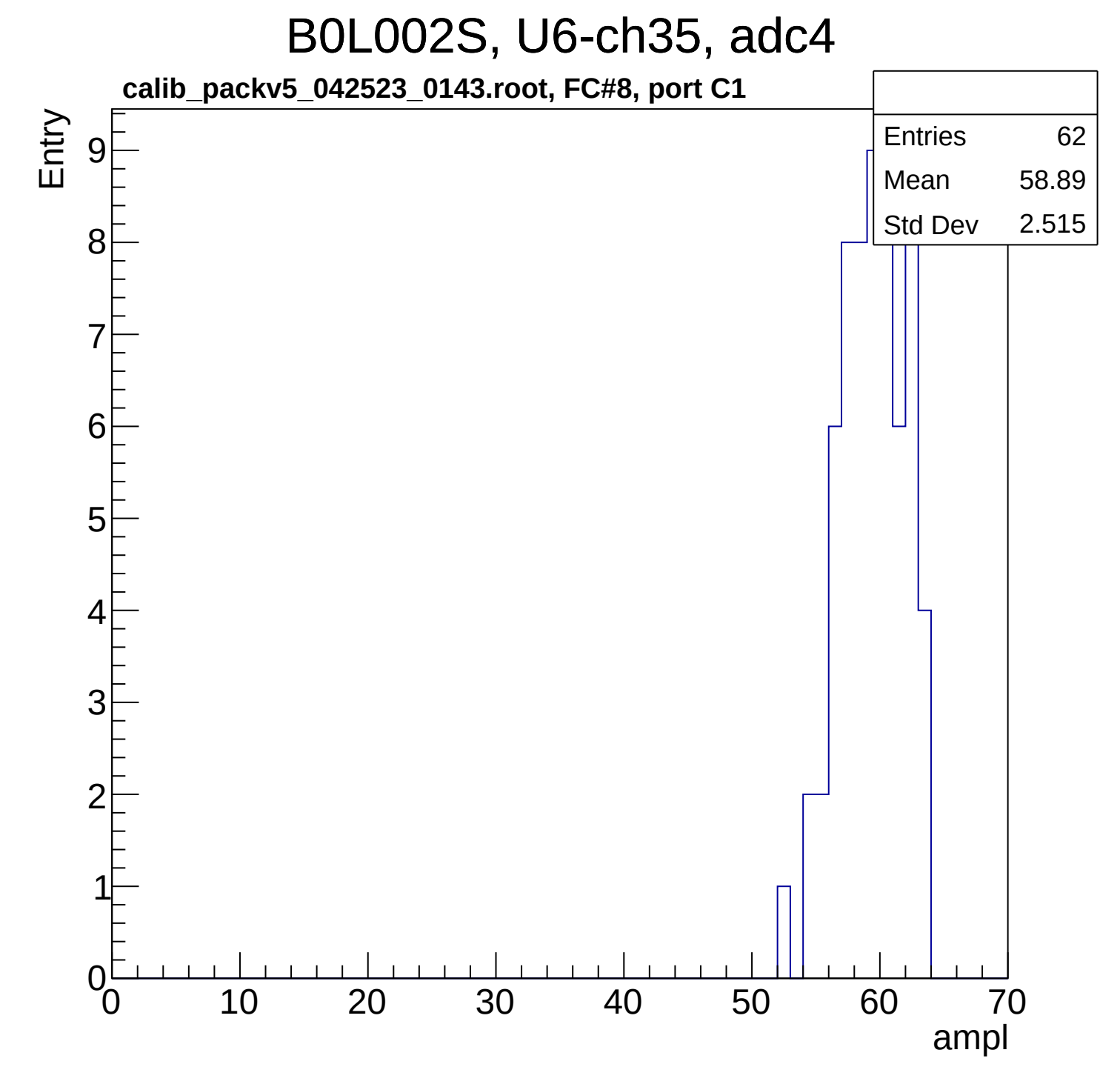
50
60
70
ampl


B0L002S, U6-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 58.89
Std Dev 2.515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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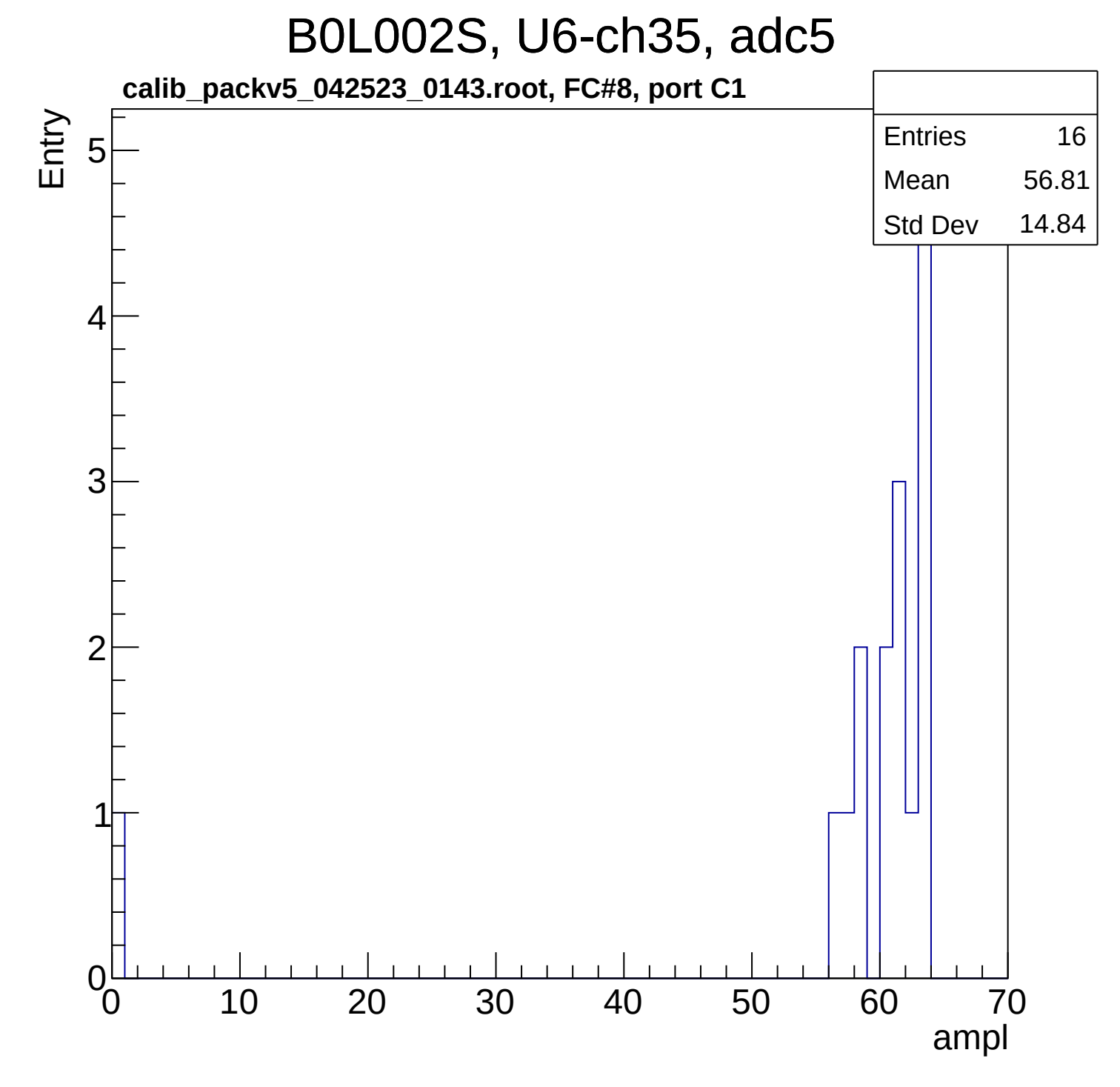
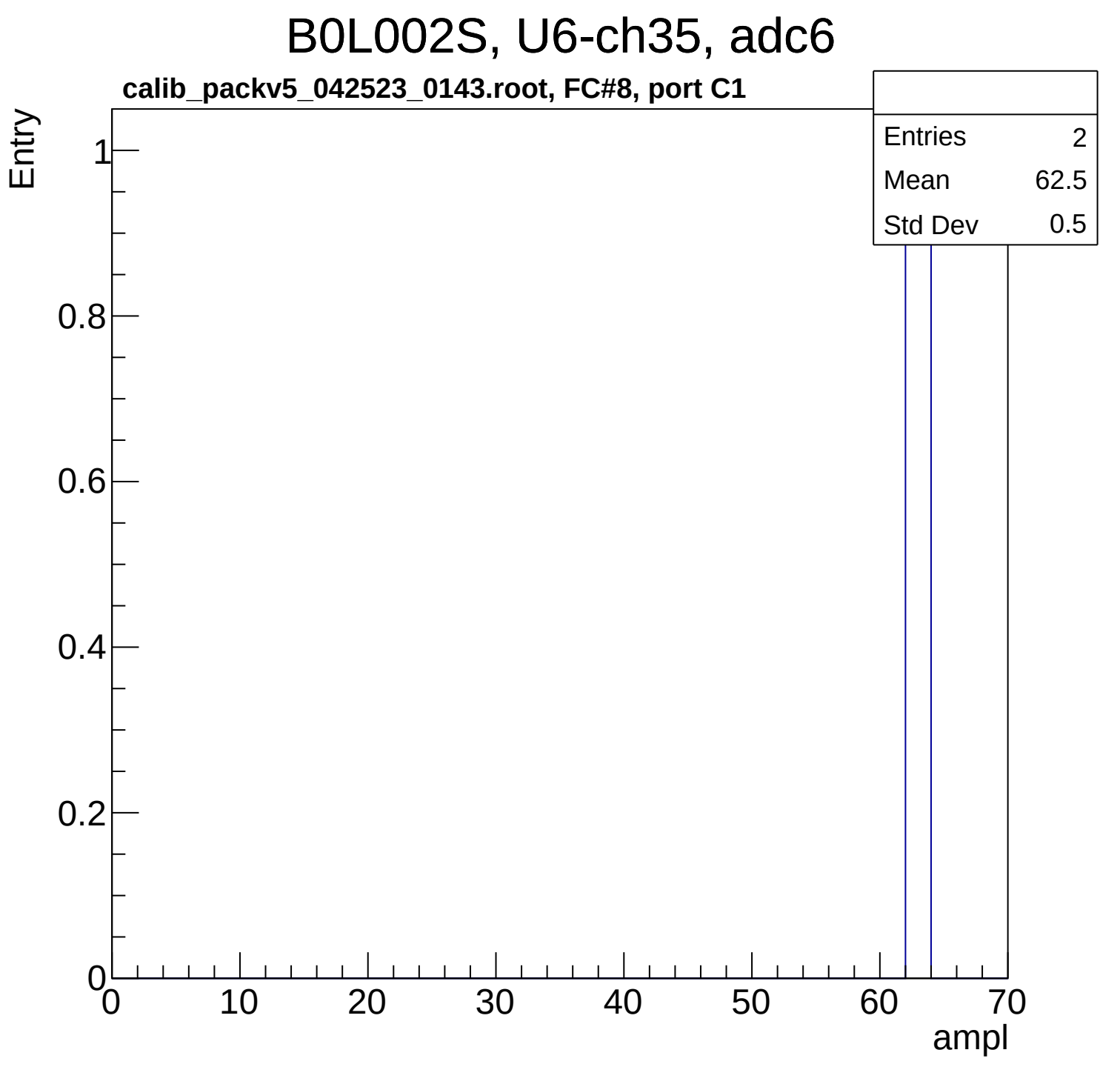
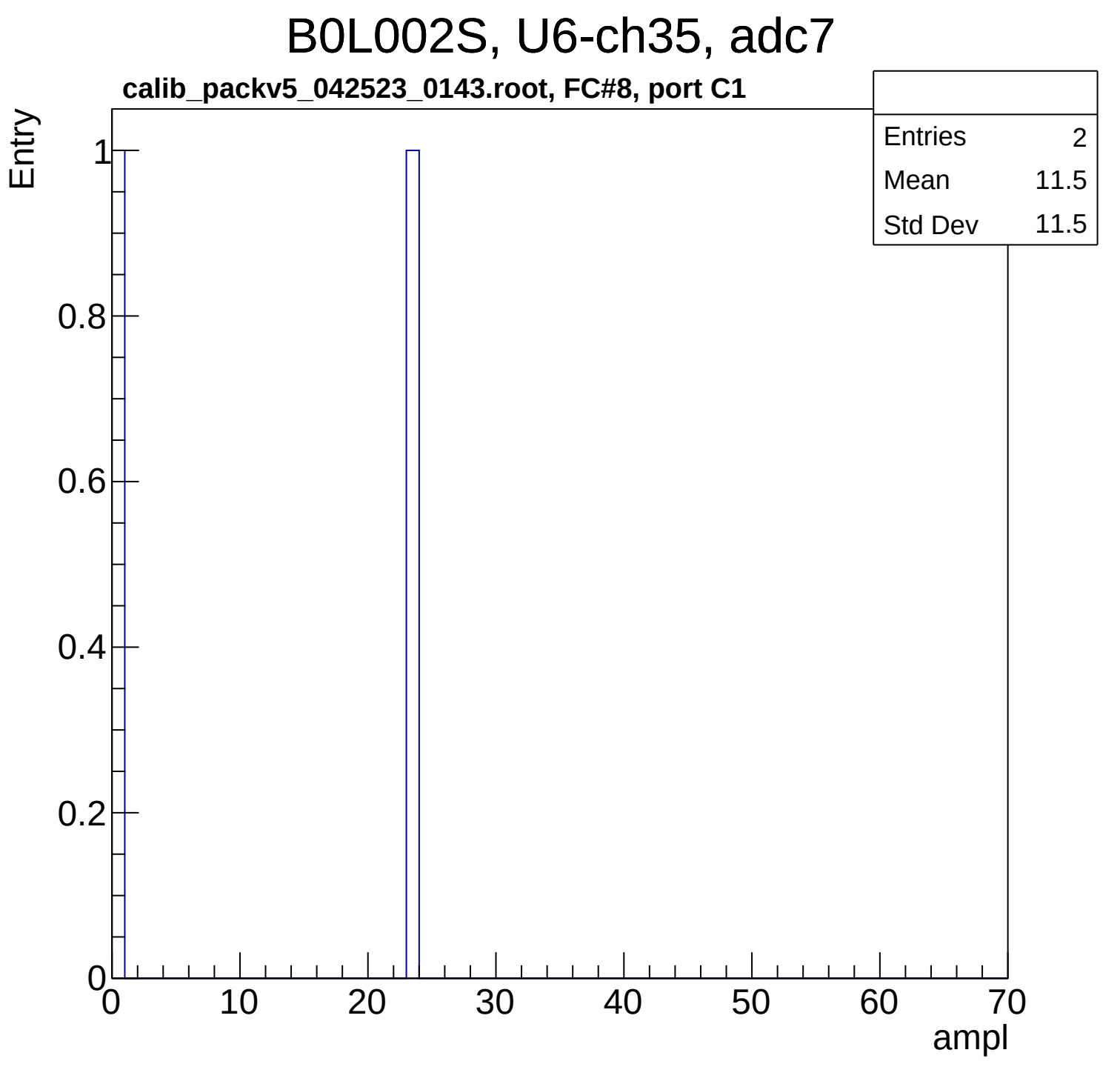
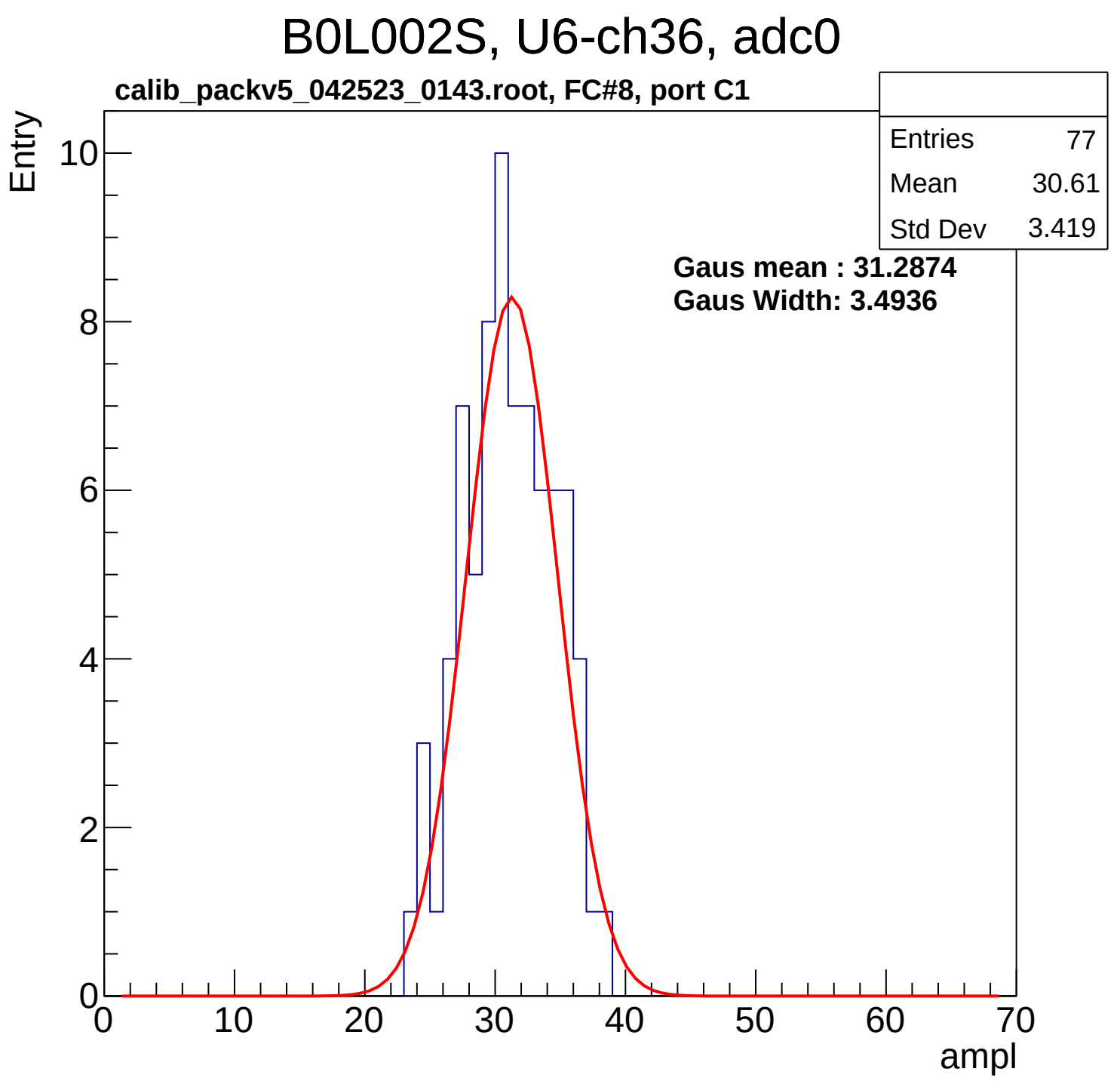
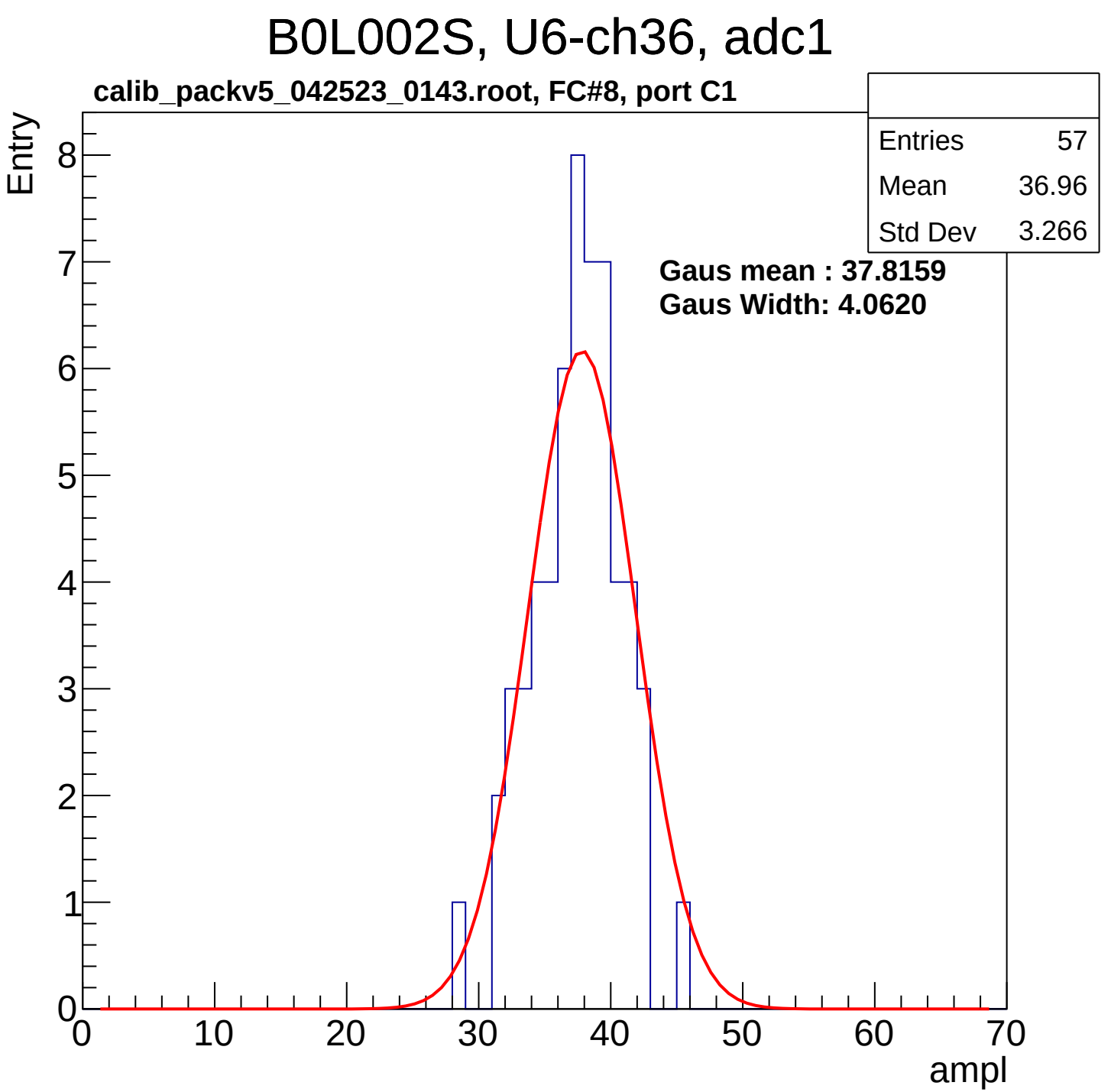

B0L002S, U6-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 56.81
Std Dev 14.84
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.61
Std Dev 3.419
Gaus mean : 31.2874
Gaus Width: 3.4936
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 36.96
Std Dev 3.266
Gaus mean : 37.8159
Gaus Width: 4.0620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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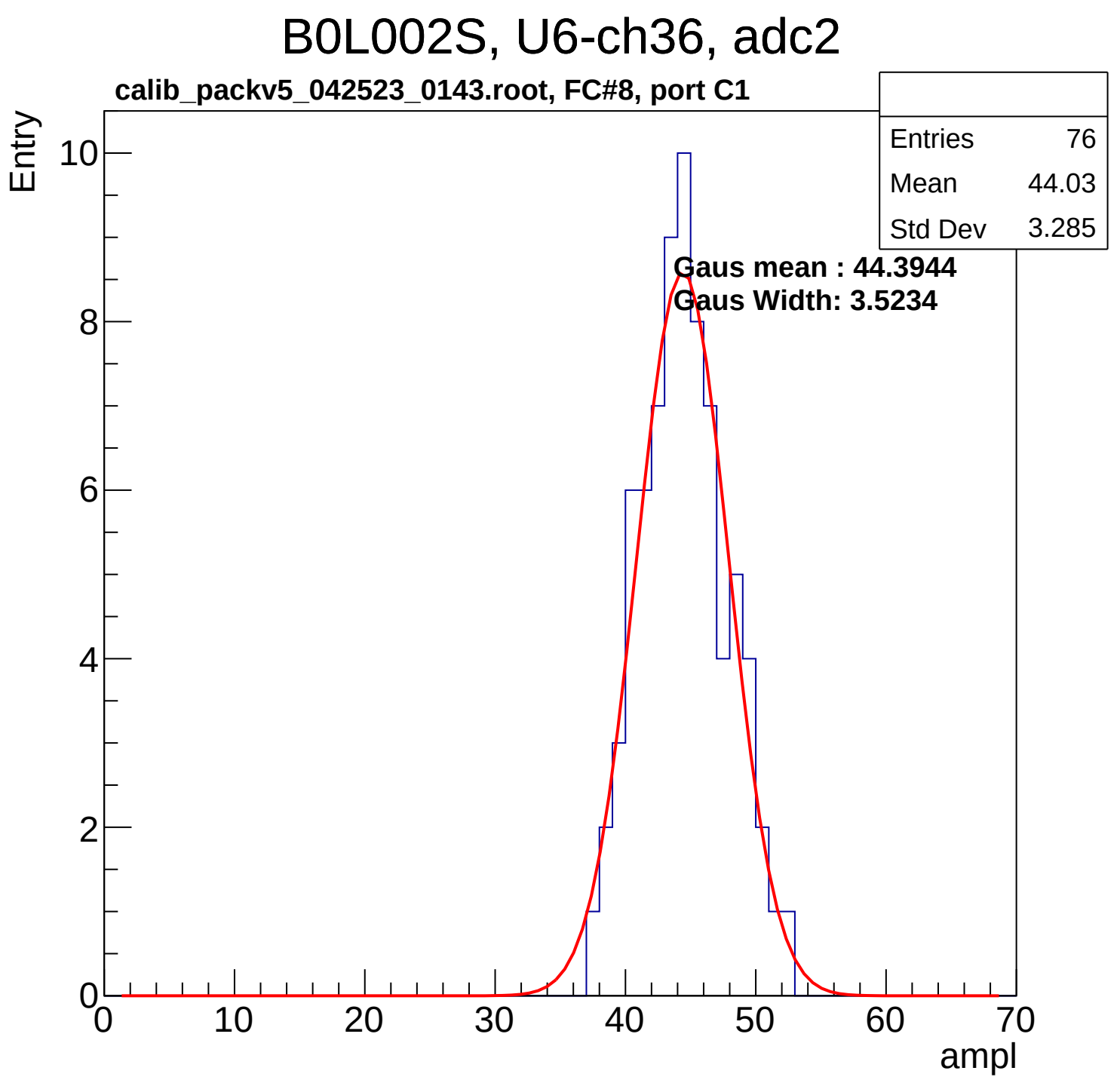
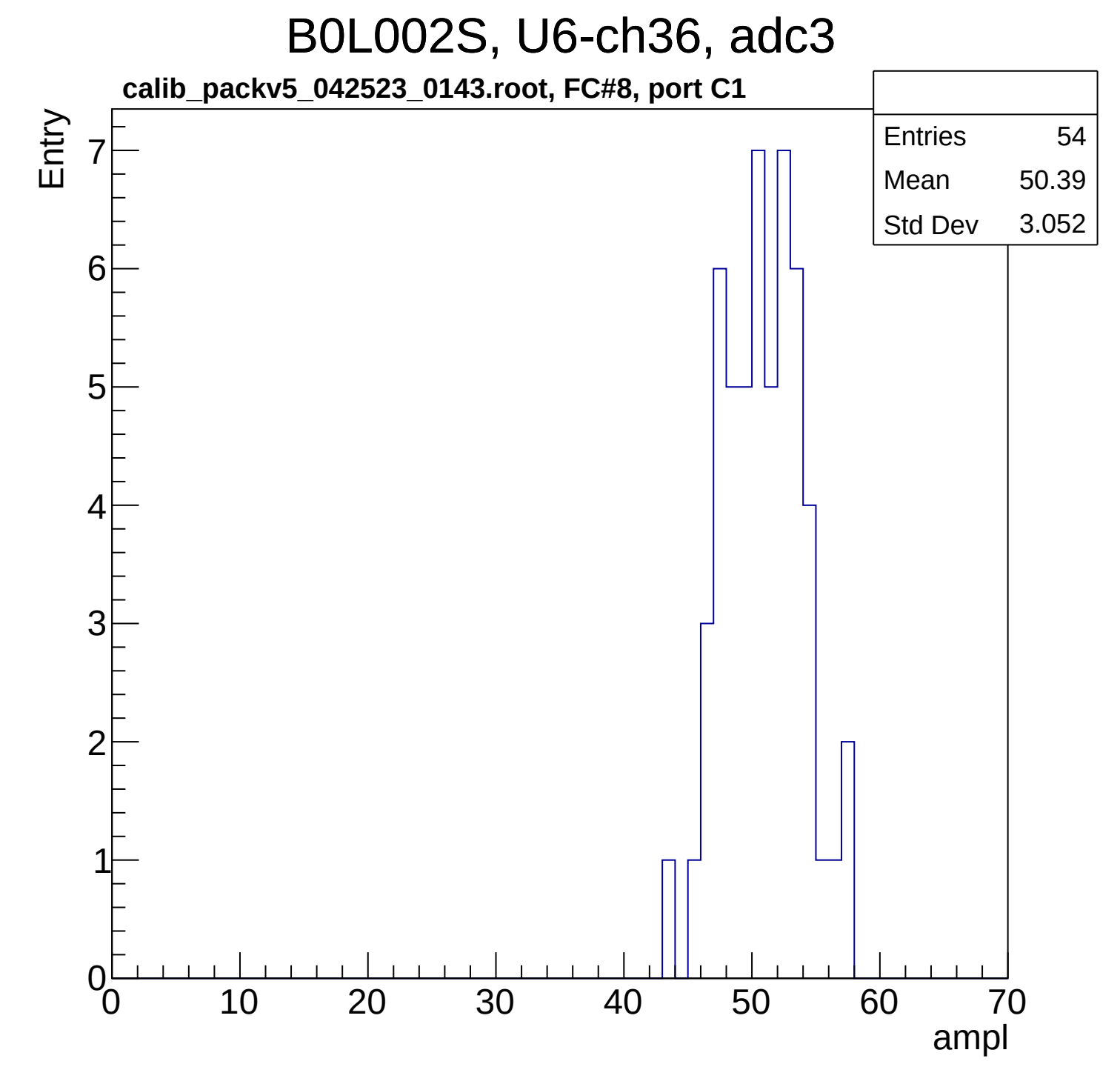
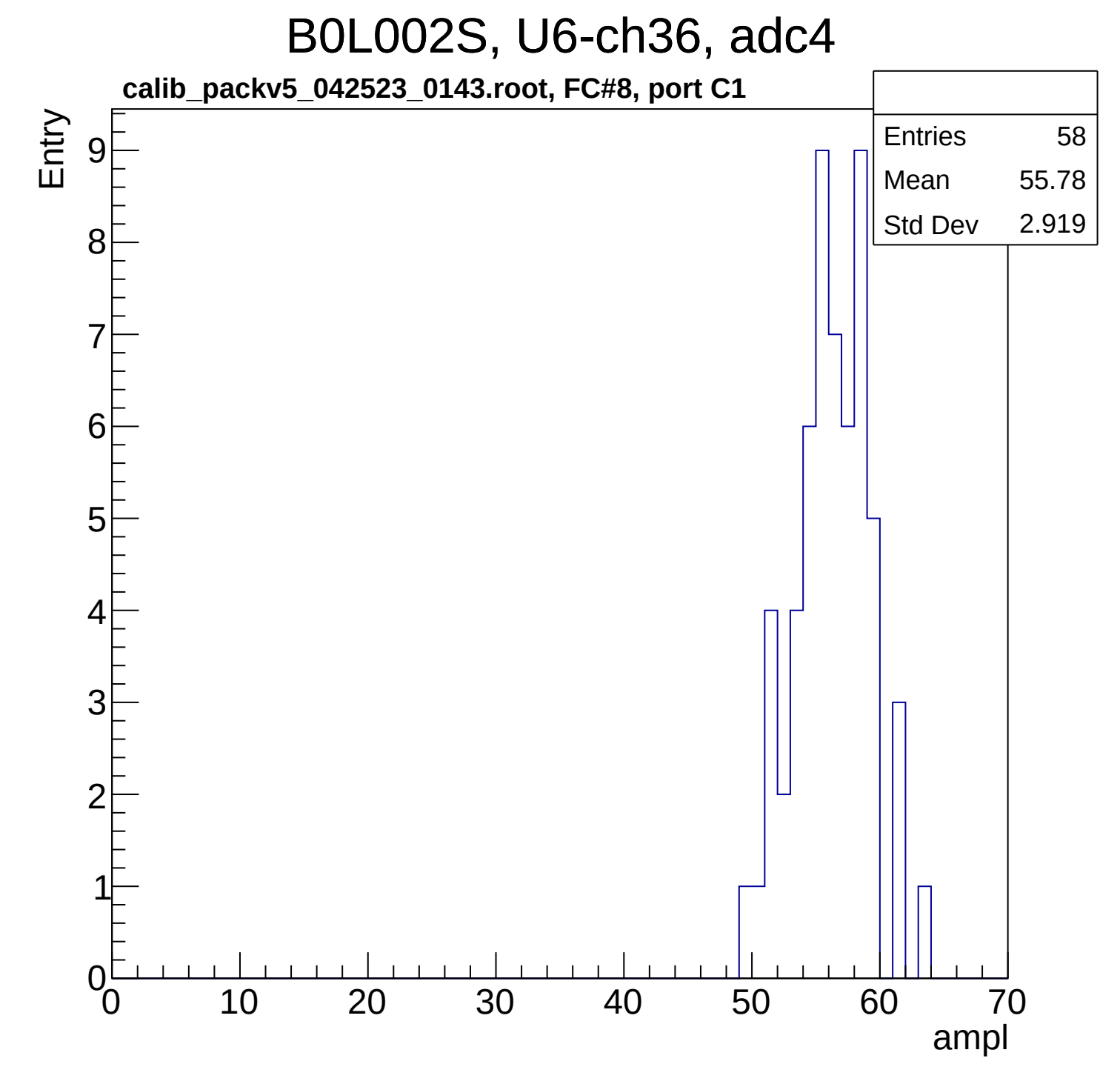
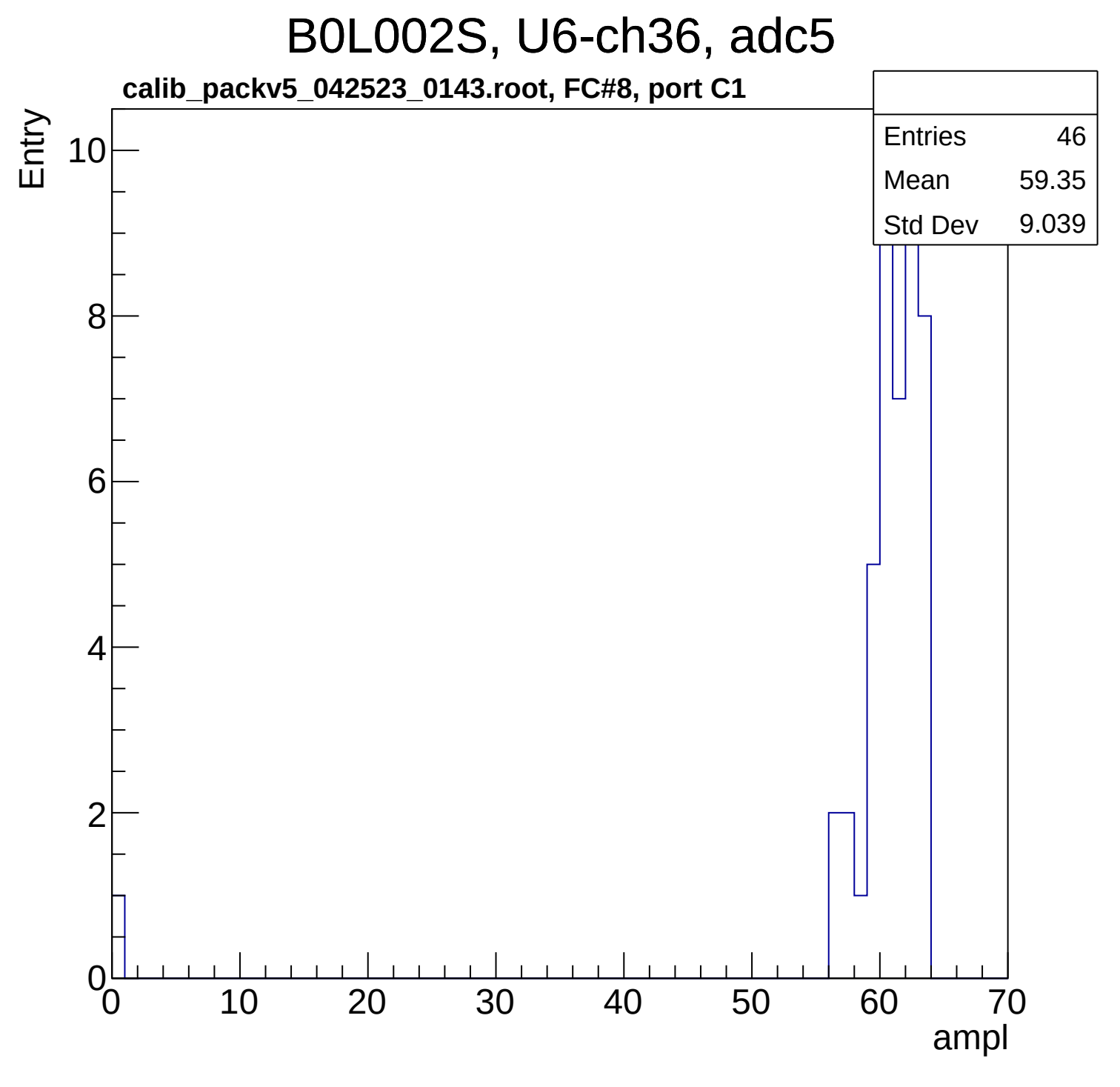
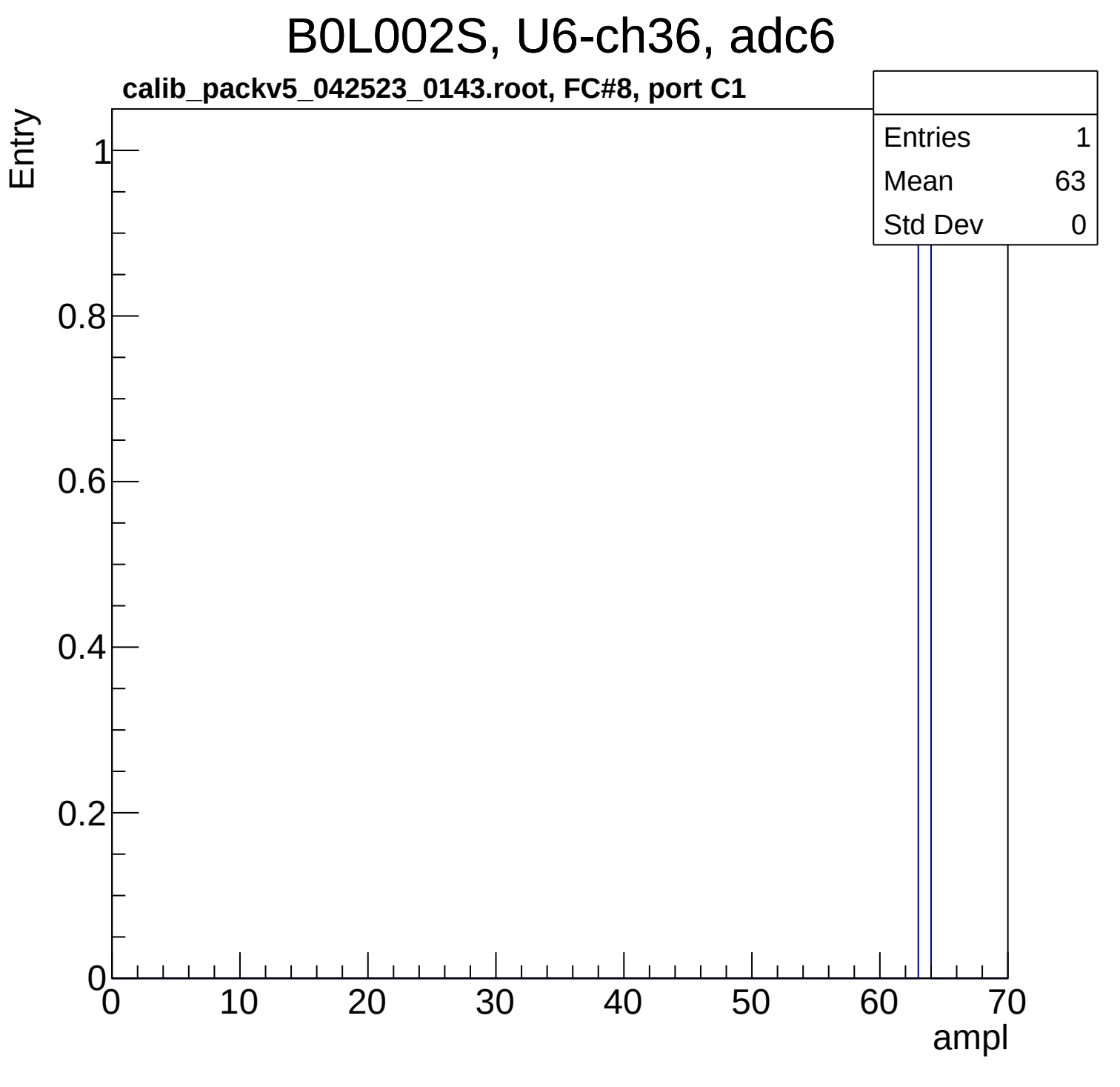
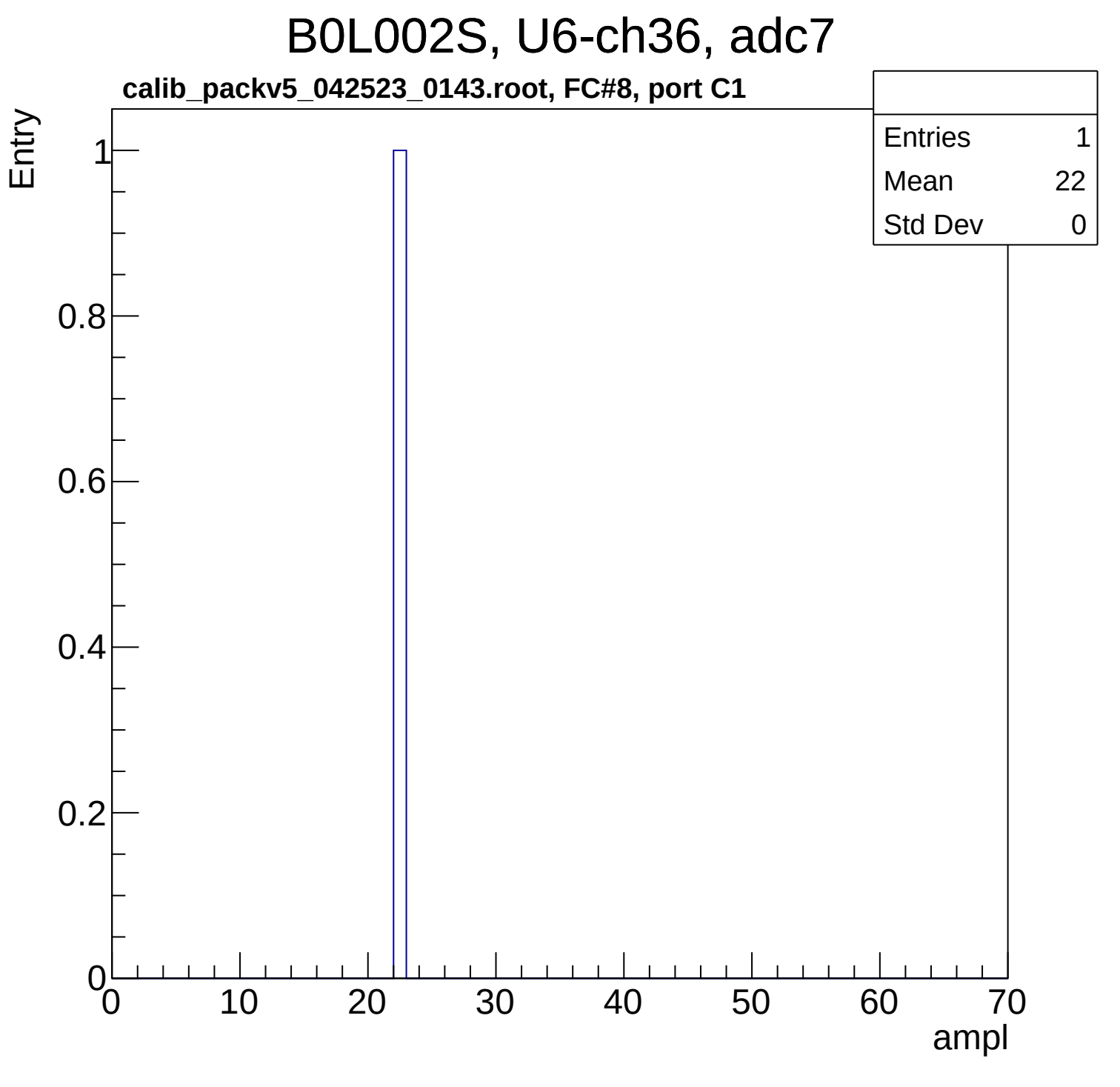

B0L002S, U6-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 44.03
Std Dev 3.285
Gaus mean : 44.3944
Gaus Width: 3.5234
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

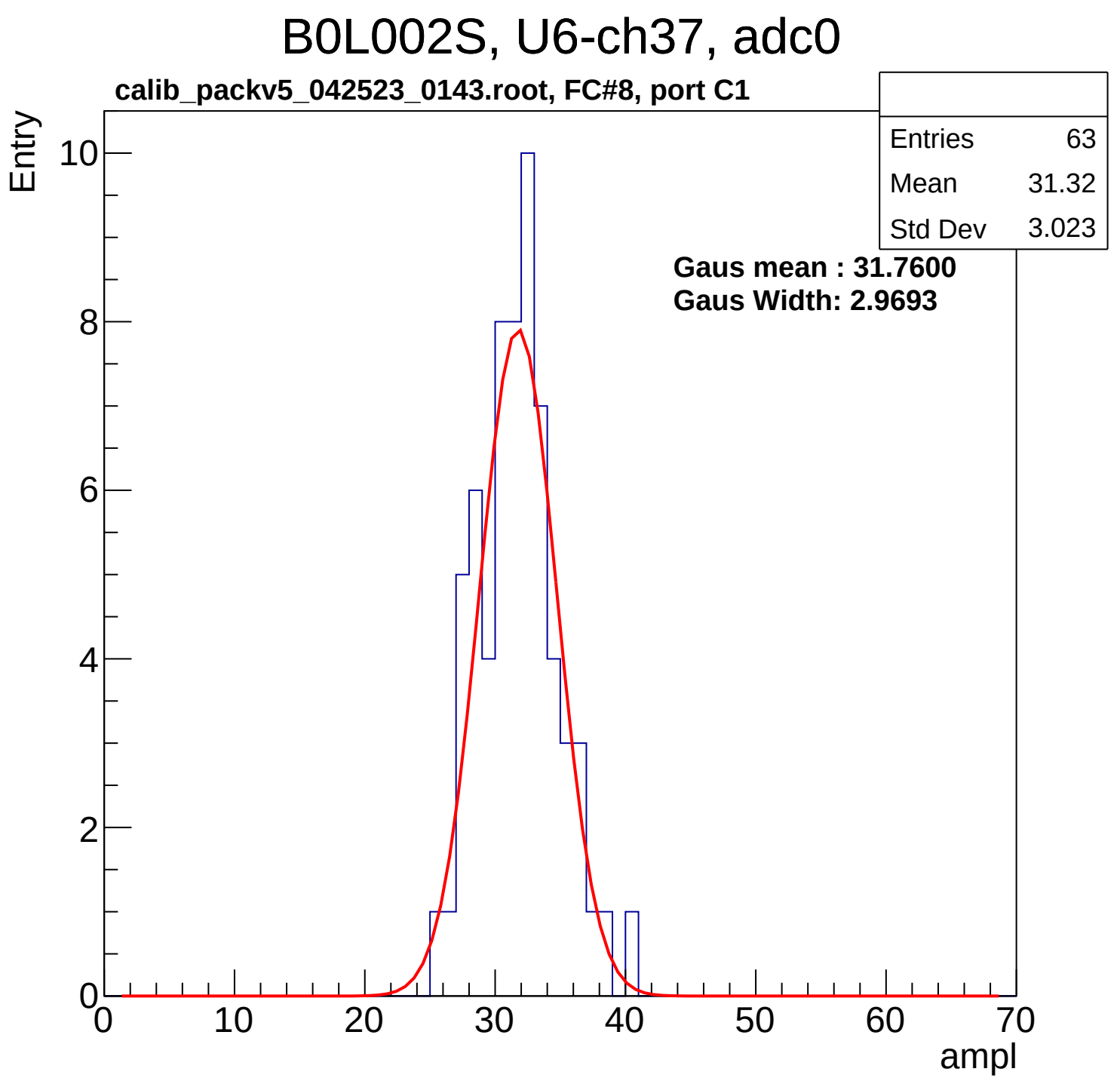

B0L002S, U6-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.39
Std Dev 3.052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.78
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.35
Std Dev 9.039
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.32
Std Dev 3.023
Gaus mean : 31.7600
Gaus Width: 2.9693
Entry
ampl
0
2
4
6
8
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0
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20
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40
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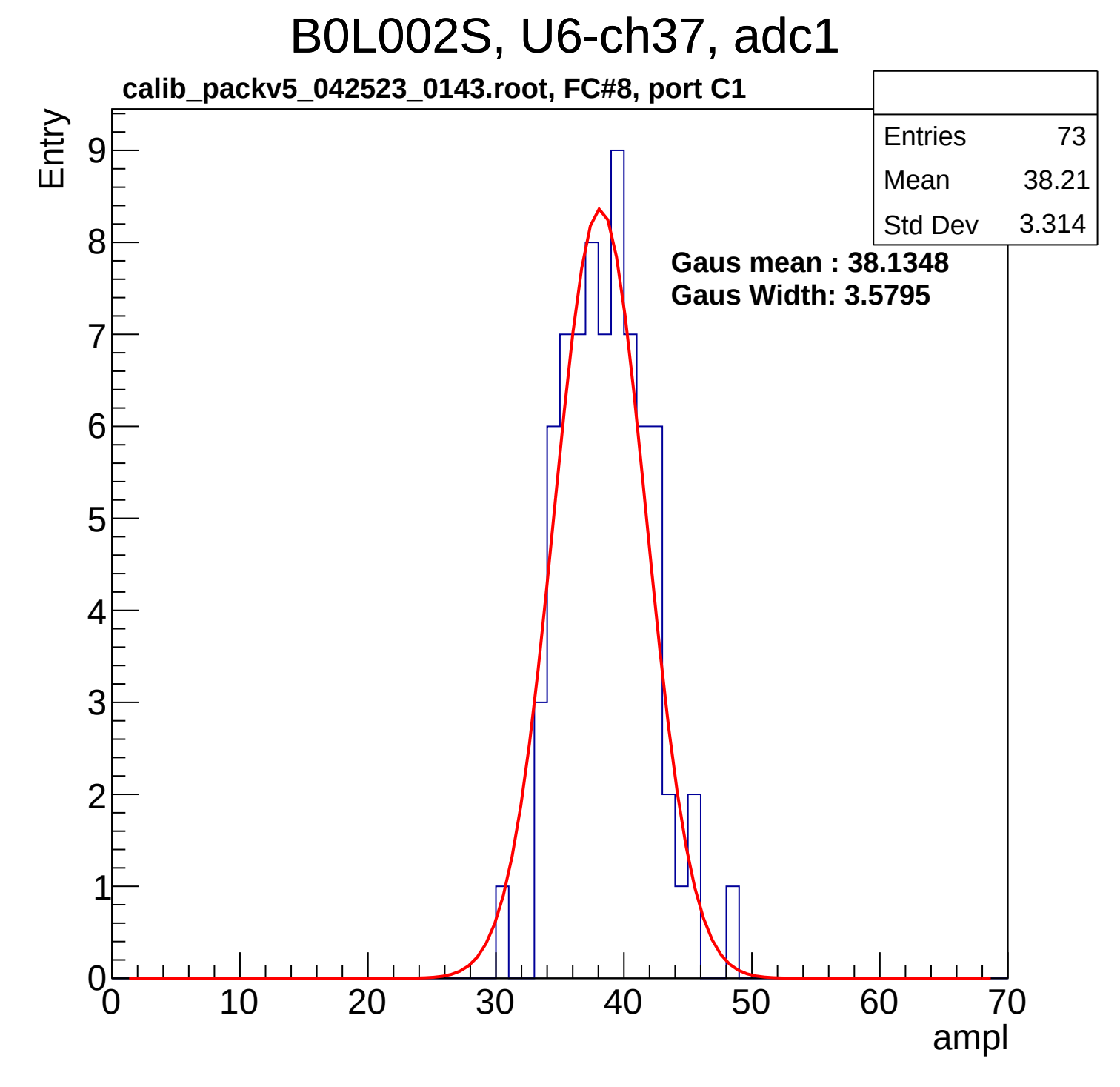
60
70


B0L002S, U6-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.21
Std Dev 3.314
Gaus mean : 38.1348
Gaus Width: 3.5795
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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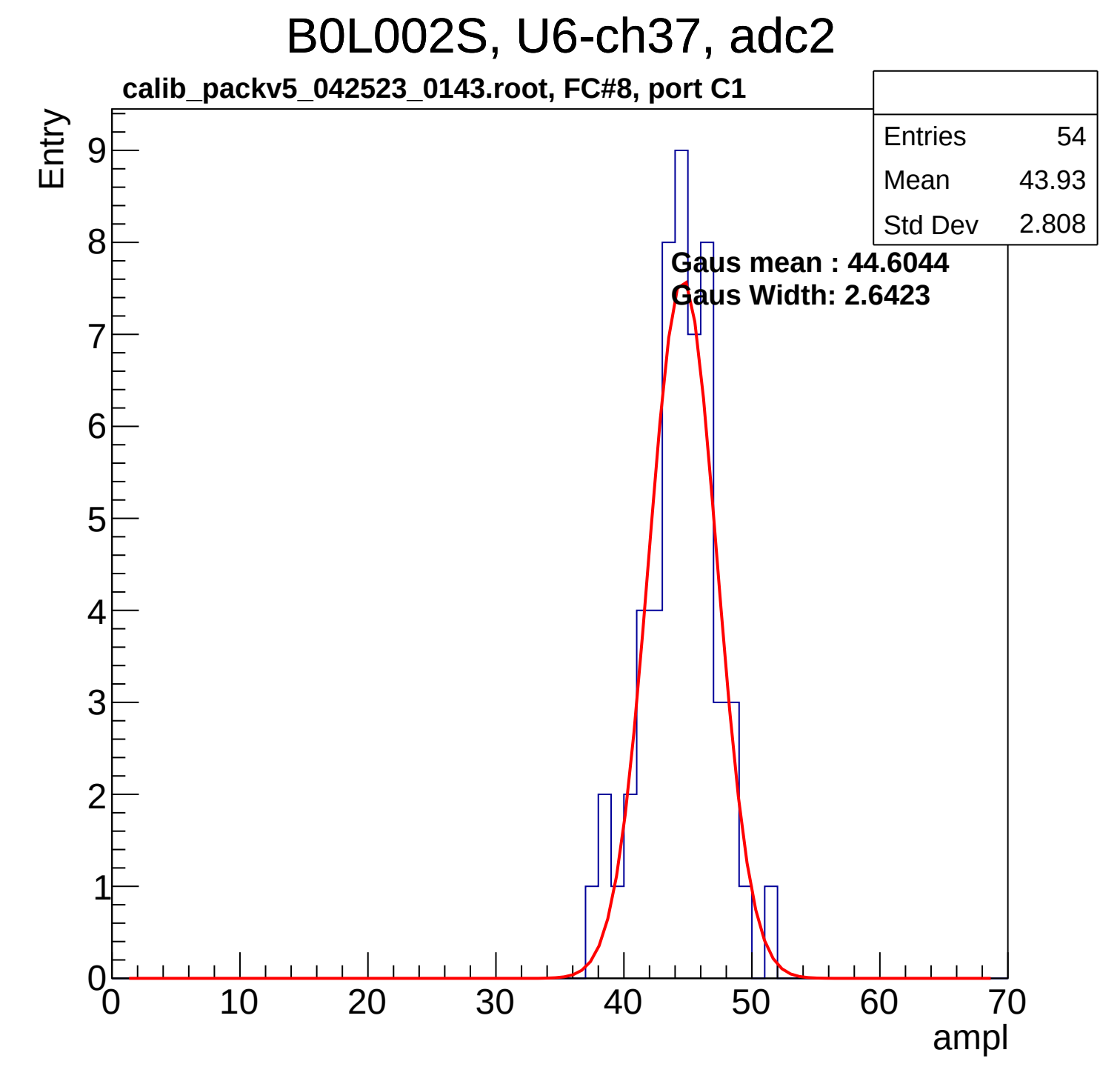
B0L002S, U6-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 43.93
Std Dev 2.808
Gaus mean : 44.6044
Gaus Width: 2.6423
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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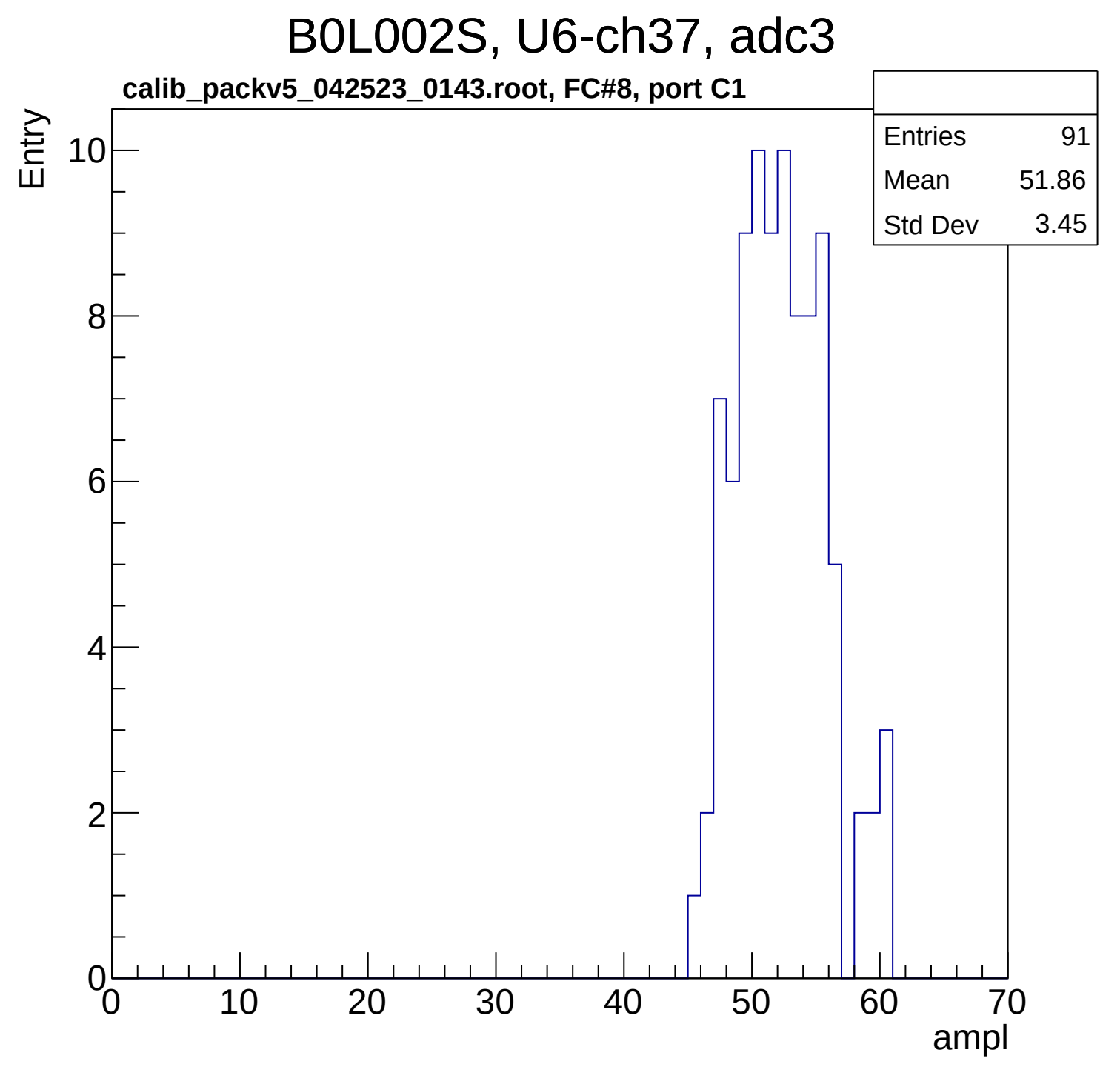
B0L002S, U6-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 51.86
Std Dev 3.45
Entry
ampl
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0
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60
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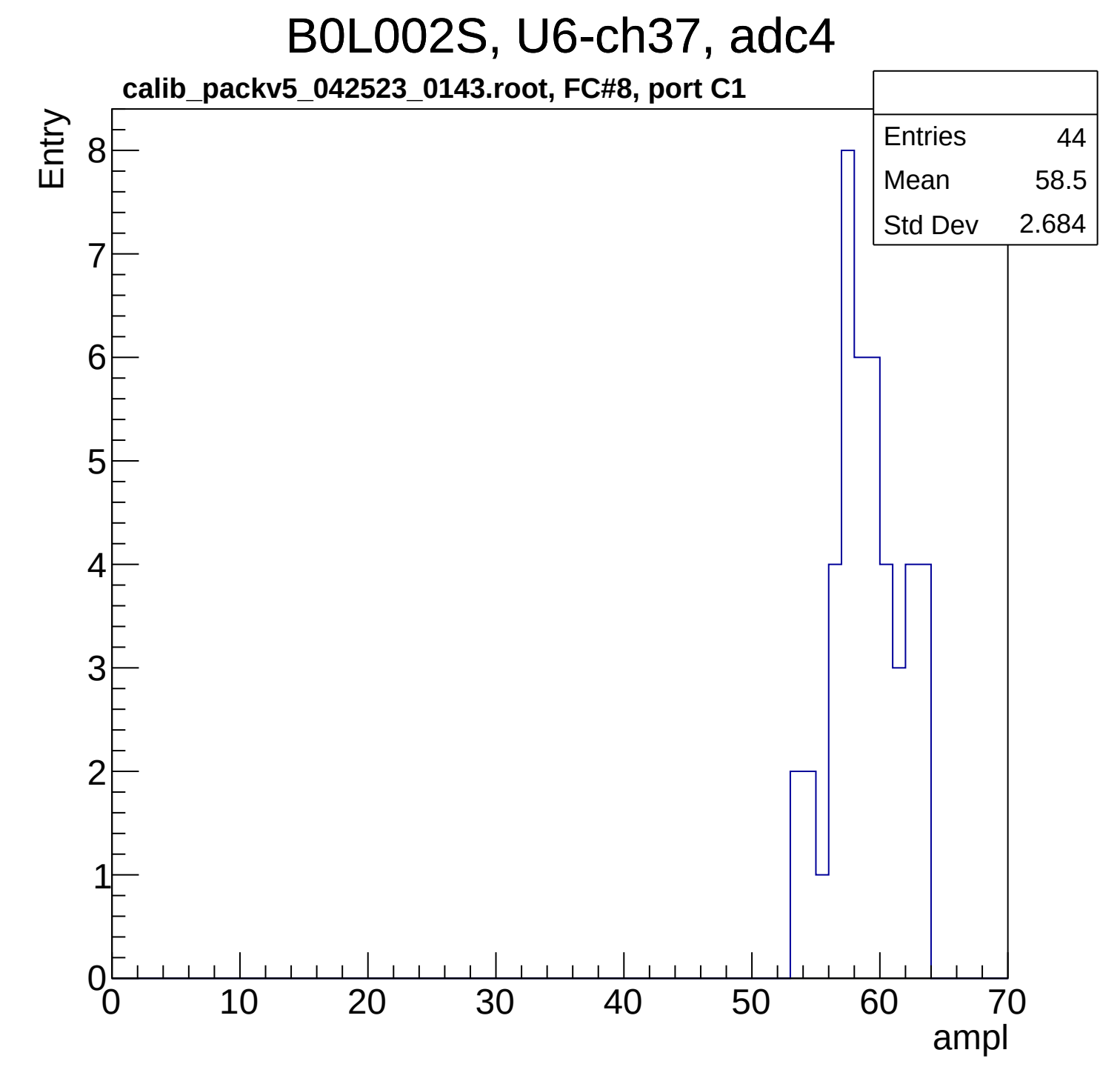
B0L002S, U6-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.5
Std Dev 2.684
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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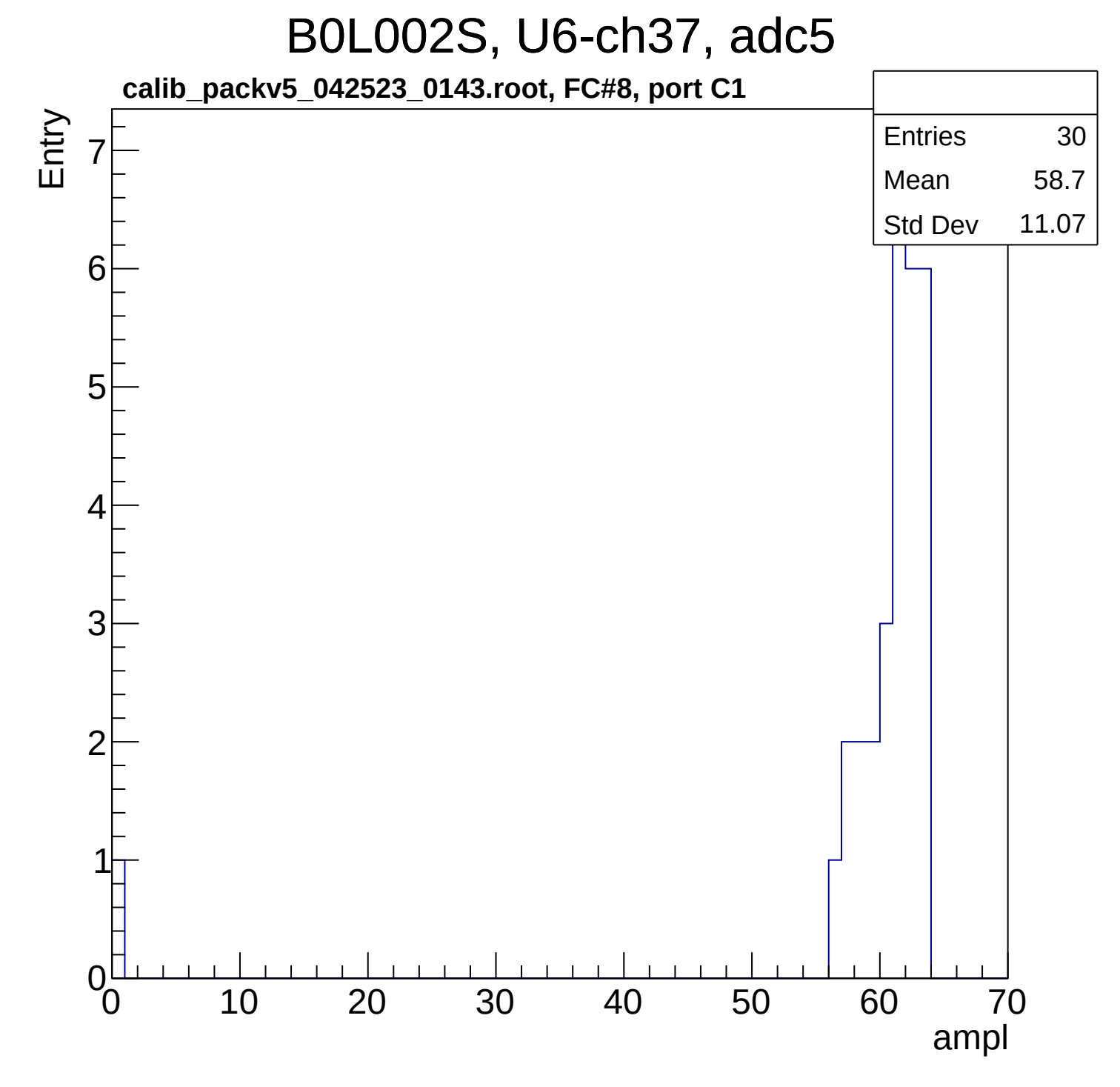
B0L002S, U6-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.7
Std Dev 11.07
Entry
ampl
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3
4
5
6
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0
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50
60
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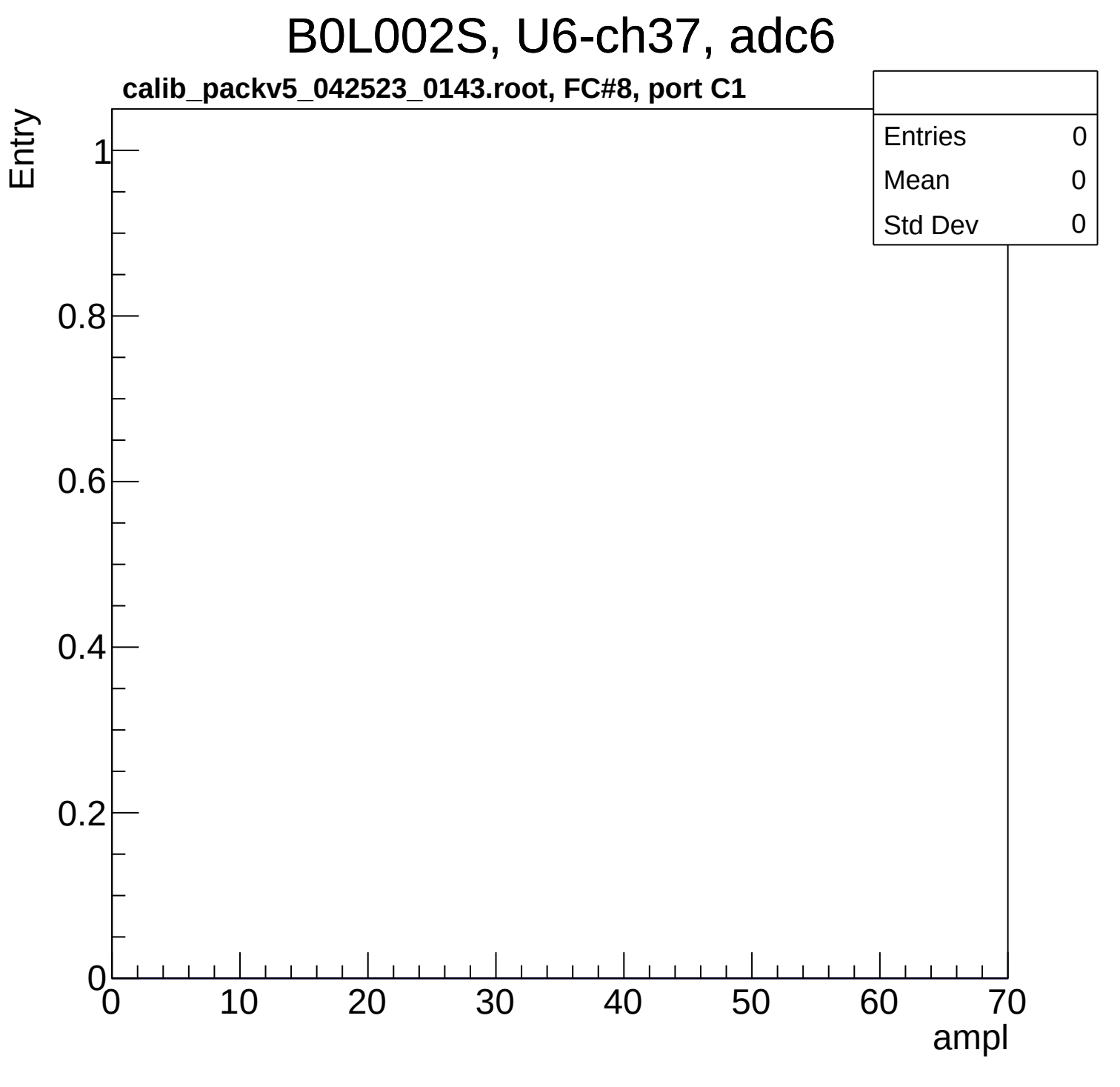
B0L002S, U6-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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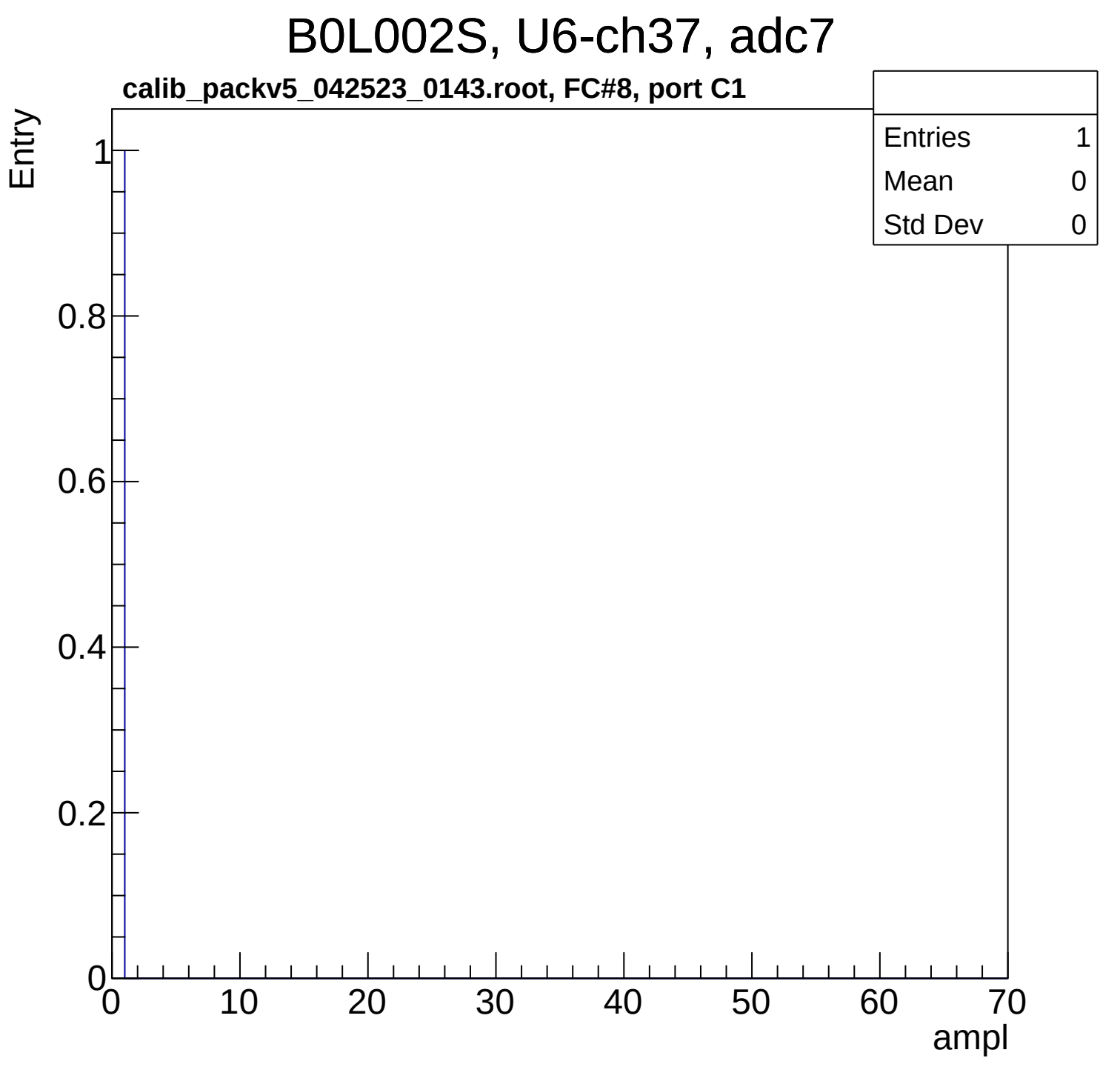
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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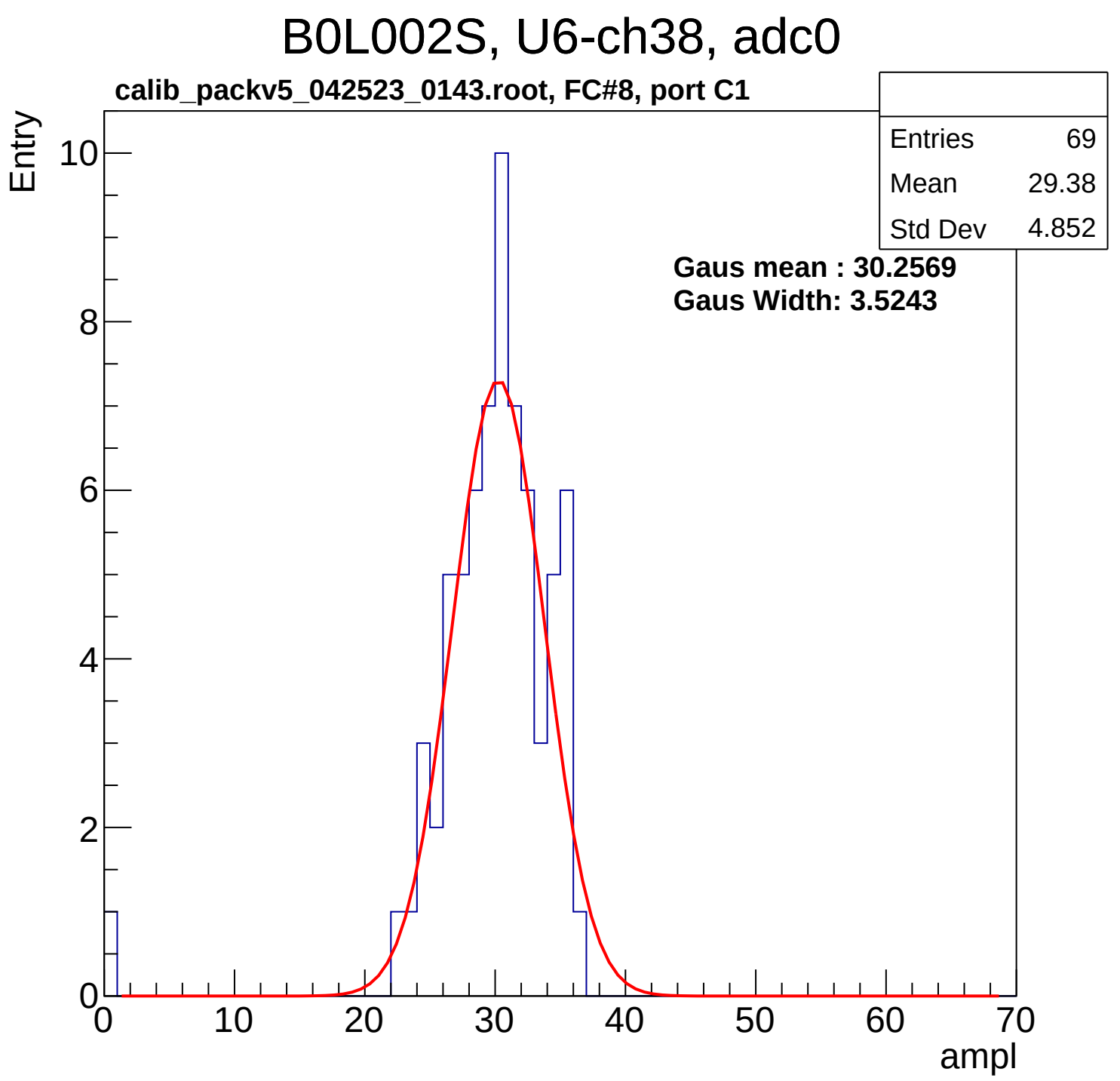
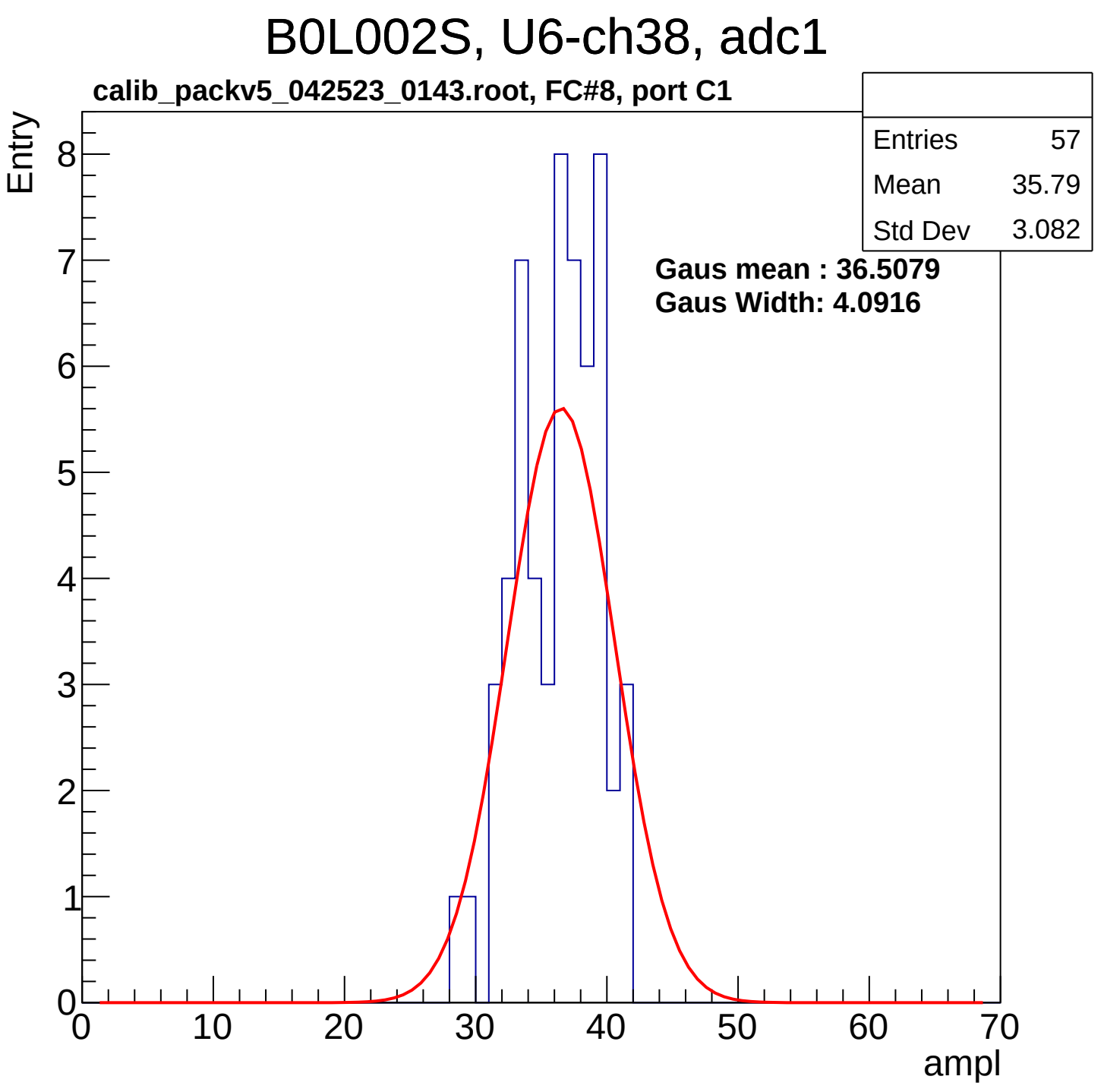
50
60
70
ampl


B0L002S, U6-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.38
Std Dev 4.852
Gaus mean : 30.2569
Gaus Width: 3.5243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 35.79
Std Dev 3.082
Gaus mean : 36.5079
Gaus Width: 4.0916
Entry
ampl
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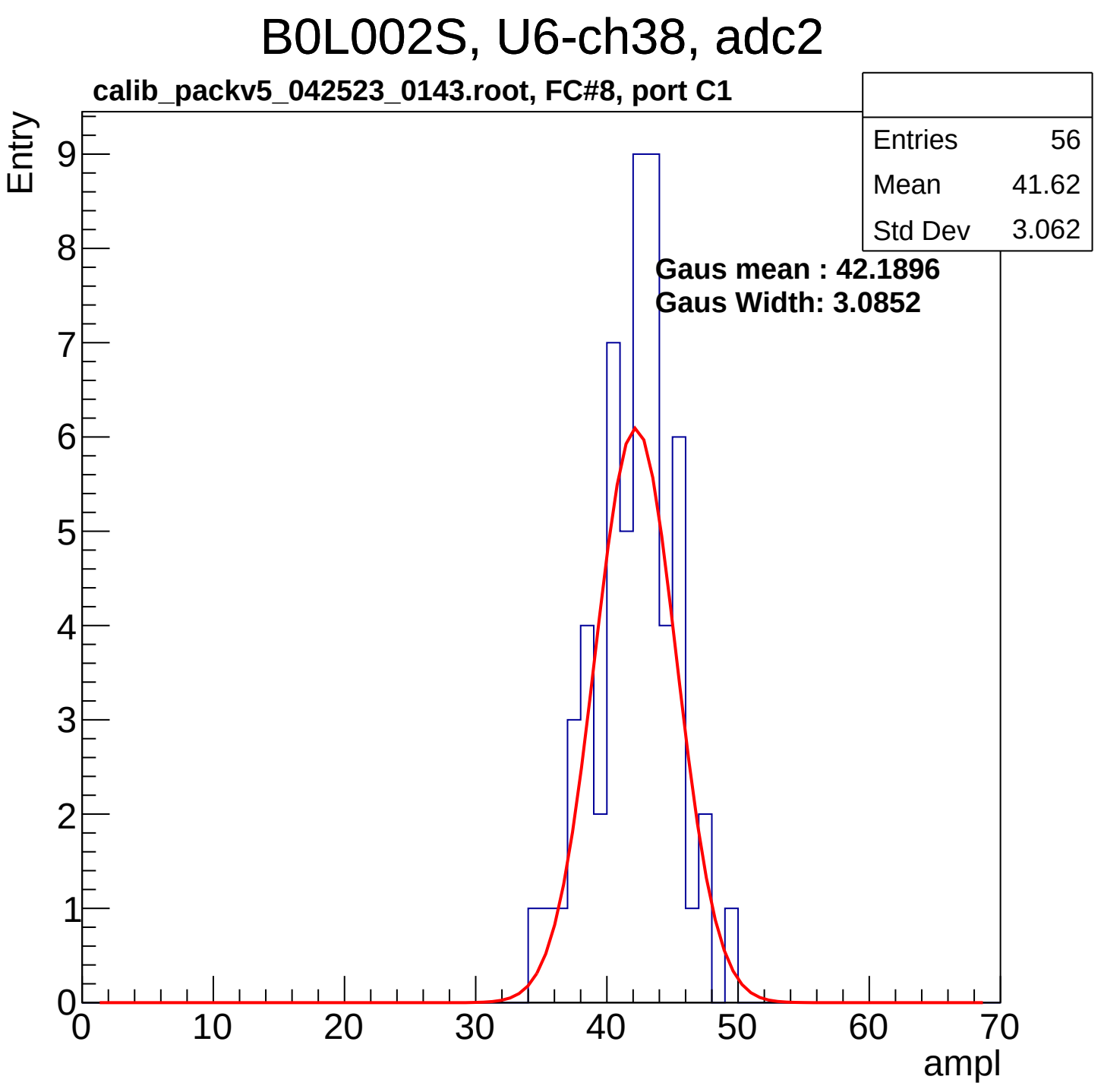
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 41.62
Std Dev 3.062
Gaus mean : 42.1896
Gaus Width: 3.0852
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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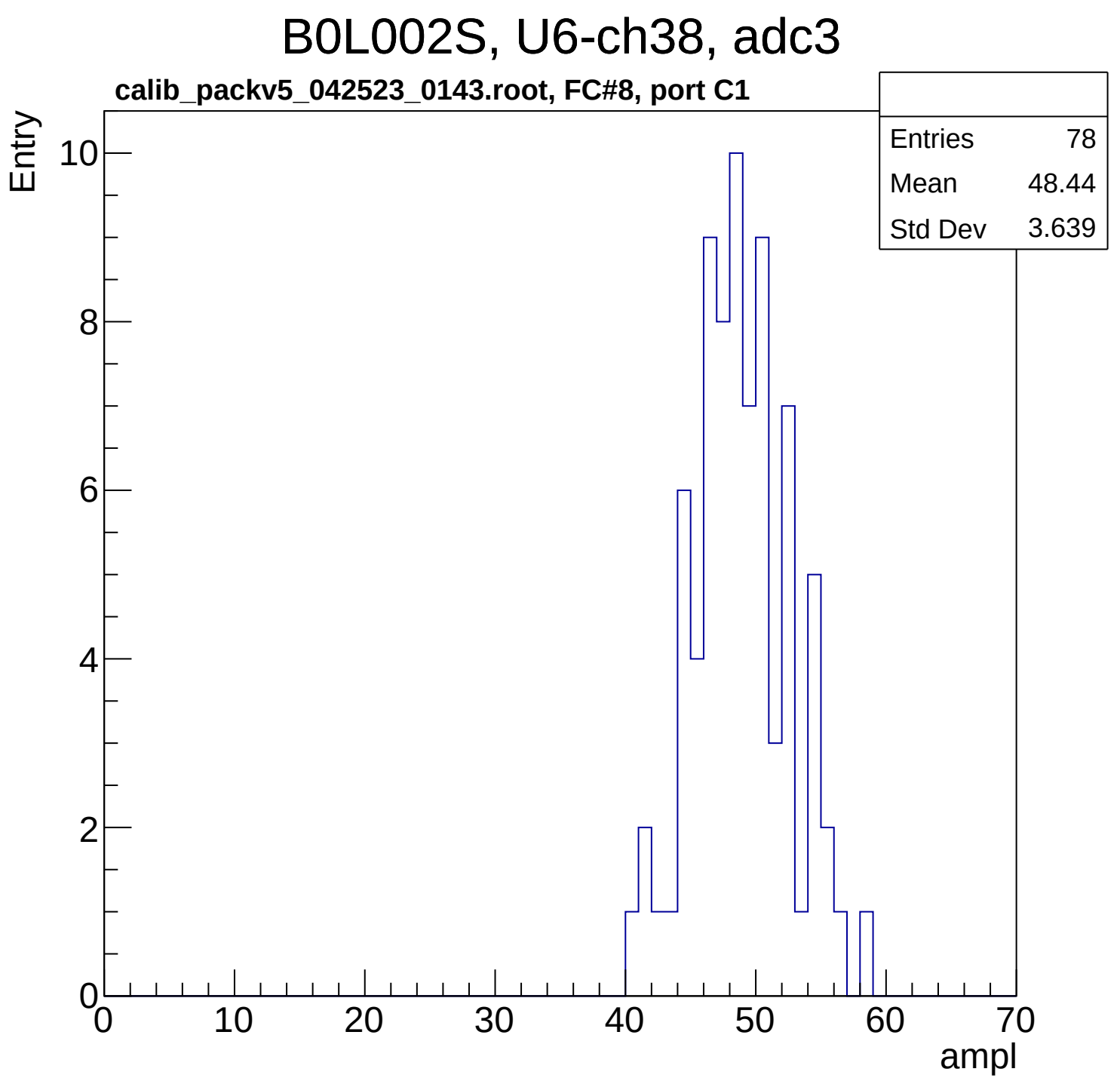
B0L002S, U6-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 48.44
Std Dev 3.639
Entry
ampl
0
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4
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8
10
0
10
20
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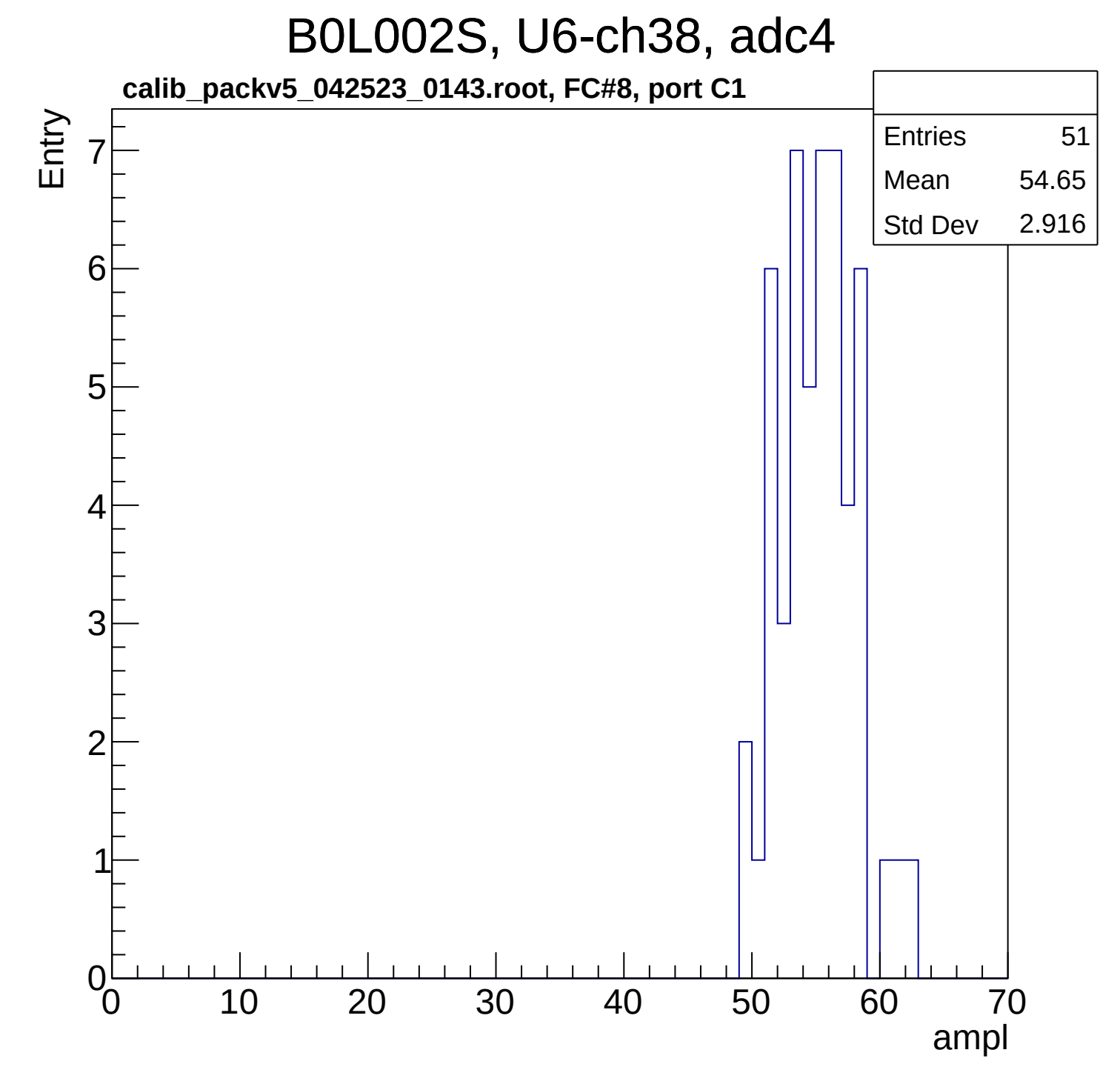
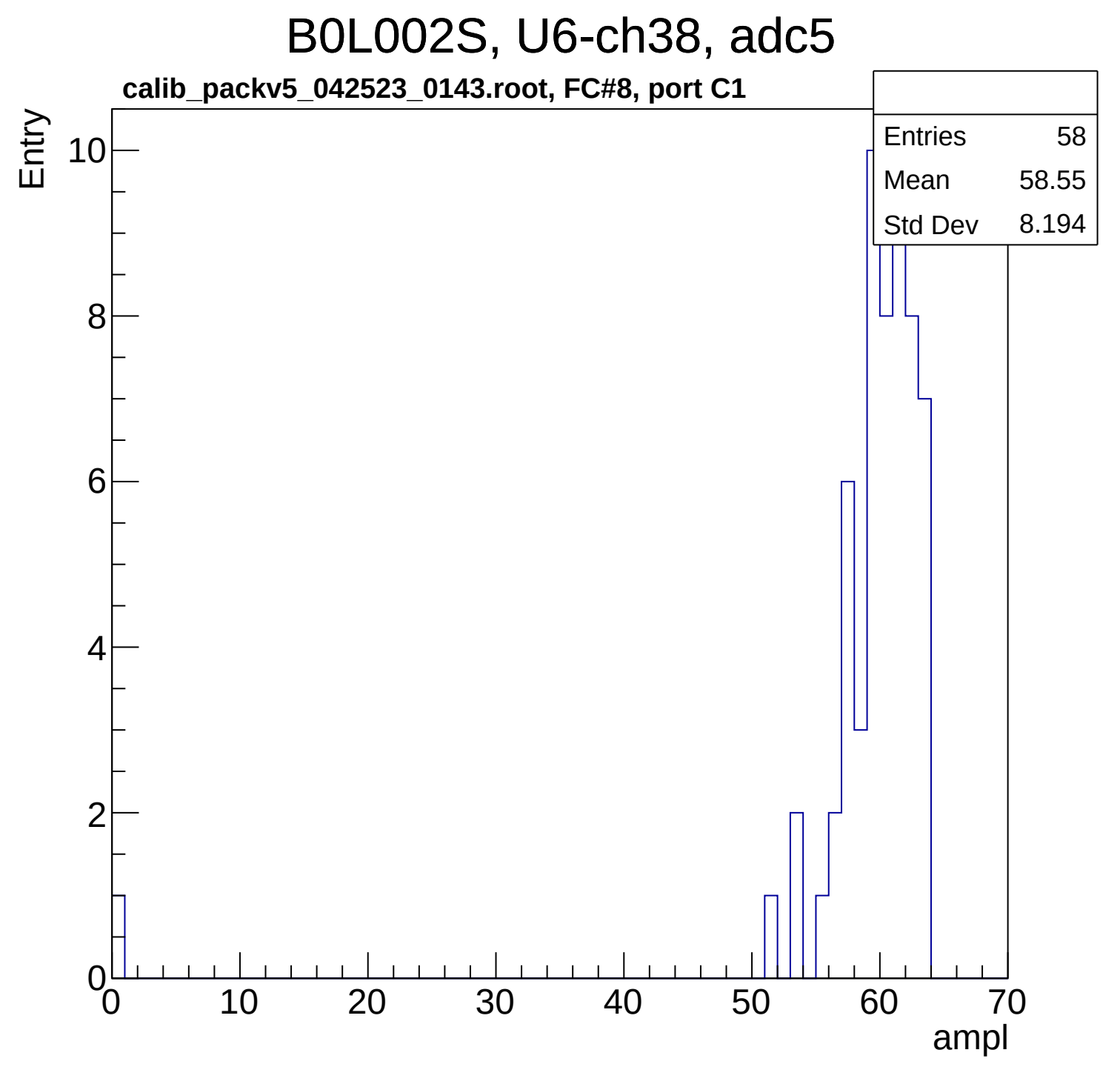
40
50
60
70


B0L002S, U6-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 54.65
Std Dev 2.916
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 58.55
Std Dev 8.194
Entry
0
2
4
6
8
10
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10
20
30
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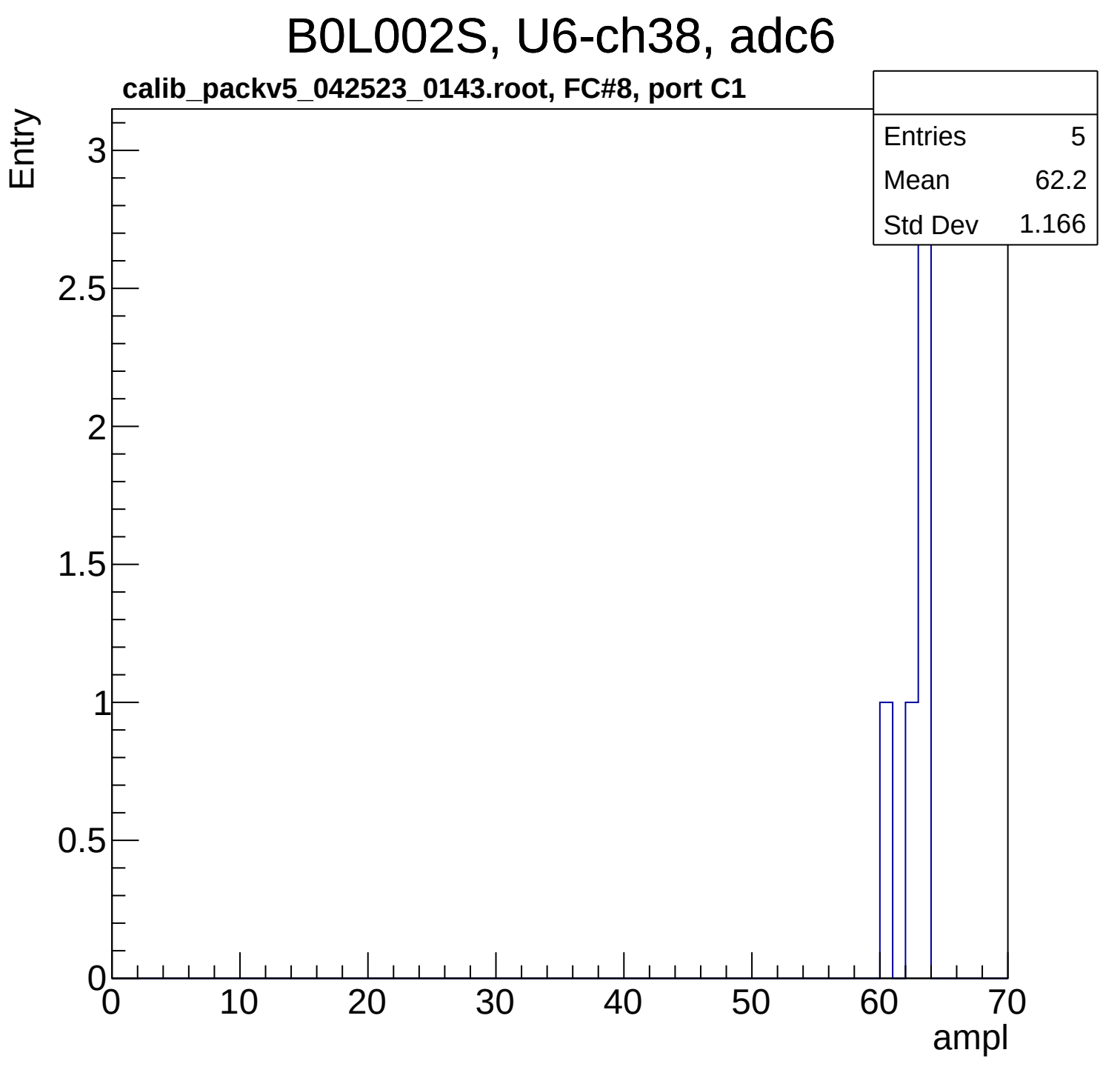
50
60
70
ampl


B0L002S, U6-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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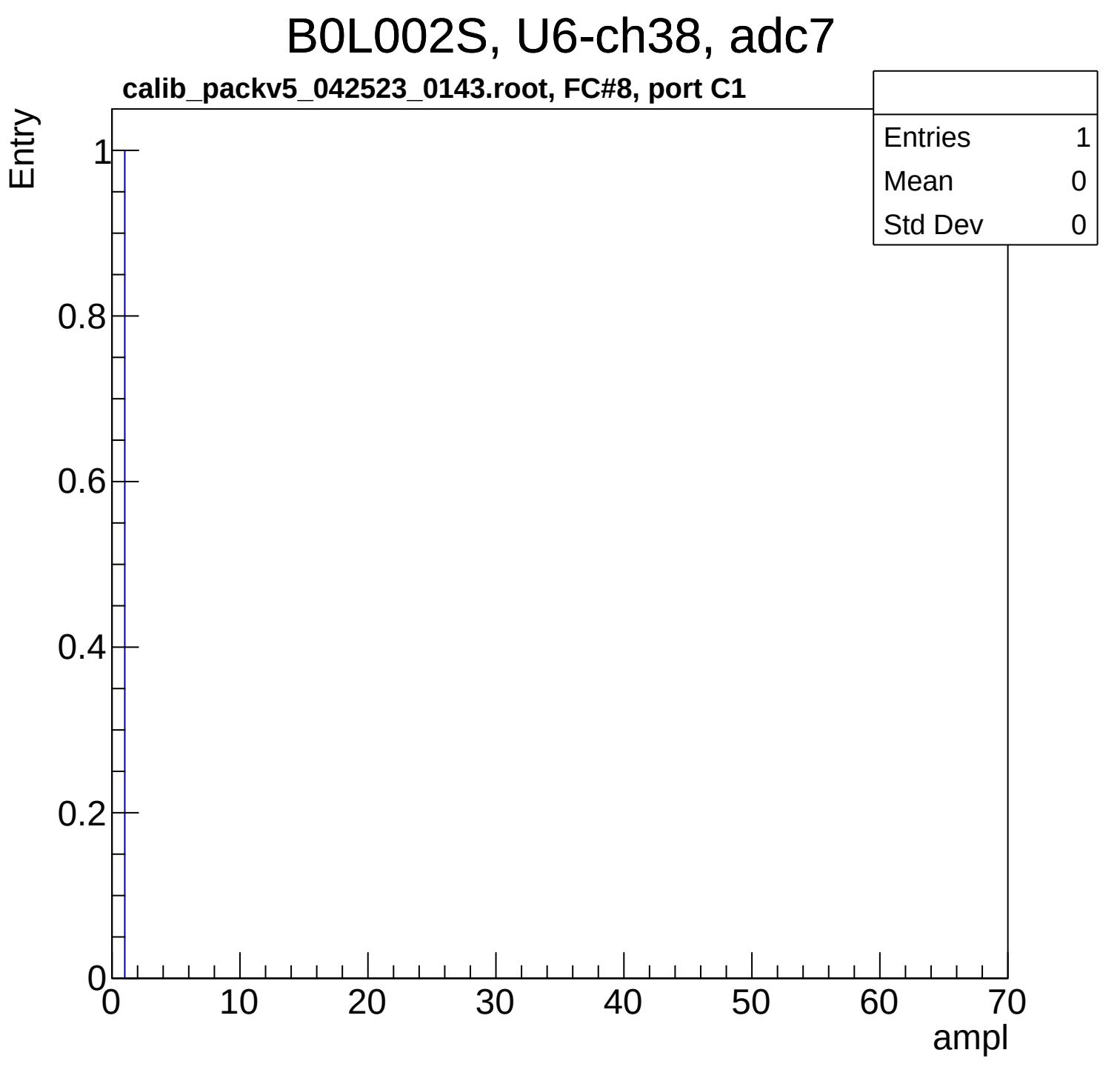
B0L002S, U6-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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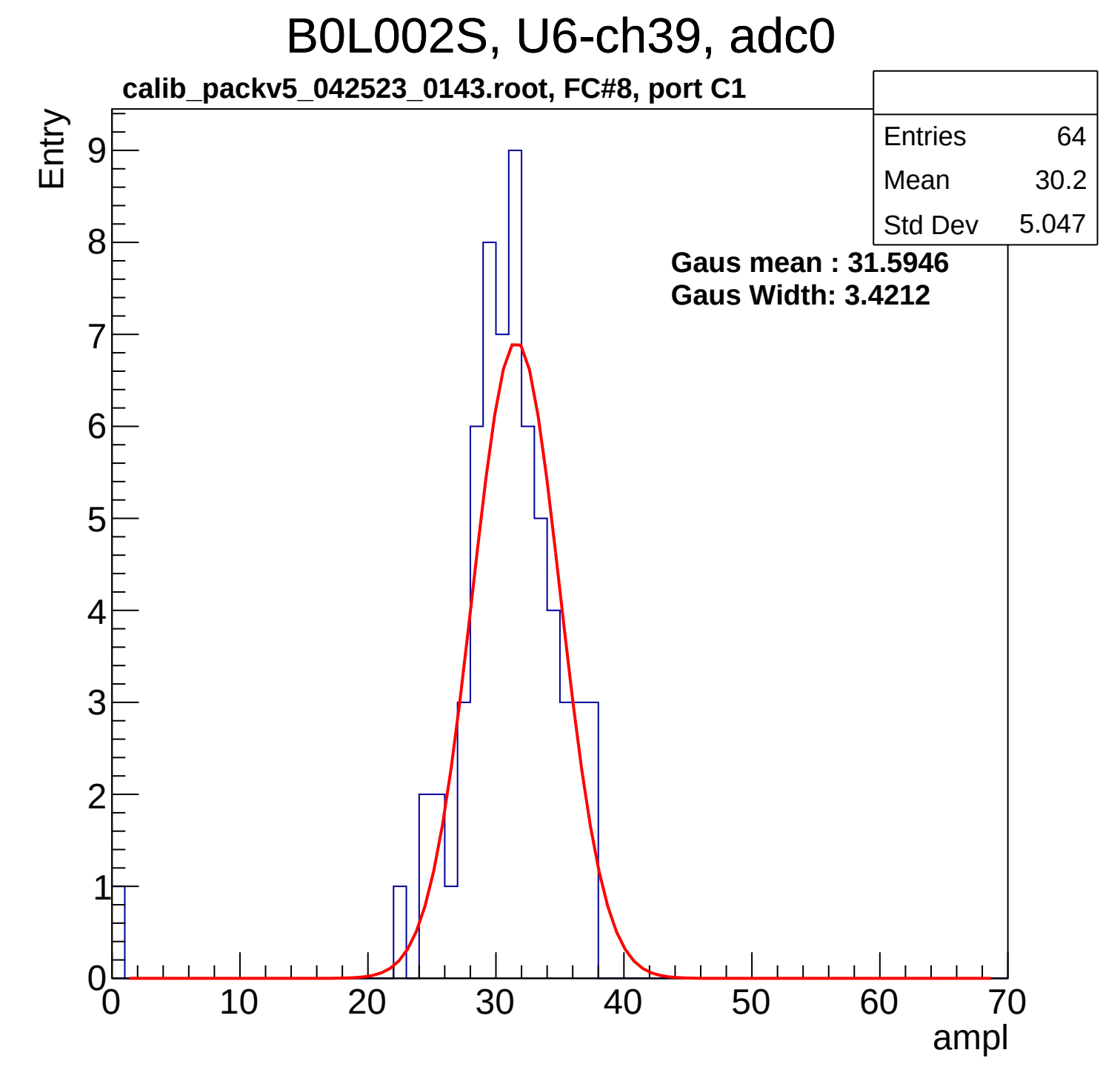
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.2
Std Dev 5.047
Gaus mean : 31.5946
Gaus Width: 3.4212
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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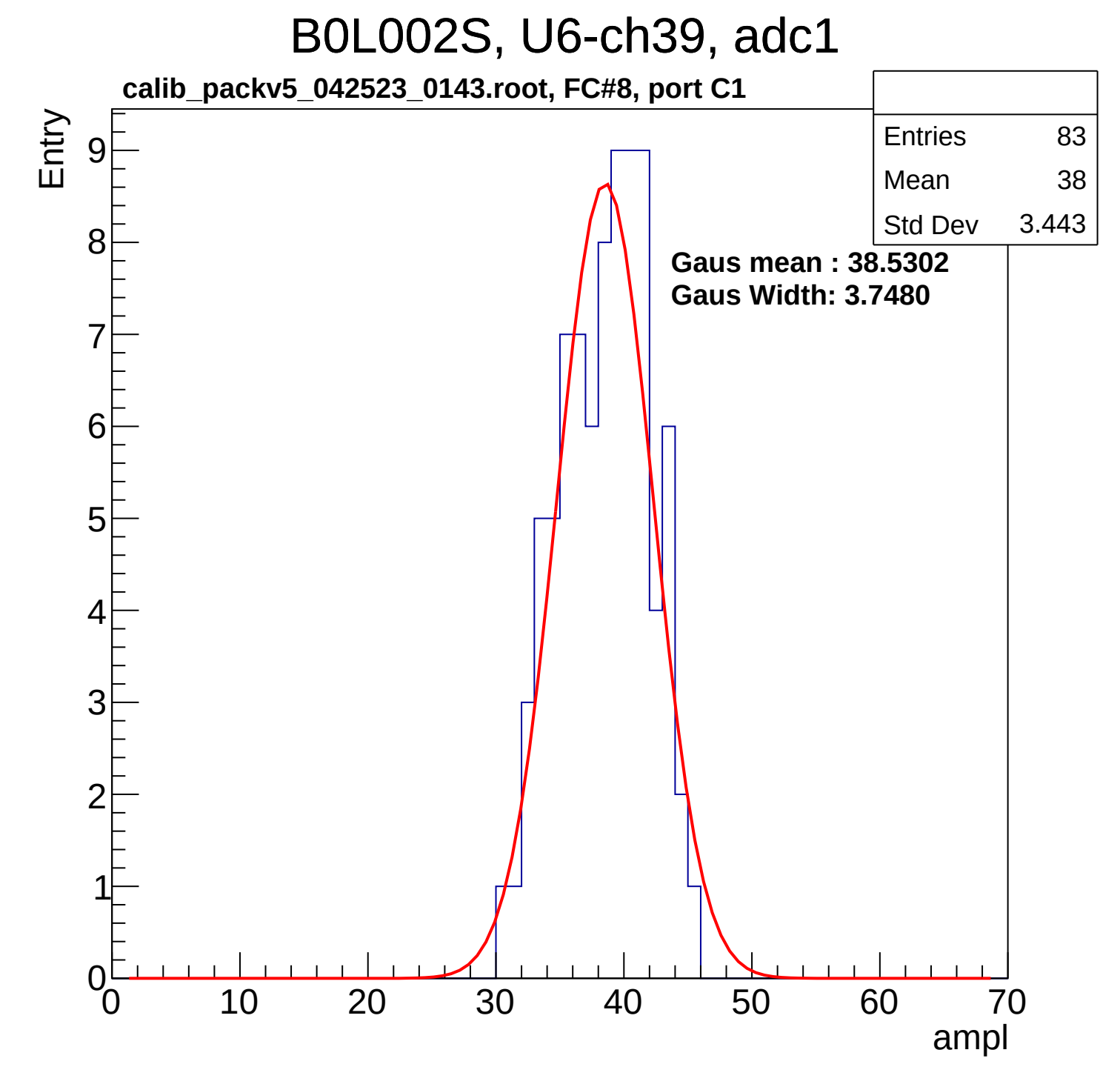
B0L002S, U6-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 38
Std Dev 3.443
Gaus mean : 38.5302
Gaus Width: 3.7480
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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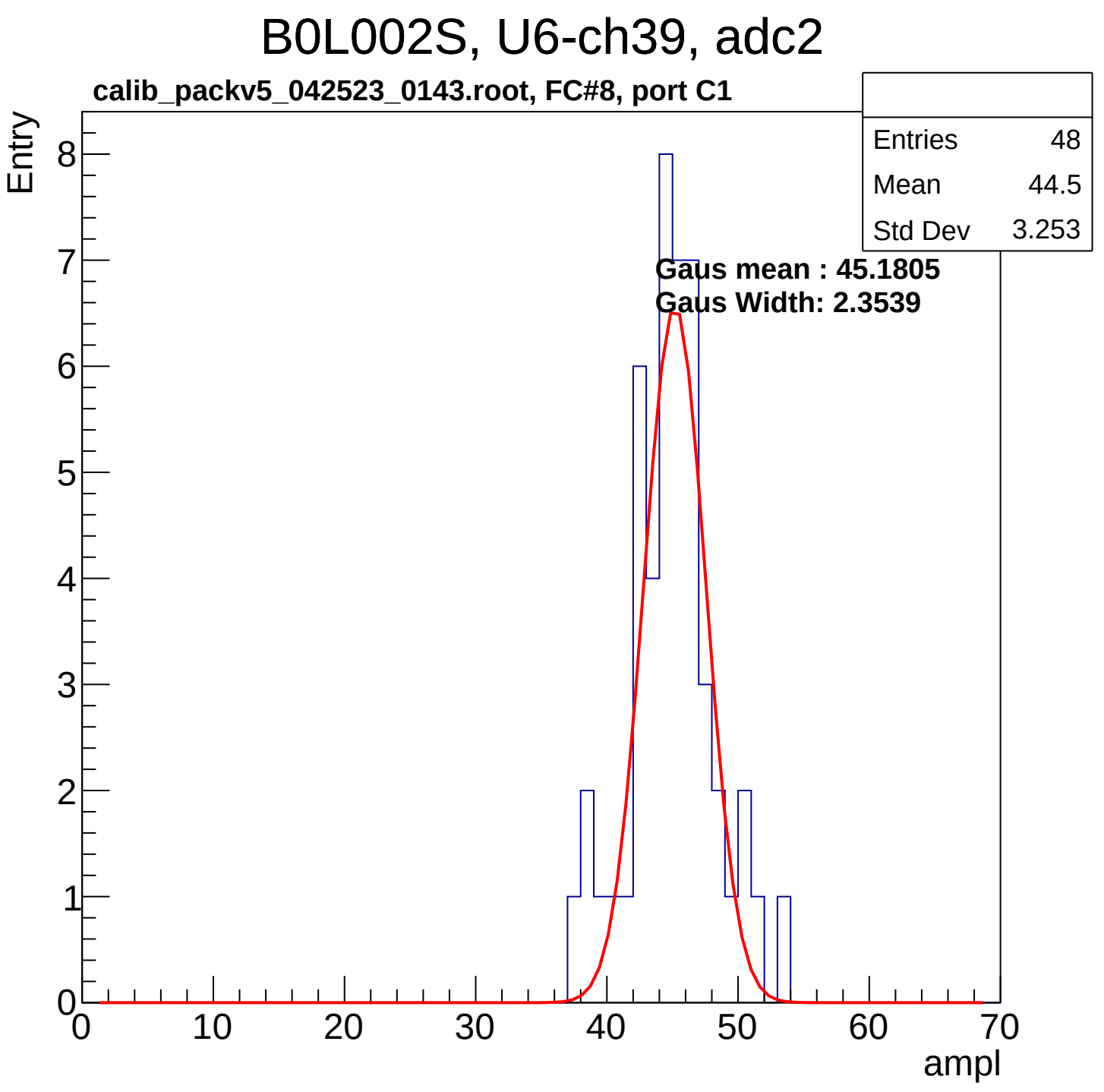
B0L002S, U6-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 44.5
Std Dev 3.253
Gaus mean : 45.1805
Gaus Width: 2.3539
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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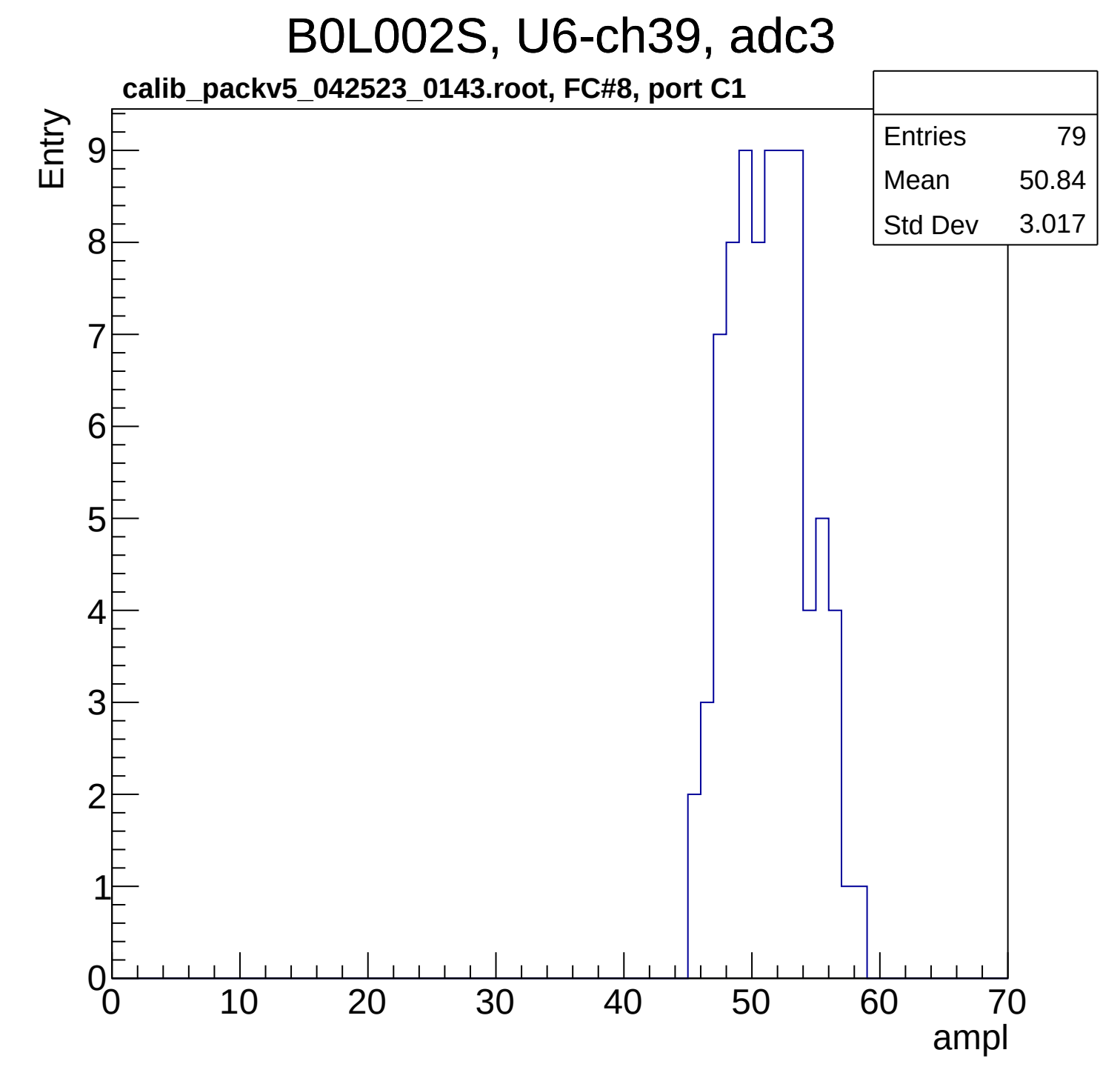
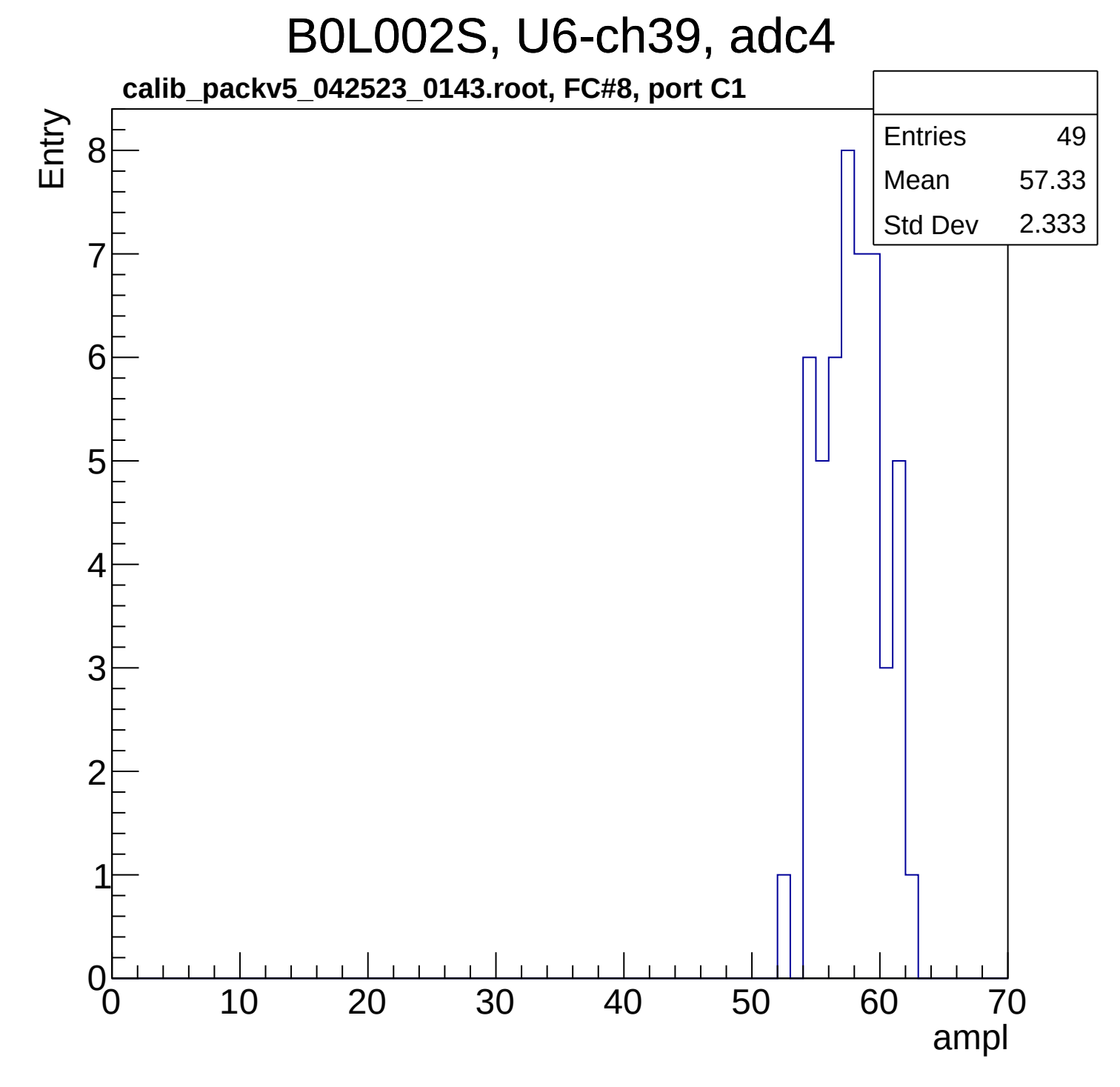

B0L002S, U6-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 50.84
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.33
Std Dev 2.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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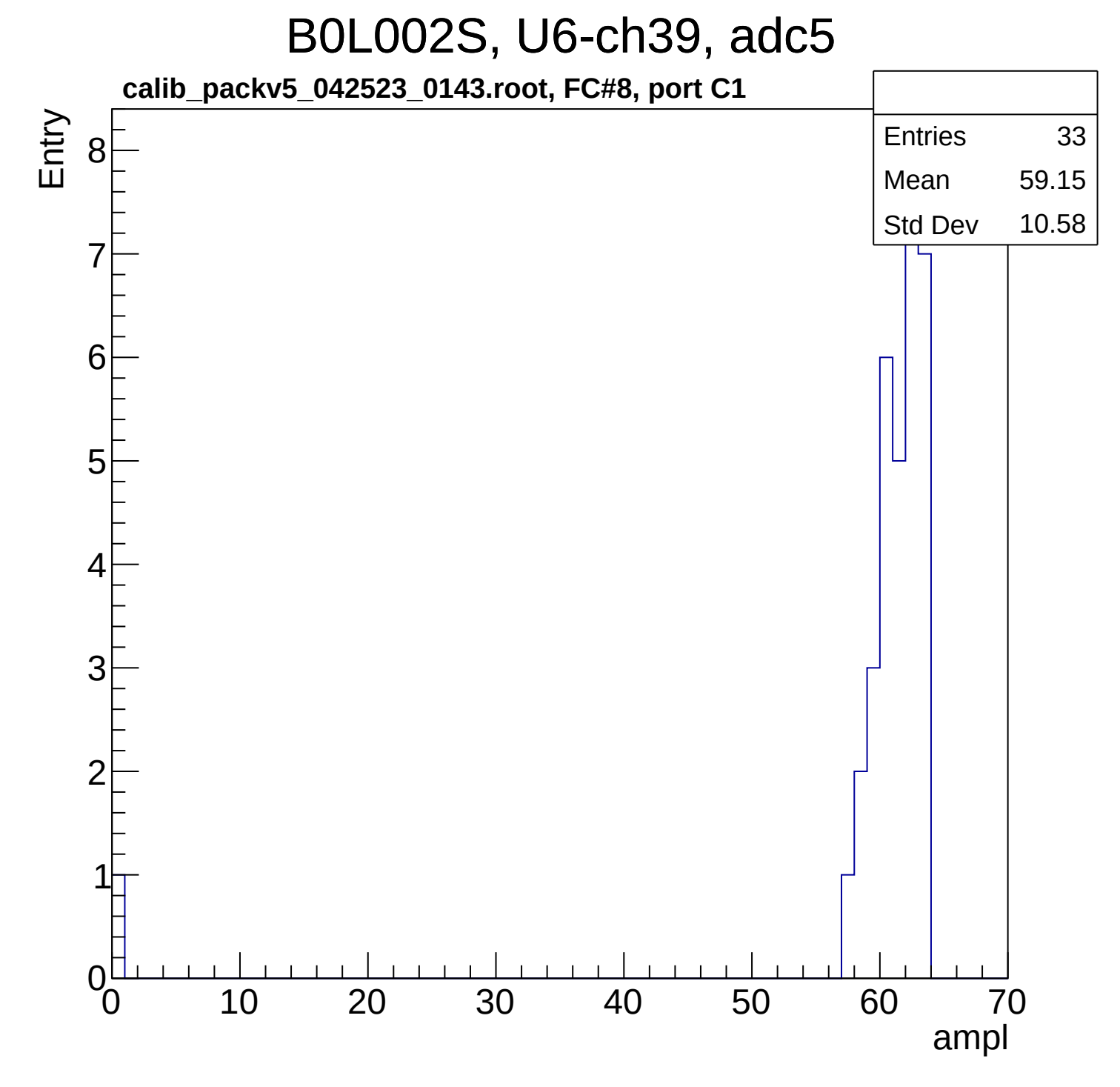
B0L002S, U6-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.15
Std Dev 10.58
Entry
ampl
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1
2
3
4
5
6
7
8
0
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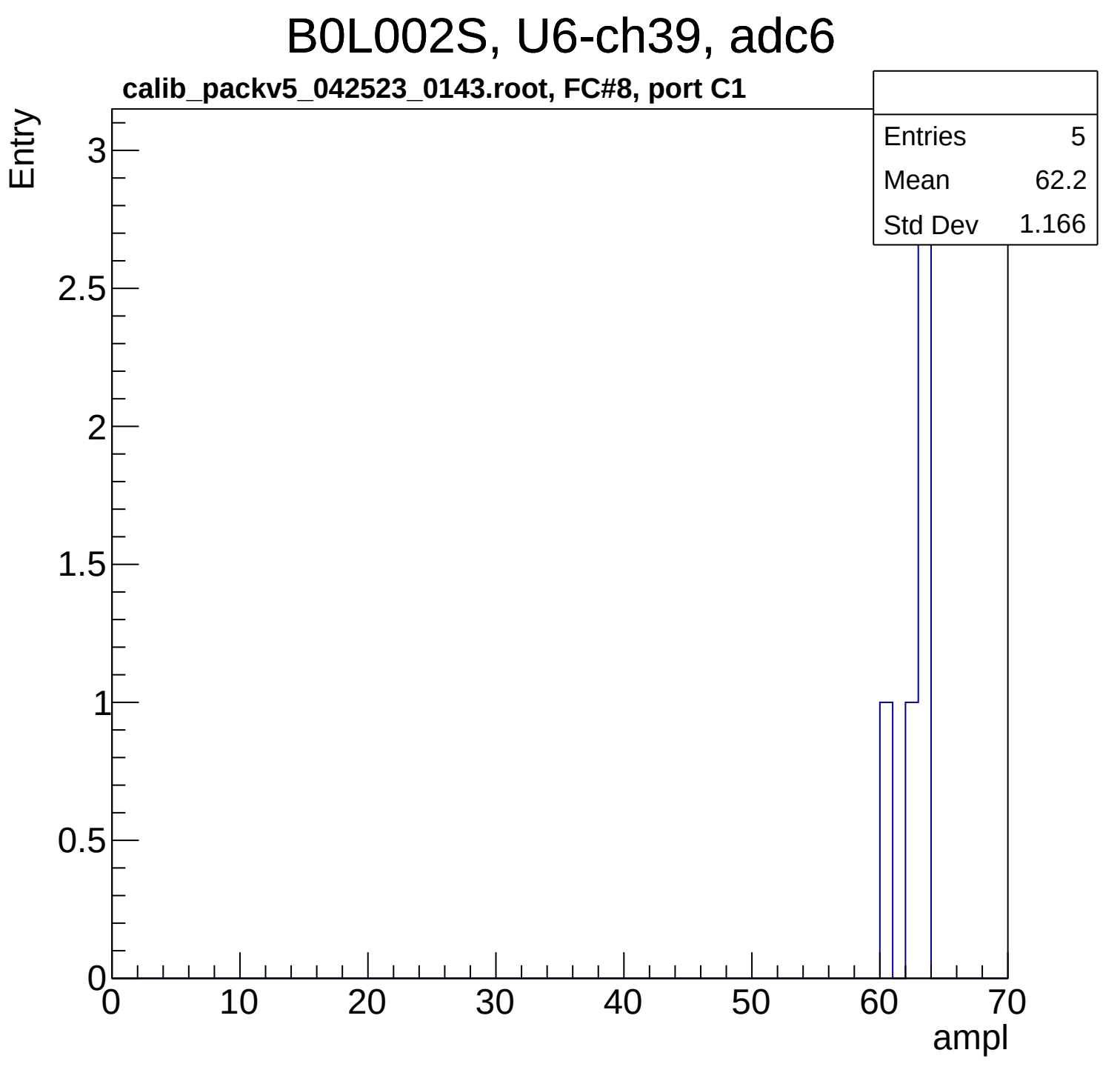
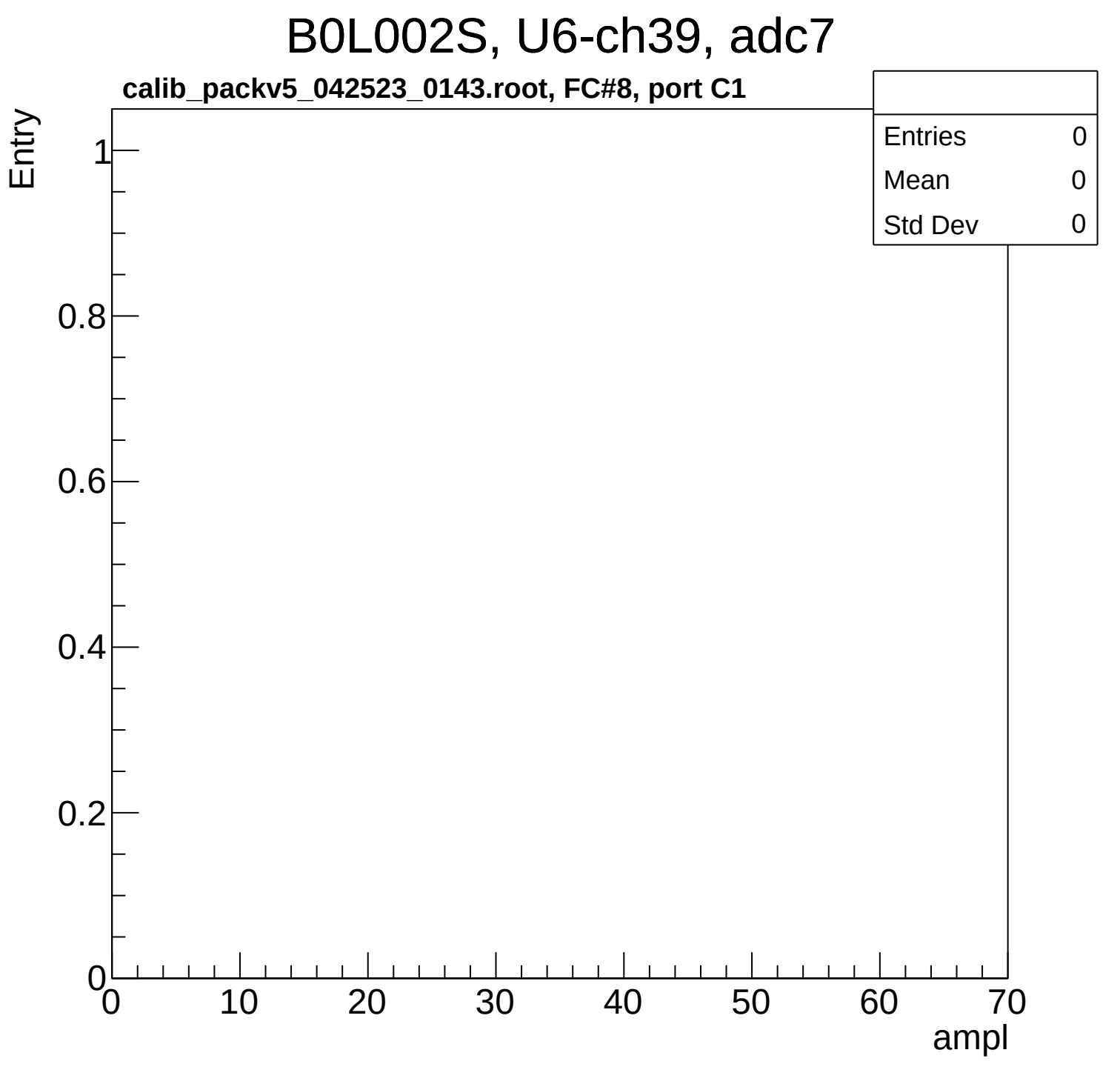
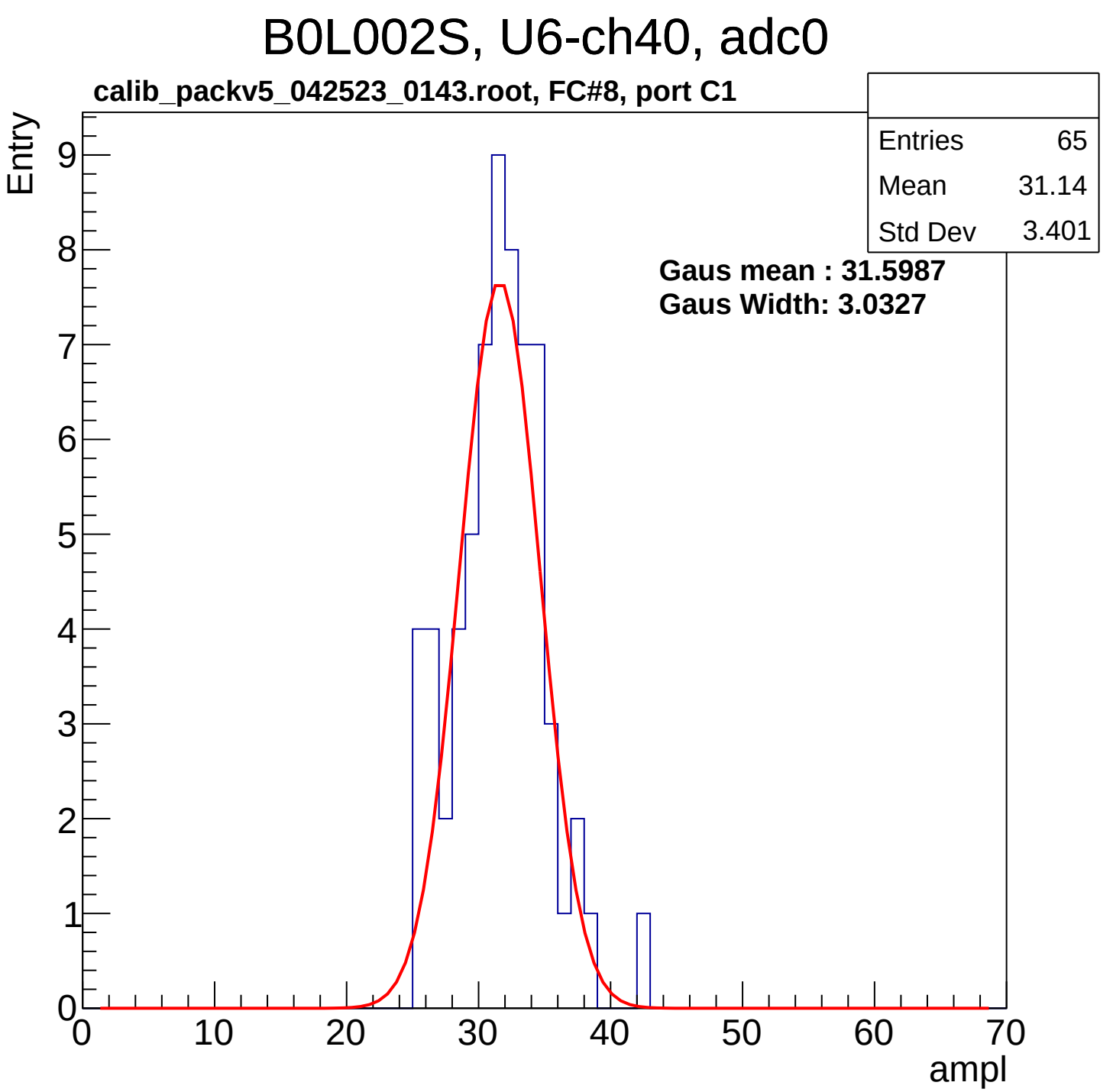
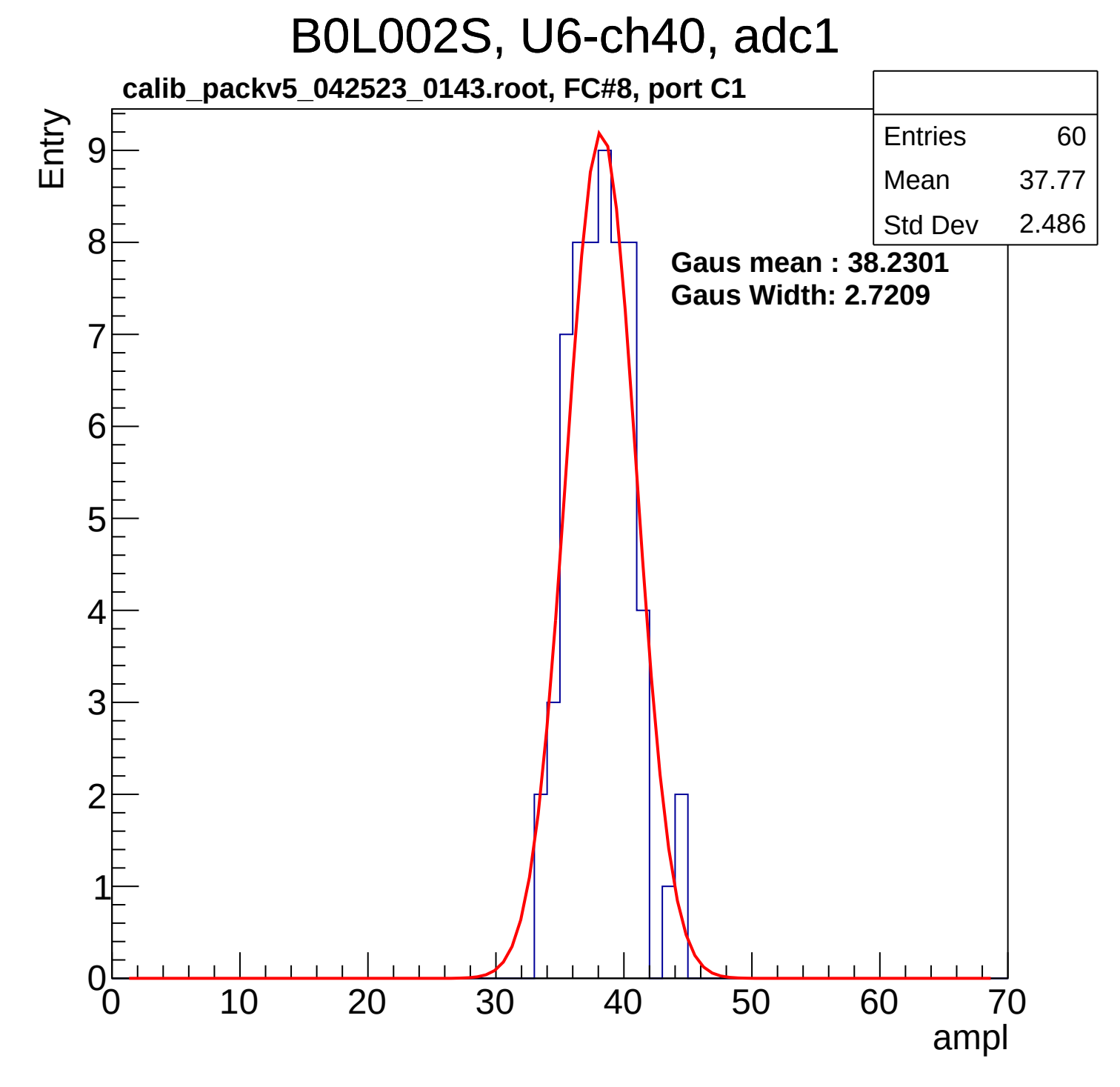
20
30
40
50
60
70


B0L002S, U6-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 31.14
Std Dev 3.401
Gaus mean : 31.5987
Gaus Width: 3.0327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.77
Std Dev 2.486
Gaus mean : 38.2301
Gaus Width: 2.7209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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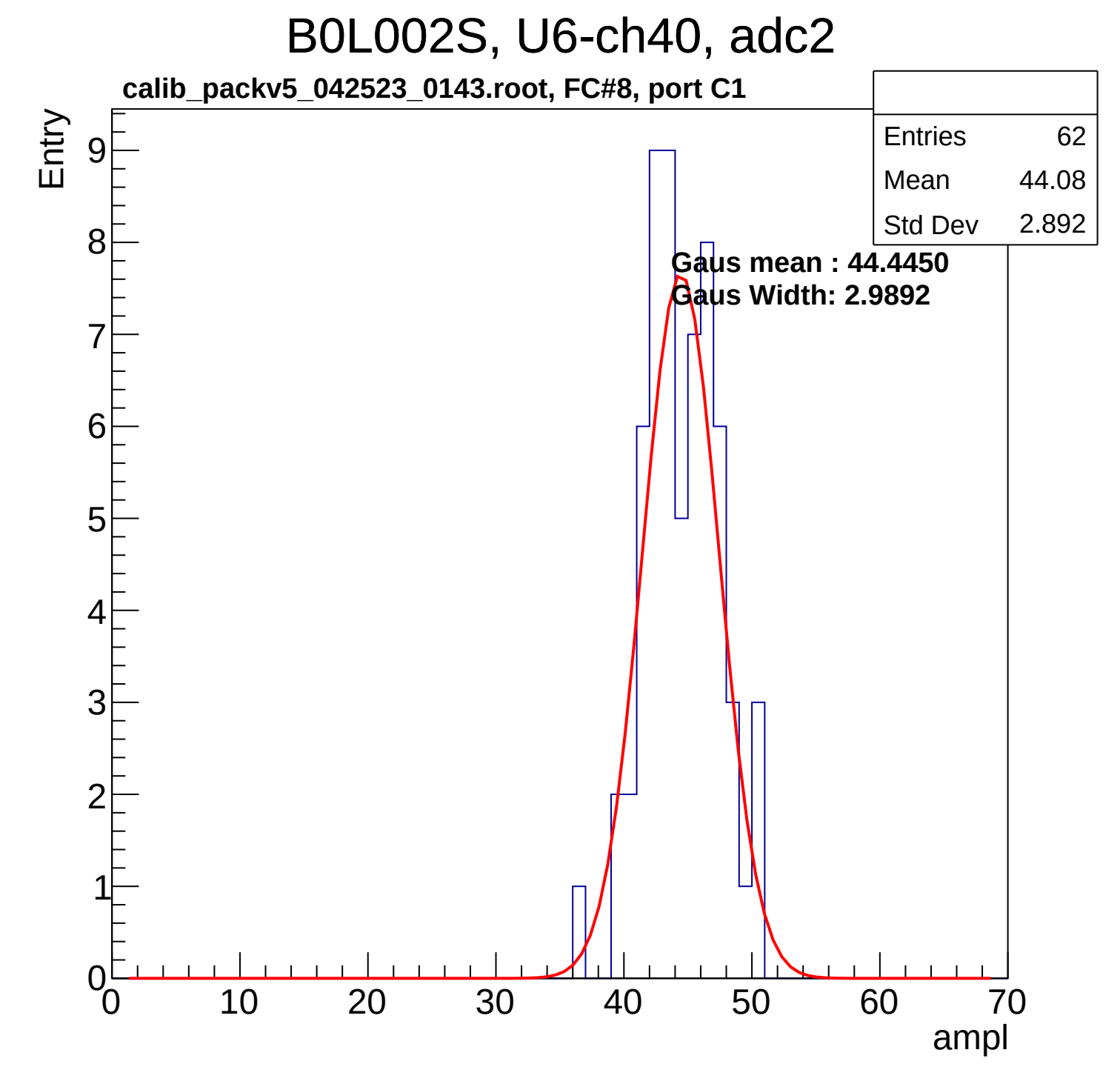
B0L002S, U6-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.08
Std Dev 2.892
Gaus mean : 44.4450
Gaus Width: 2.9892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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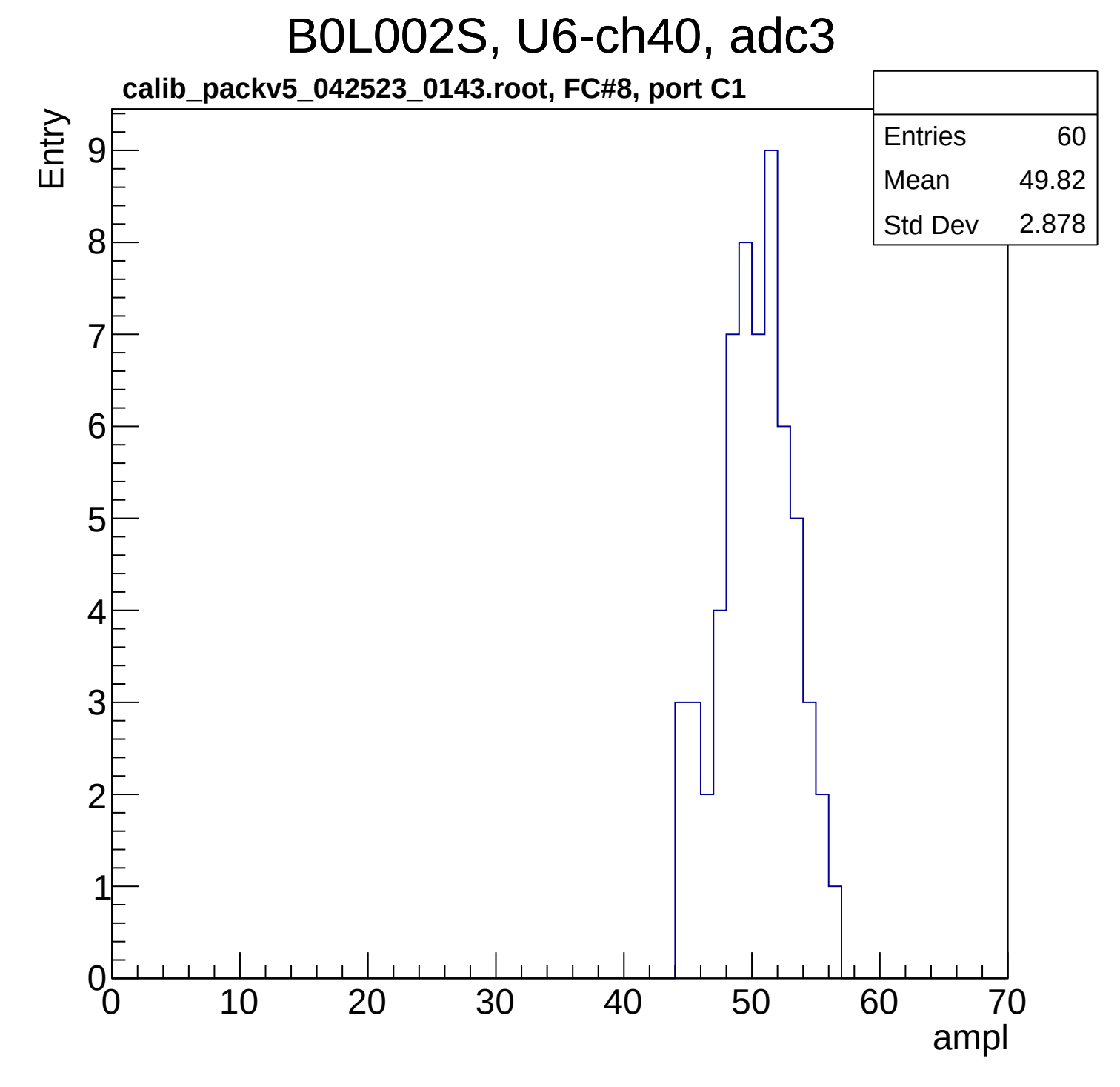
B0L002S, U6-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 49.82
Std Dev 2.878
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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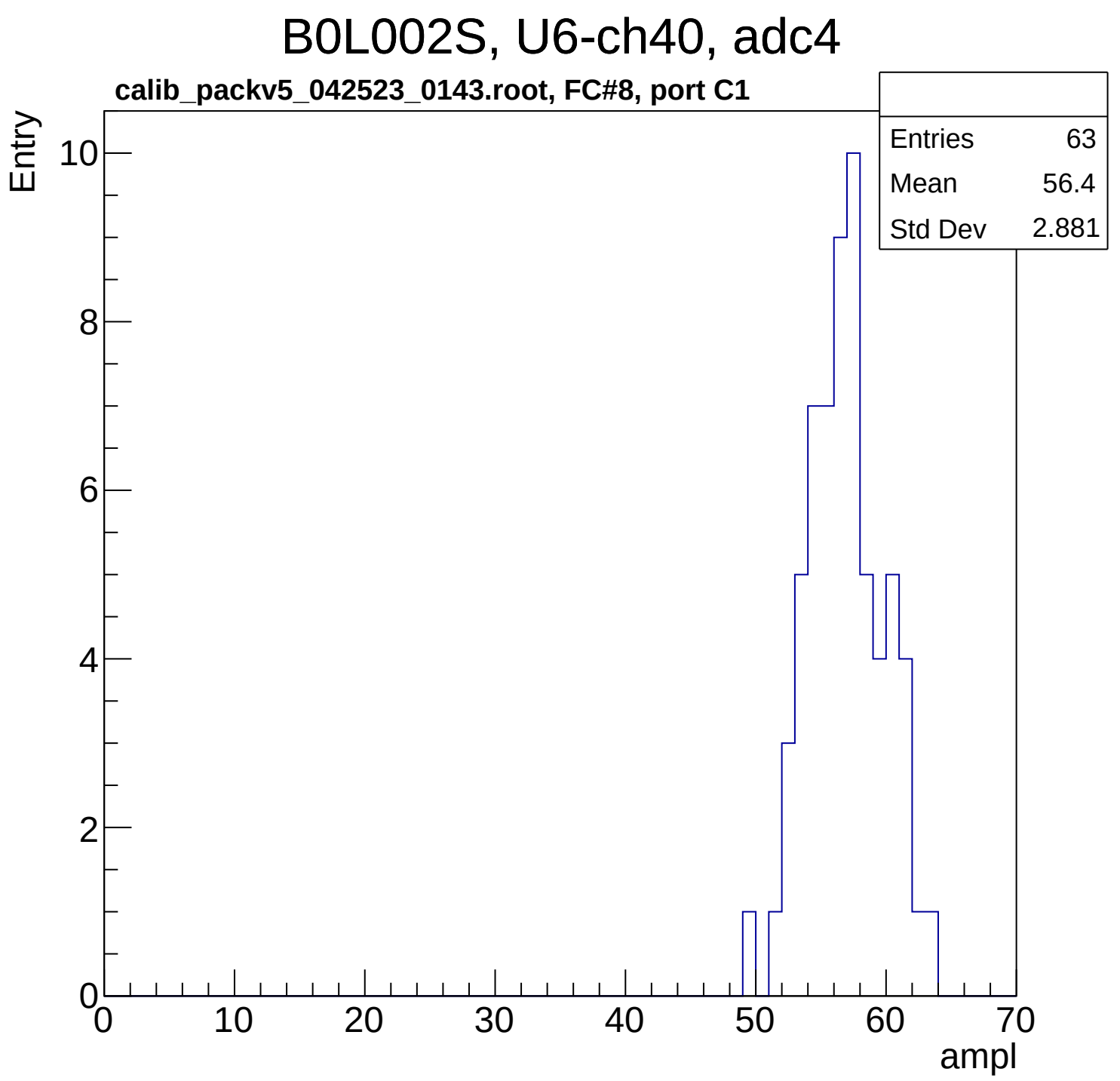
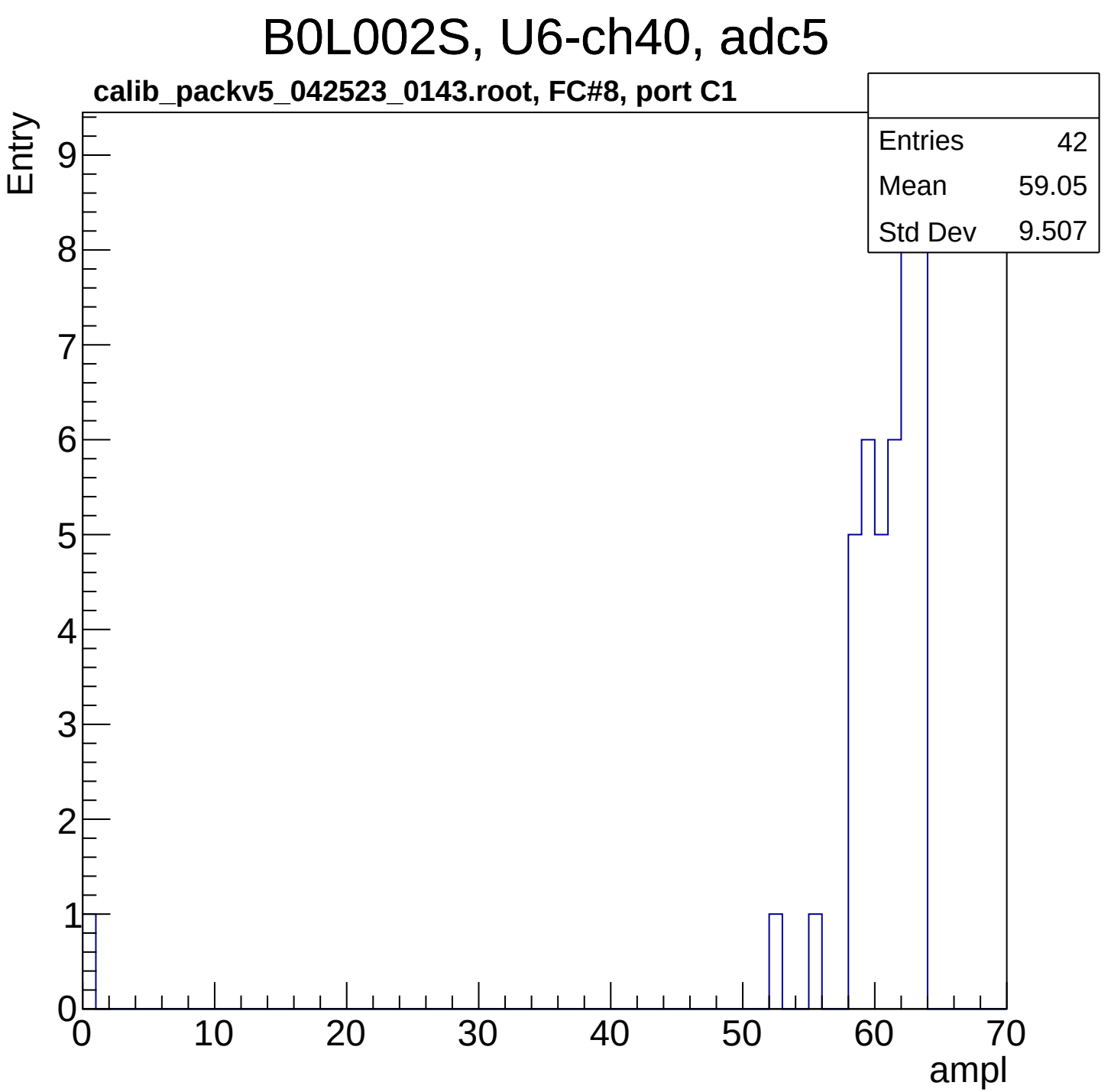
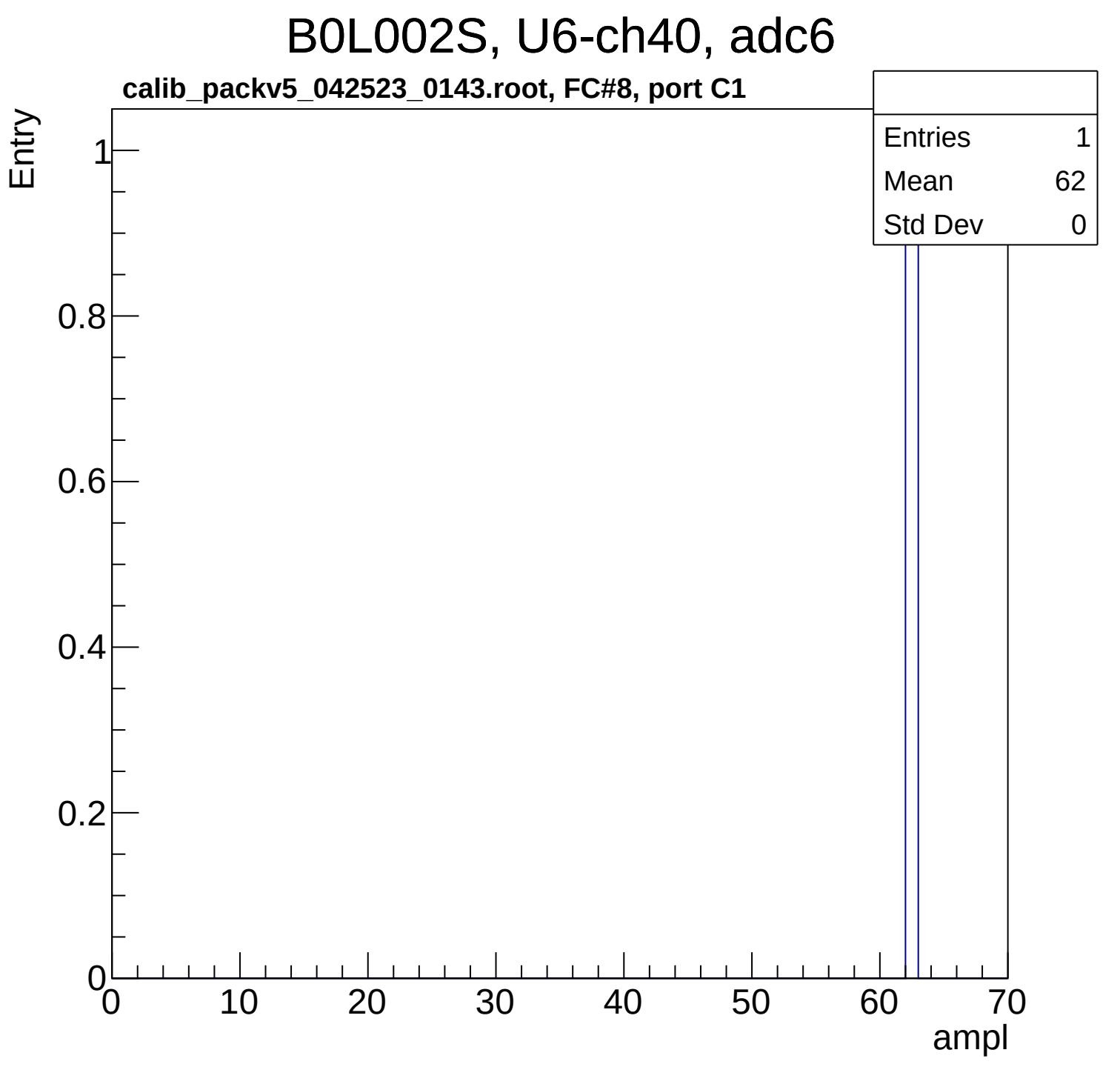

B0L002S, U6-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.4
Std Dev 2.881
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.05
Std Dev 9.507
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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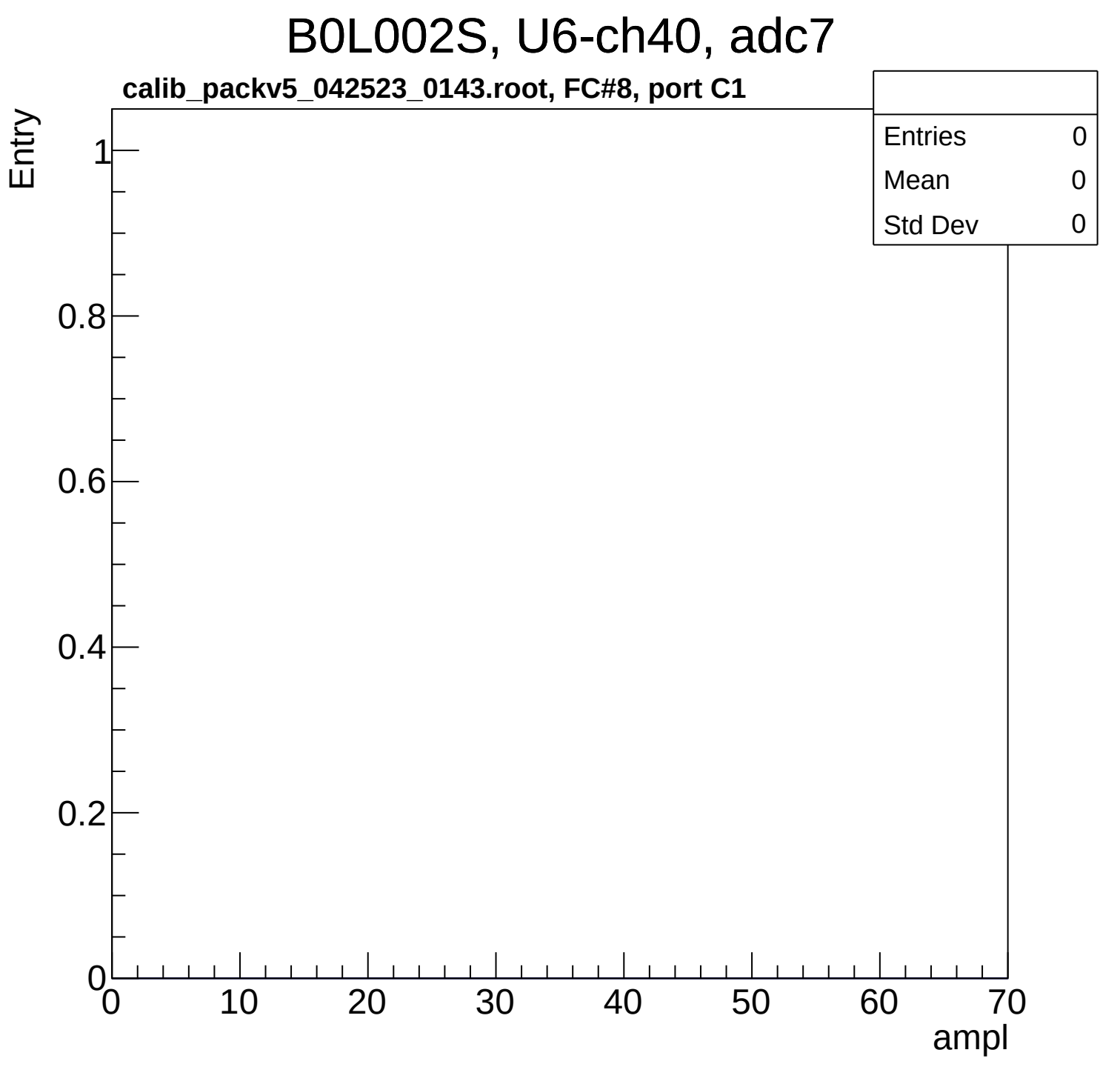
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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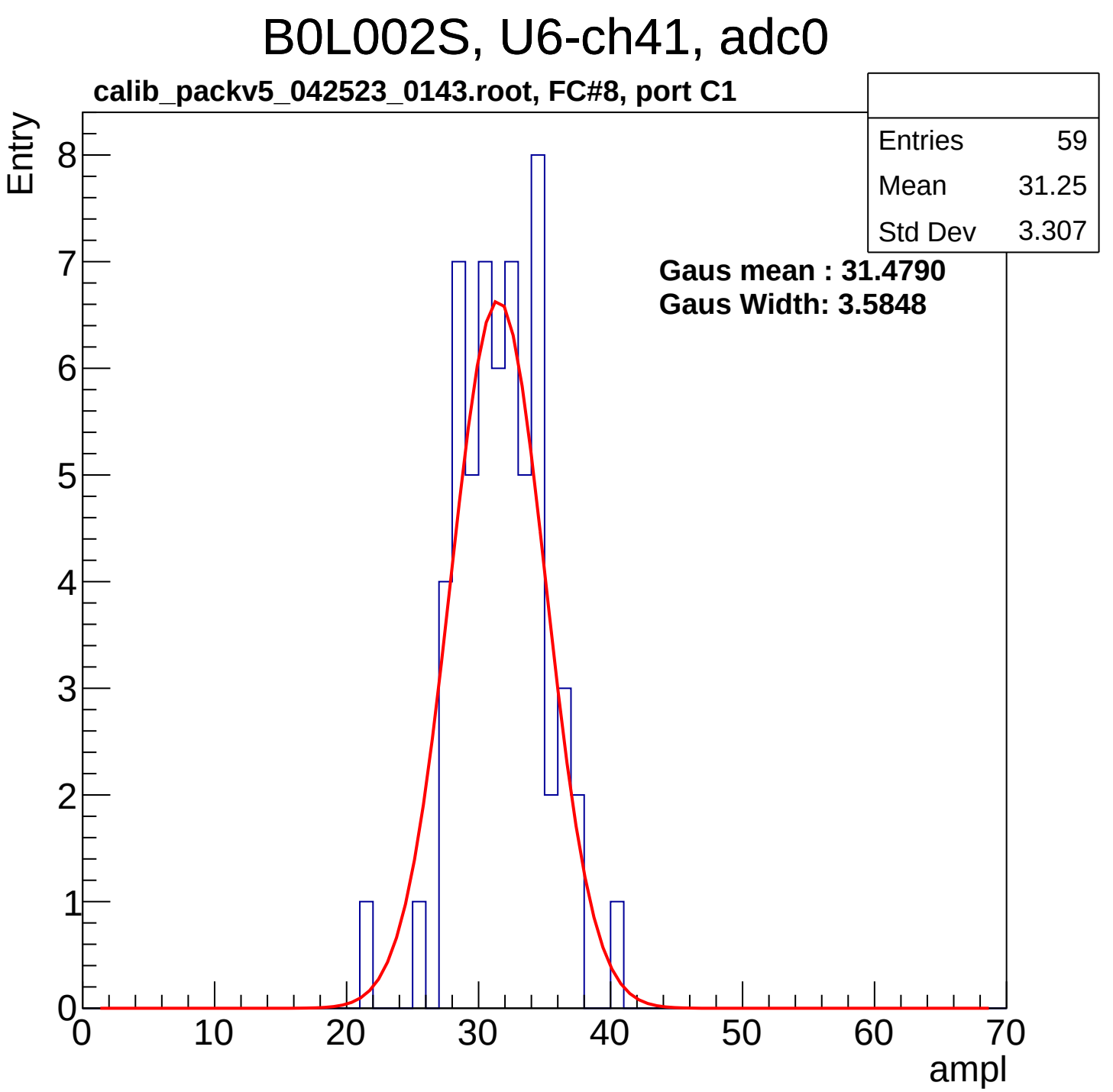
30
40
50
60
70
ampl


B0L002S, U6-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 31.25
Std Dev 3.307
Gaus mean : 31.4790
Gaus Width: 3.5848
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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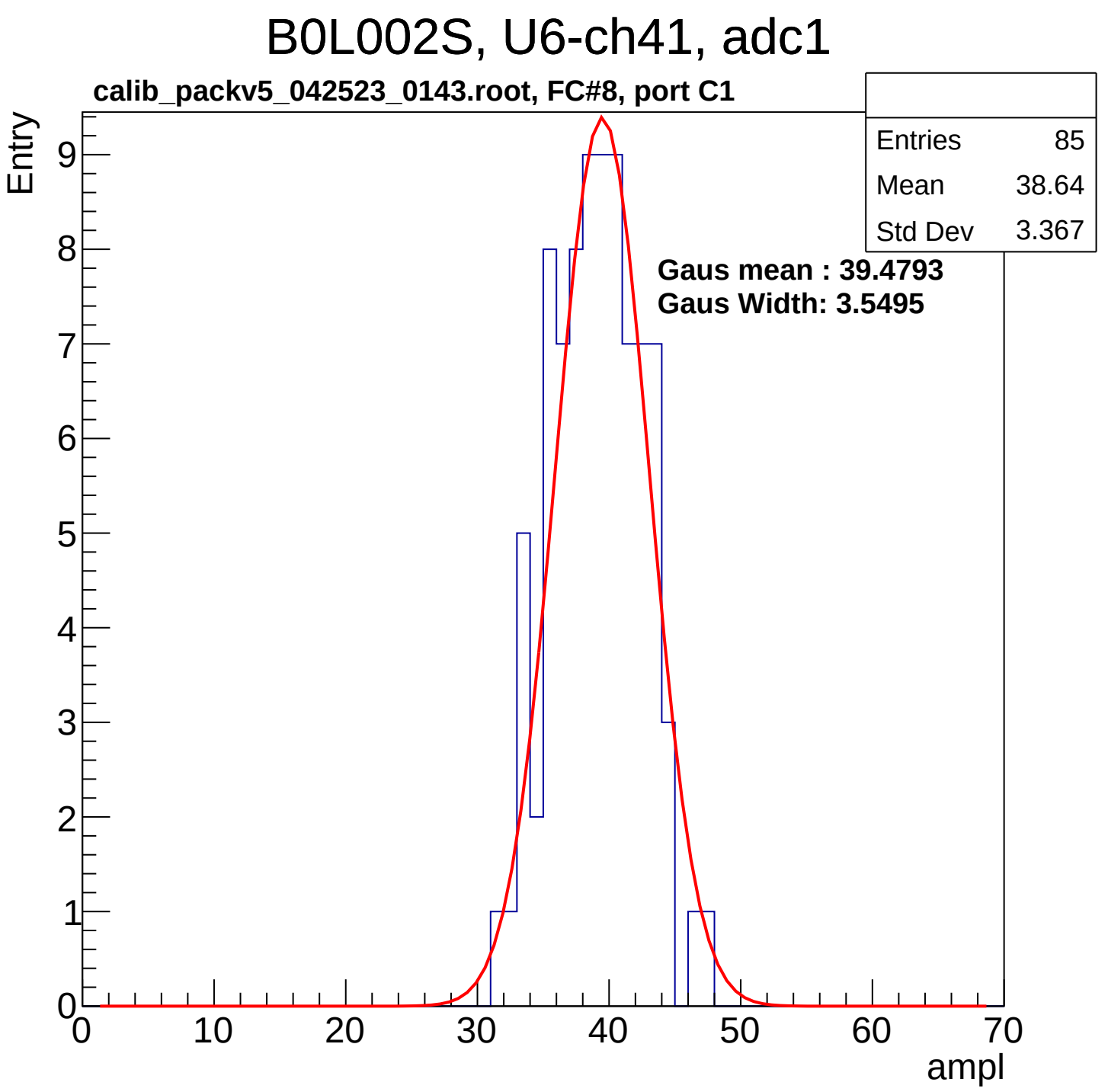
40
50
60
70


B0L002S, U6-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 38.64
Std Dev 3.367
Gaus mean : 39.4793
Gaus Width: 3.5495
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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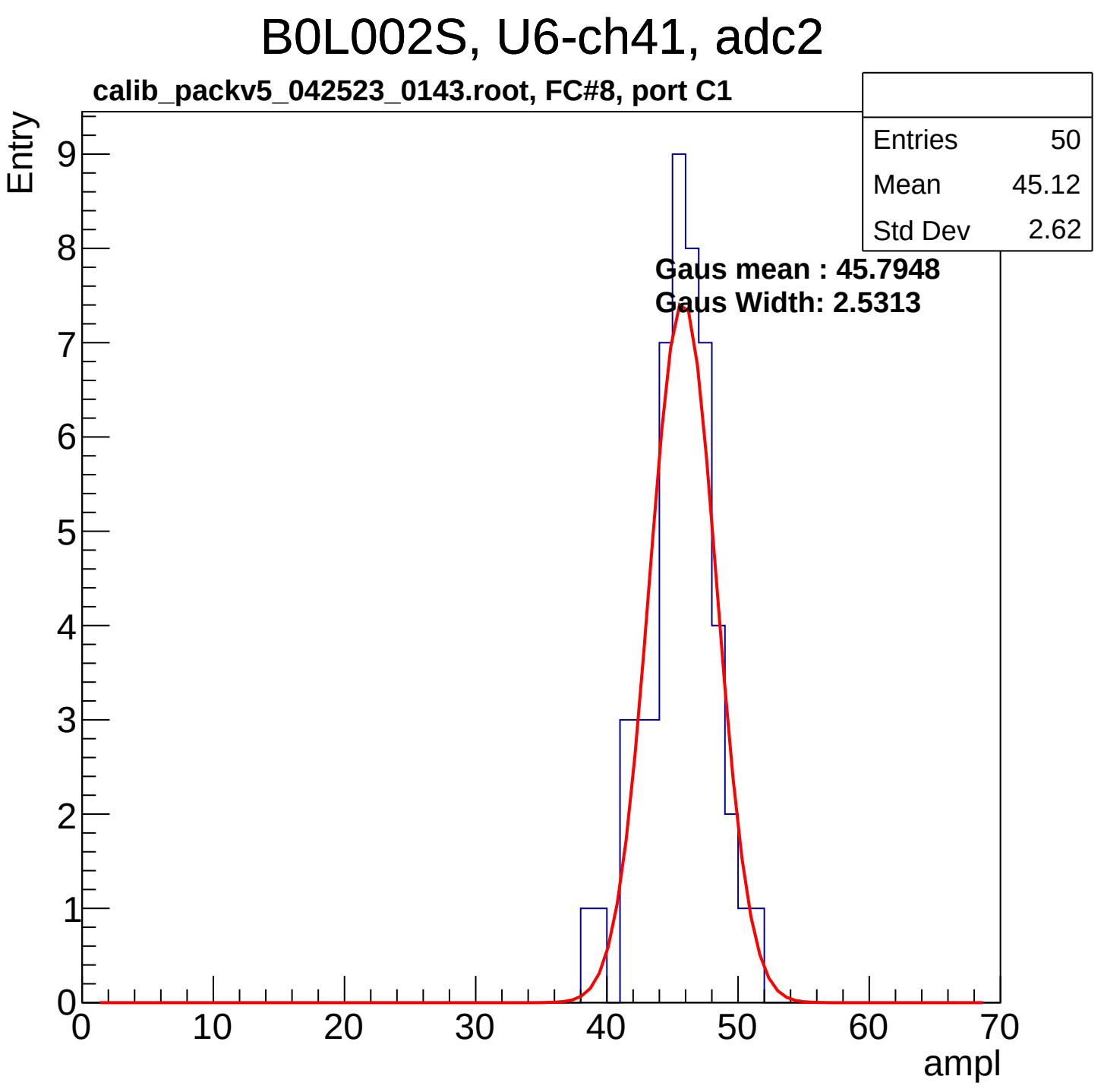
B0L002S, U6-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 45.12
Std Dev 2.62
Gaus mean : 45.7948
Gaus Width: 2.5313
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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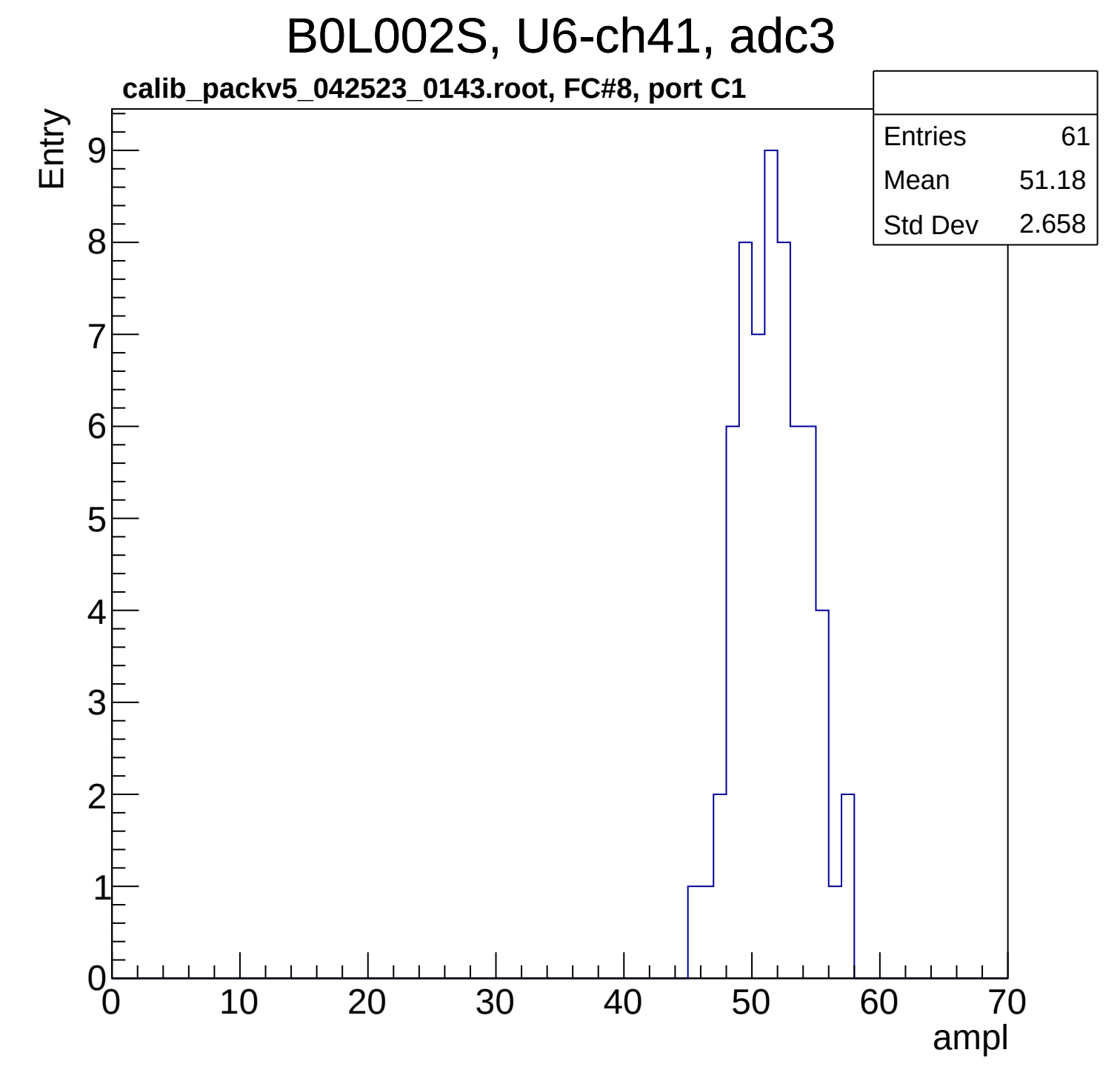
B0L002S, U6-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.18
Std Dev 2.658
Entry
ampl
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6
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0
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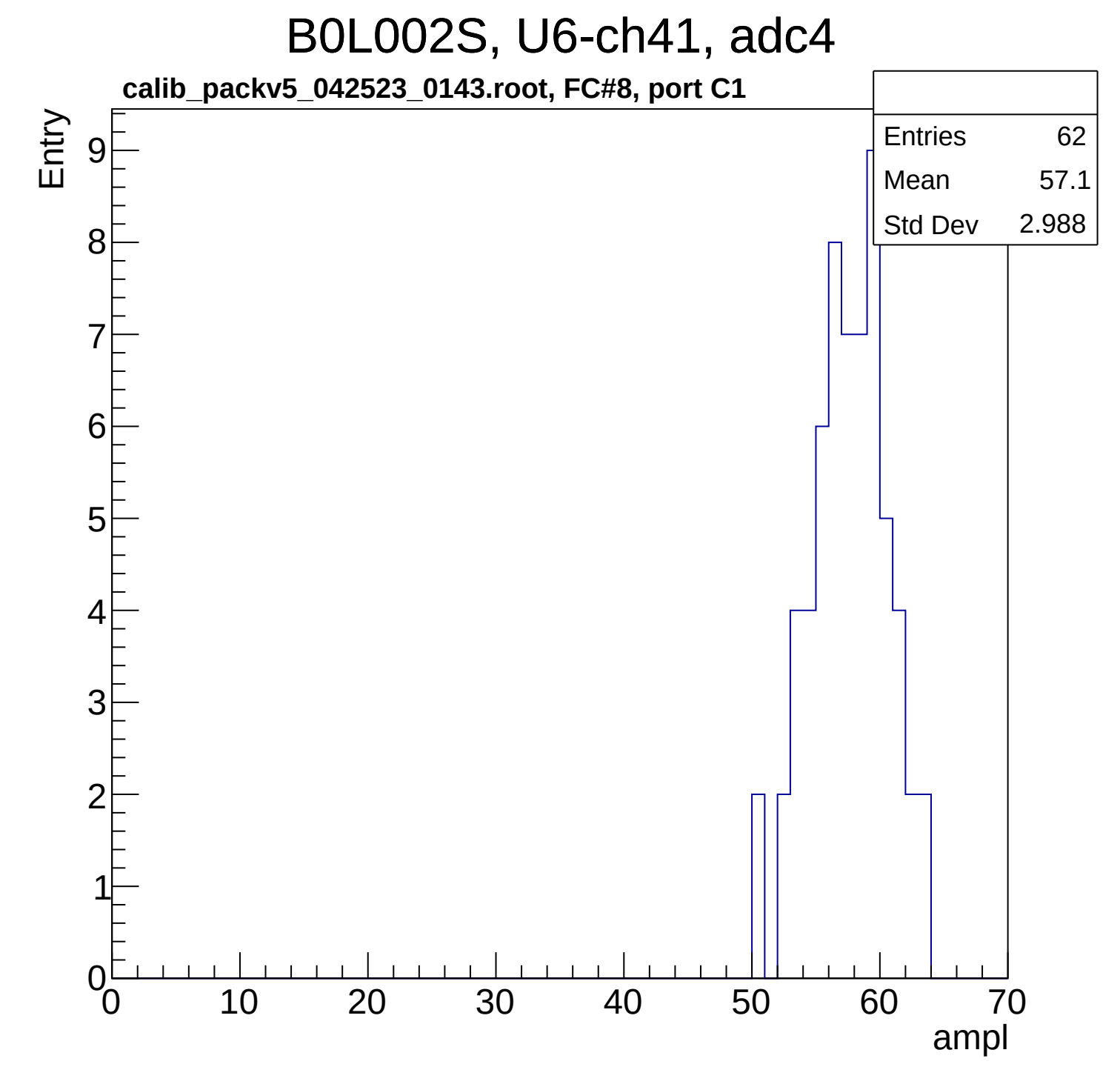
B0L002S, U6-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.1
Std Dev 2.988
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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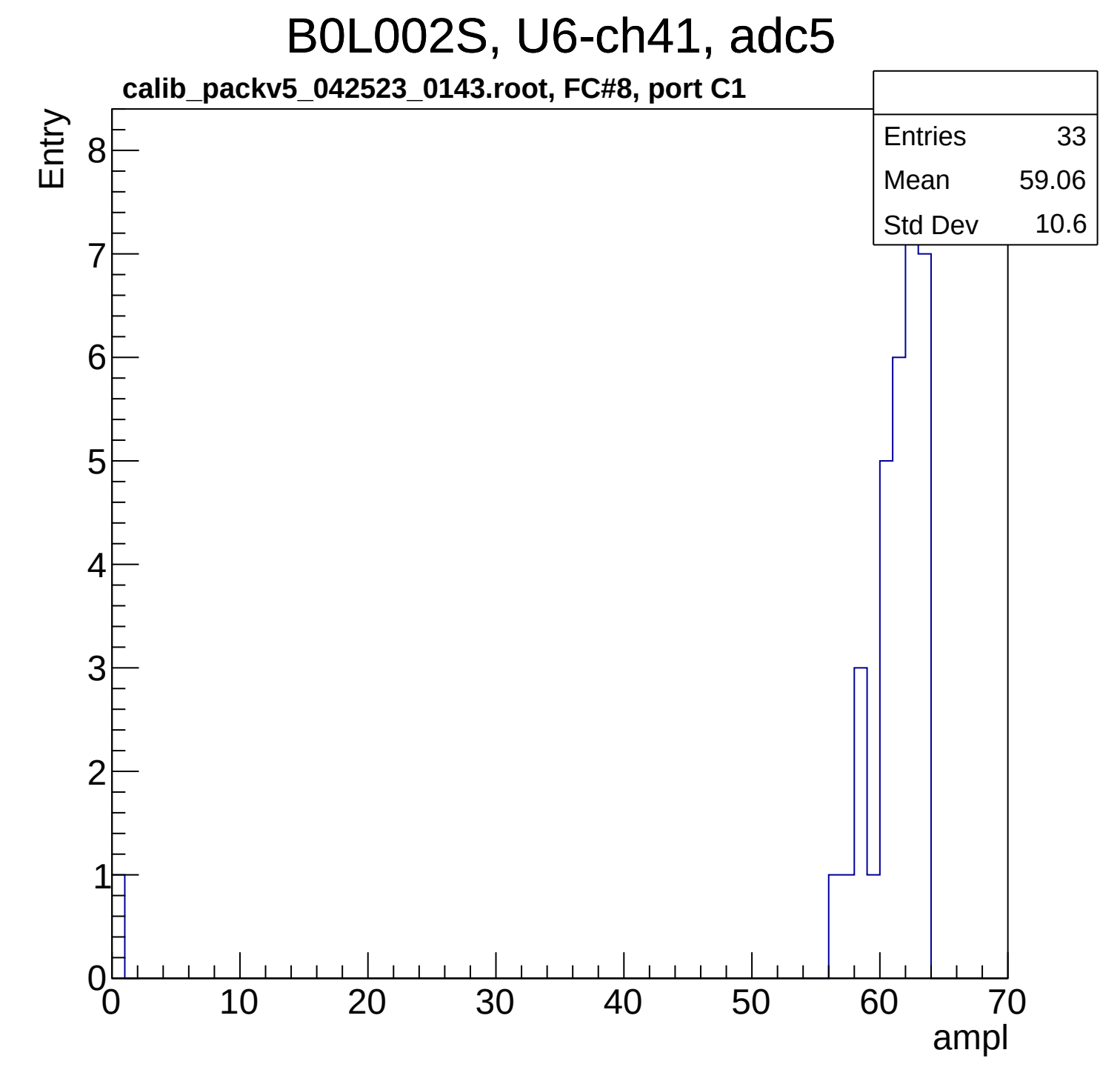
B0L002S, U6-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.06
Std Dev 10.6
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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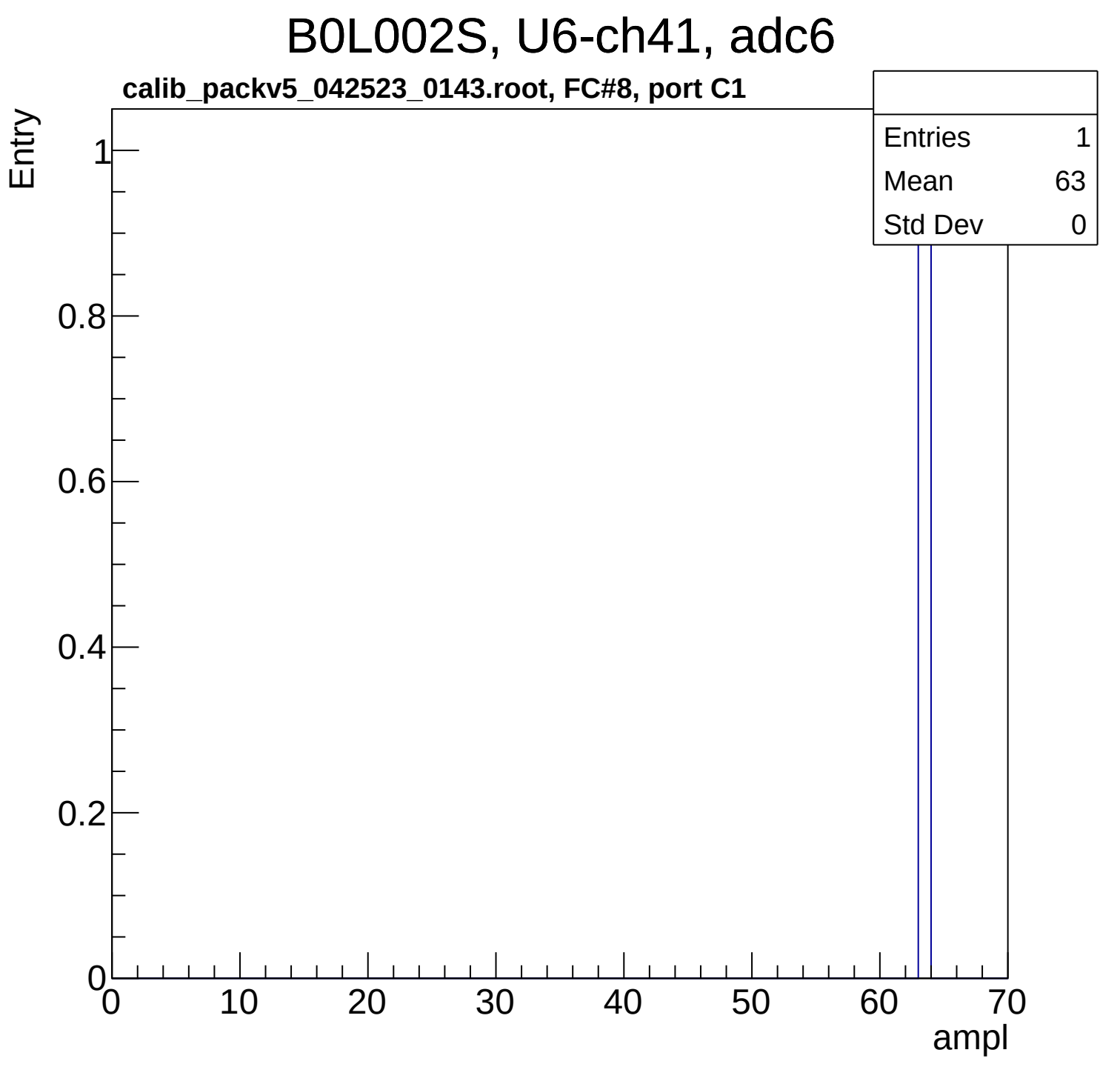
B0L002S, U6-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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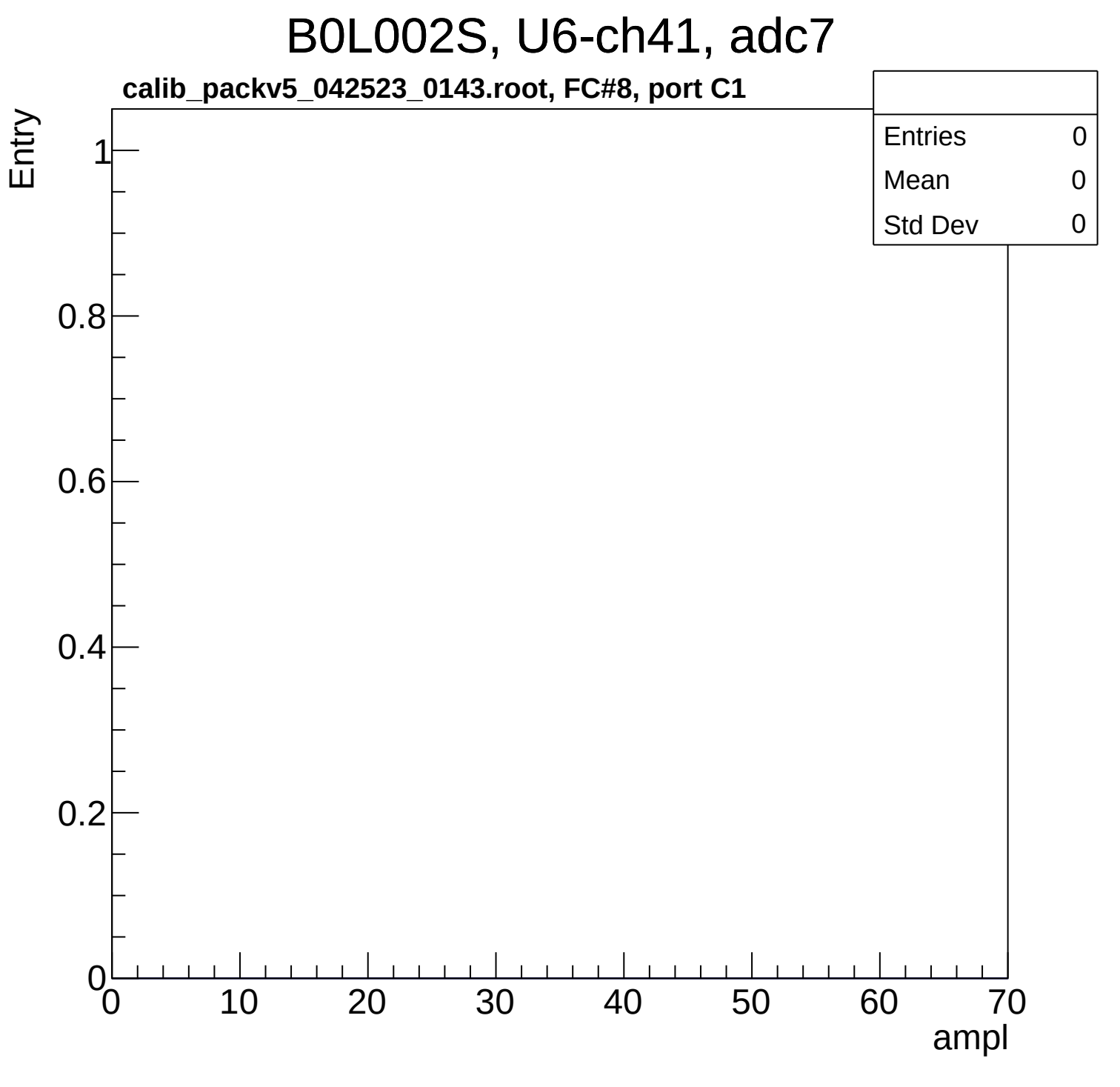
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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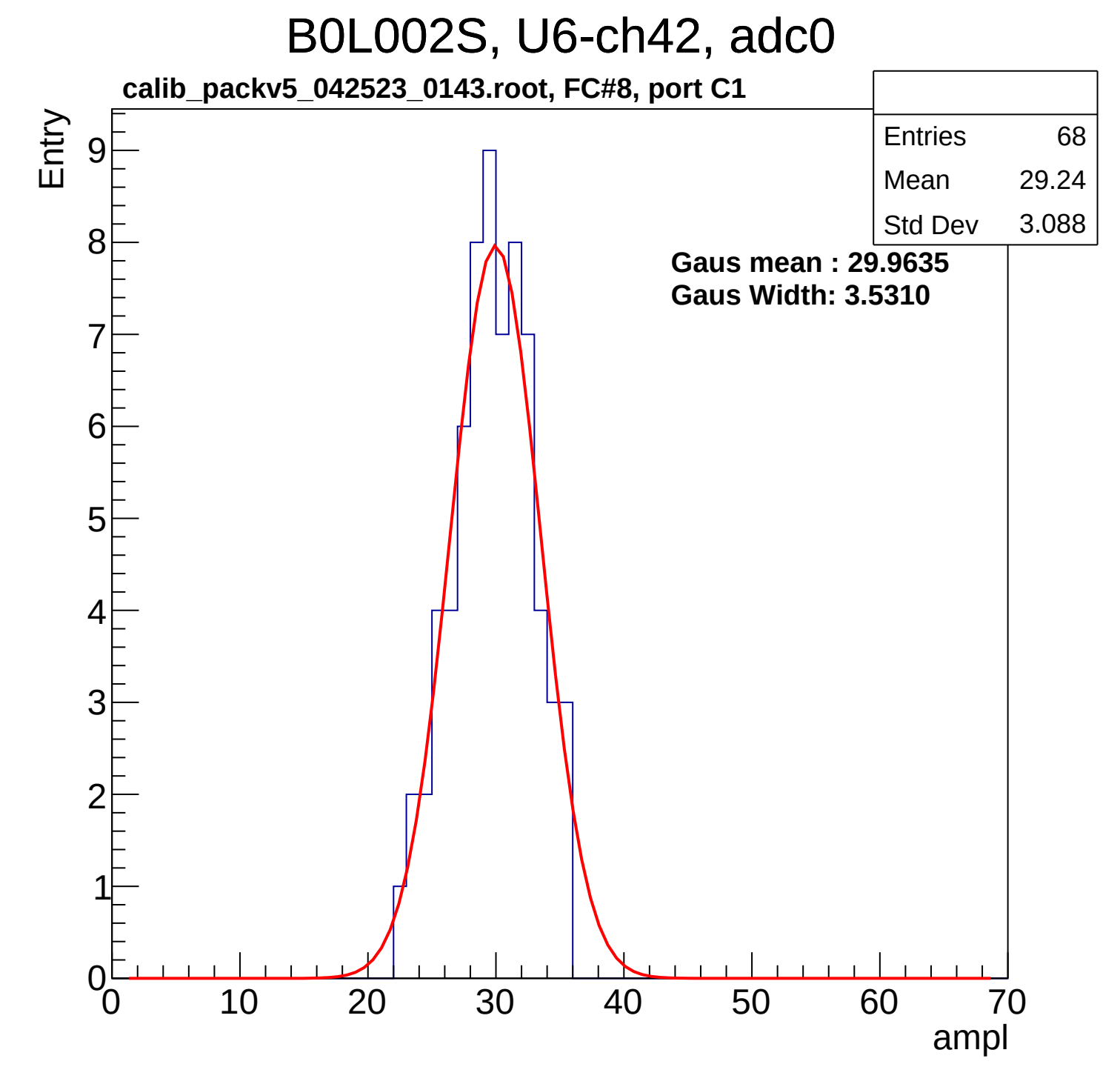
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.24
Std Dev 3.088
Gaus mean : 29.9635
Gaus Width: 3.5310
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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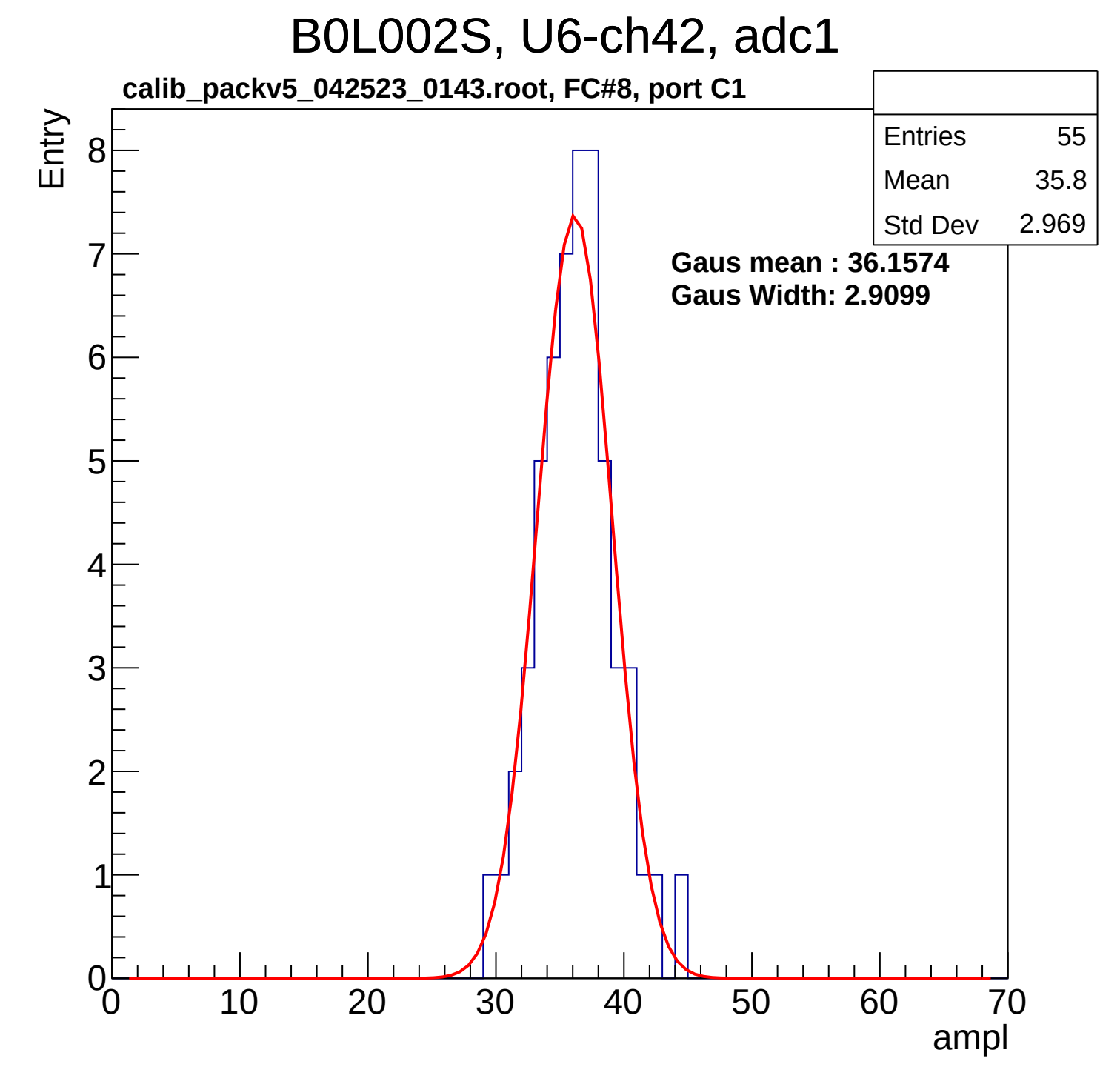
B0L002S, U6-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 35.8
Std Dev 2.969
Gaus mean : 36.1574
Gaus Width: 2.9099
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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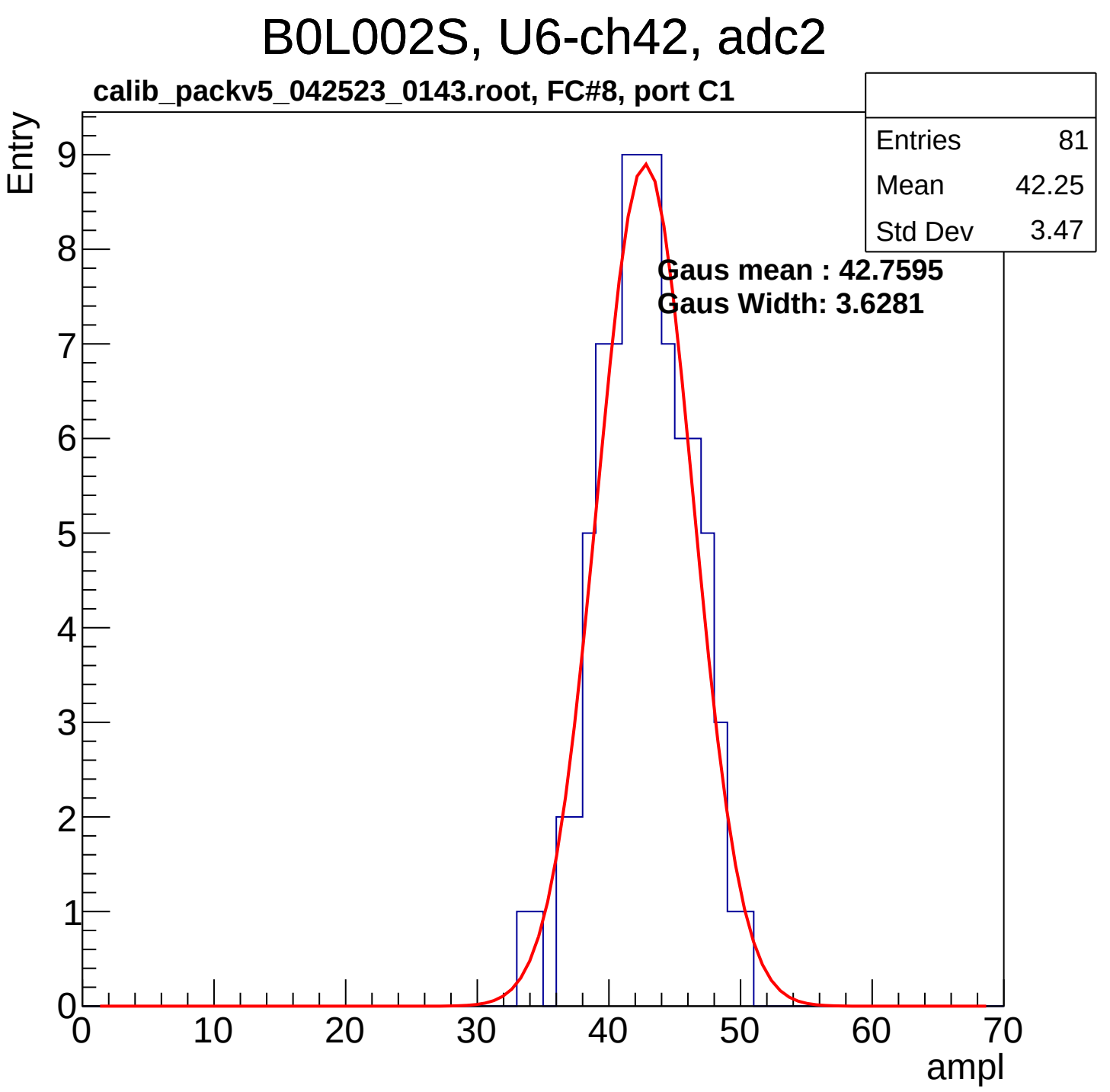
B0L002S, U6-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 42.25
Std Dev 3.47
Gaus mean : 42.7595
Gaus Width: 3.6281
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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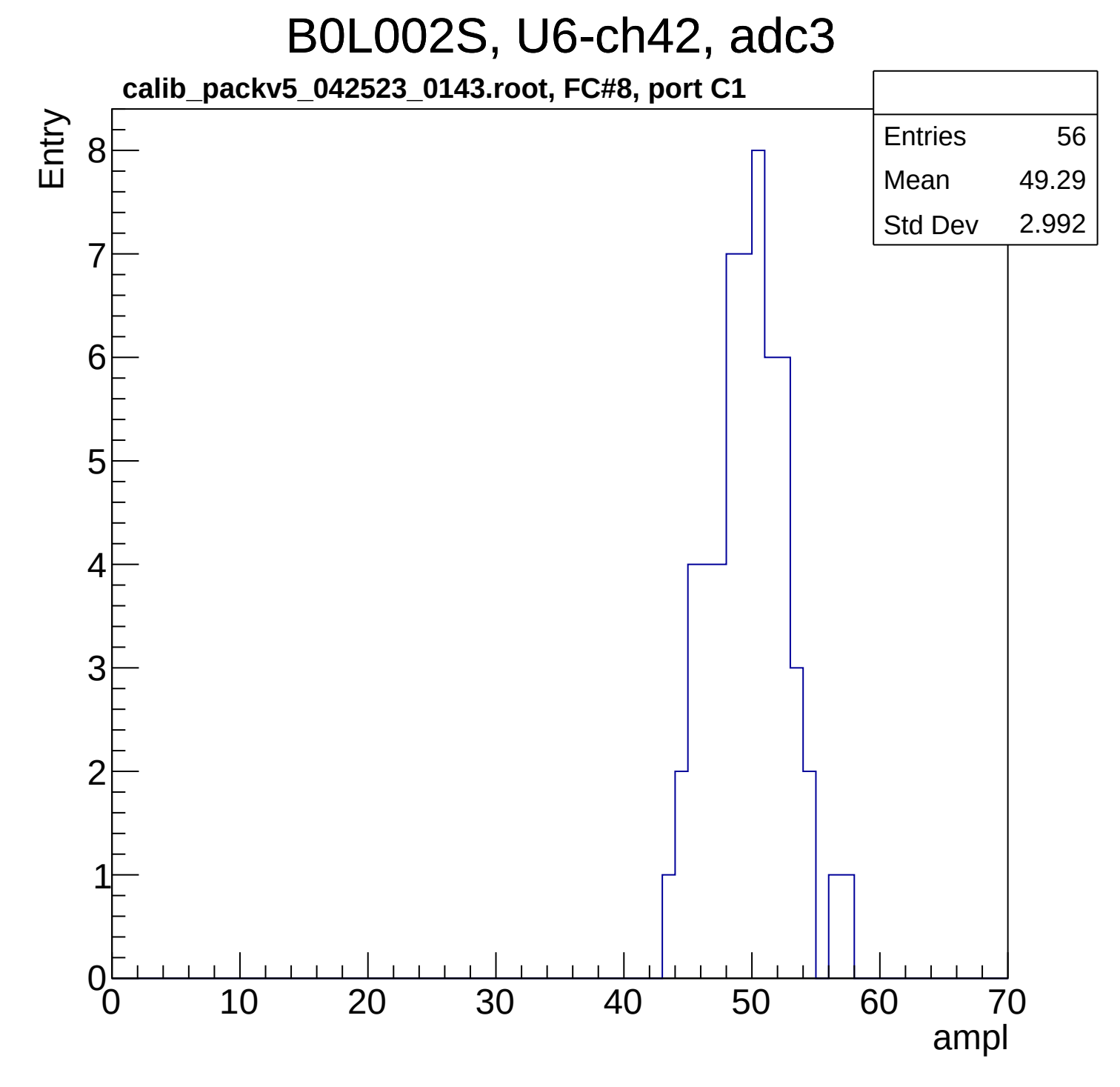
B0L002S, U6-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 49.29
Std Dev 2.992
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
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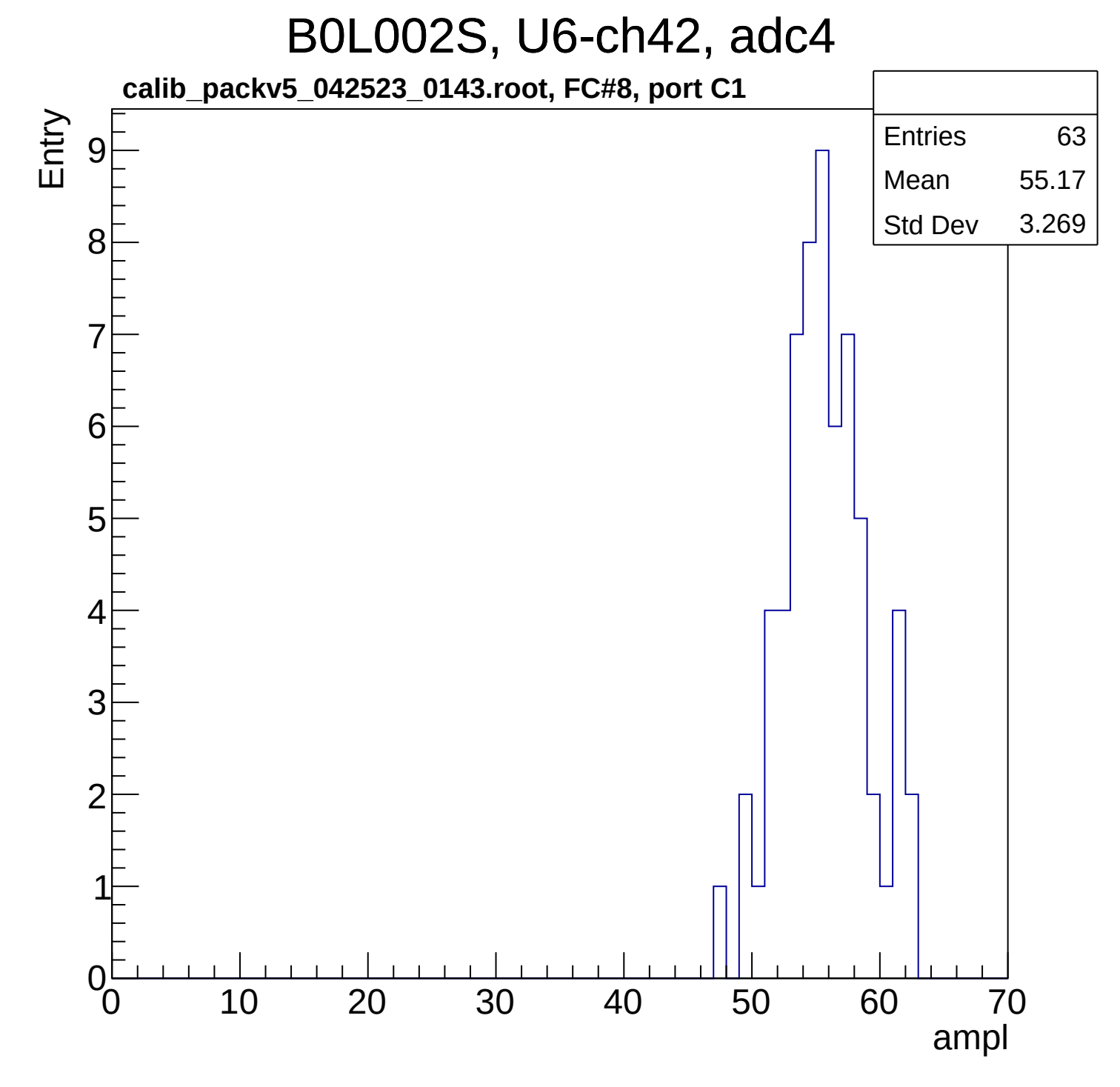
B0L002S, U6-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.17
Std Dev 3.269
Entry
ampl
0
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2
3
4
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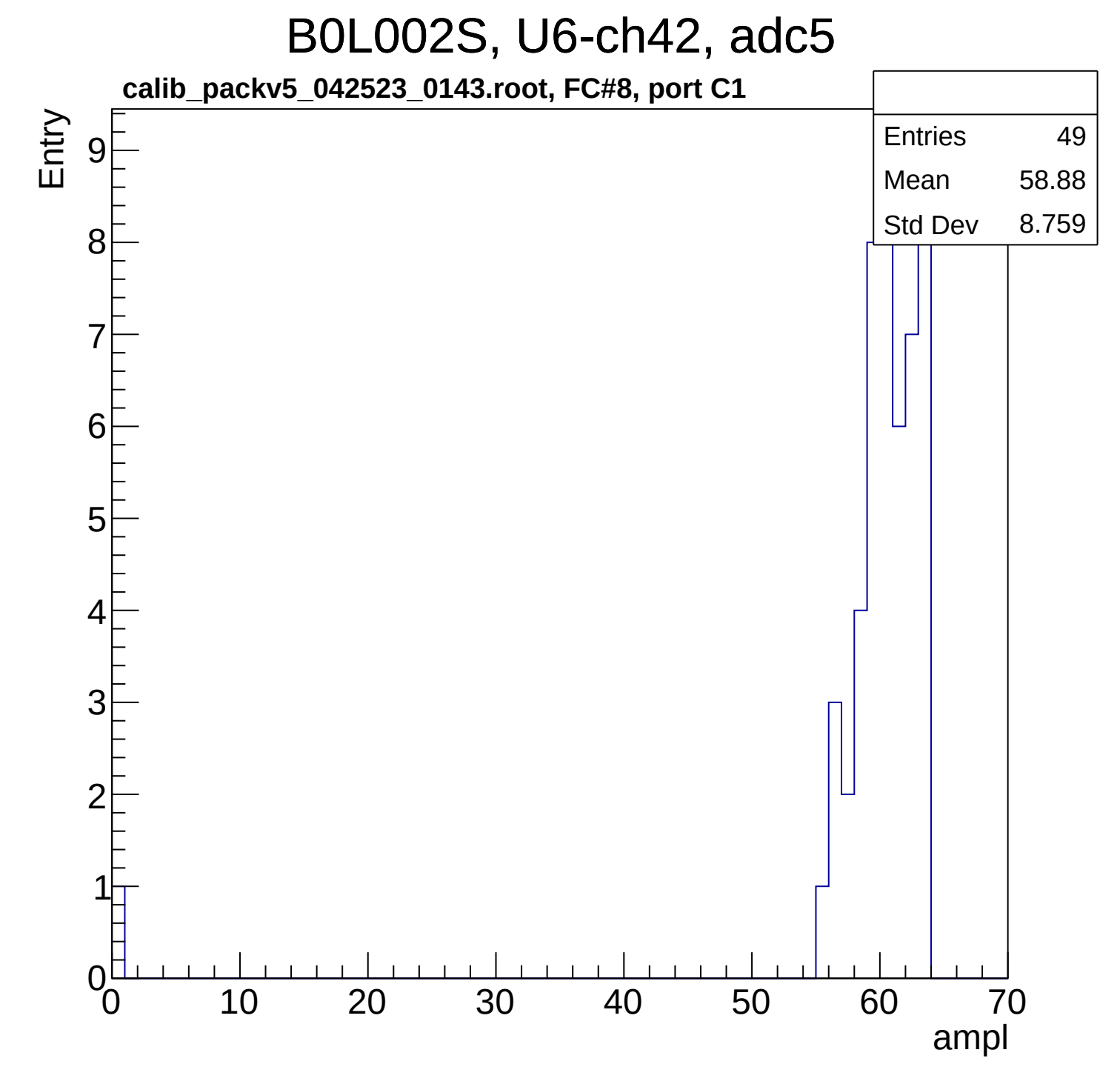
B0L002S, U6-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.88
Std Dev 8.759
Entry
ampl
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2
3
4
5
6
7
8
9
0
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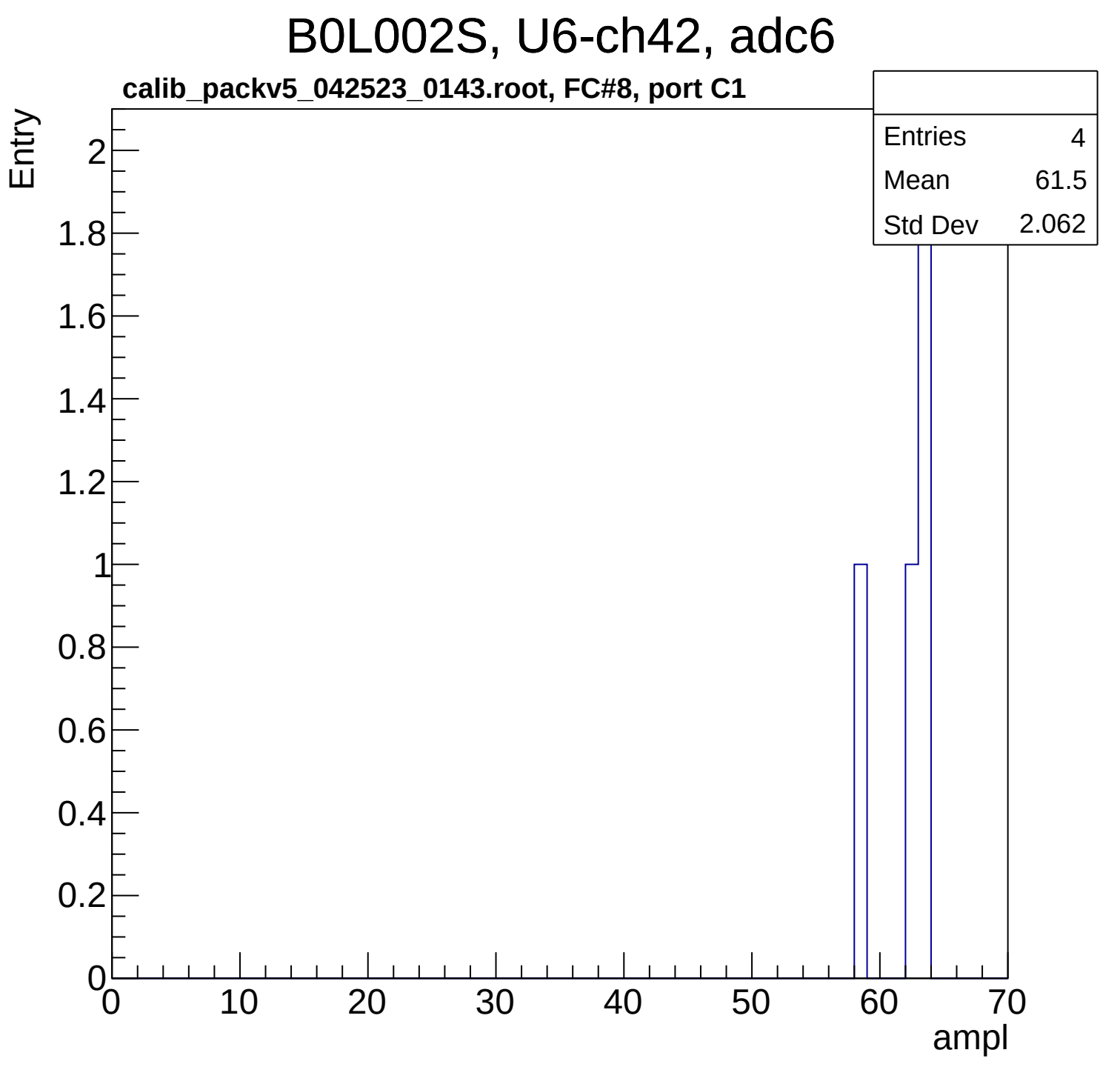
B0L002S, U6-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 2.062
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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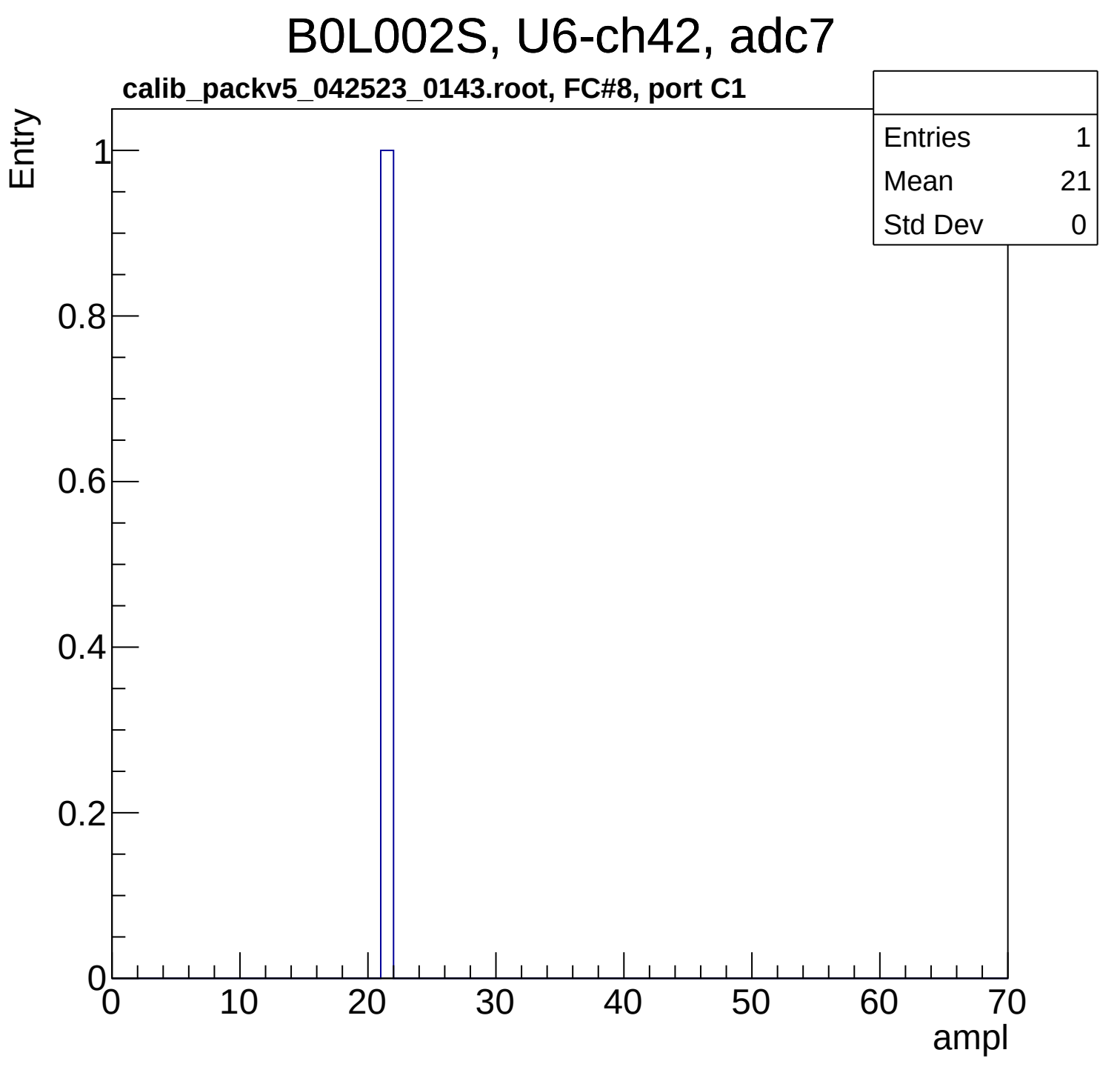
B0L002S, U6-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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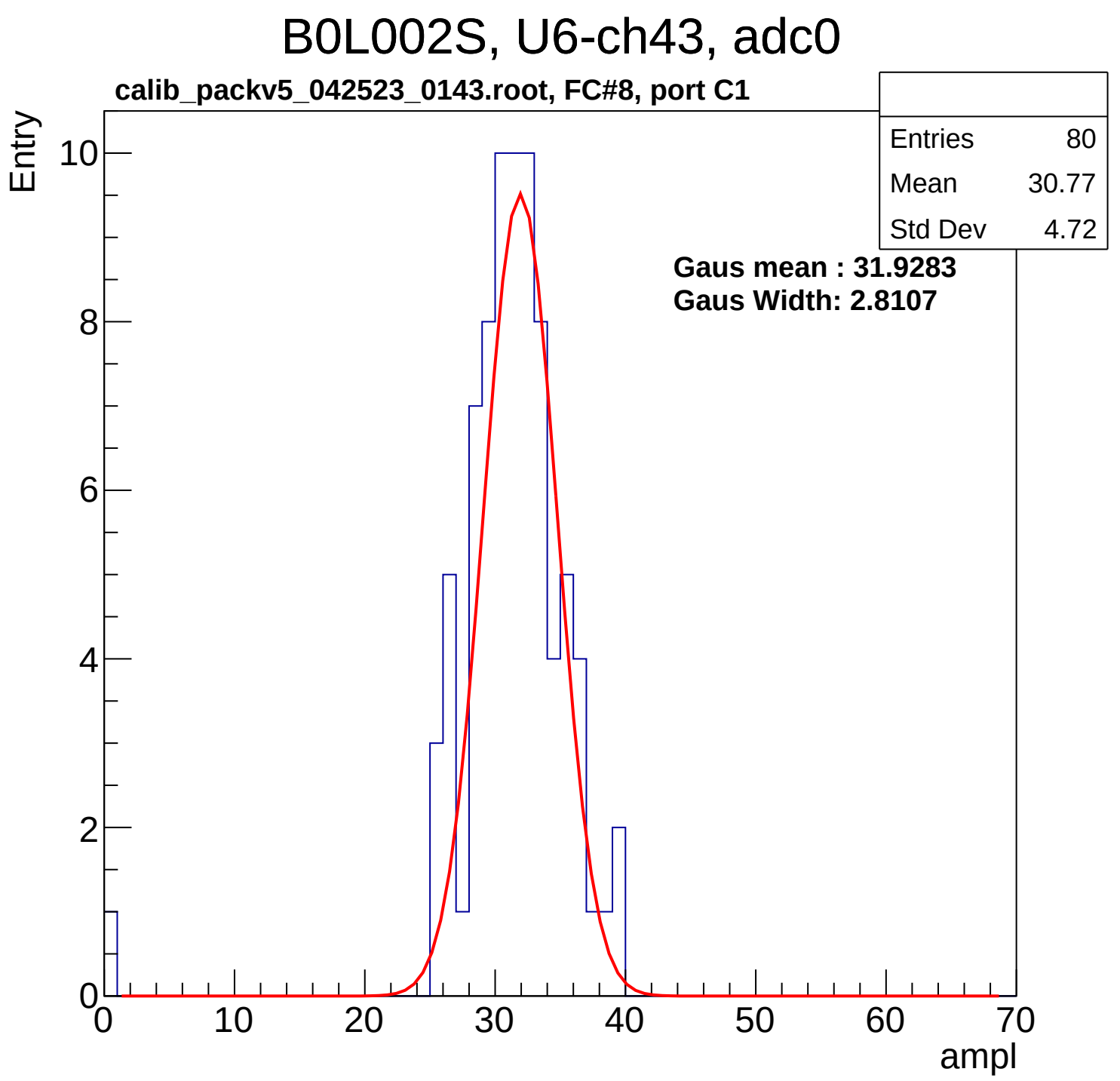
30
40
50
60
70
ampl


B0L002S, U6-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.77
Std Dev 4.72
Gaus mean : 31.9283
Gaus Width: 2.8107
Entry
ampl
0
2
4
6
8
10
0
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40
50
60
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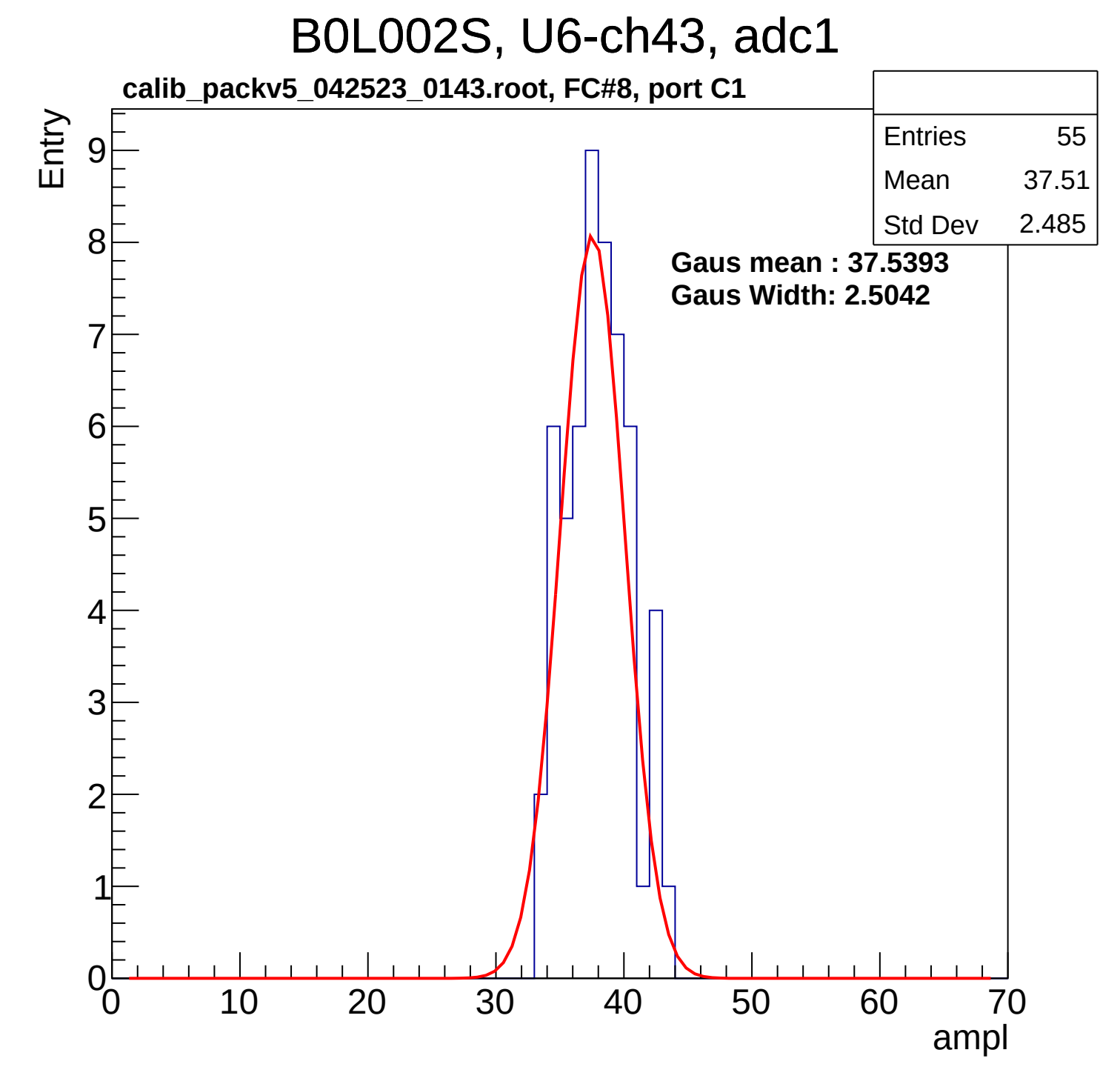
B0L002S, U6-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 37.51
Std Dev 2.485
Gaus mean : 37.5393
Gaus Width: 2.5042
Entry
ampl
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2
3
4
5
6
7
8
9
0
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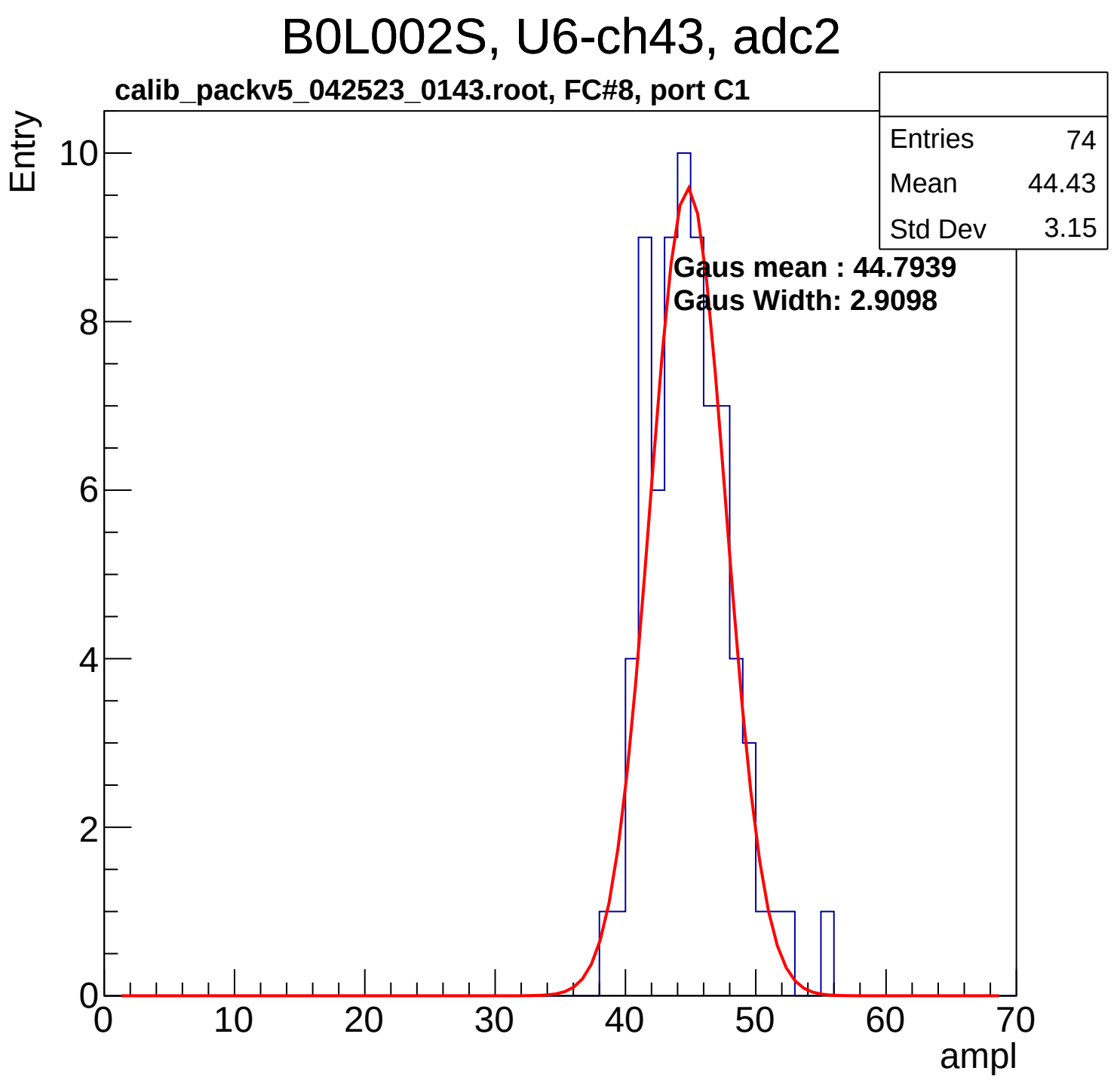
B0L002S, U6-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 44.43
Std Dev 3.15
Gaus mean : 44.7939
Gaus Width: 2.9098
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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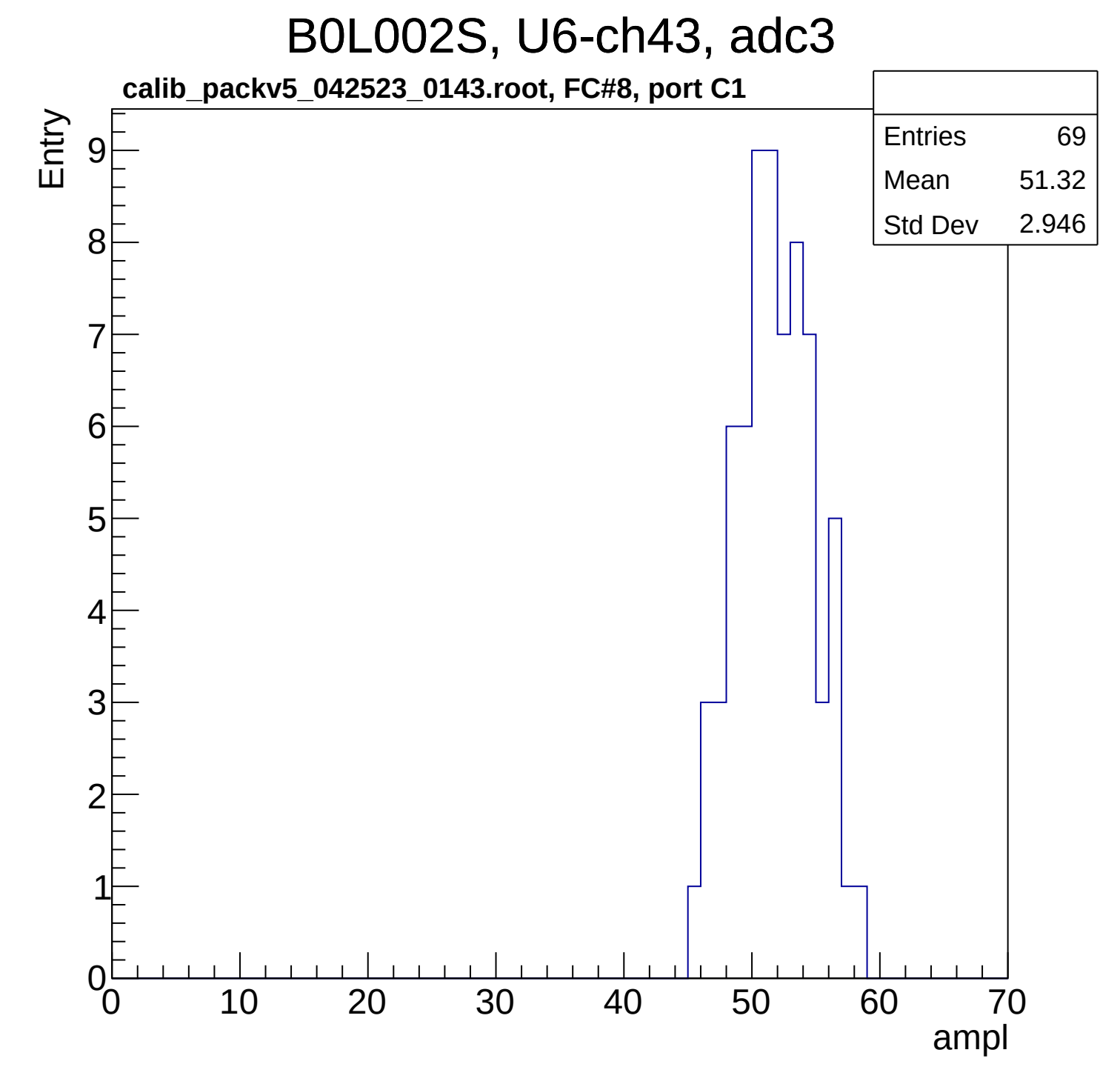
B0L002S, U6-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.32
Std Dev 2.946
Entry
ampl
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4
5
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0
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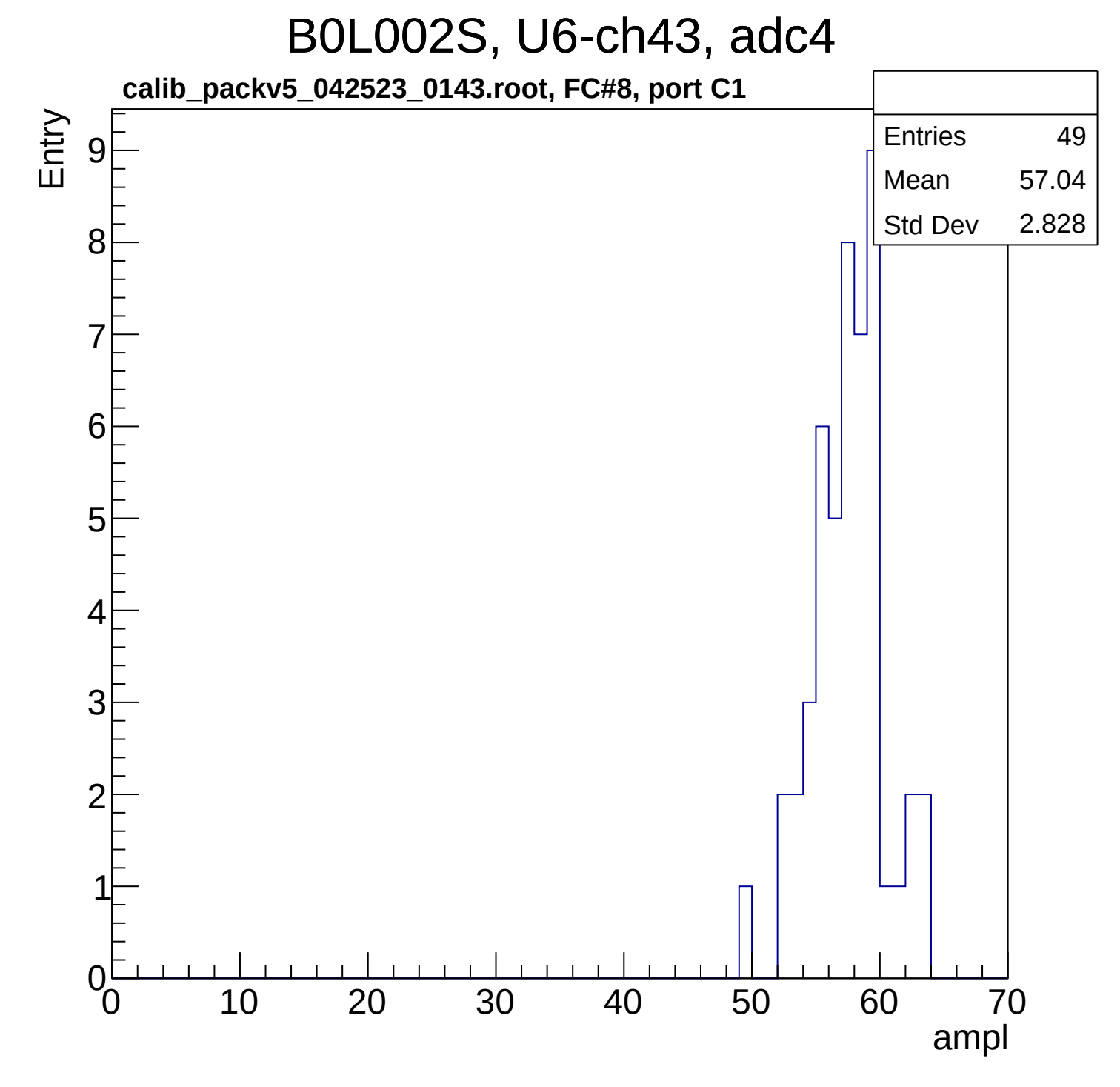
B0L002S, U6-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.04
Std Dev 2.828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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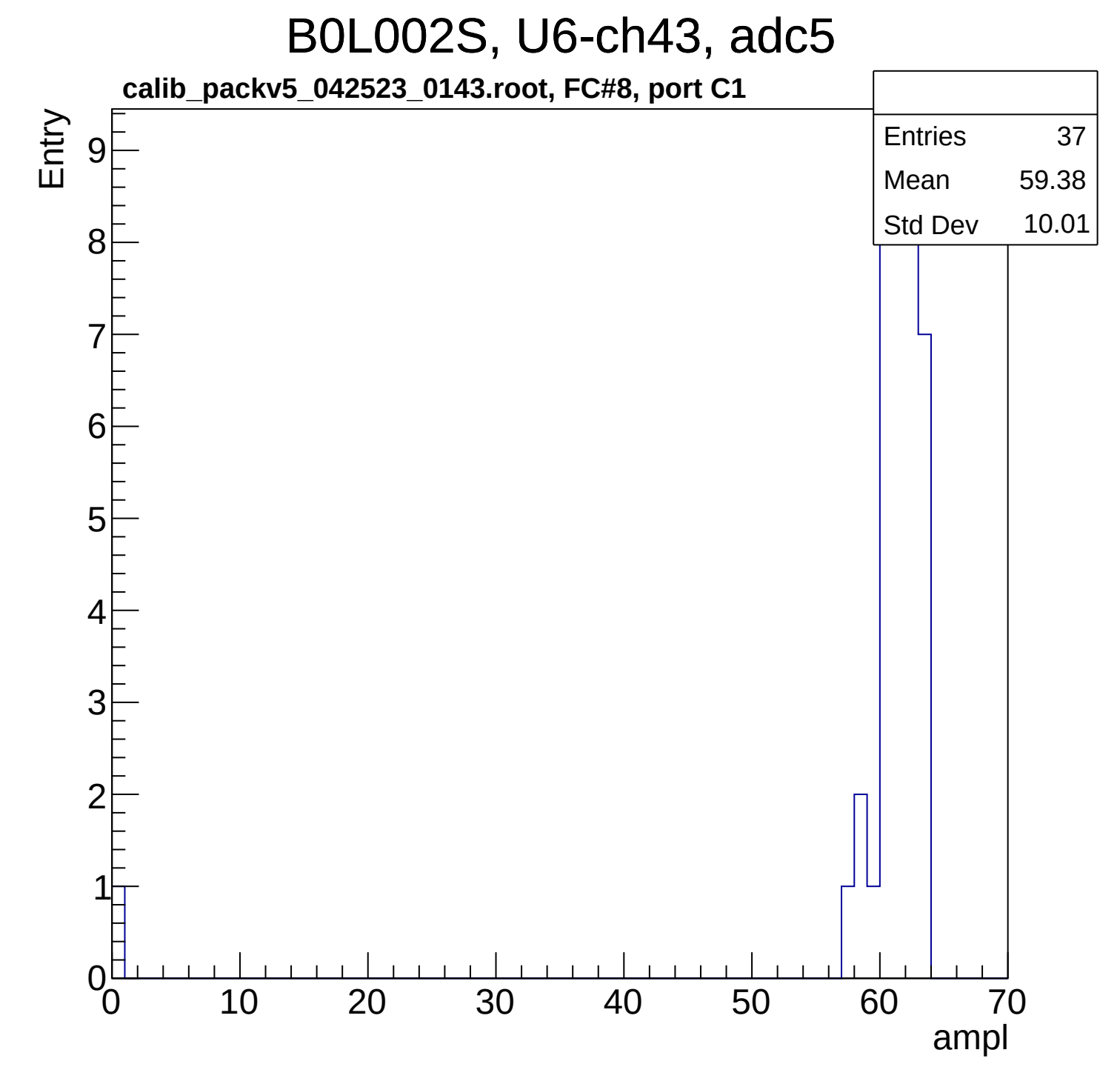
B0L002S, U6-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.38
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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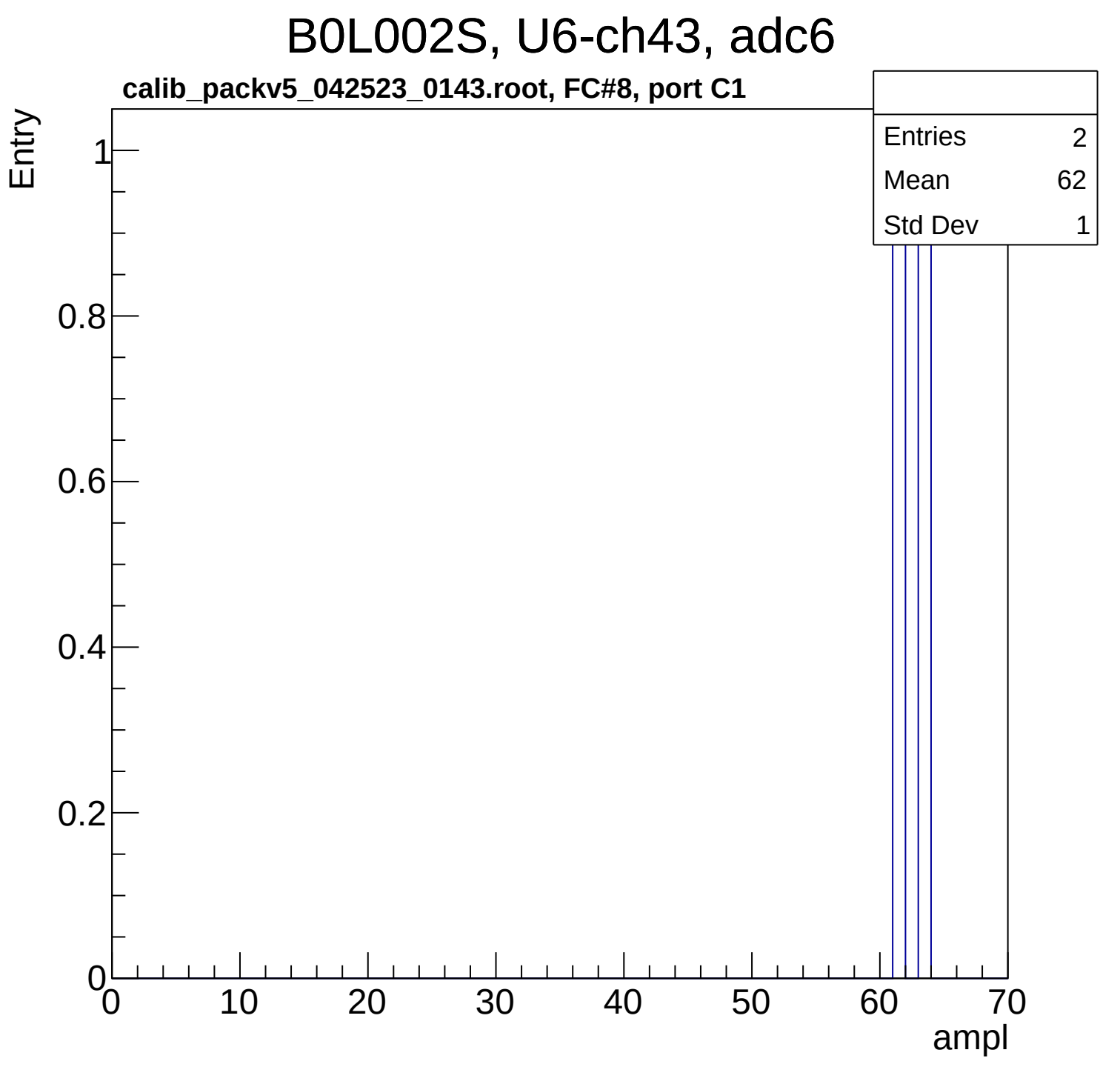
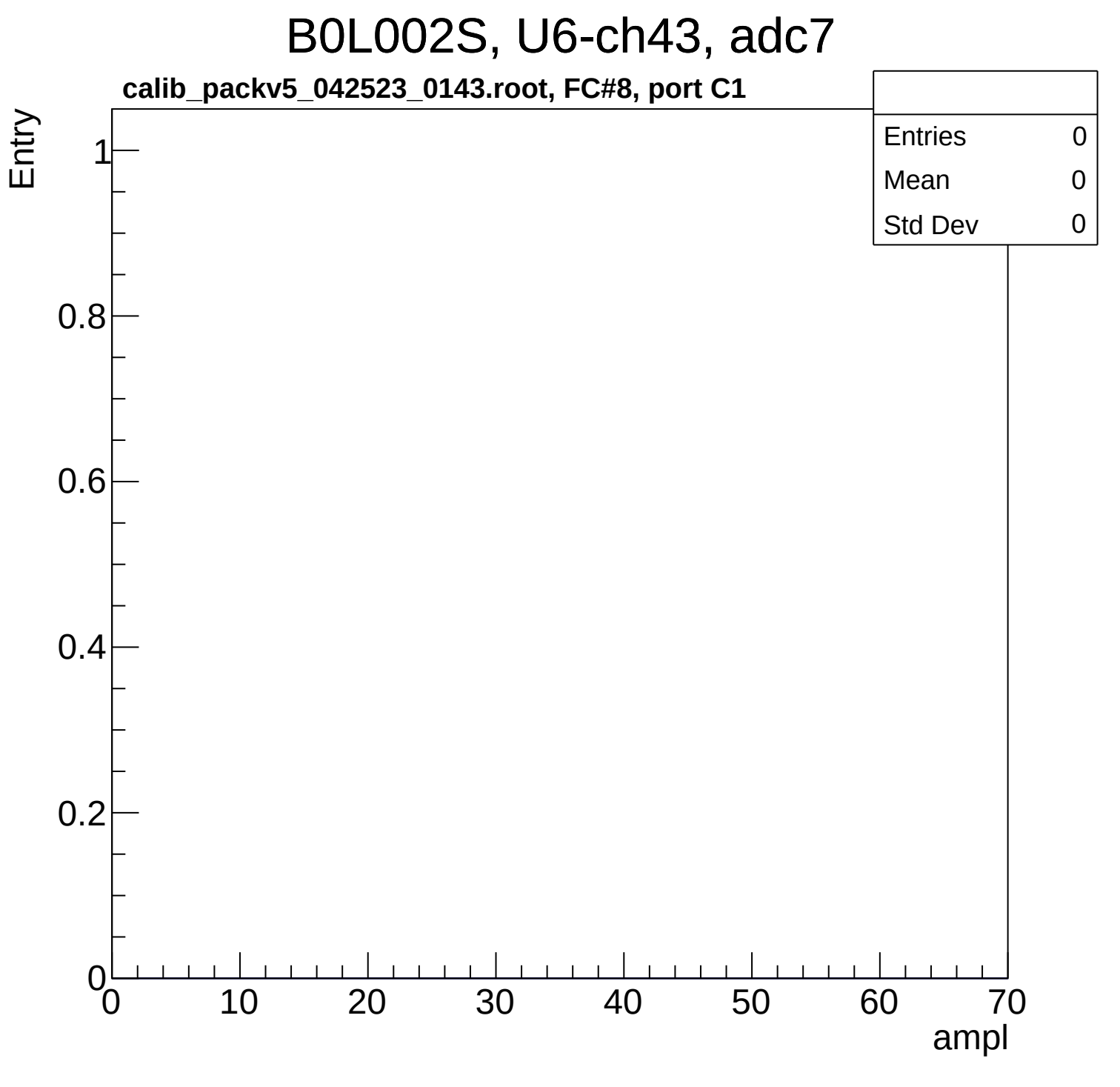
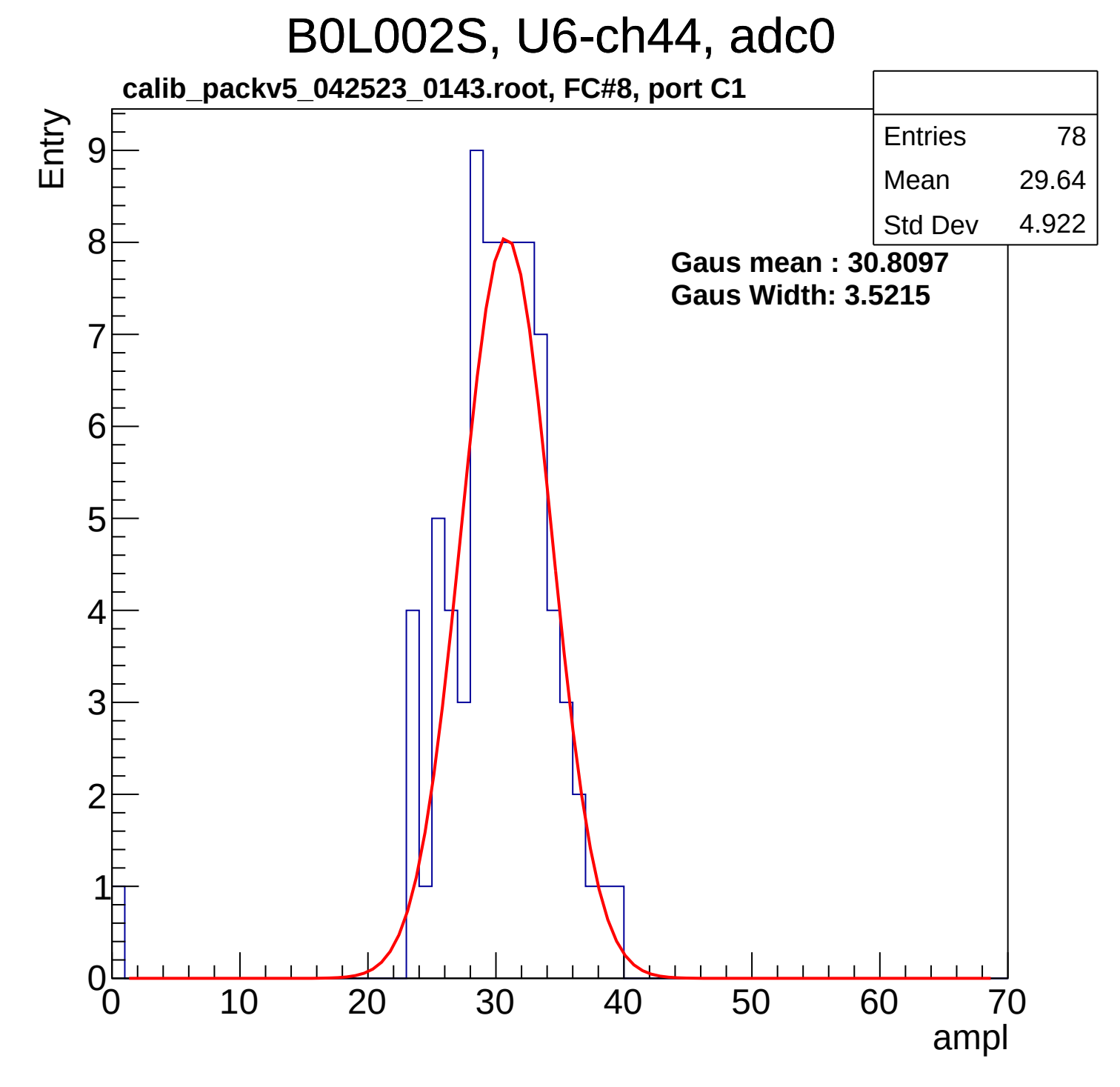
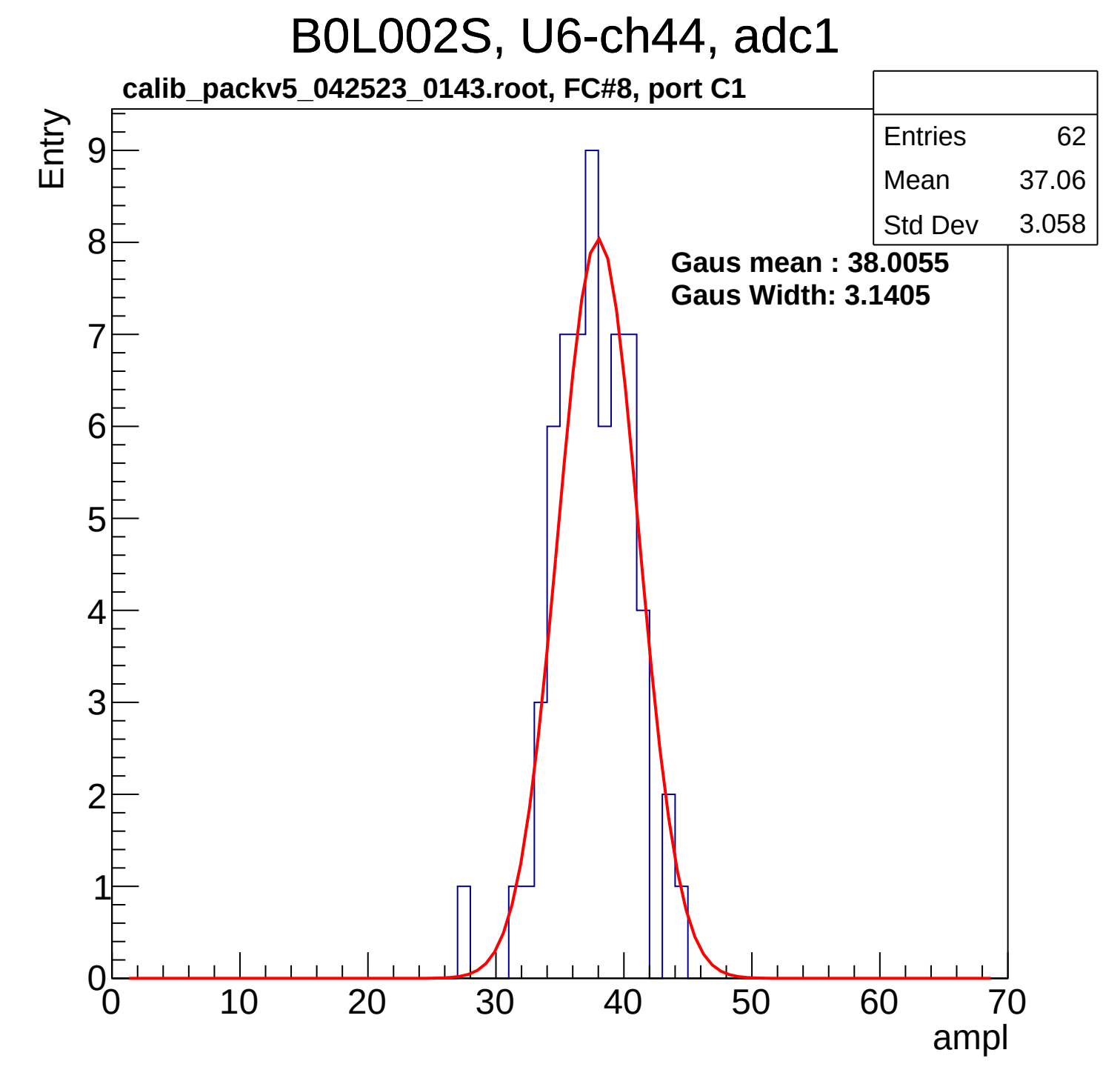
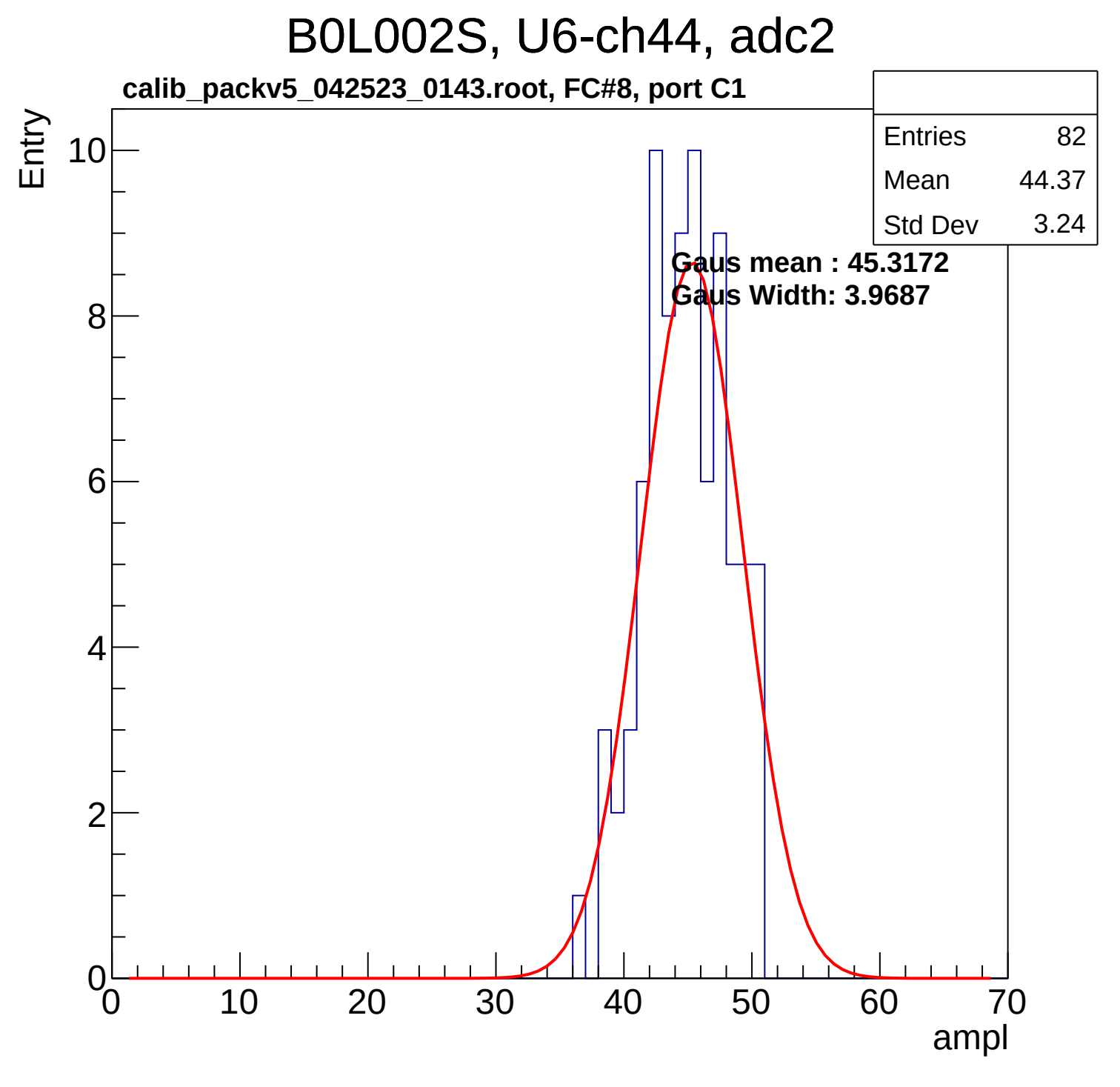
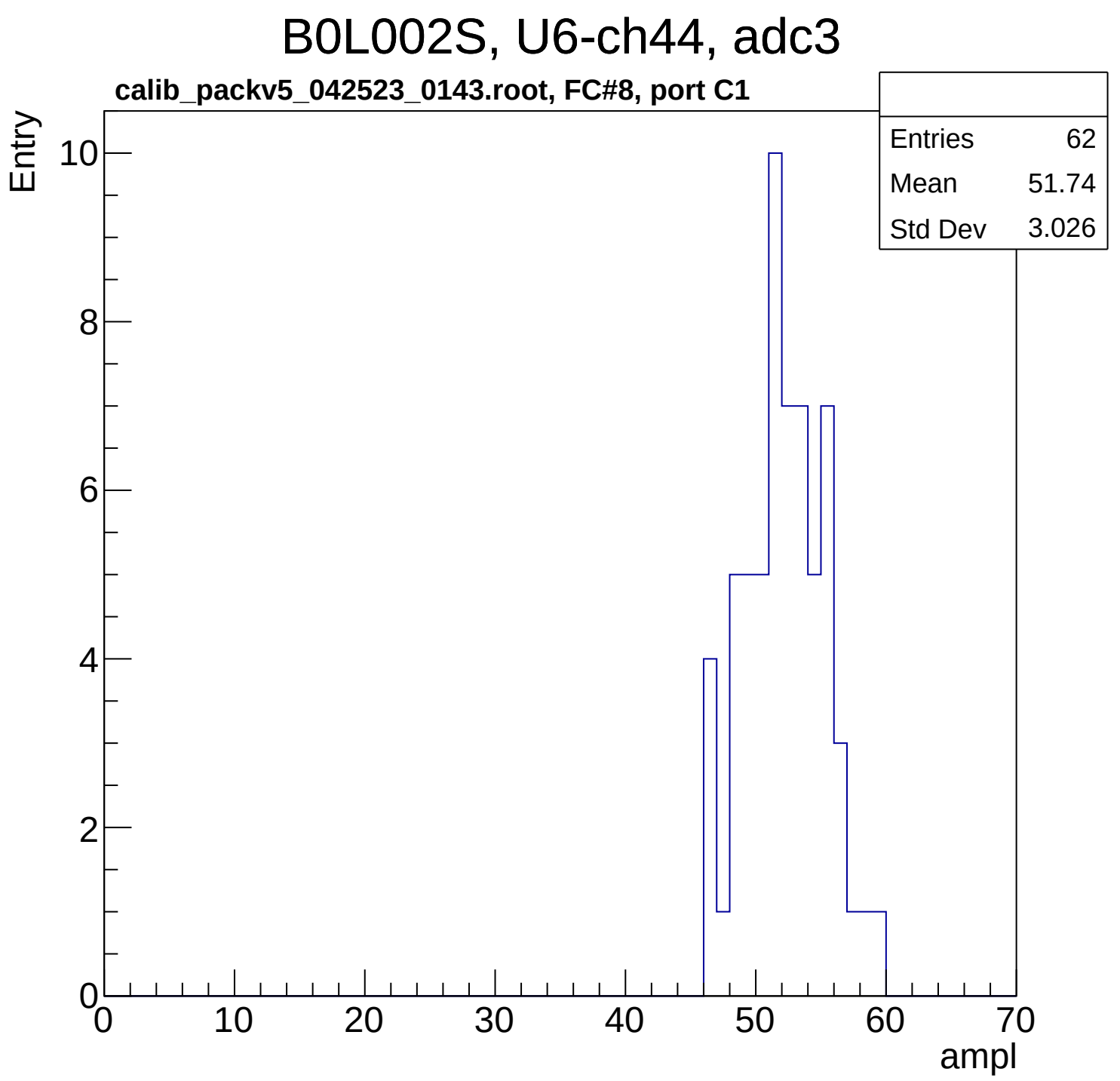

B0L002S, U6-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

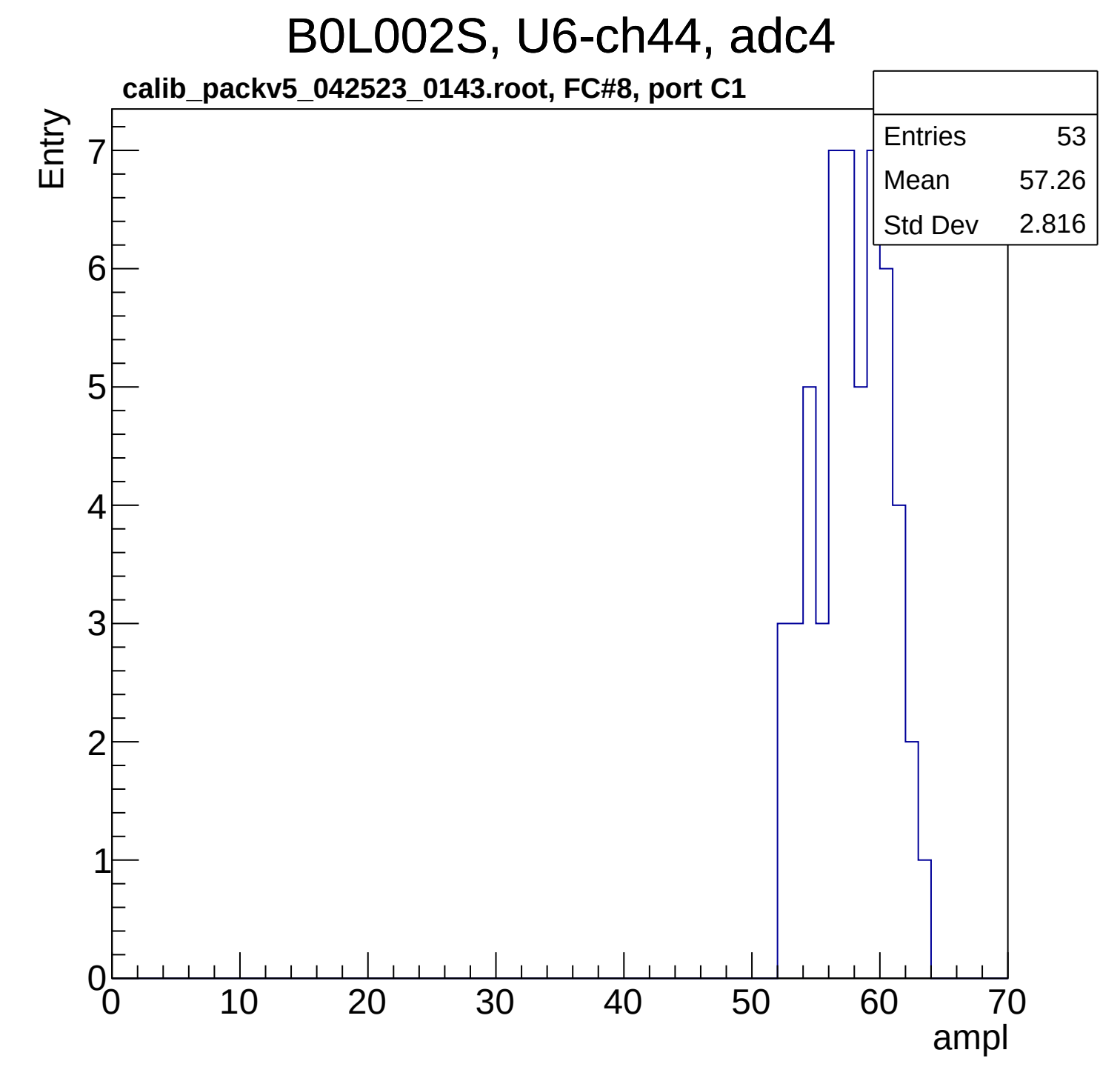

B0L002S, U6-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.64
Std Dev 4.922
Gaus mean : 30.8097
Gaus Width: 3.5215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.06
Std Dev 3.058
Gaus mean : 38.0055
Gaus Width: 3.1405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 44.37
Std Dev 3.24
Gaus mean : 45.3172
Gaus Width: 3.9687
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.74
Std Dev 3.026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.26
Std Dev 2.816
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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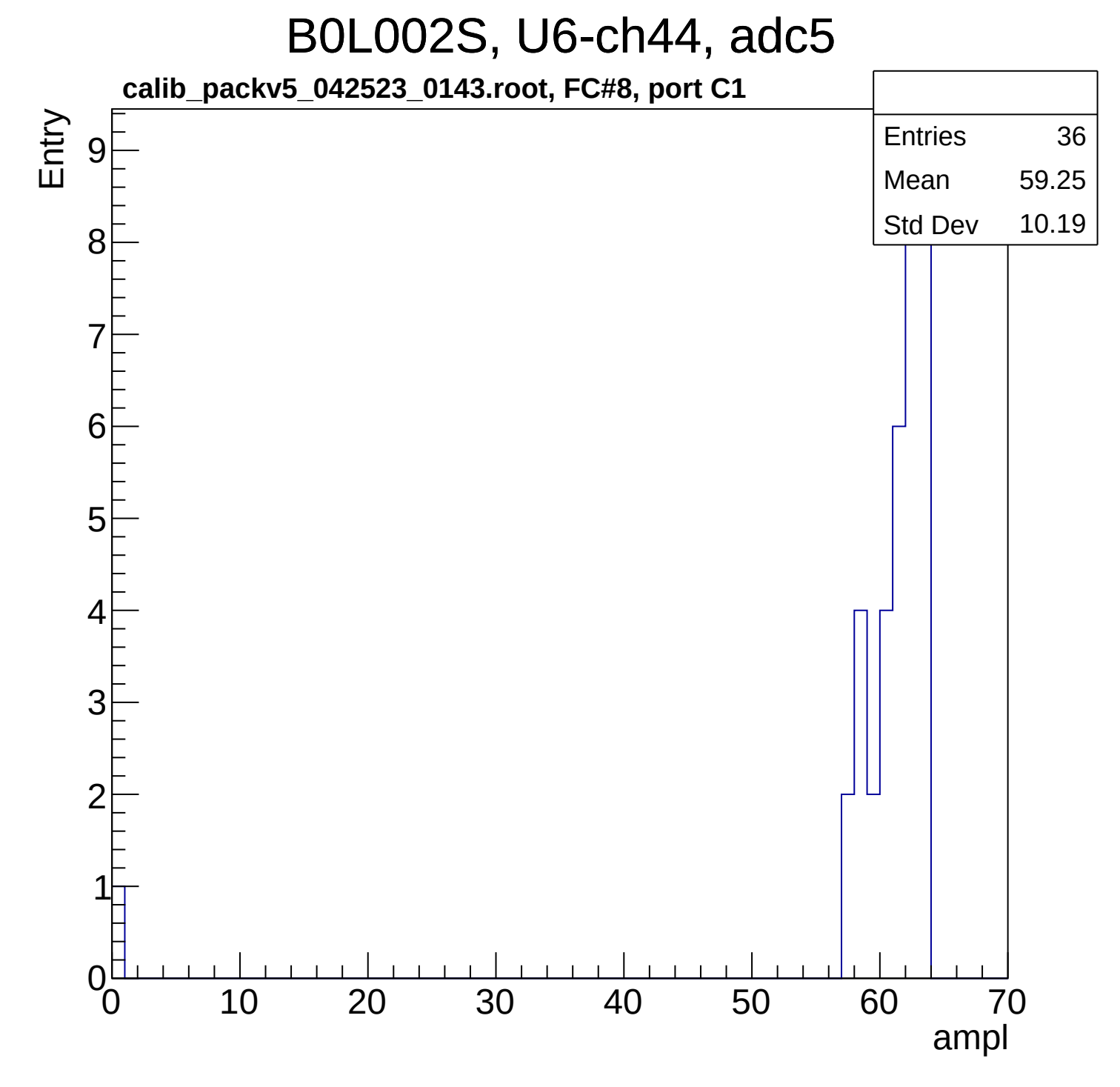
B0L002S, U6-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.25
Std Dev 10.19
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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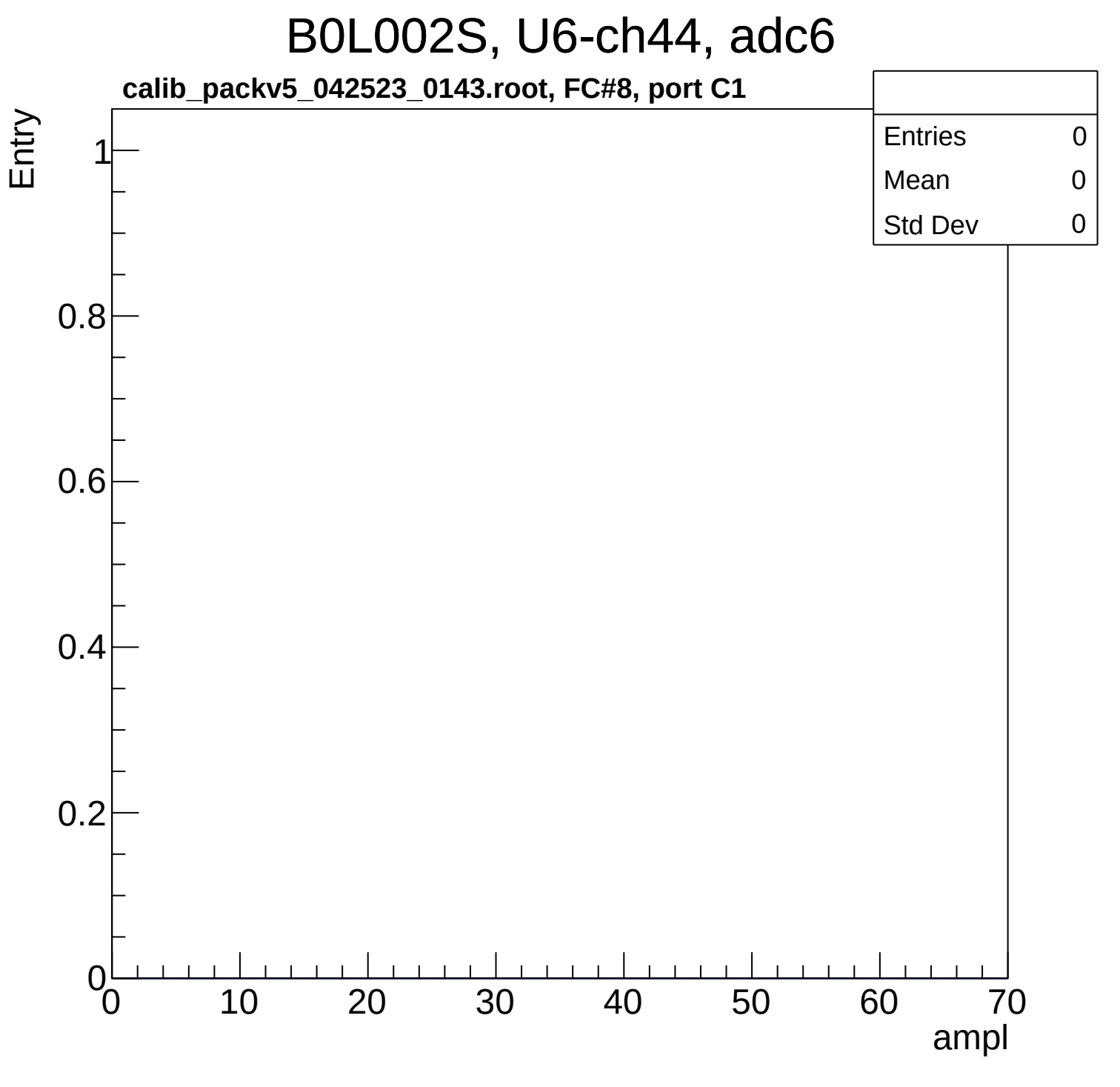
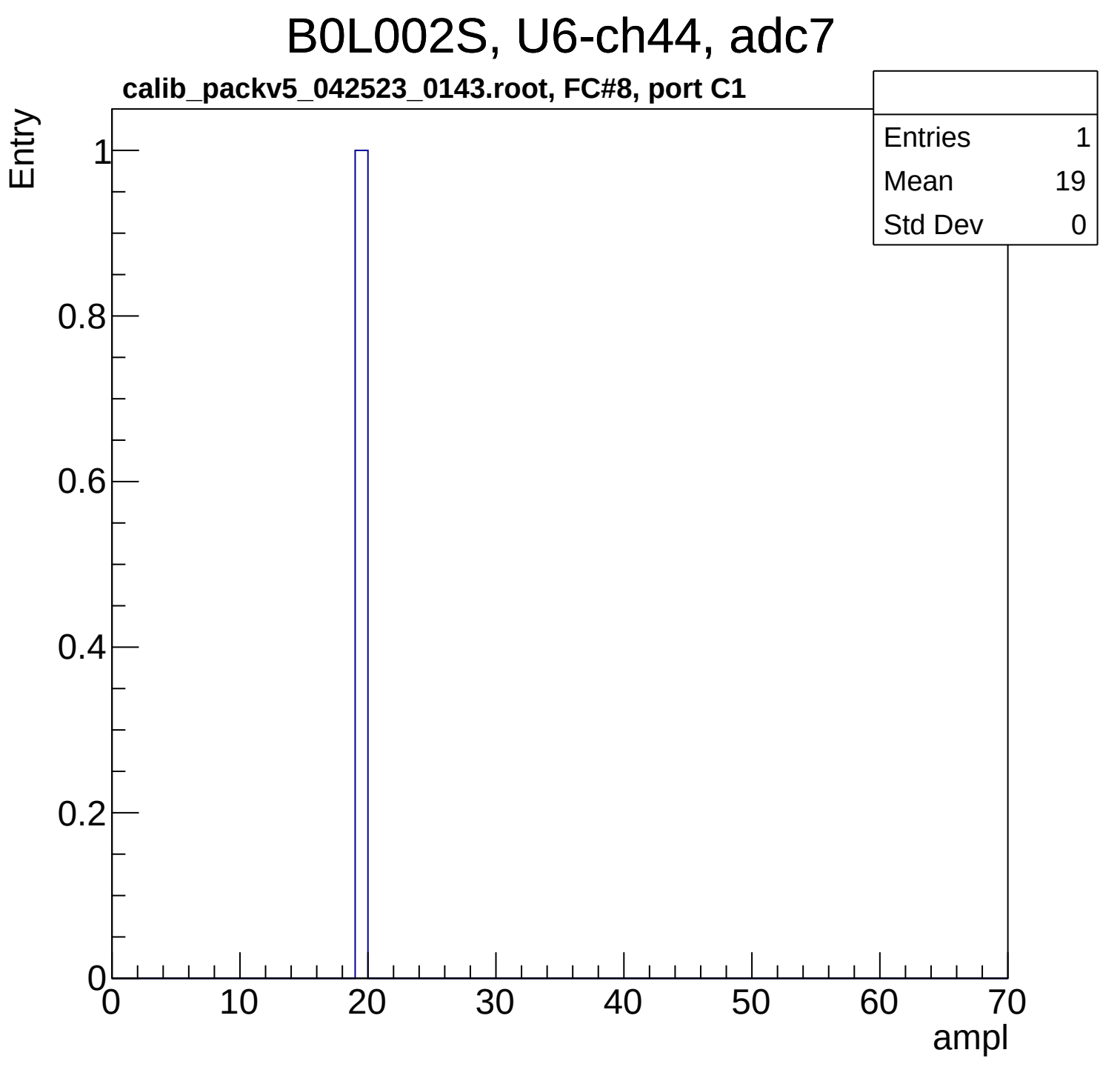

B0L002S, U6-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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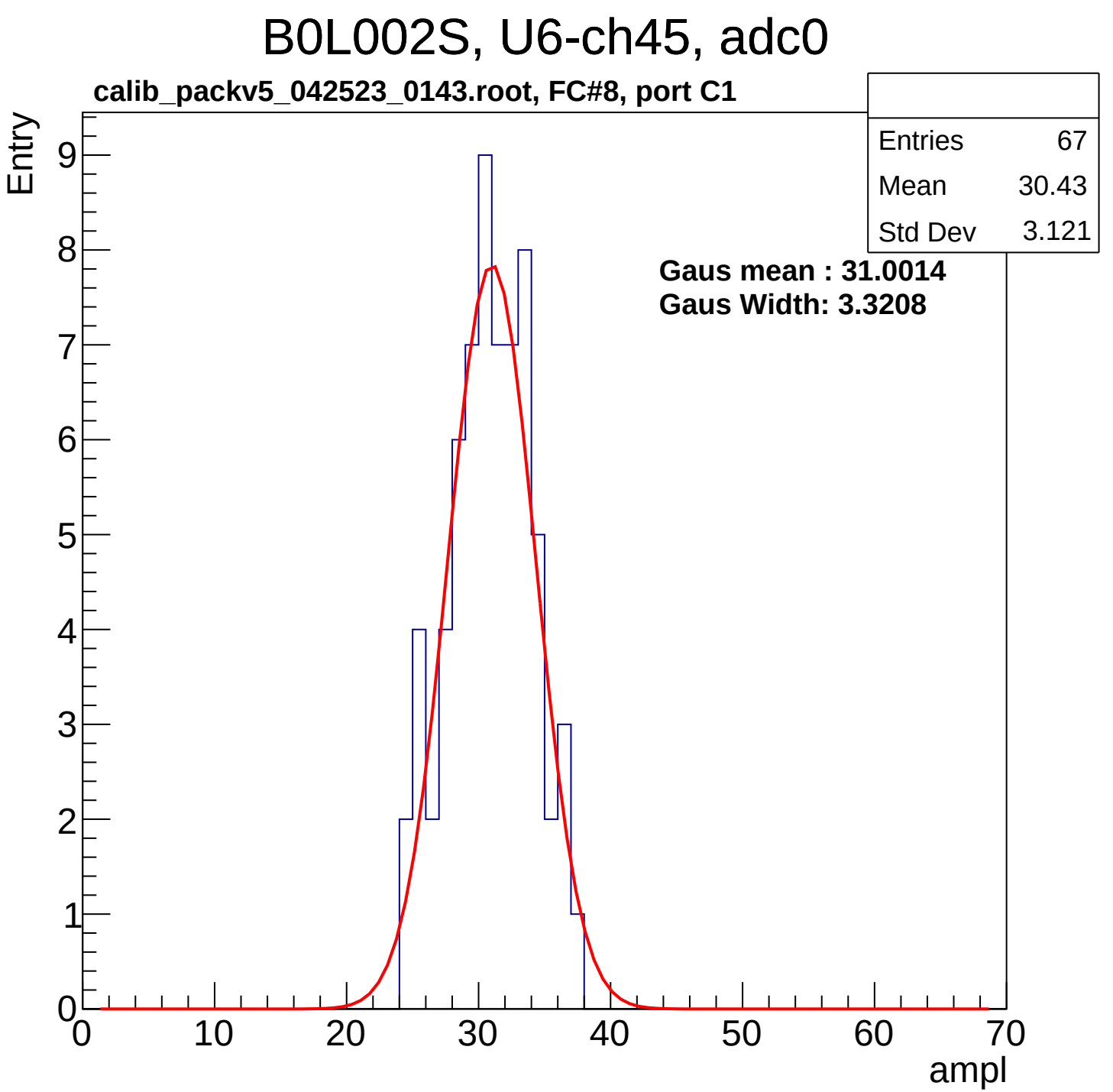
50
60
70
ampl


B0L002S, U6-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.43
Std Dev 3.121
Gaus mean : 31.0014
Gaus Width: 3.3208
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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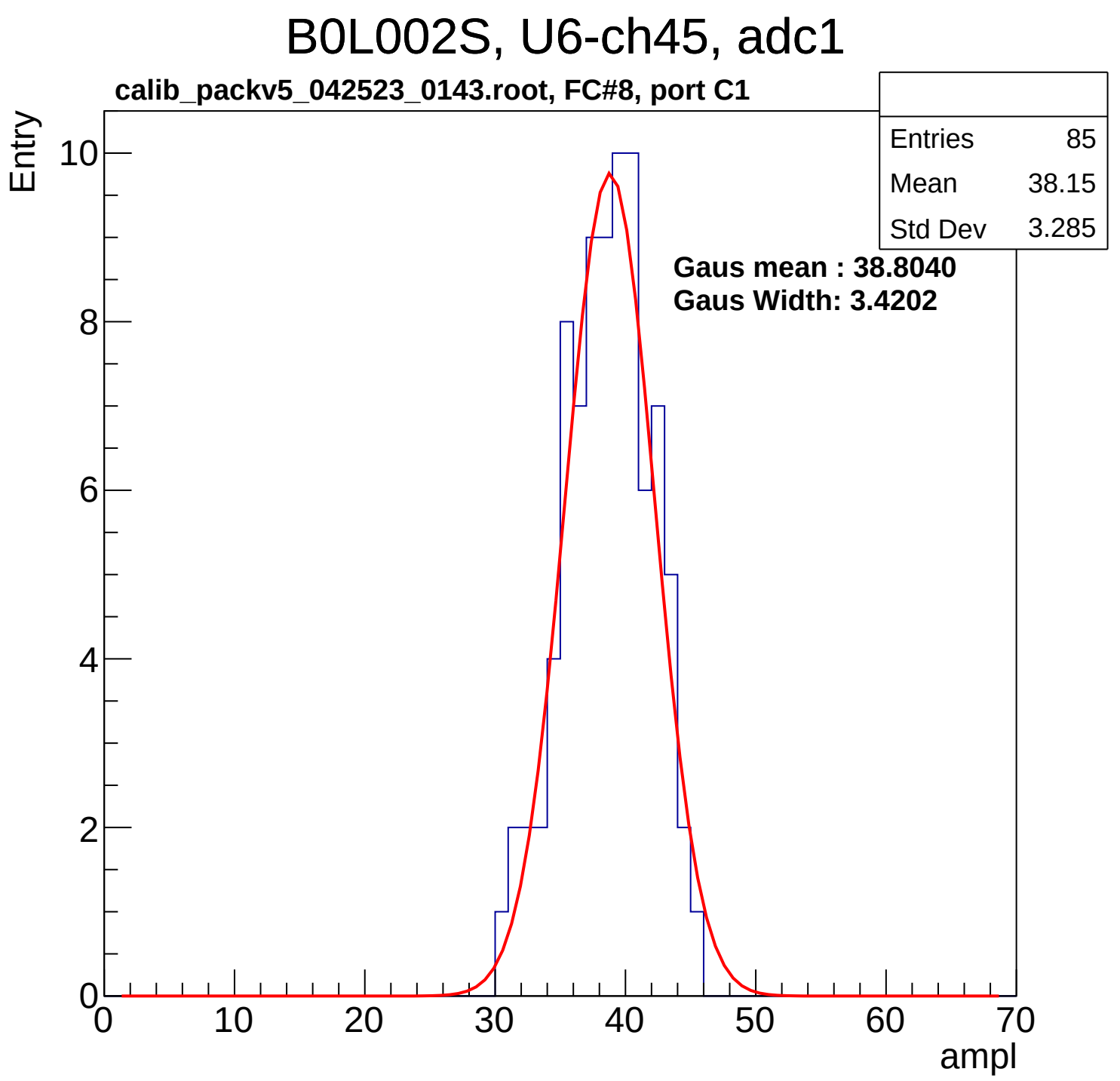
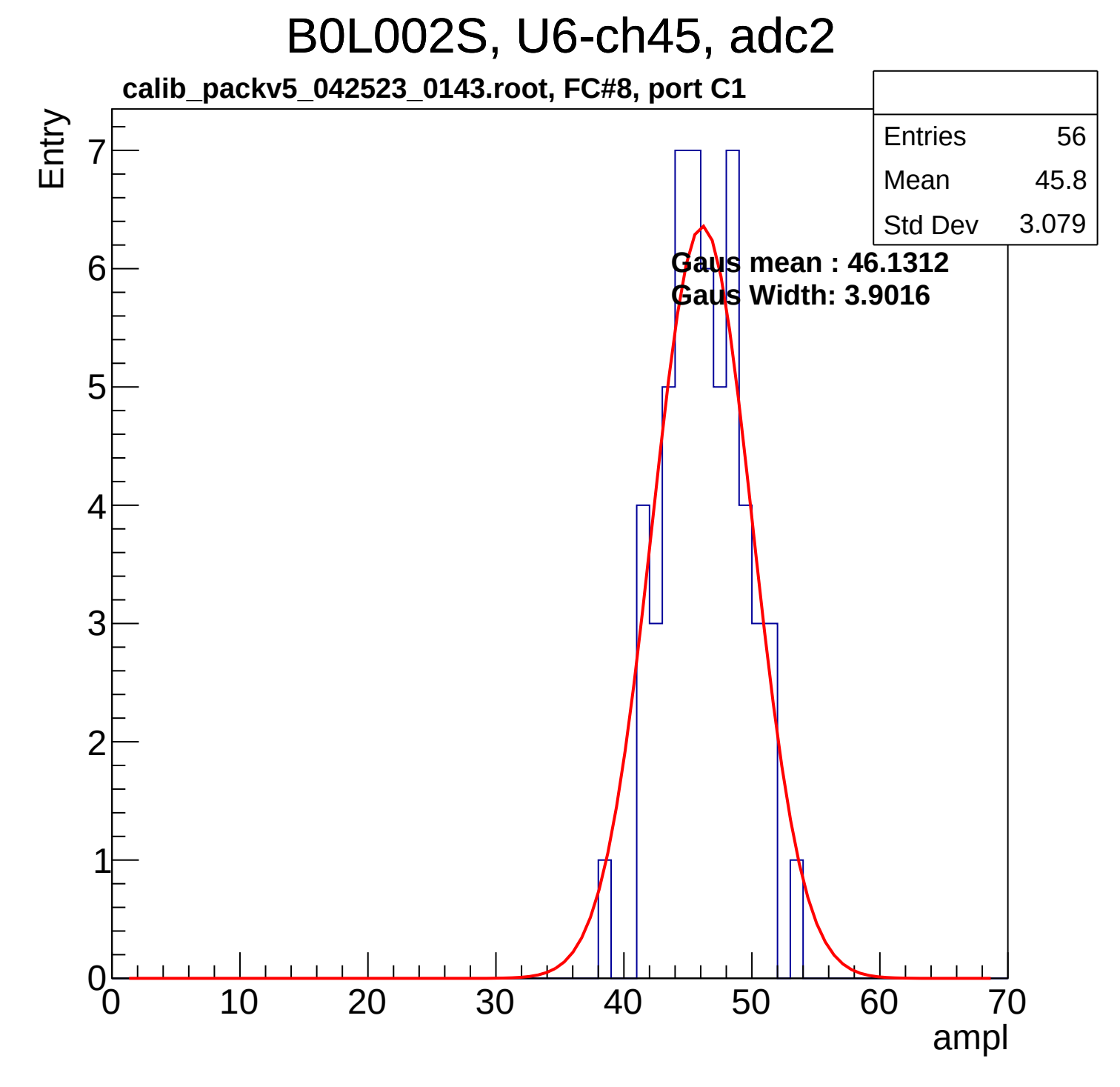
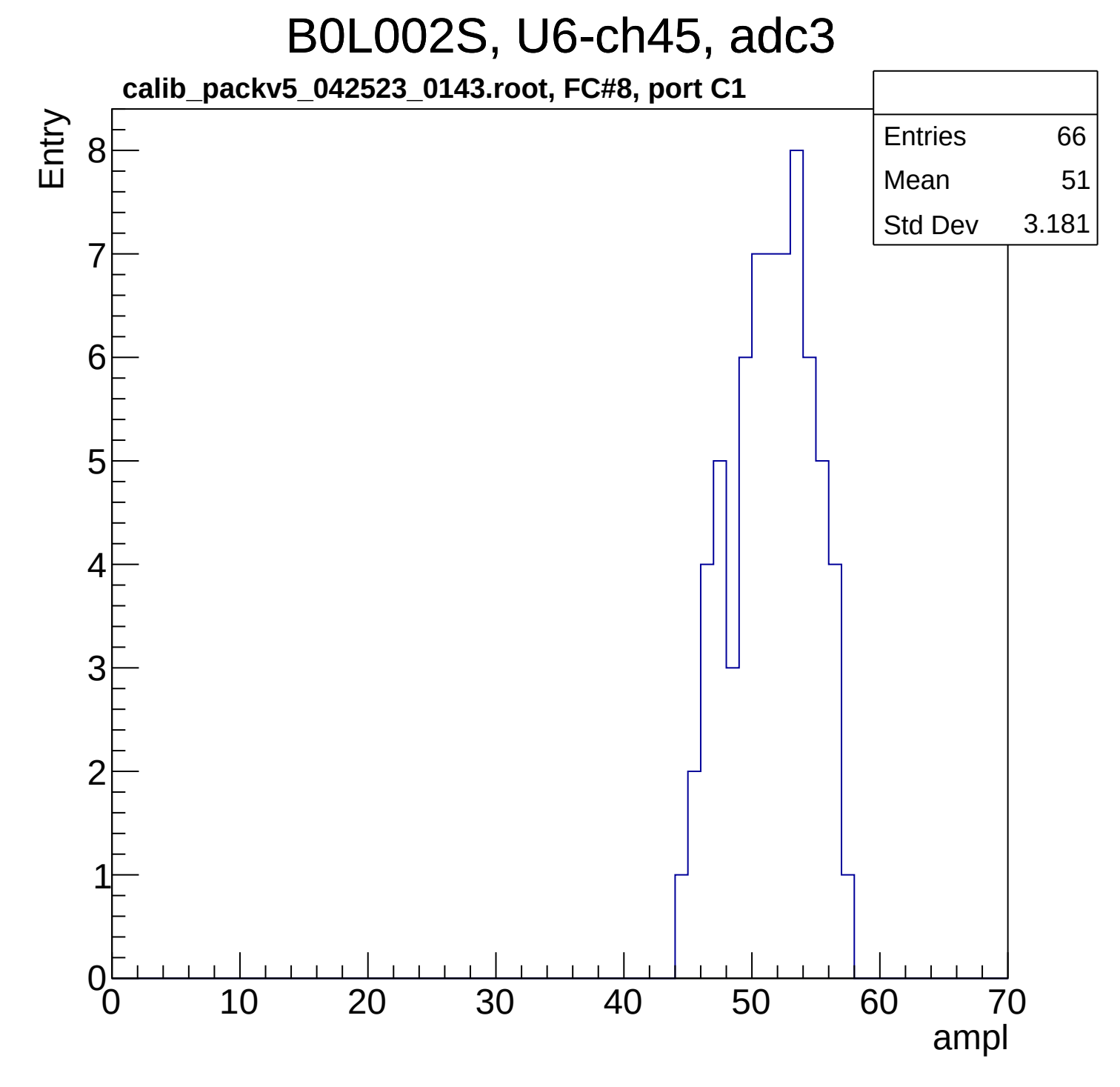

B0L002S, U6-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 38.15
Std Dev 3.285
Gaus mean : 38.8040
Gaus Width: 3.4202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.8
Std Dev 3.079
Gaus mean : 46.1312
Gaus Width: 3.9016
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51
Std Dev 3.181
Entry
ampl
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7
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0
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40
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60
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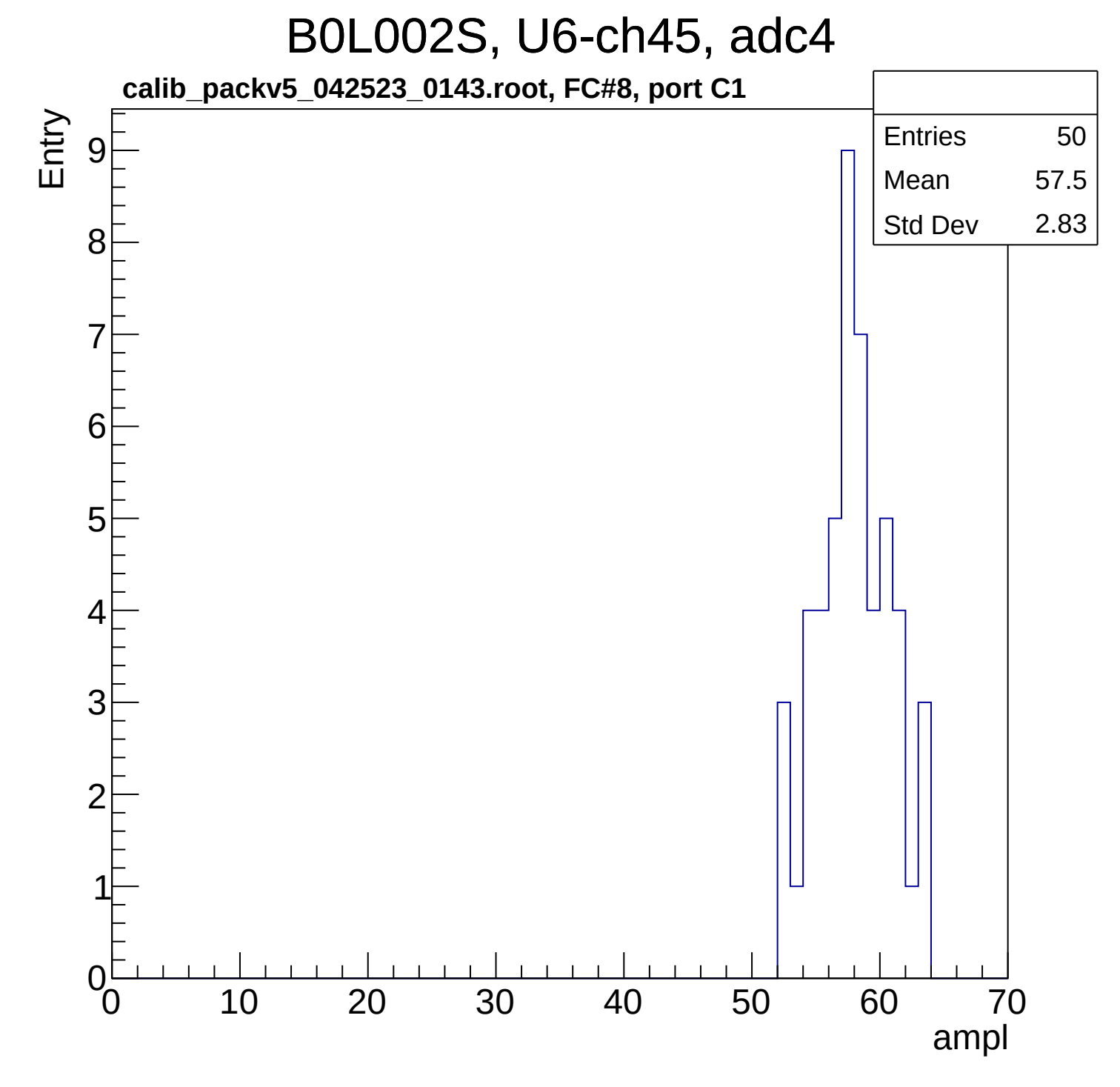
B0L002S, U6-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.5
Std Dev 2.83
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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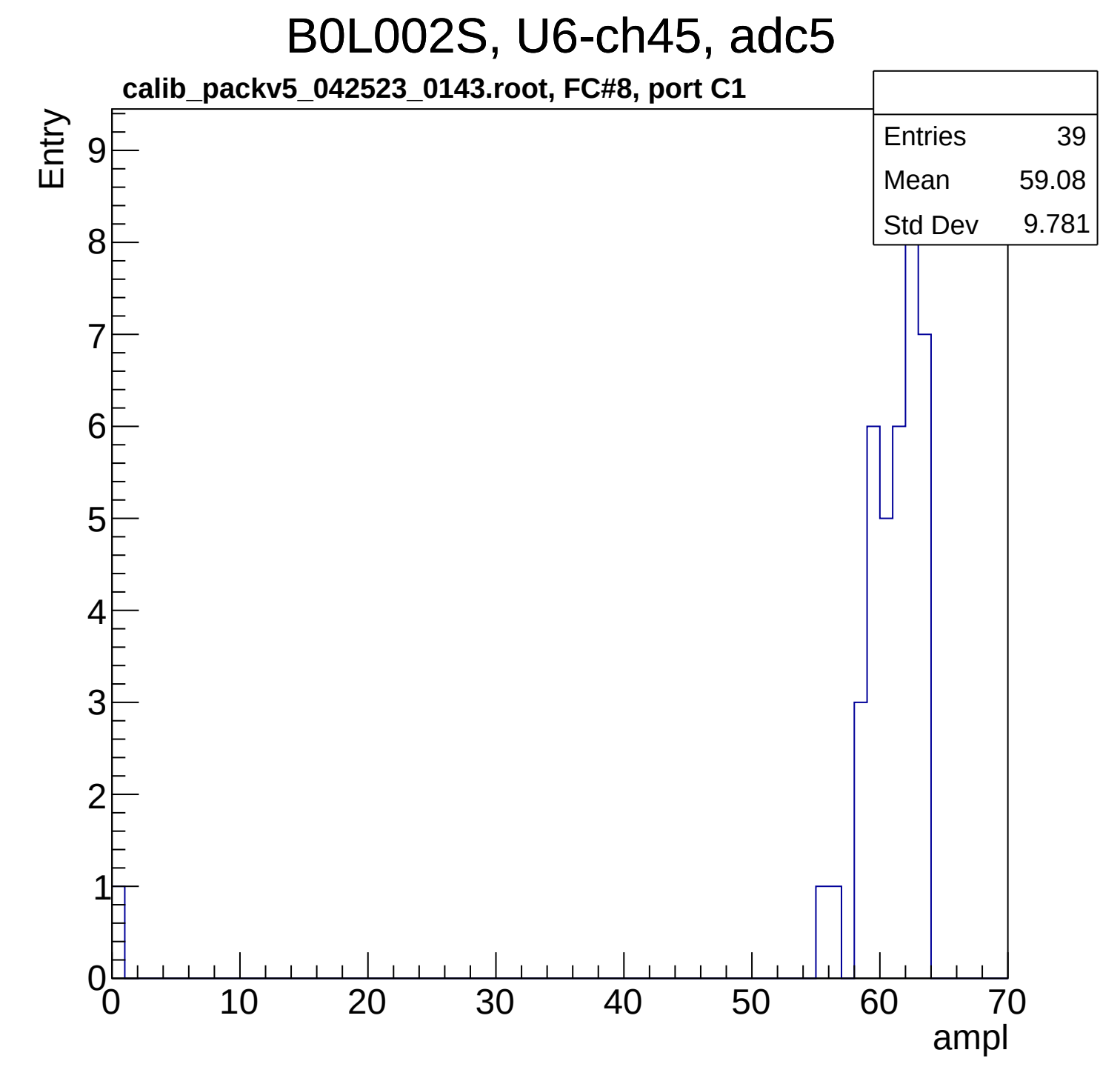
B0L002S, U6-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.08
Std Dev 9.781
Entry
ampl
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2
3
4
5
6
7
8
9
0
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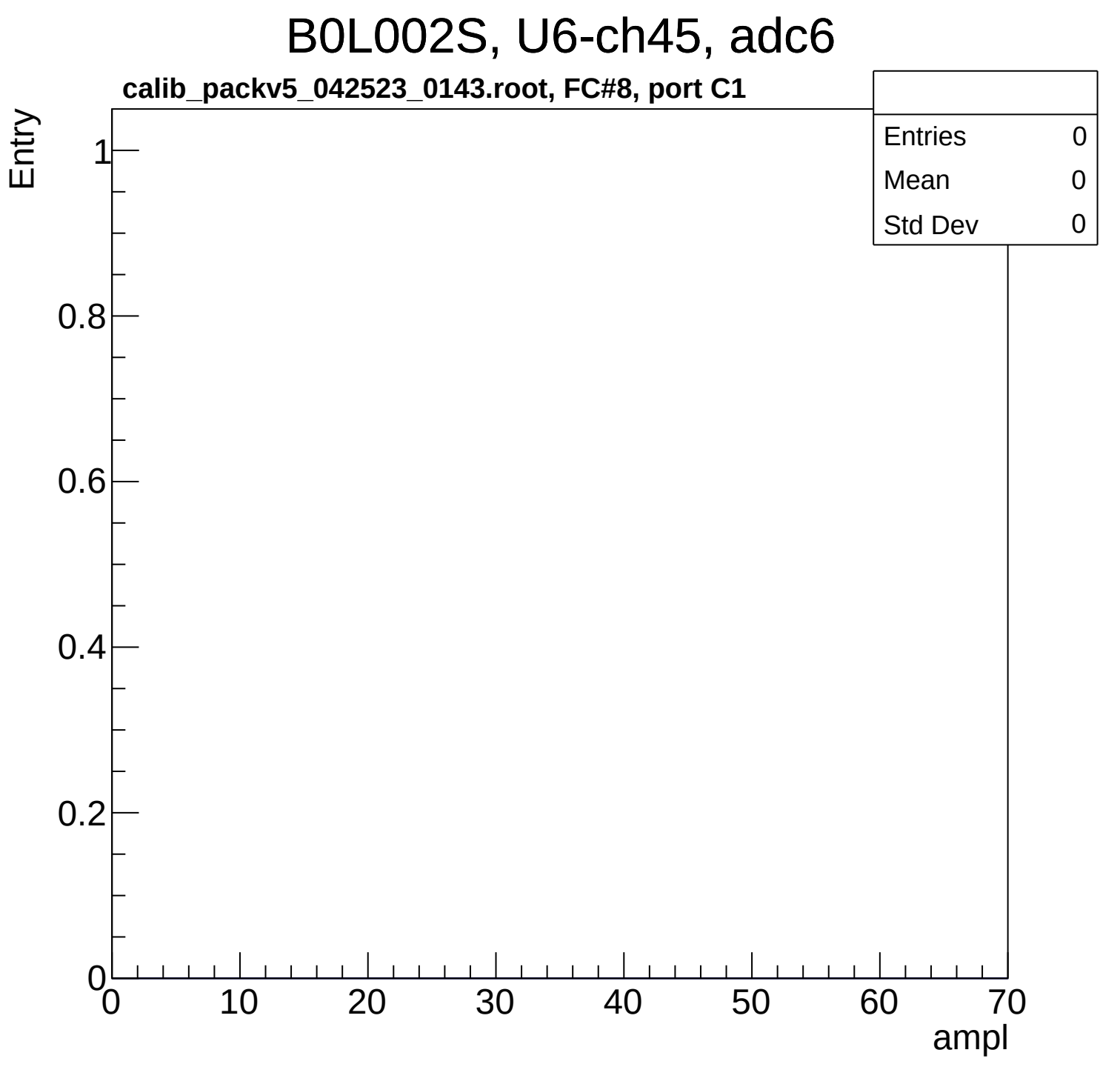
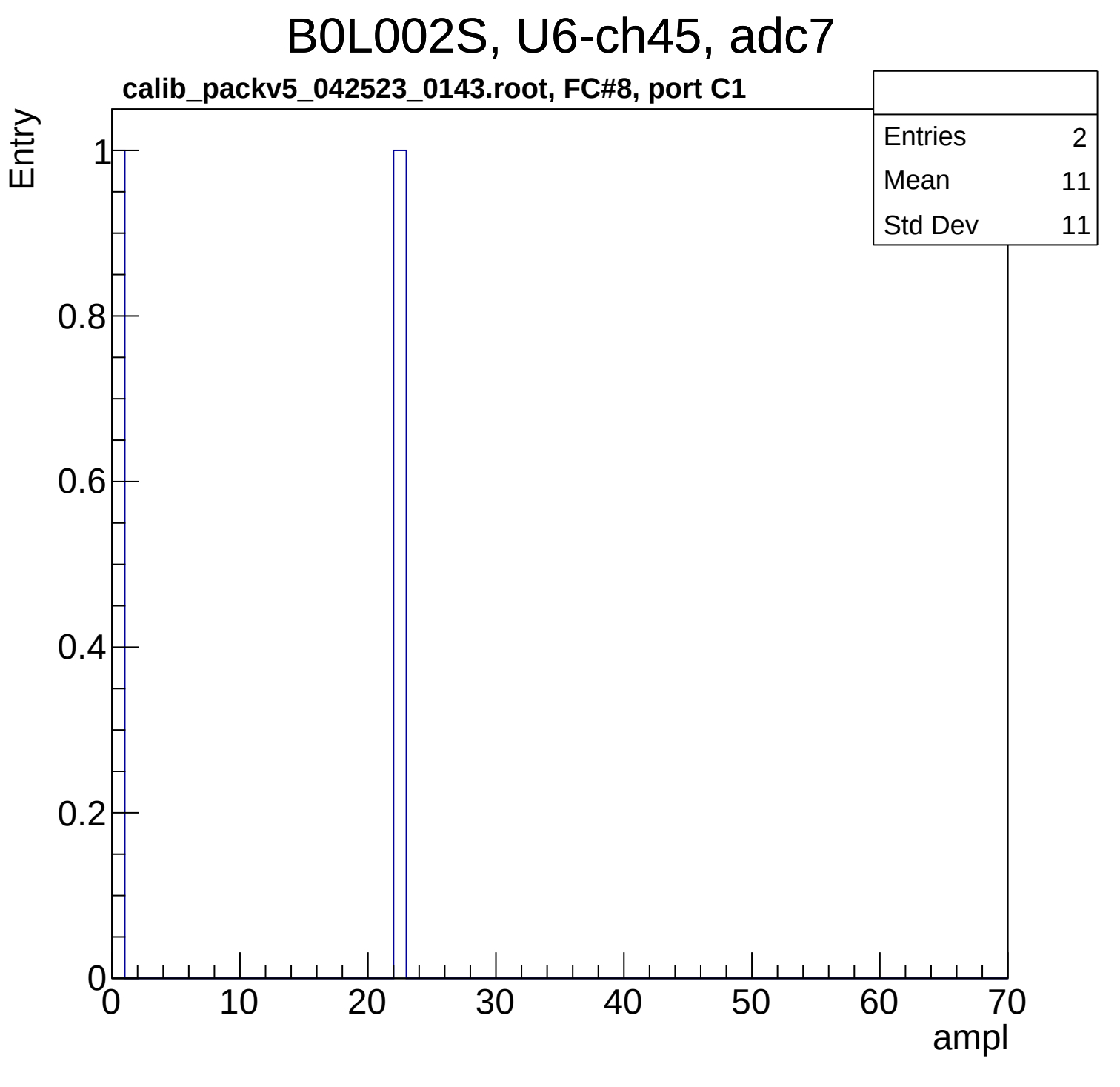
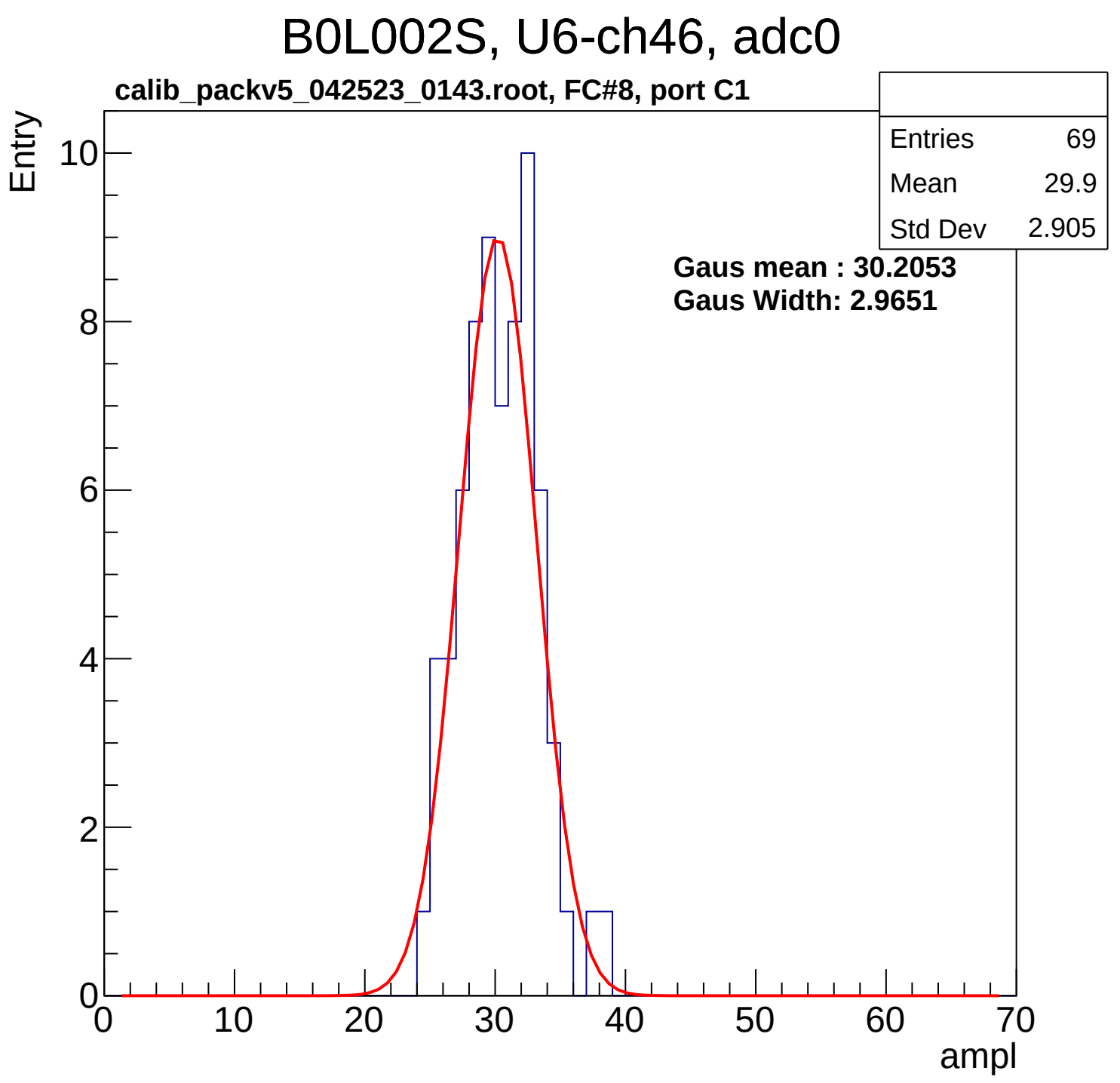

B0L002S, U6-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.9
Std Dev 2.905
Gaus mean : 30.2053
Gaus Width: 2.9651
Entry
ampl
0
2
4
6
8
10
0
10
20
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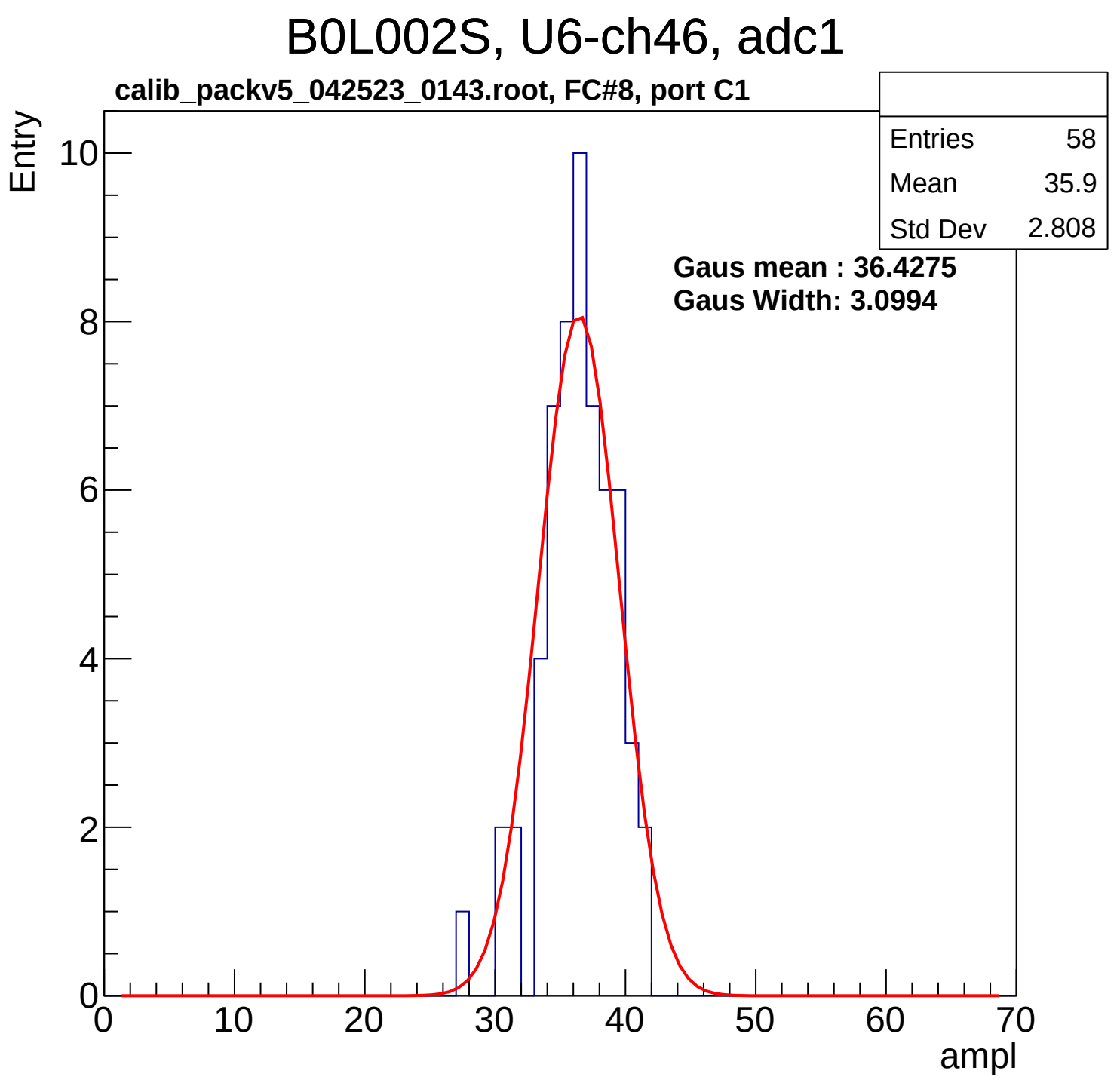
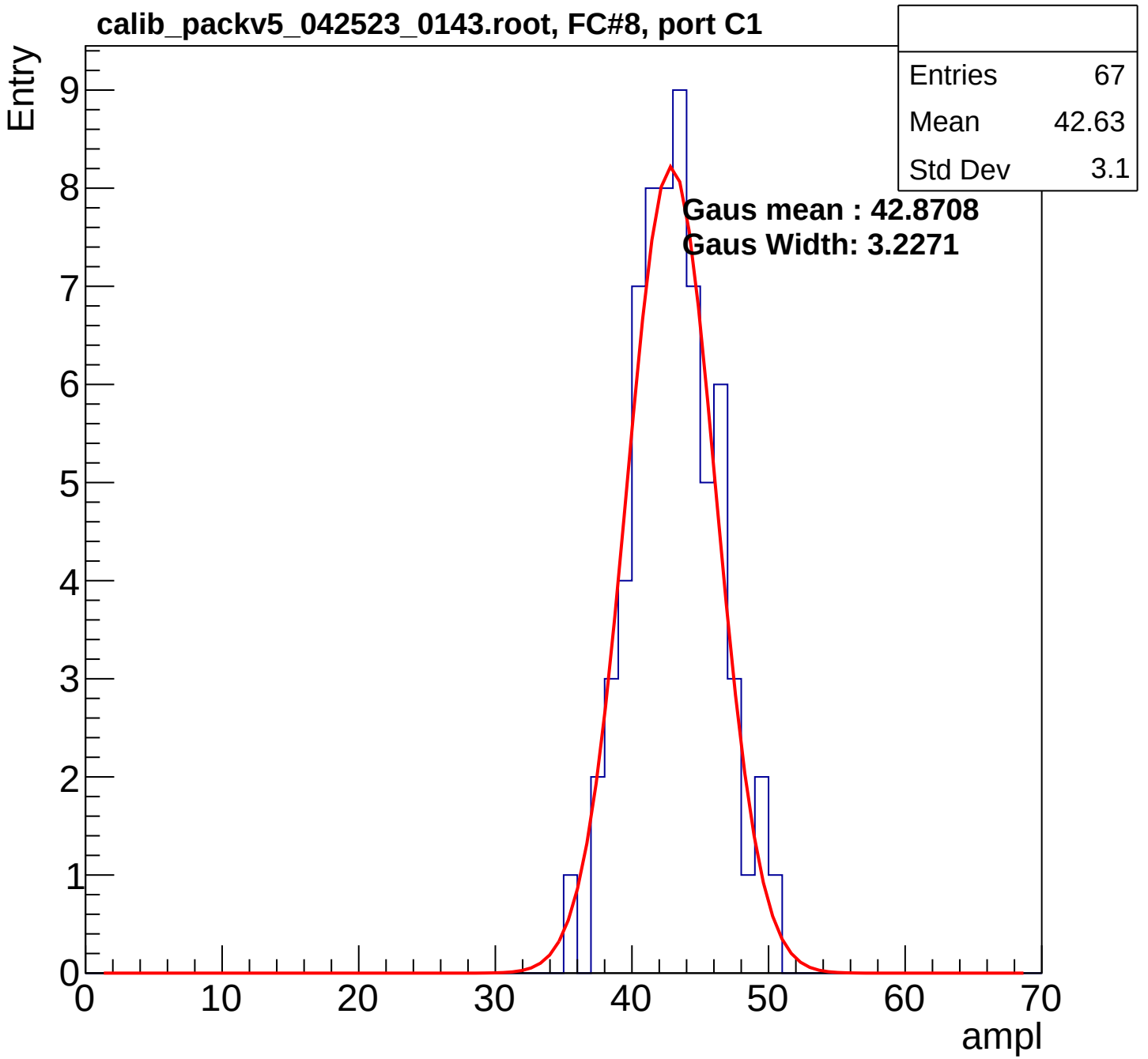
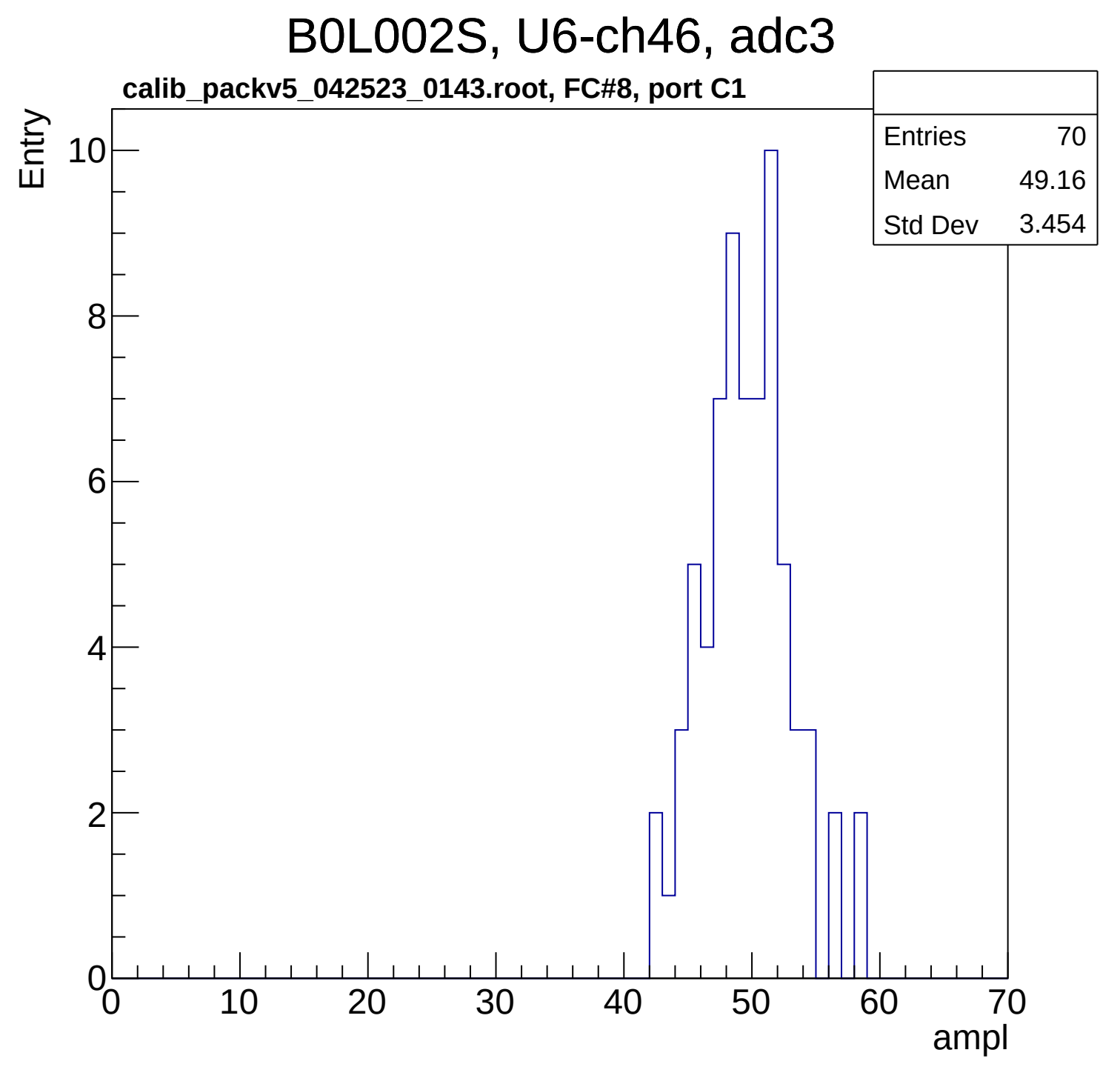
40
50
60
70


B0L002S, U6-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 35.9
Std Dev 2.808
Gaus mean : 36.4275
Gaus Width: 3.0994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42.63
Std Dev 3.1
Gaus mean : 42.8708
Gaus Width: 3.2271
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 49.16
Std Dev 3.454
Entry
ampl
0
2
4
6
8
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0
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40
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60
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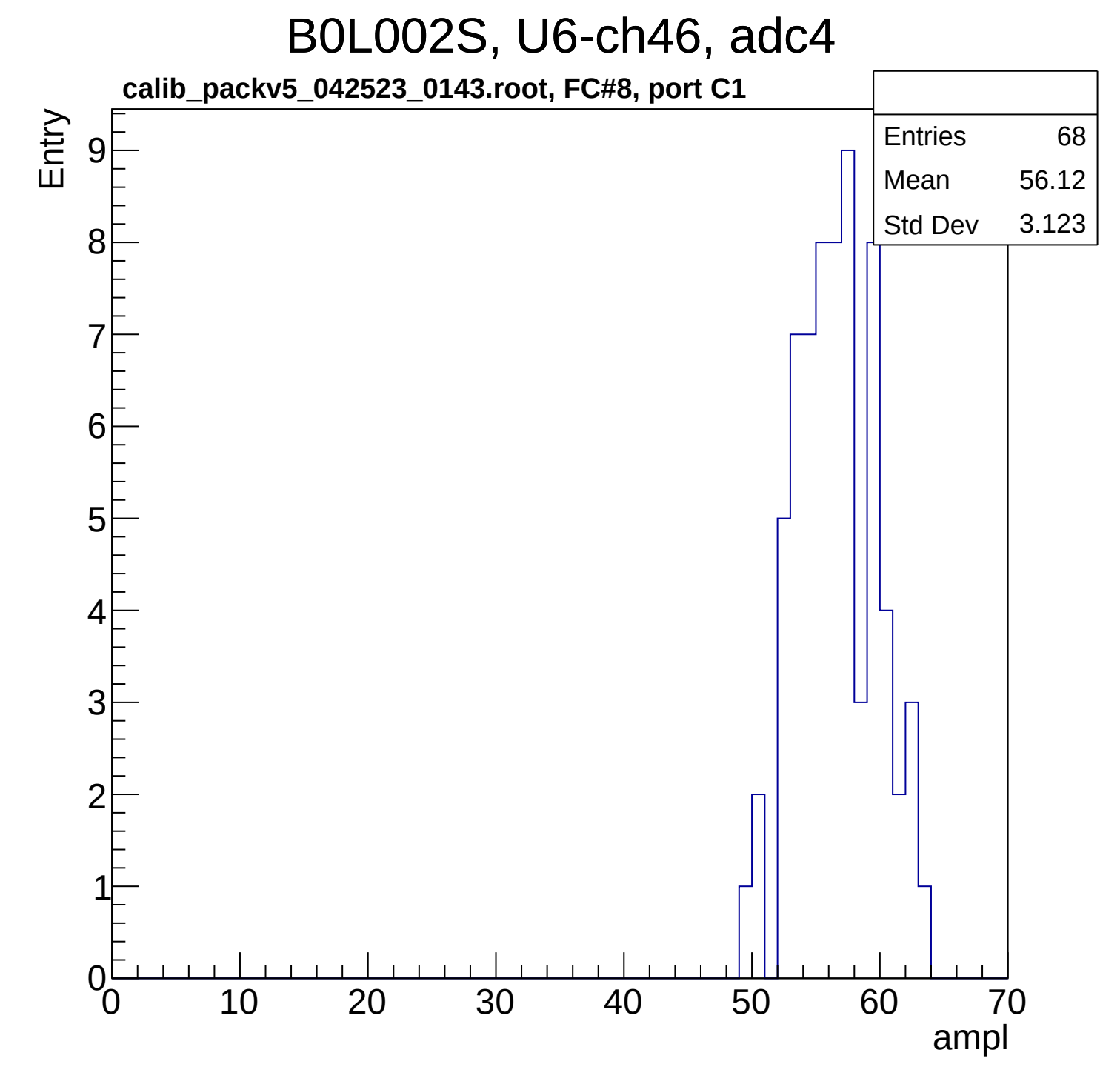
B0L002S, U6-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.12
Std Dev 3.123
Entry
ampl
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2
3
4
5
6
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9
0
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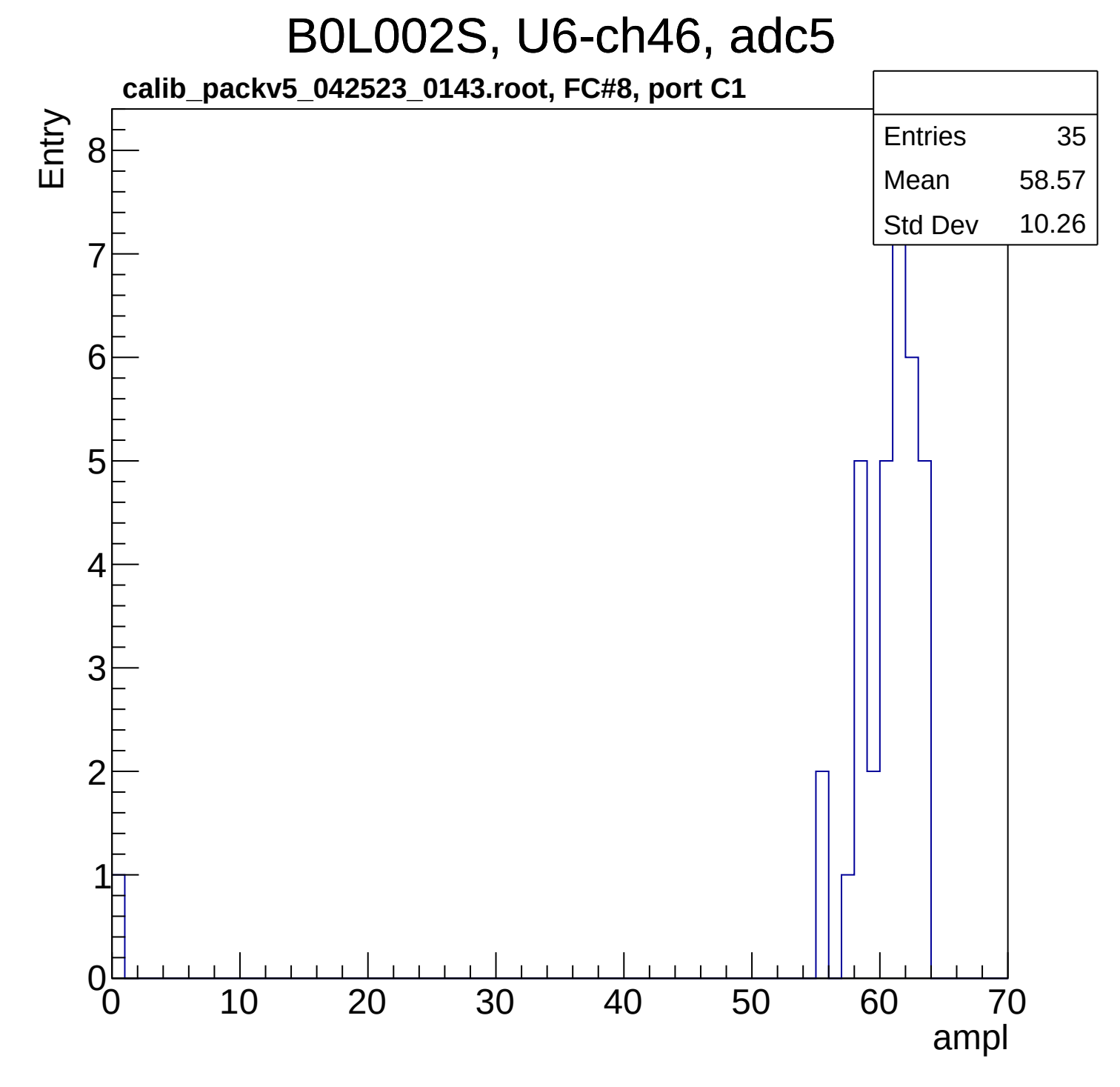
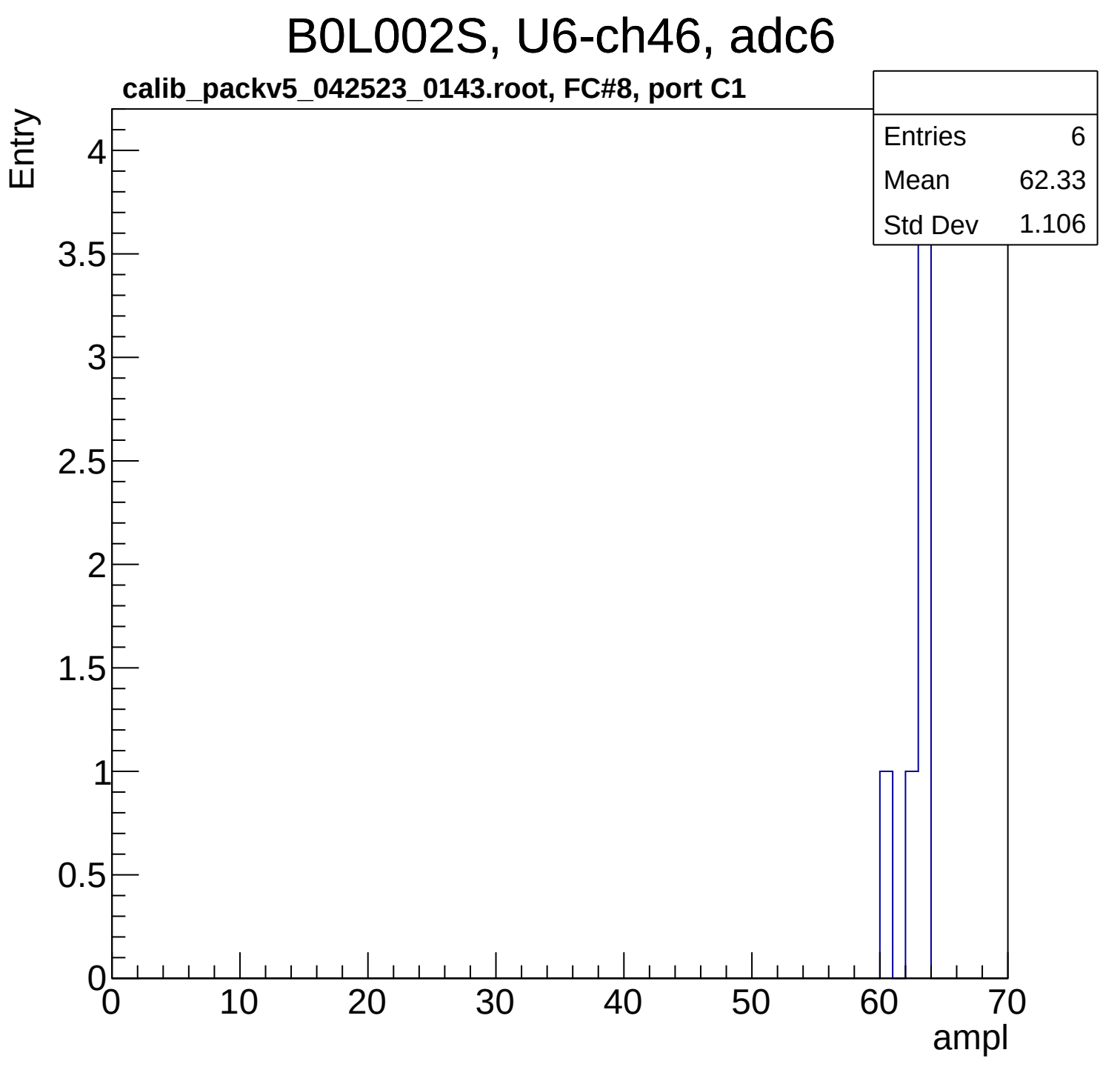

B0L002S, U6-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.57
Std Dev 10.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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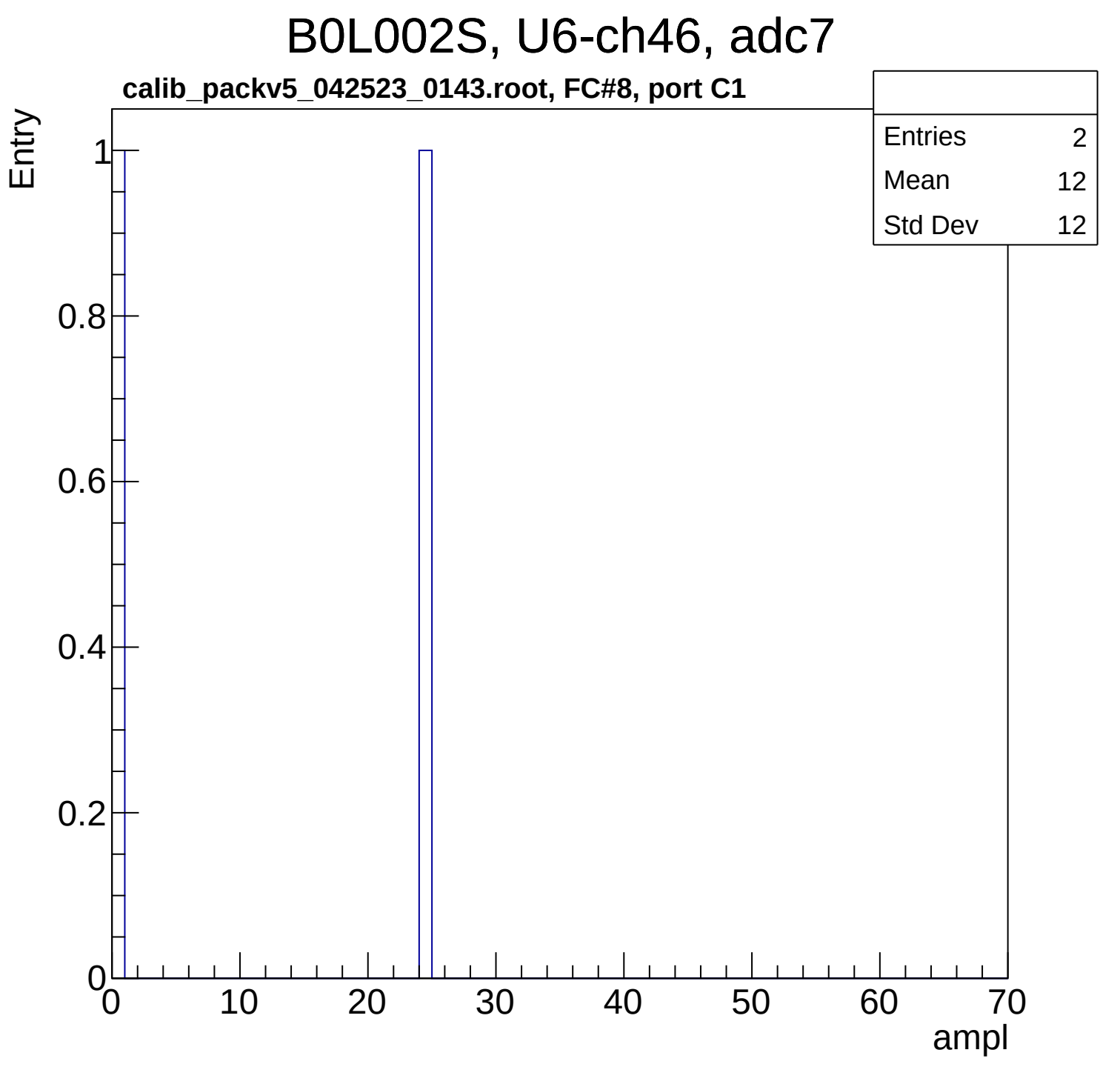
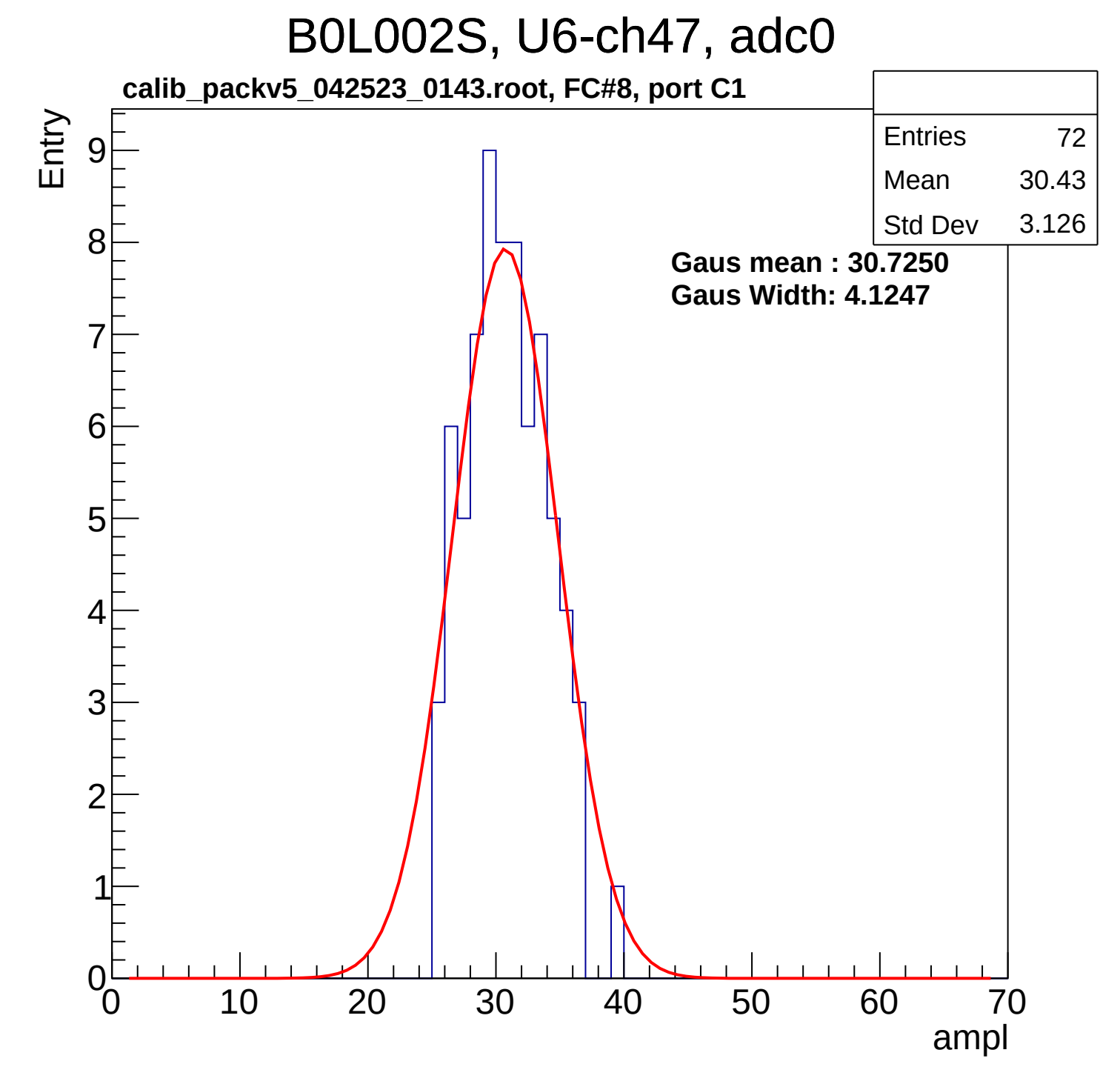
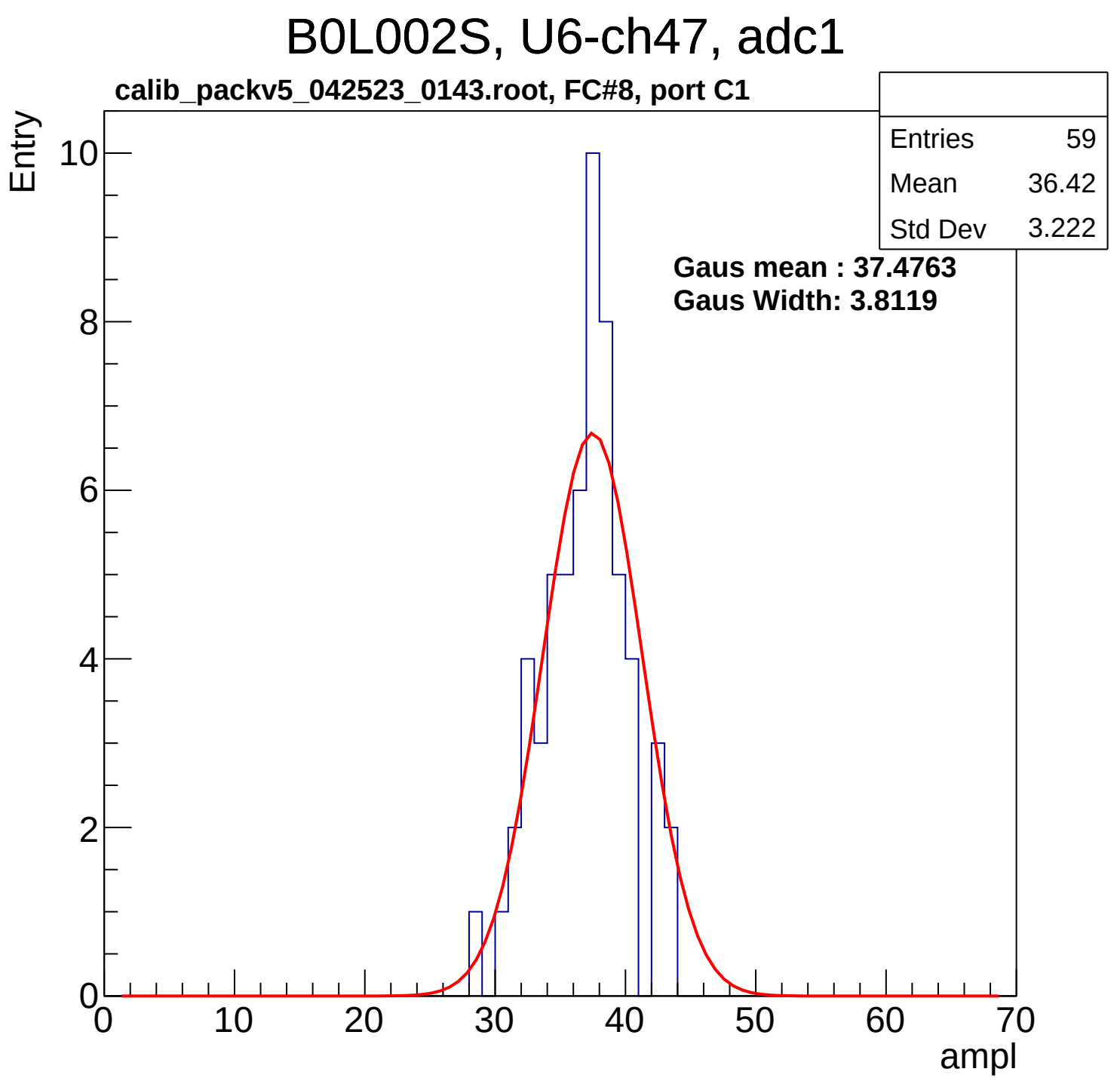
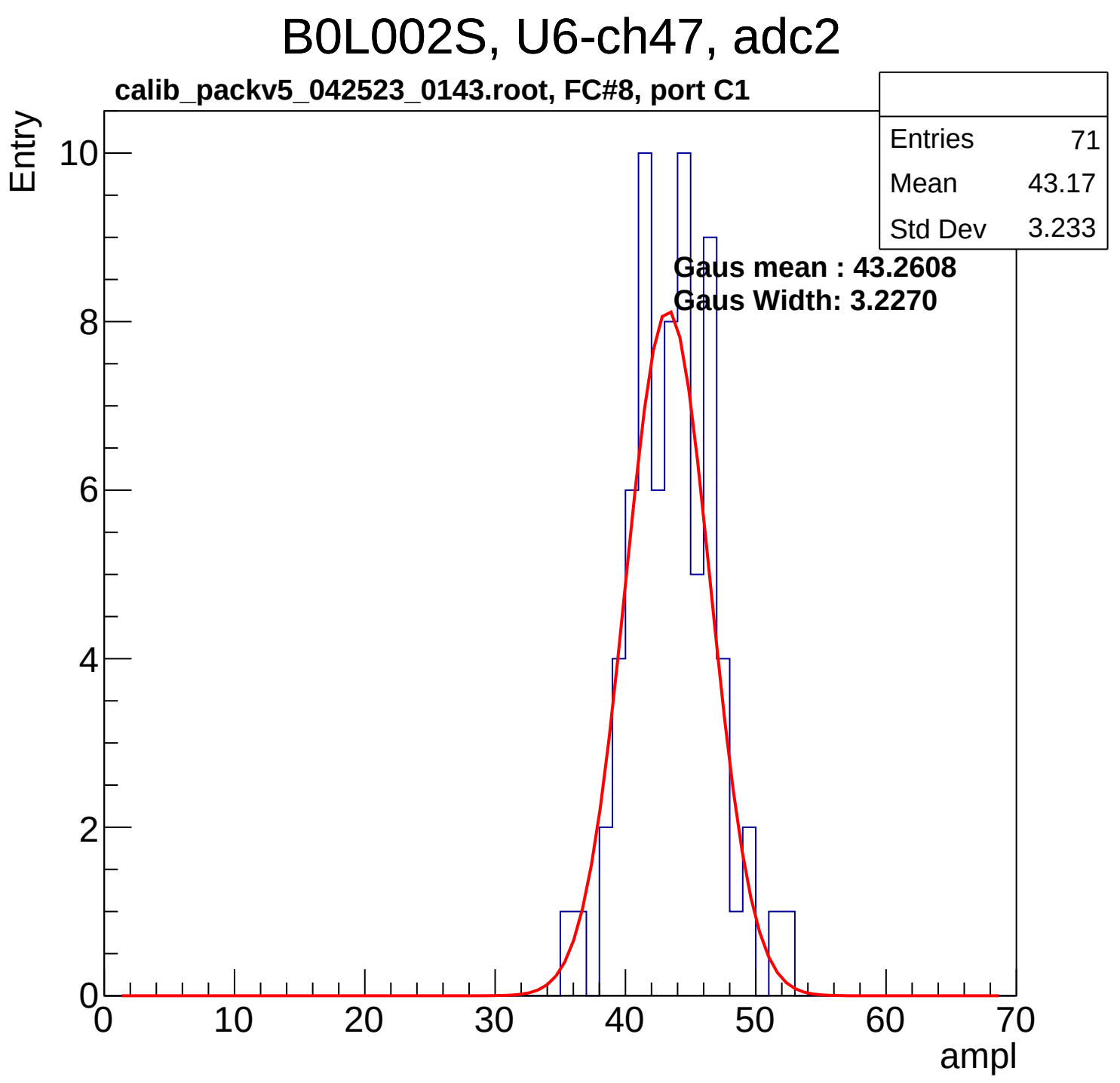
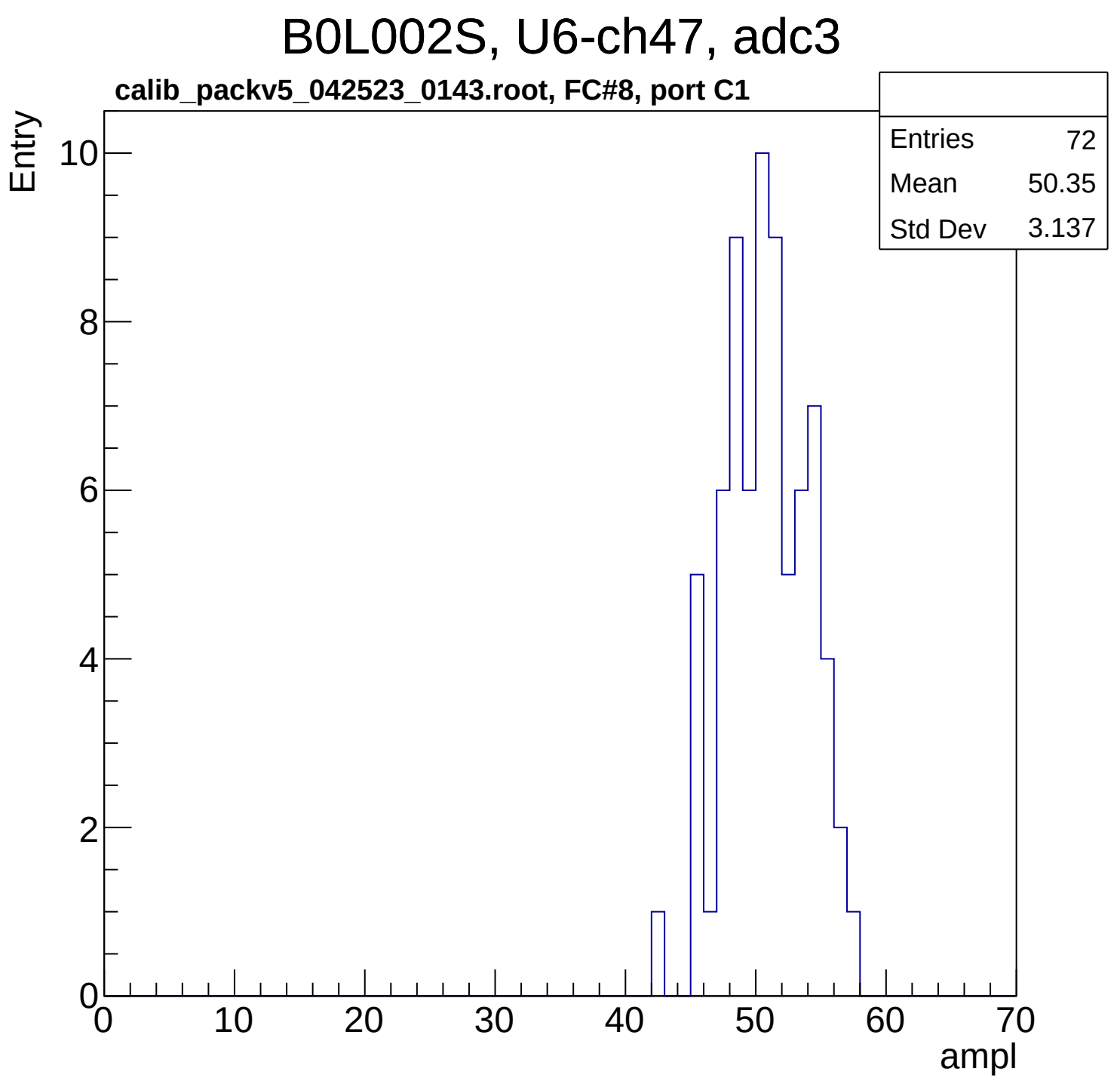
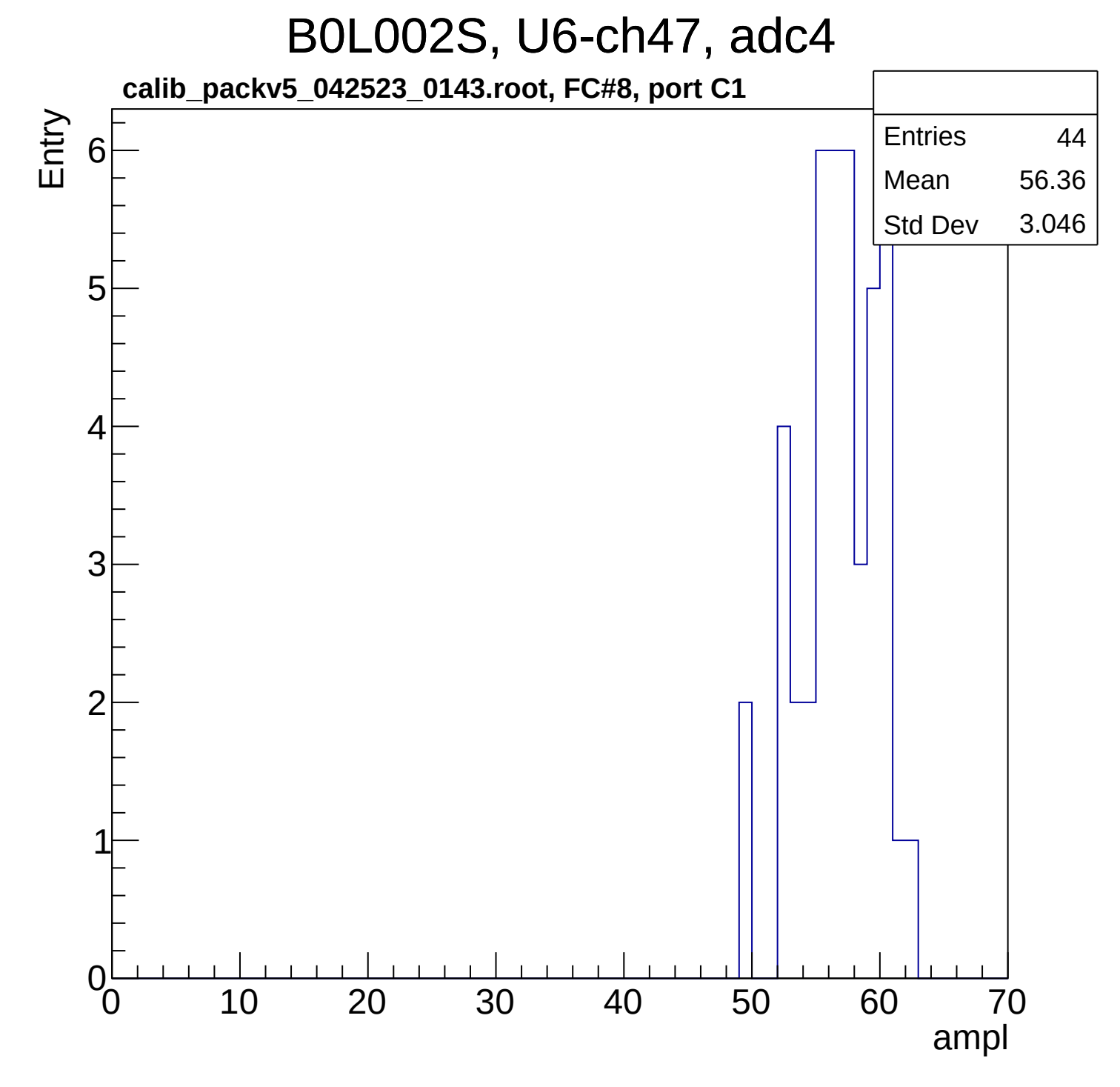

B0L002S, U6-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

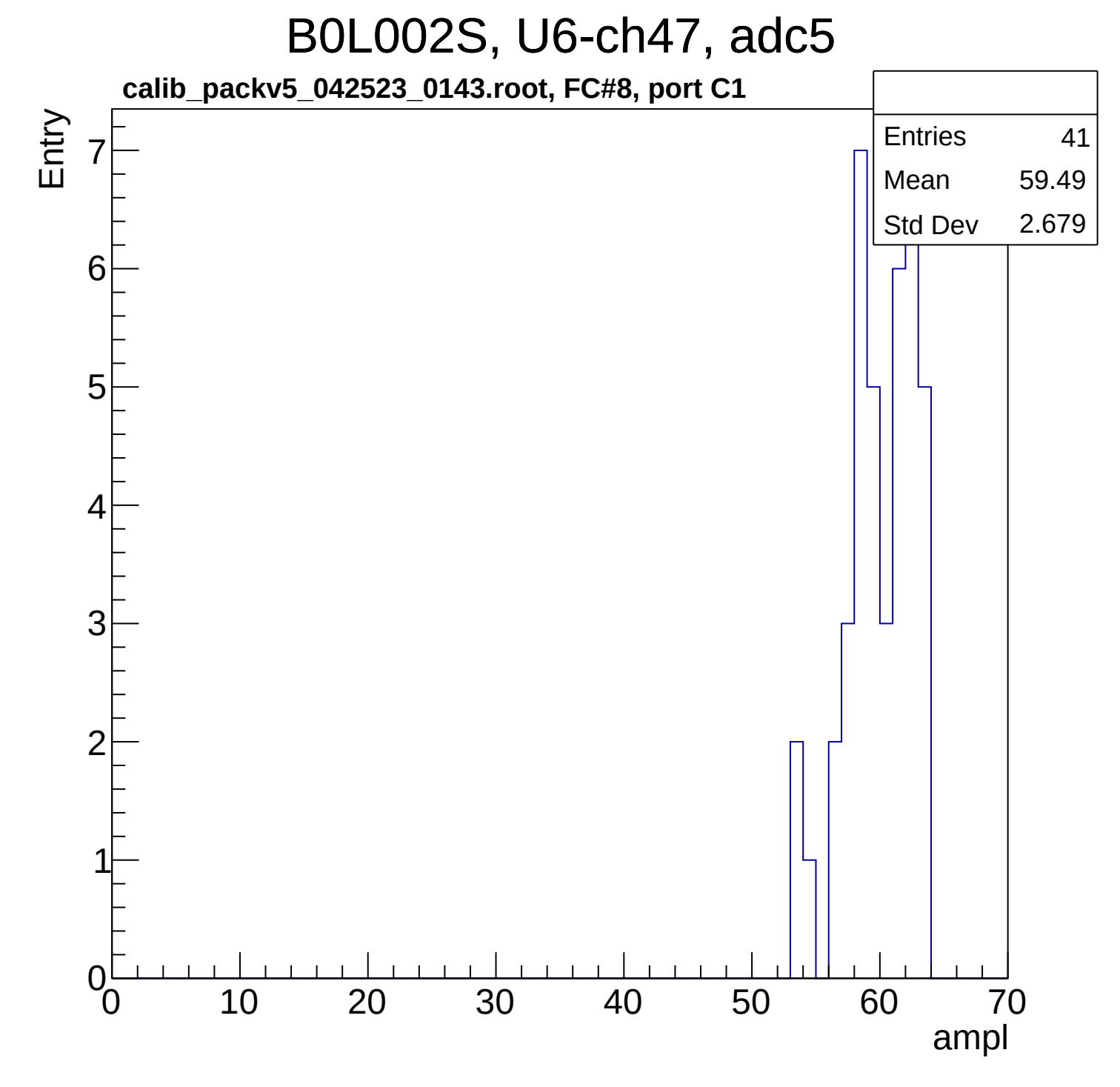

B0L002S, U6-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.43
Std Dev 3.126
Gaus mean : 30.7250
Gaus Width: 4.1247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

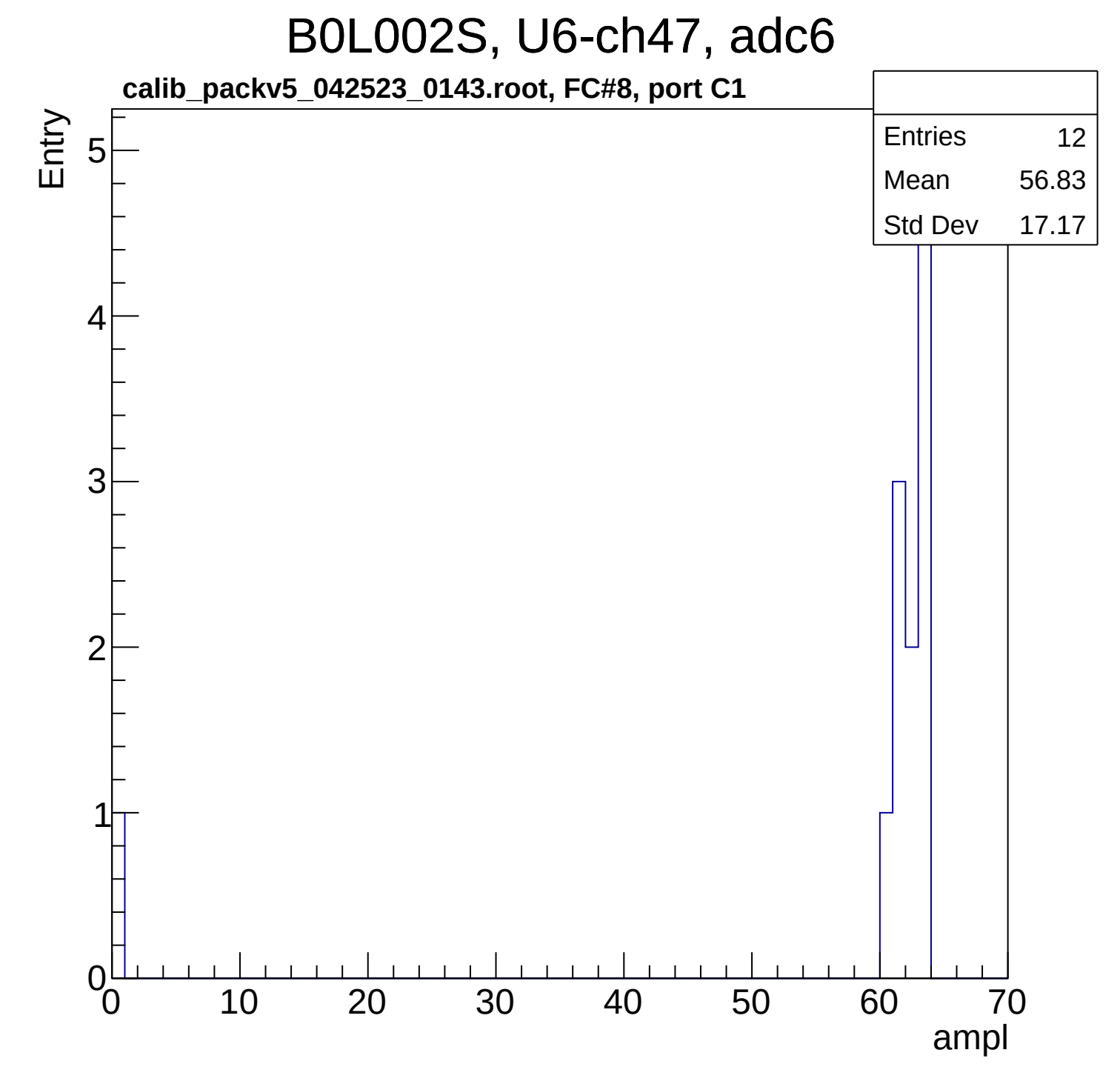

B0L002S, U6-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 36.42
Std Dev 3.222
Gaus mean : 37.4763
Gaus Width: 3.8119
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

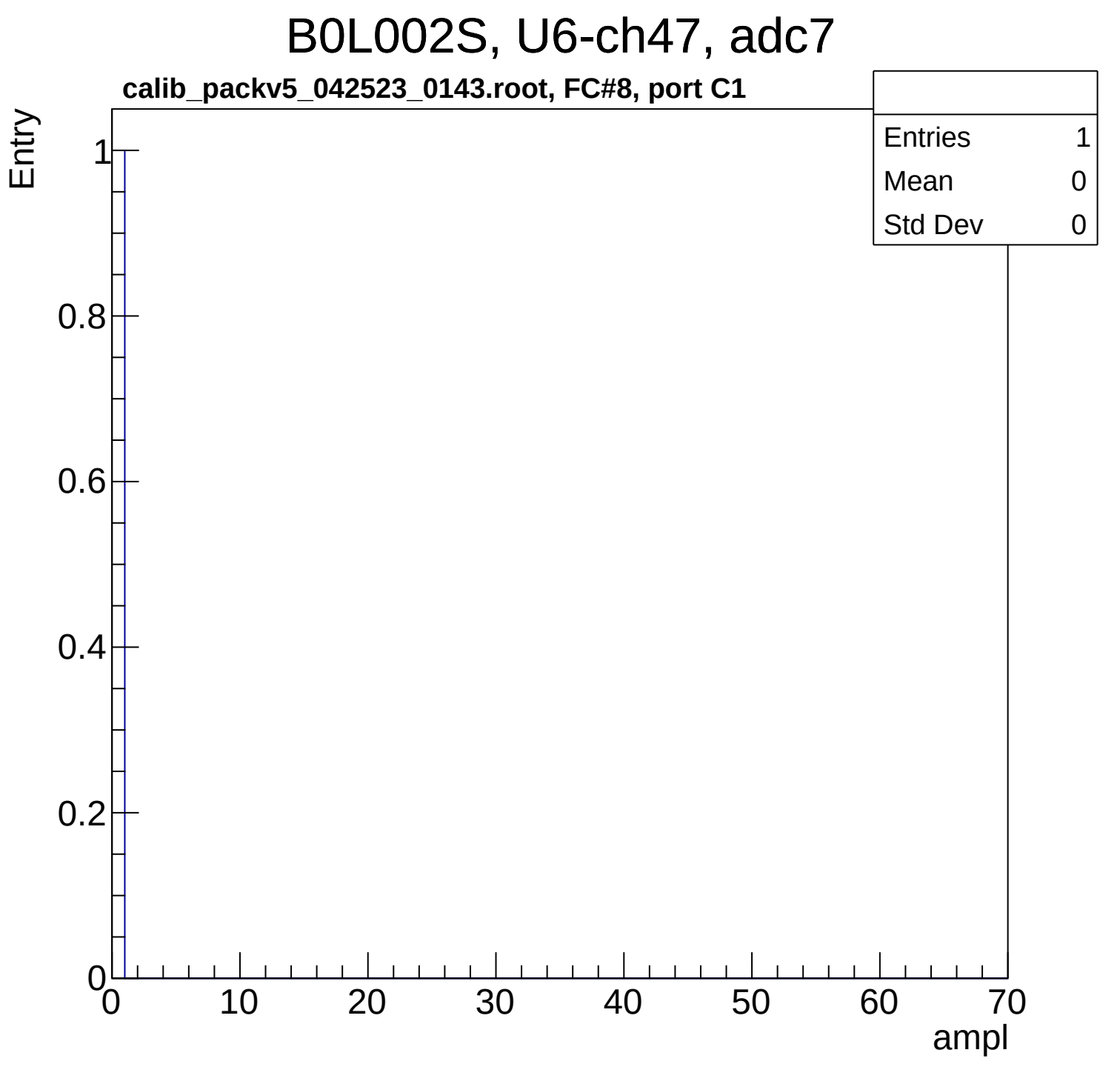

B0L002S, U6-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.17
Std Dev 3.233
Gaus mean : 43.2608
Gaus Width: 3.2270
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

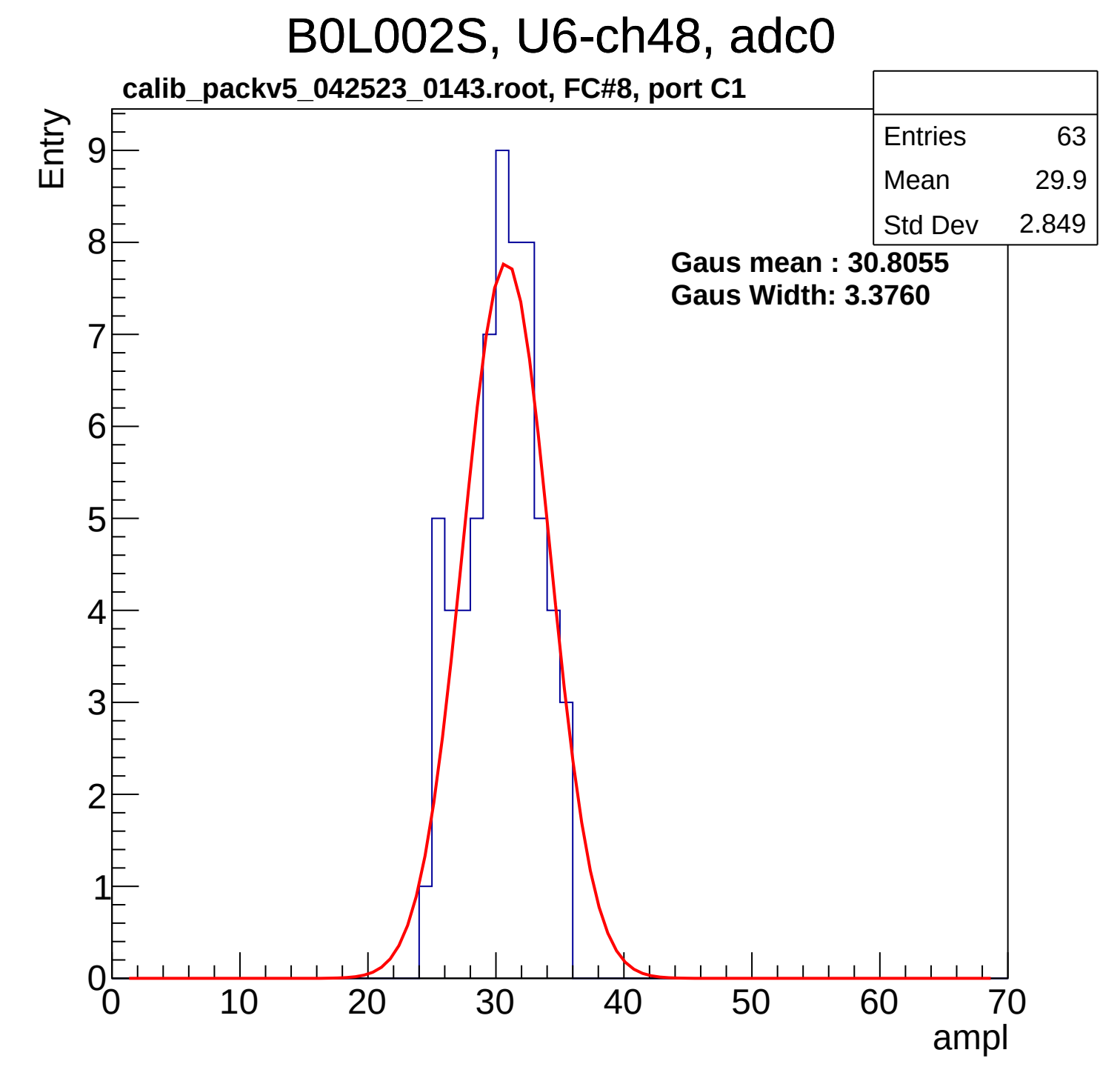

B0L002S, U6-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.35
Std Dev 3.137
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

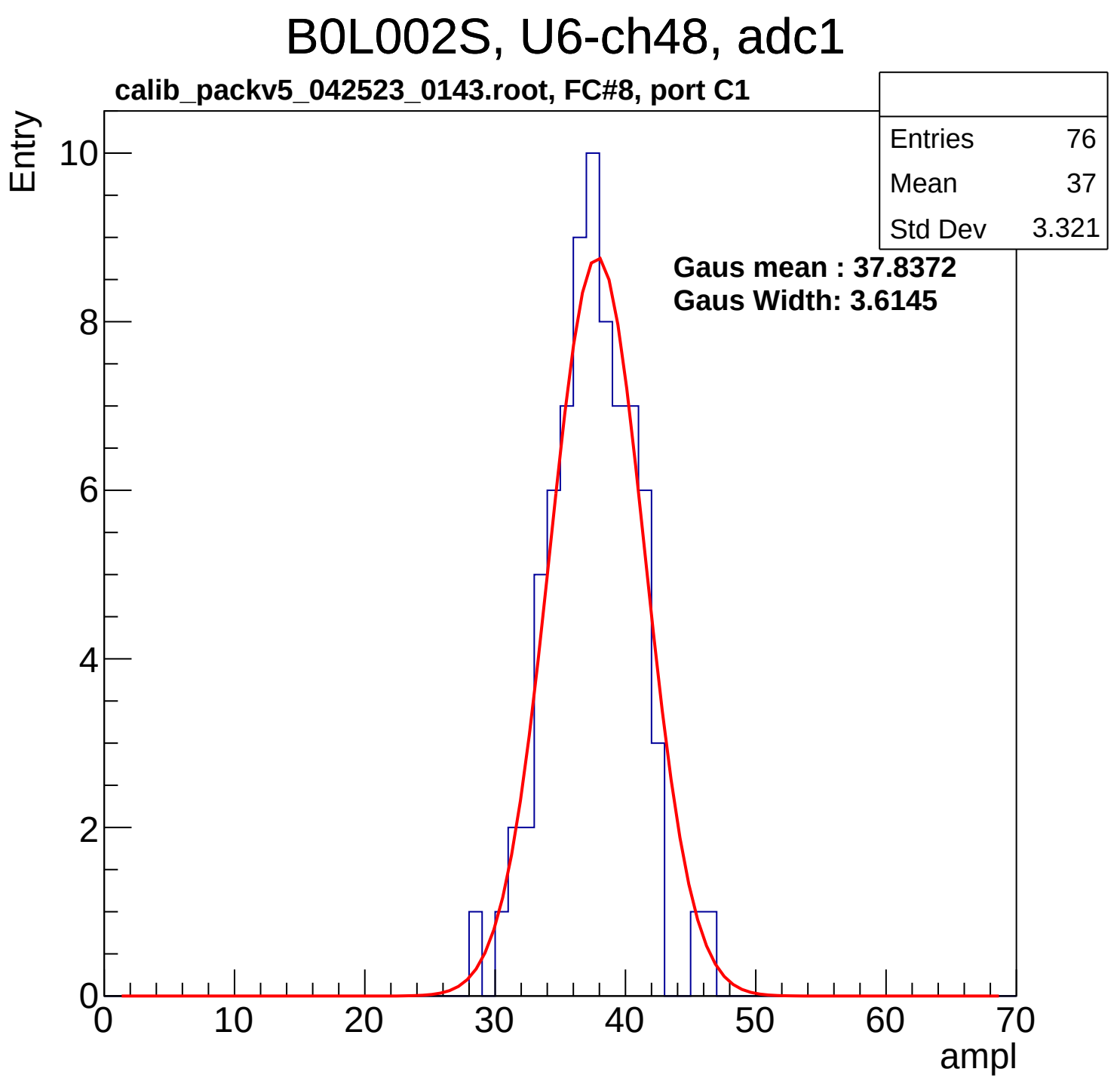

B0L002S, U6-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 56.36
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

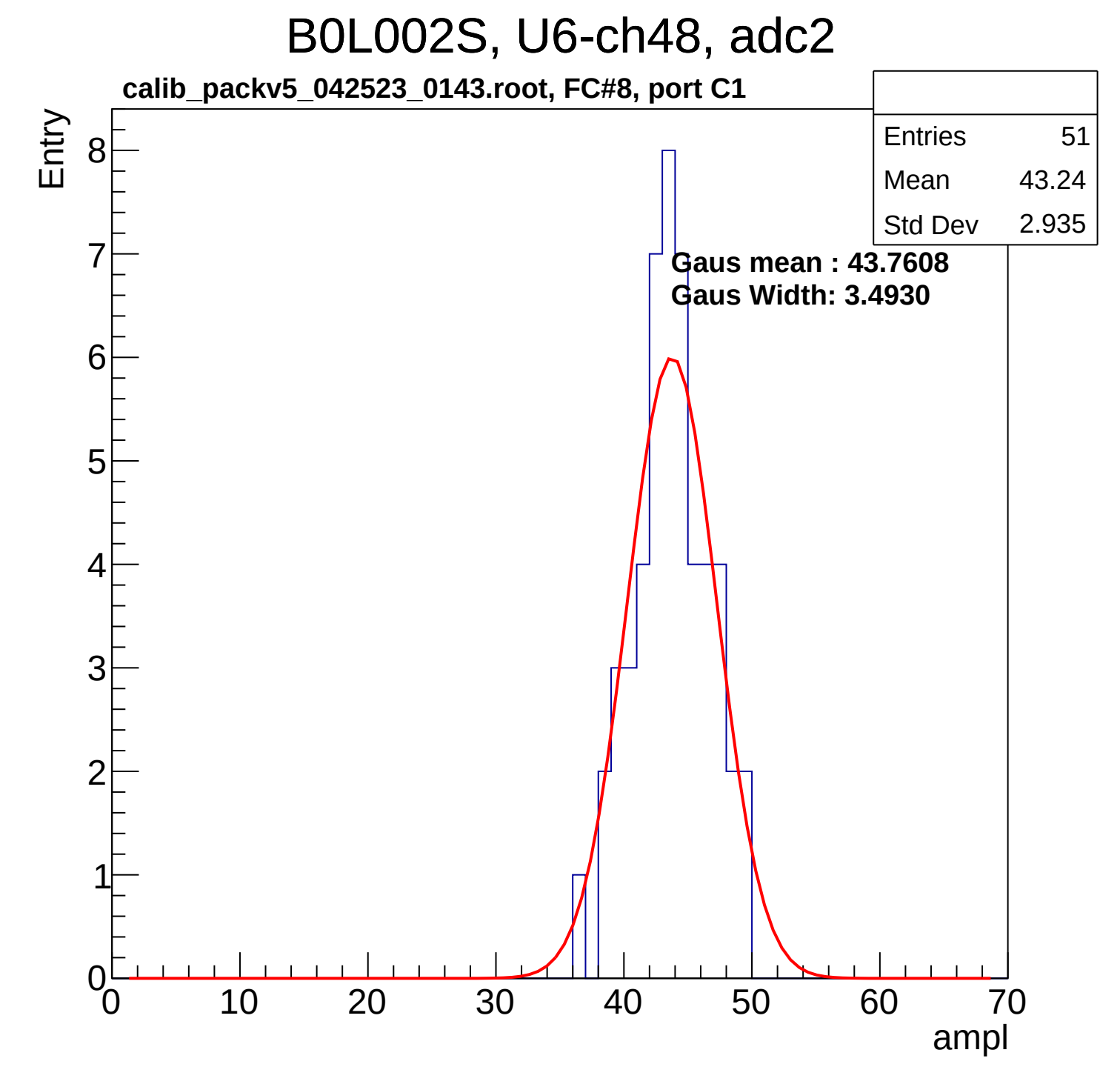

B0L002S, U6-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.49
Std Dev 2.679
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

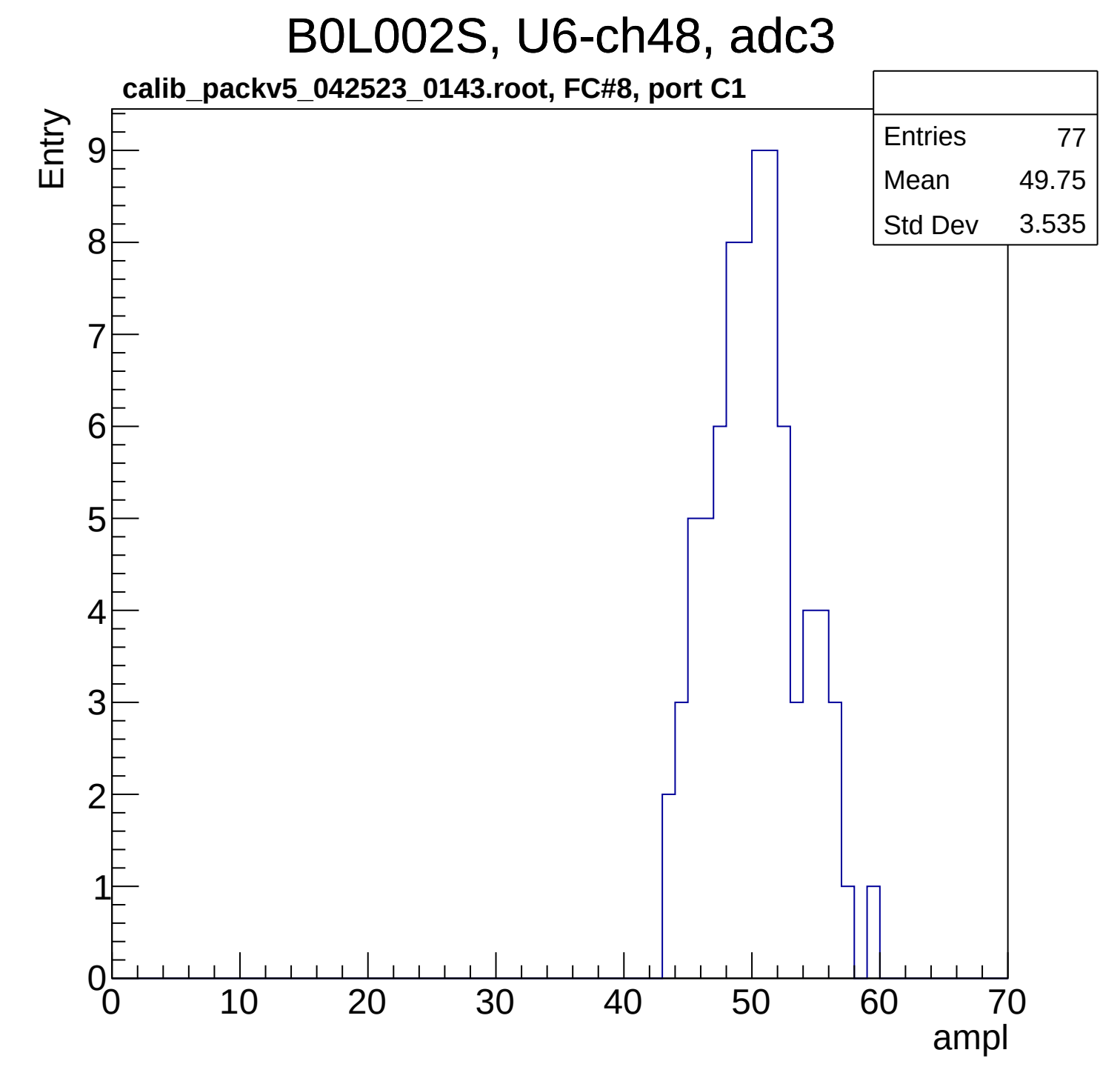

B0L002S, U6-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.9
Std Dev 2.849
Gaus mean : 30.8055
Gaus Width: 3.3760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37
Std Dev 3.321
Gaus mean : 37.8372
Gaus Width: 3.6145
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 43.24
Std Dev 2.935
Gaus mean : 43.7608
Gaus Width: 3.4930
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 49.75
Std Dev 3.535
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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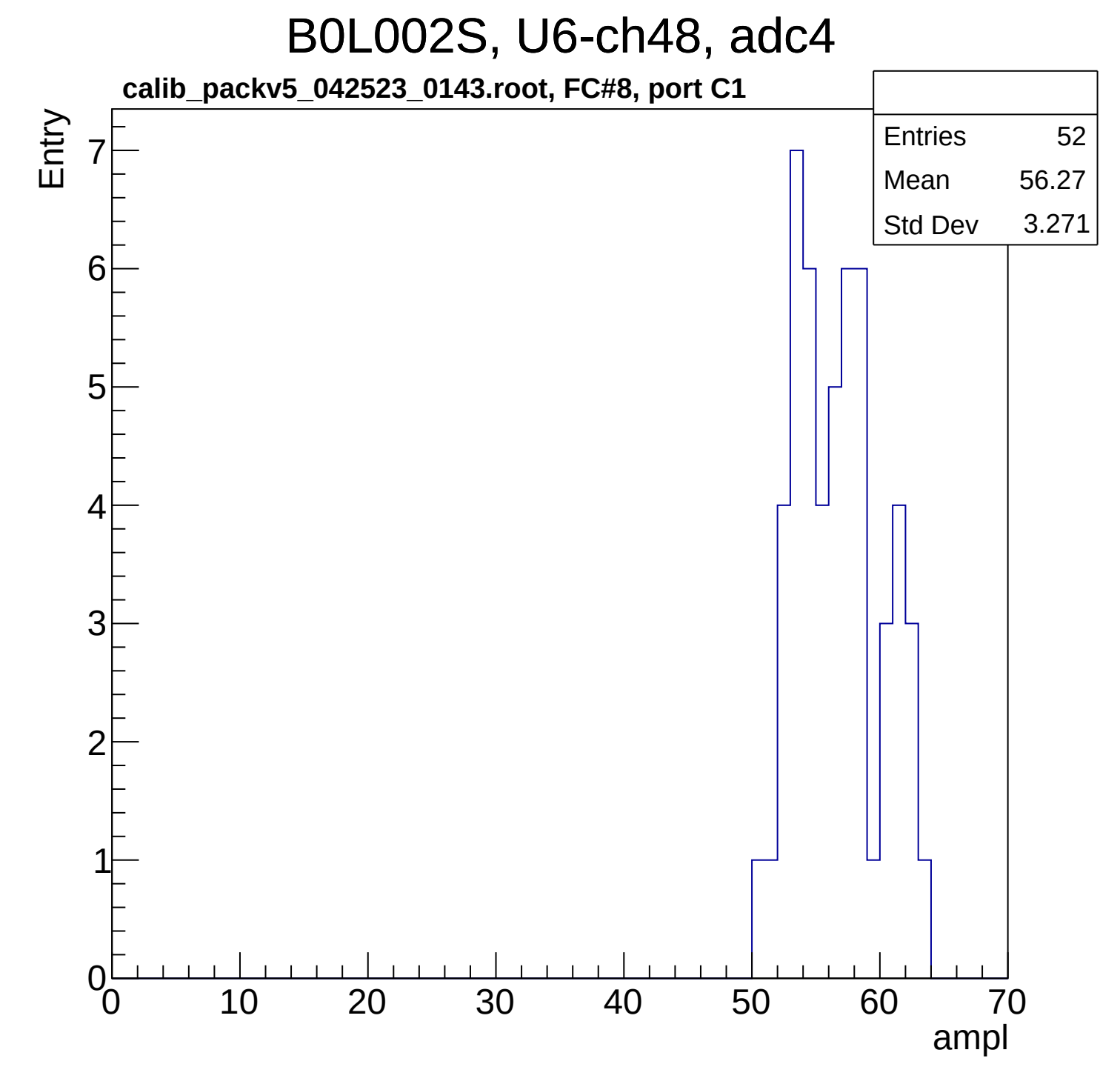
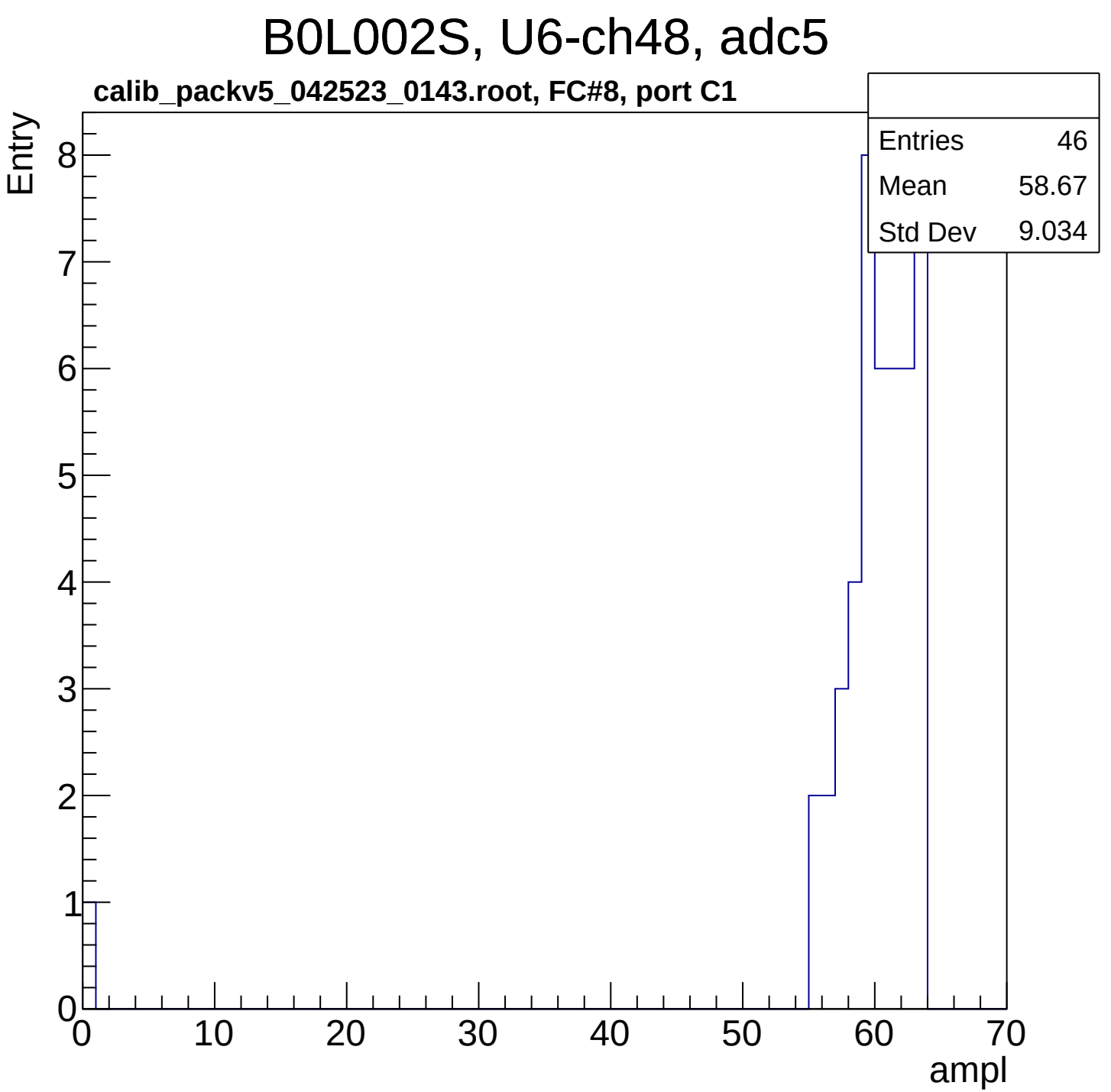
40
50
60
70


B0L002S, U6-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.27
Std Dev 3.271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 58.67
Std Dev 9.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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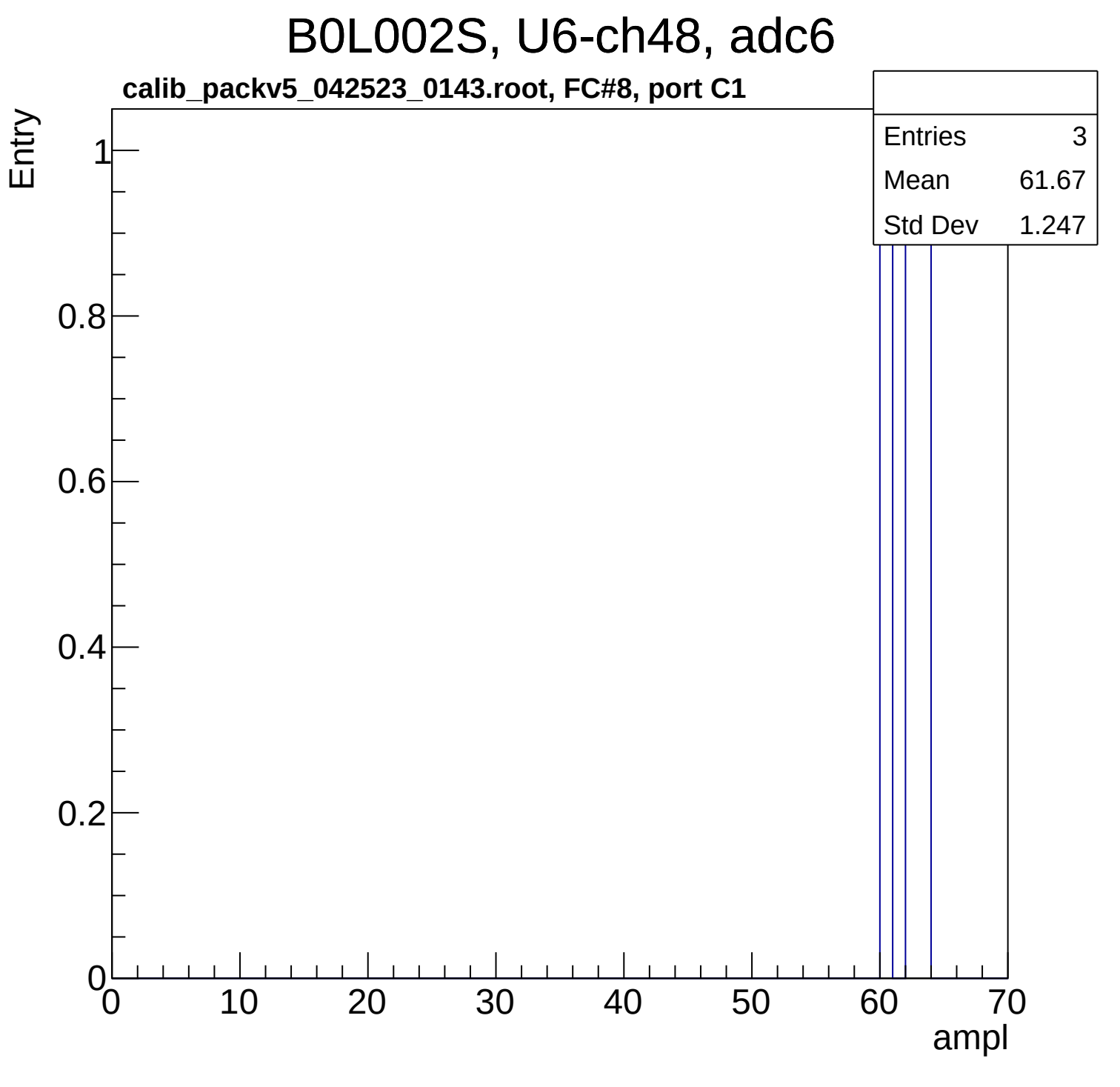
B0L002S, U6-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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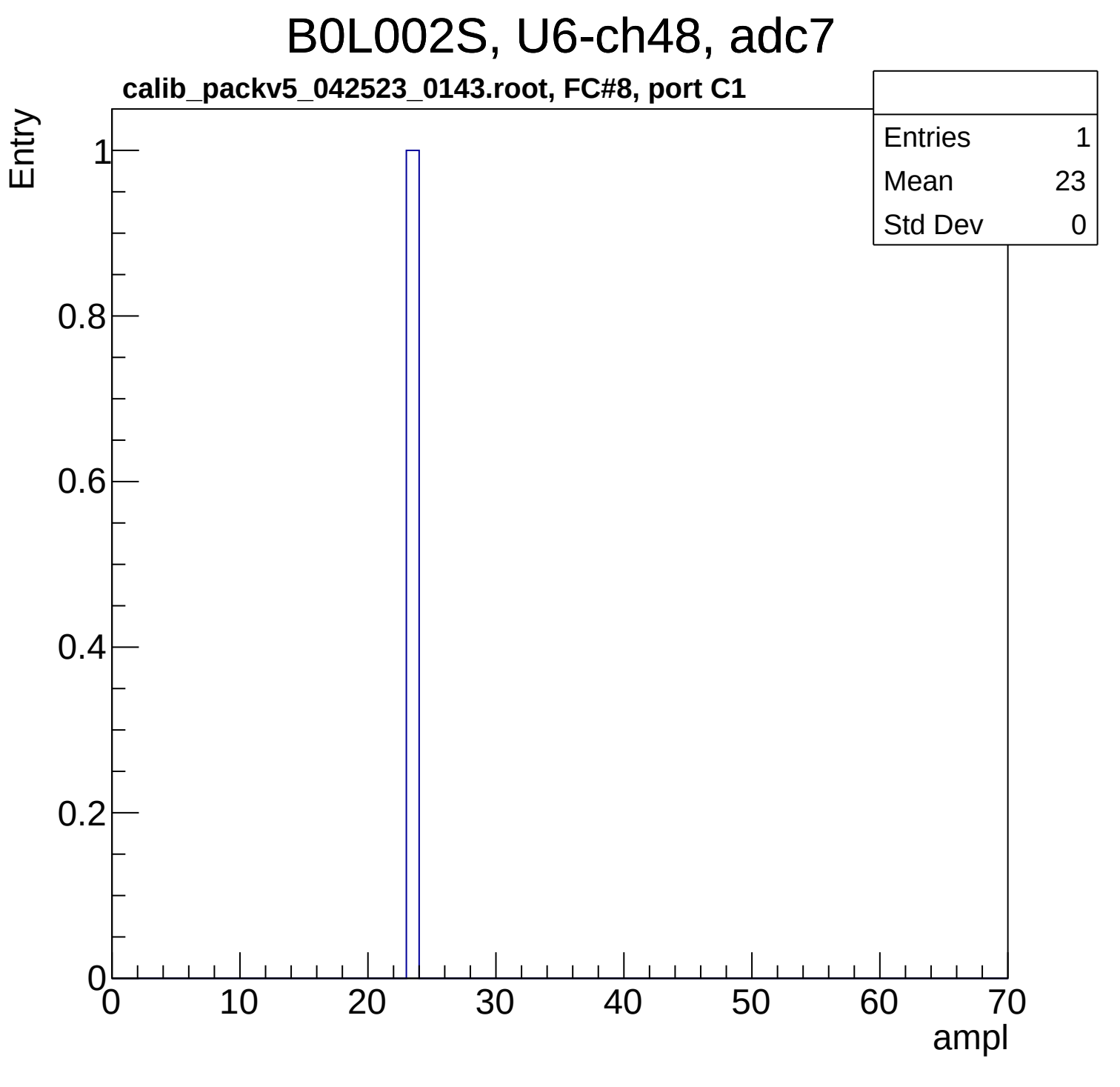
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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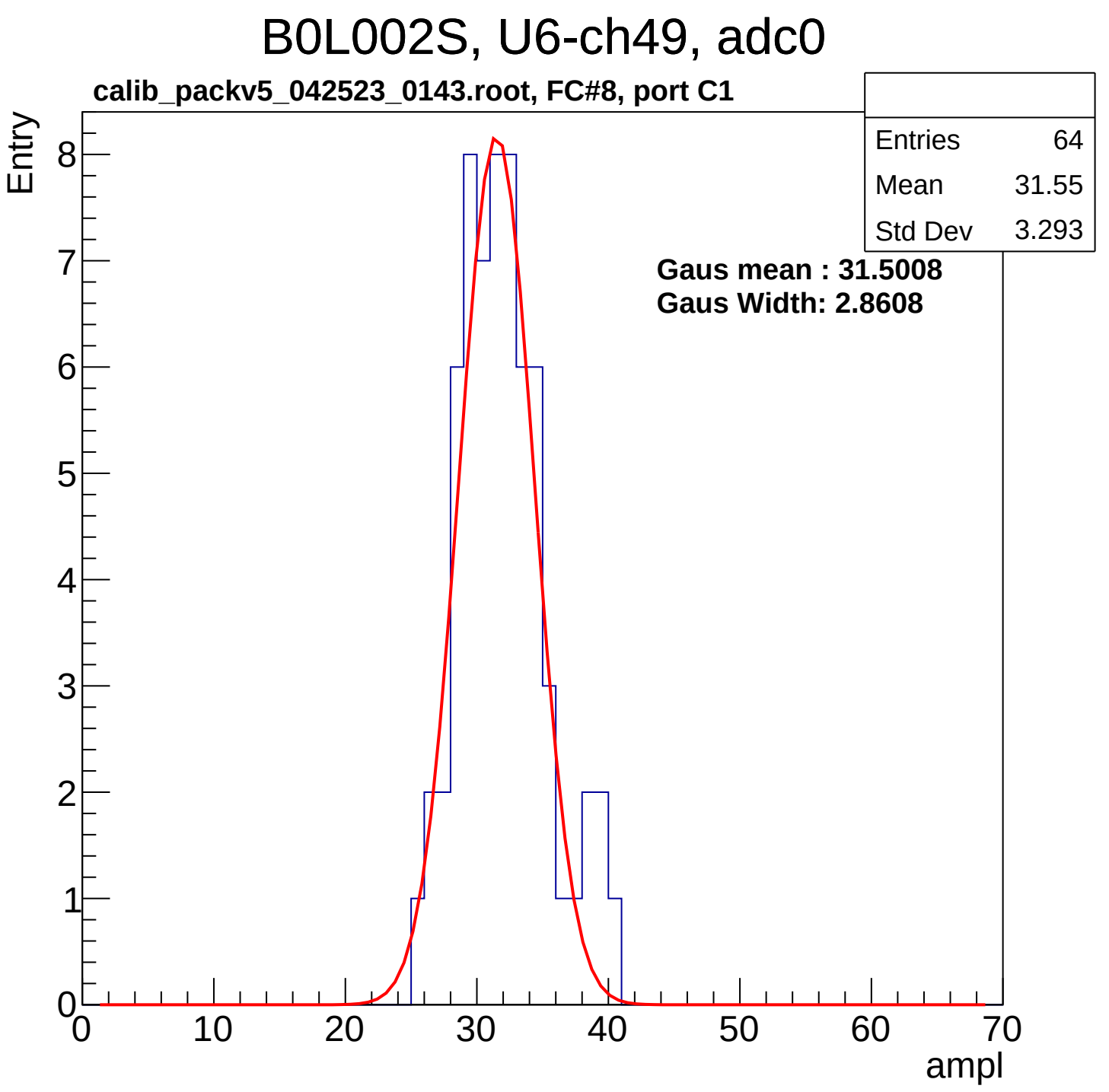
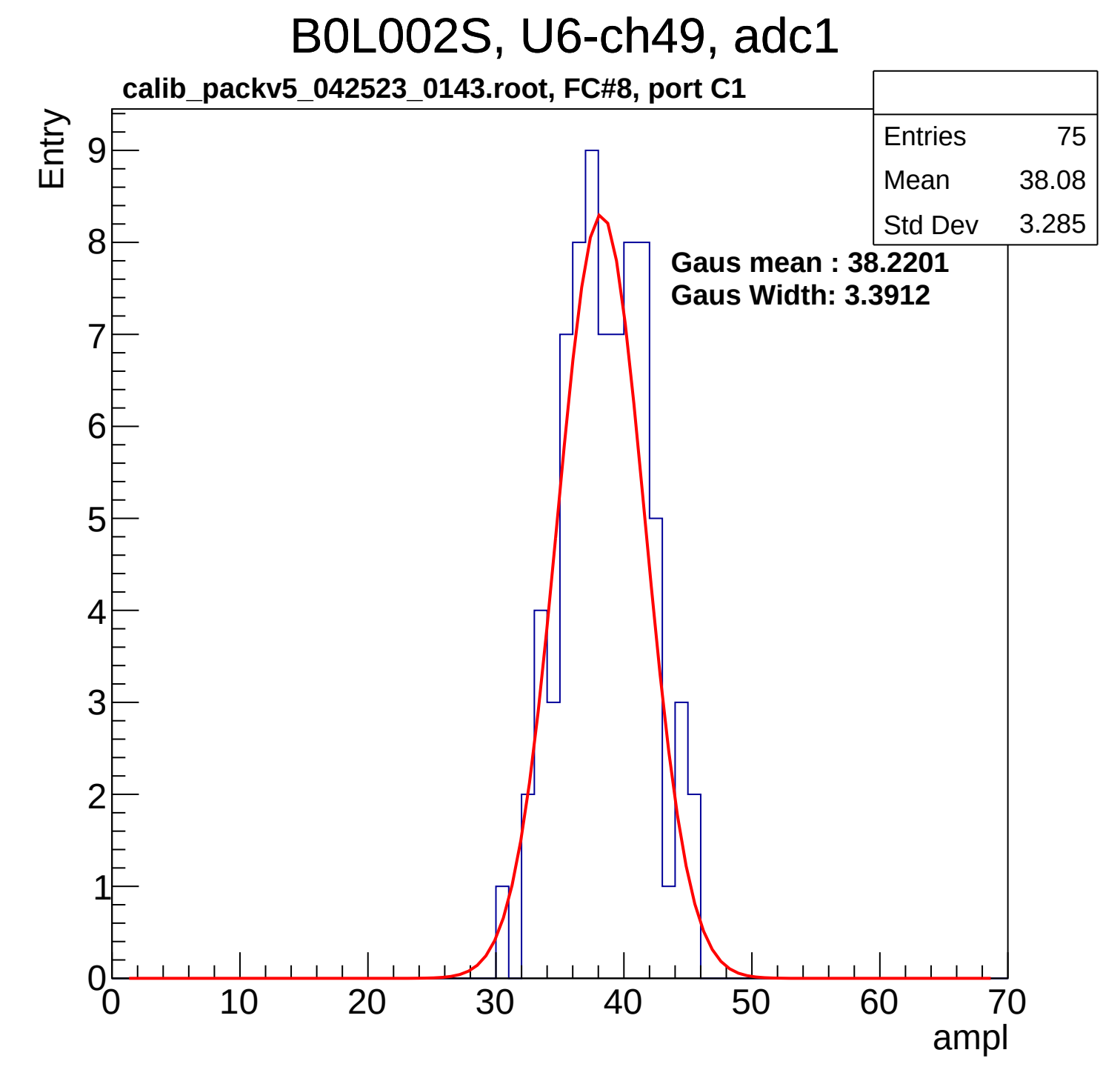
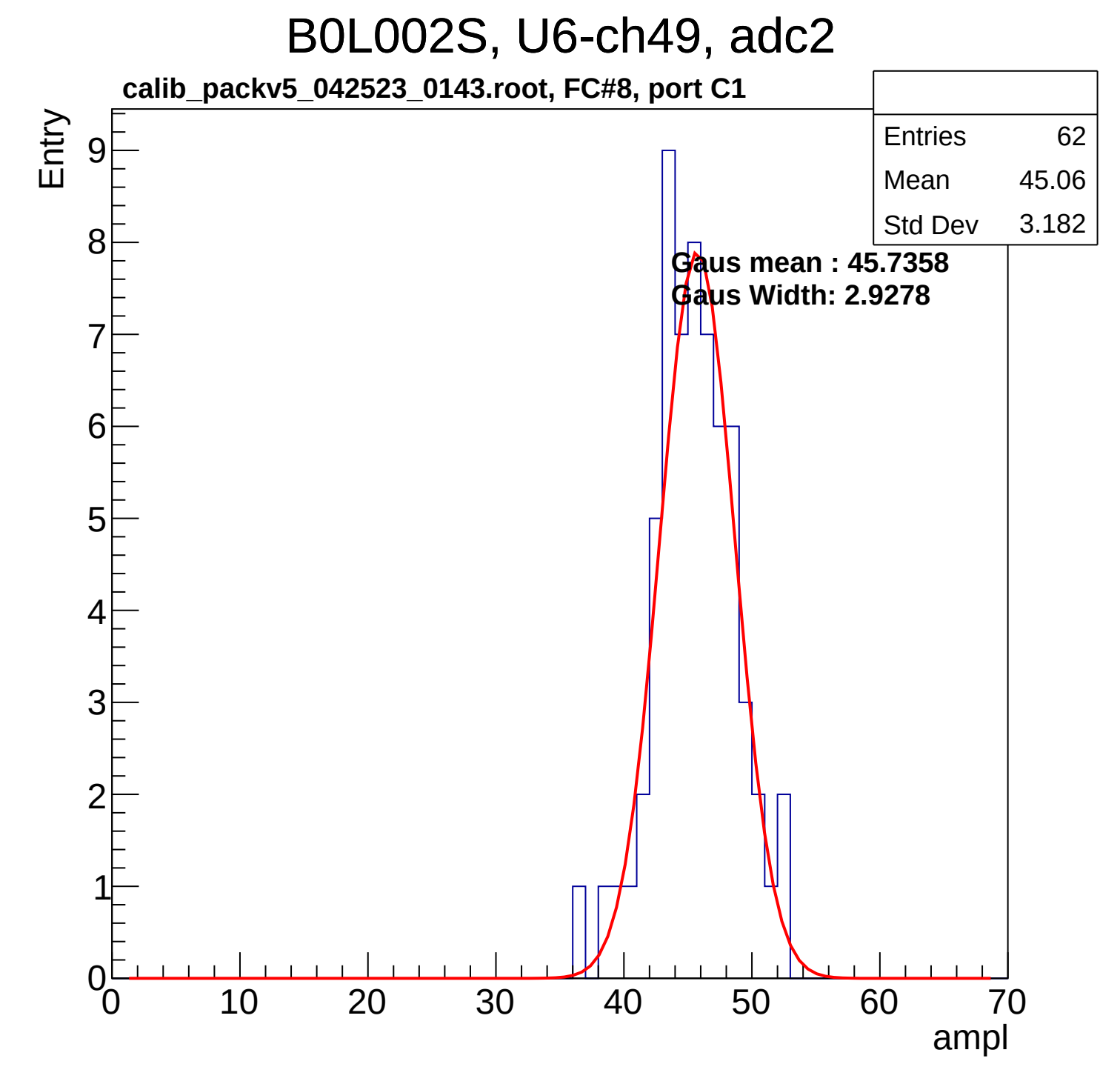
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.55
Std Dev 3.293
Gaus mean : 31.5008
Gaus Width: 2.8608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 38.08
Std Dev 3.285
Gaus mean : 38.2201
Gaus Width: 3.3912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.06
Std Dev 3.182
Gaus mean : 45.7358
Gaus Width: 2.9278
Entry
ampl
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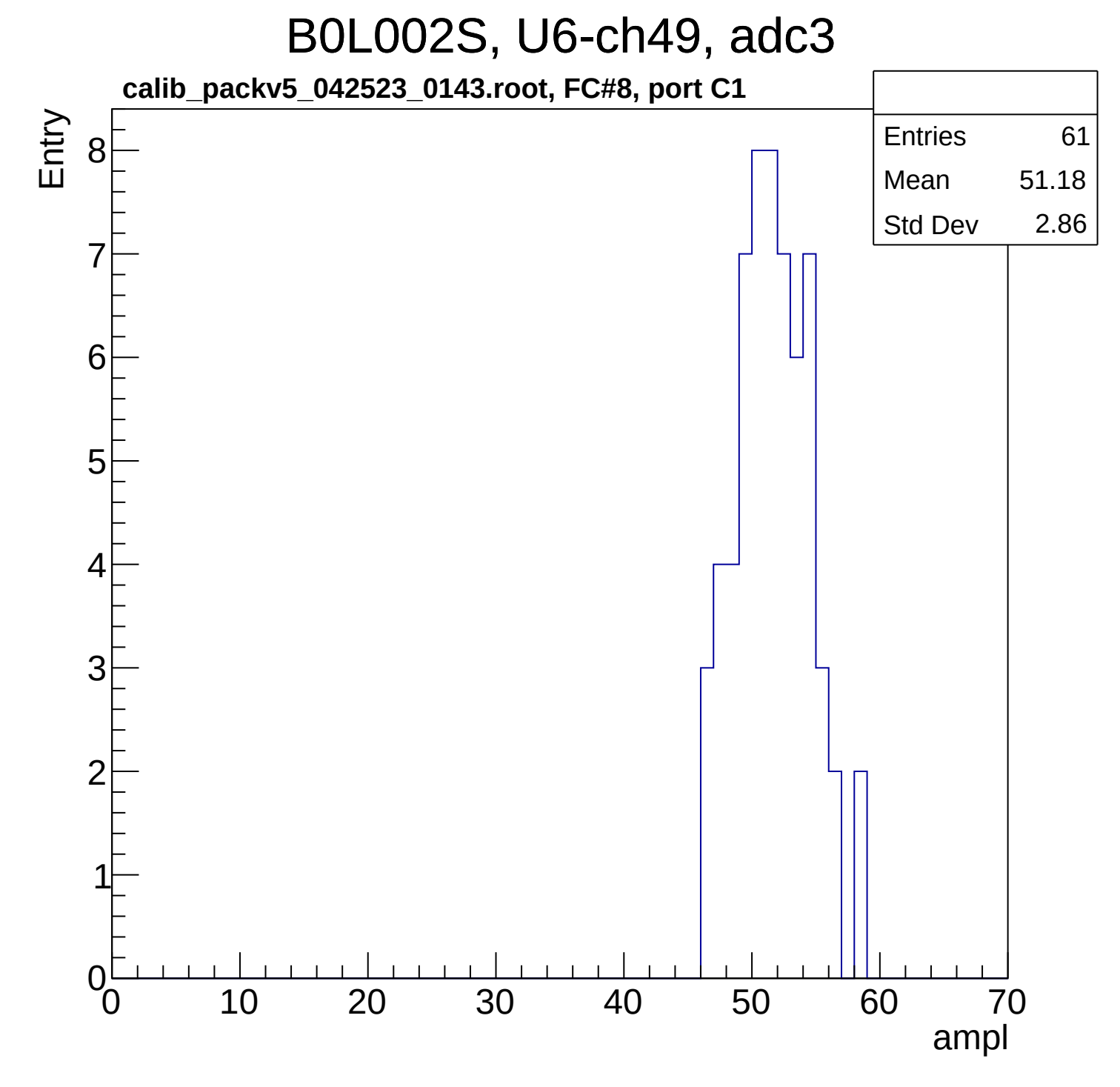
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.18
Std Dev 2.86
Entry
ampl
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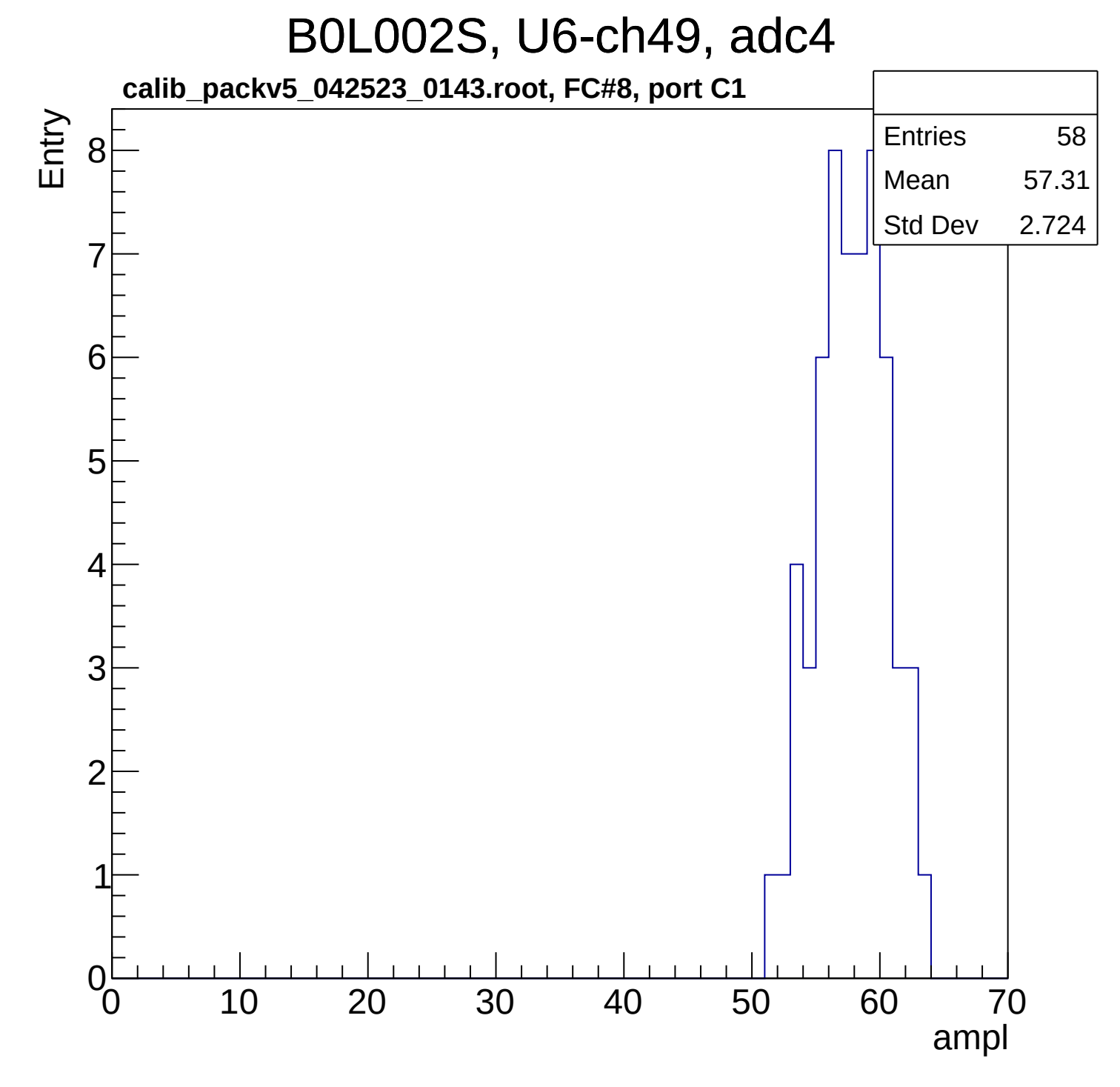
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.31
Std Dev 2.724
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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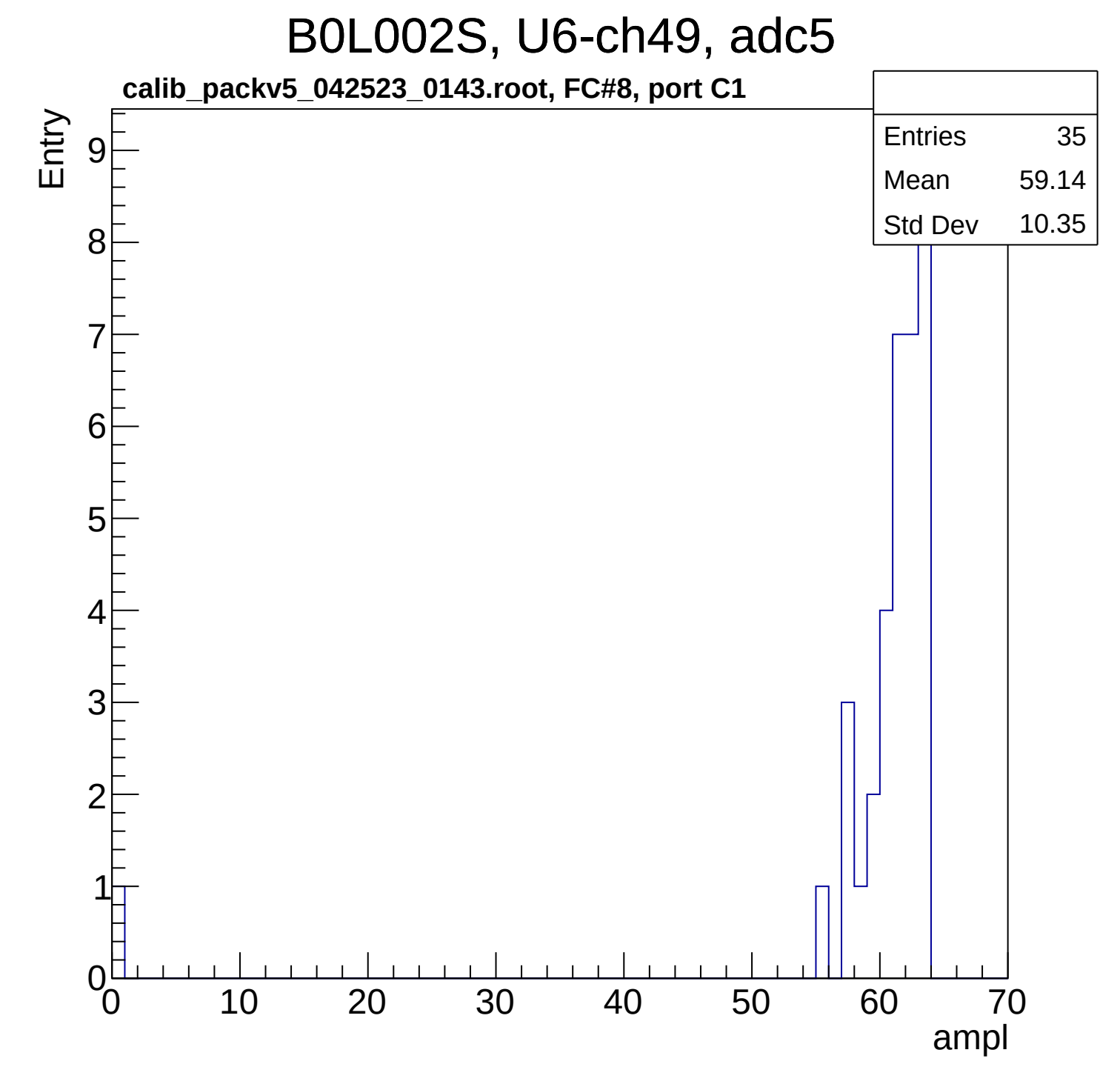
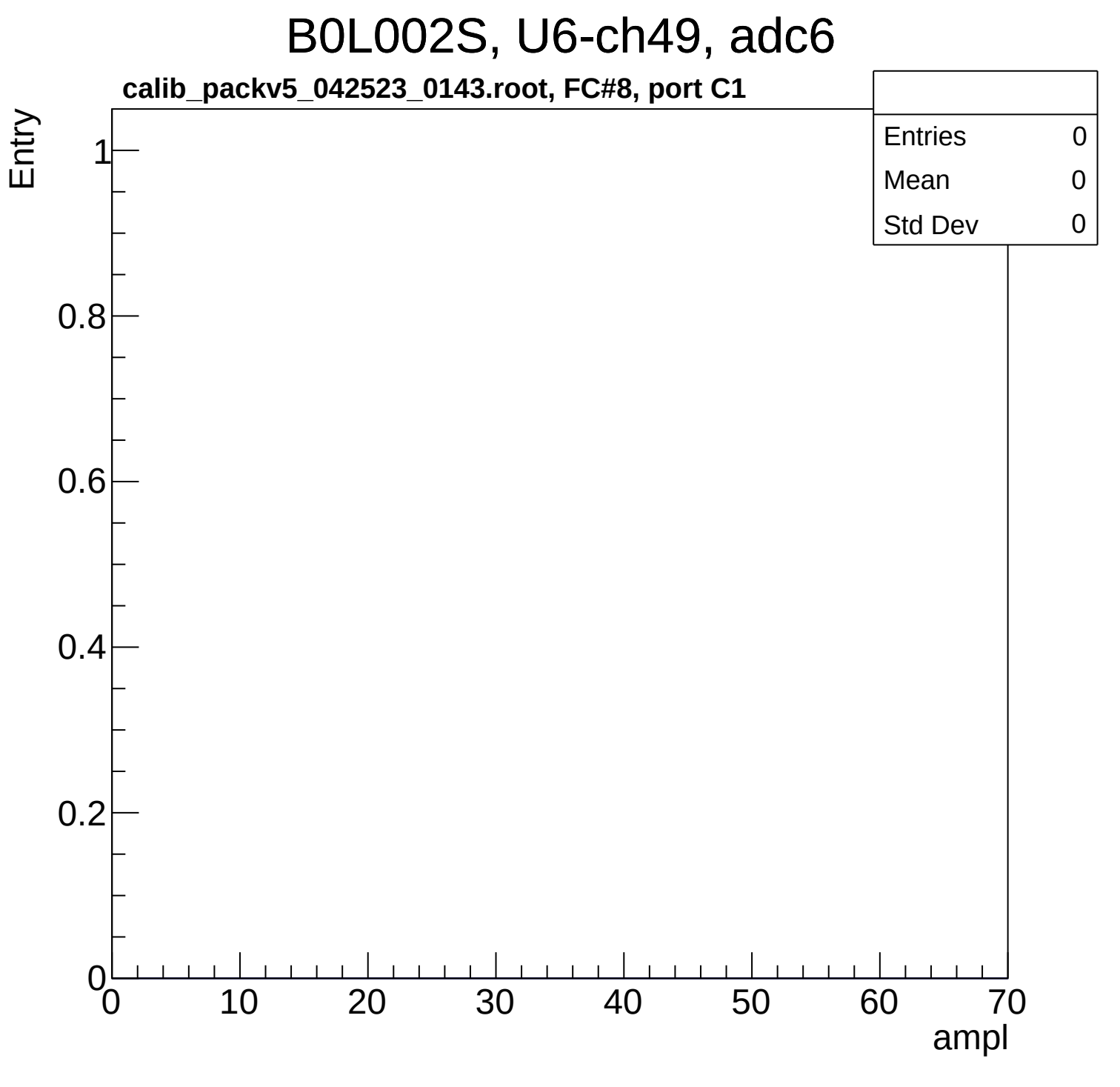
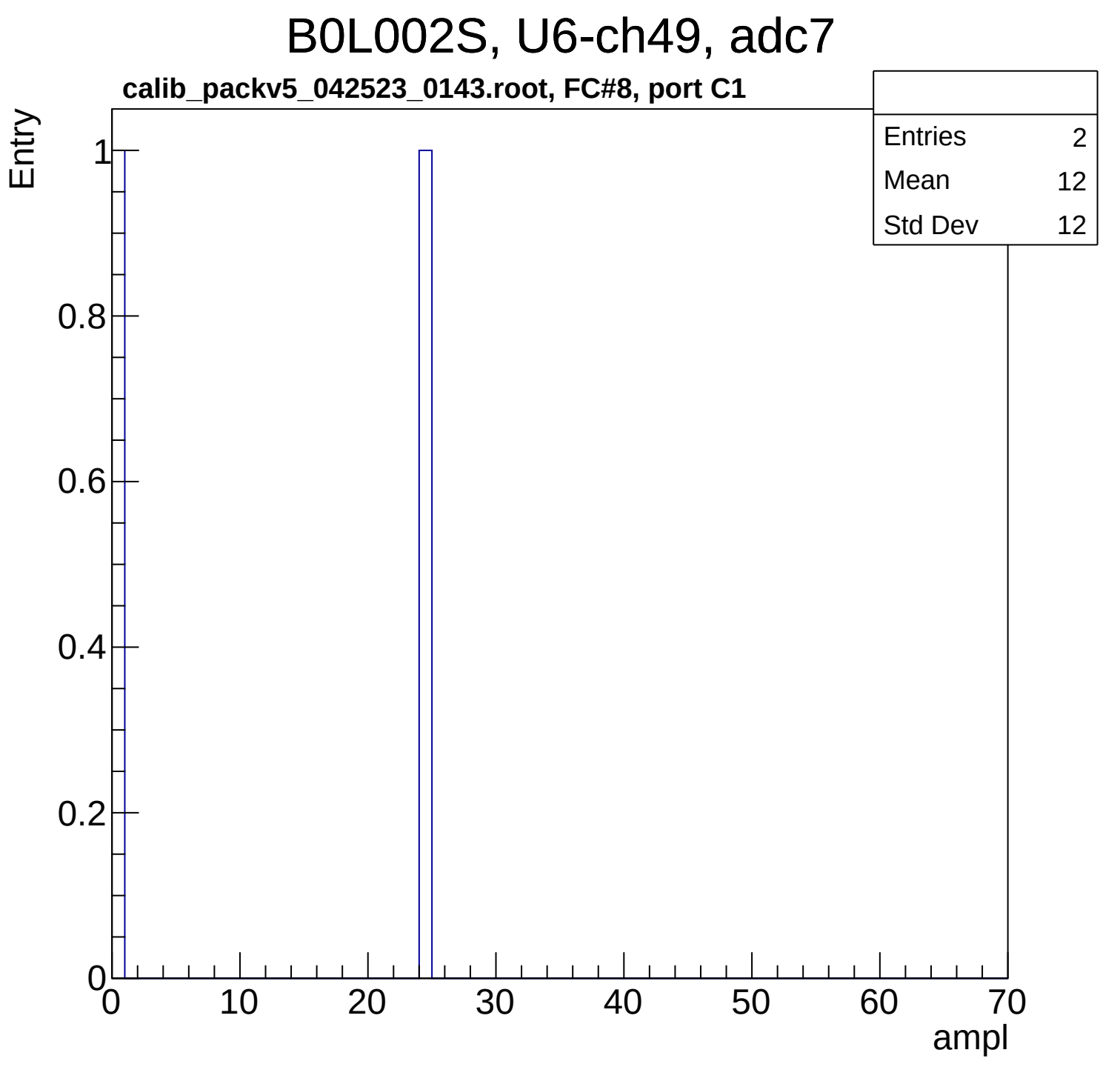
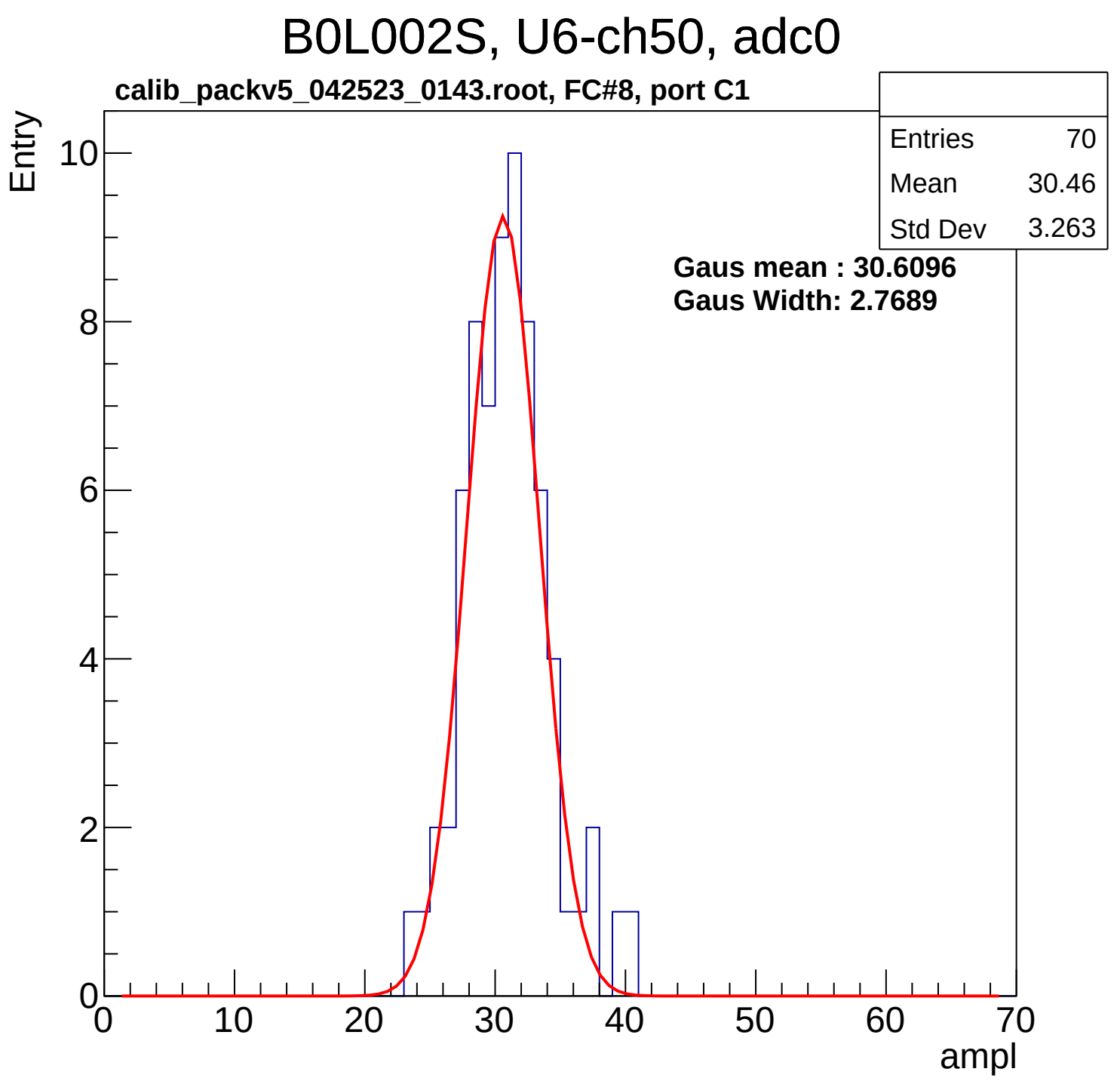
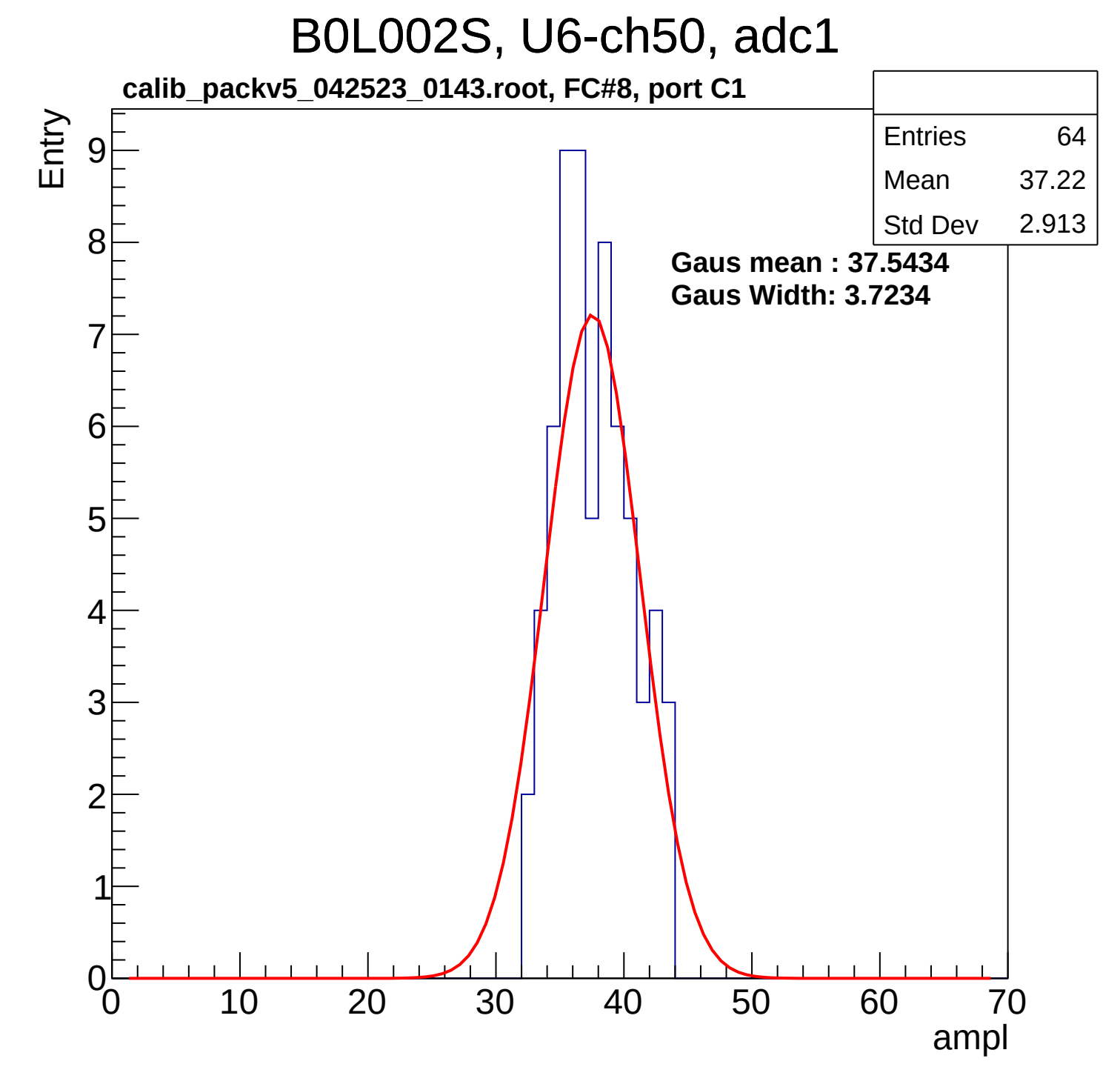
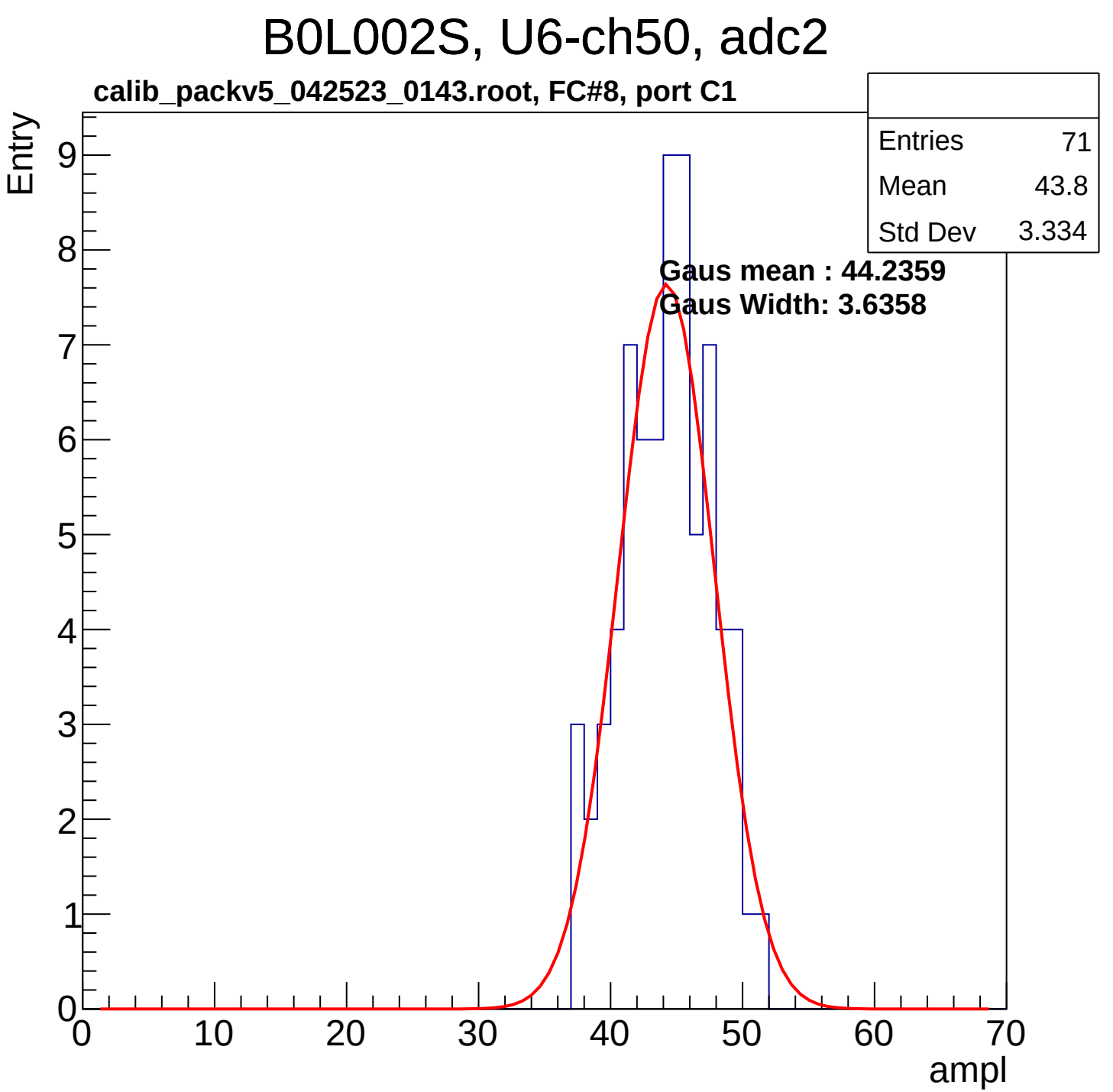

B0L002S, U6-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.14
Std Dev 10.35
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

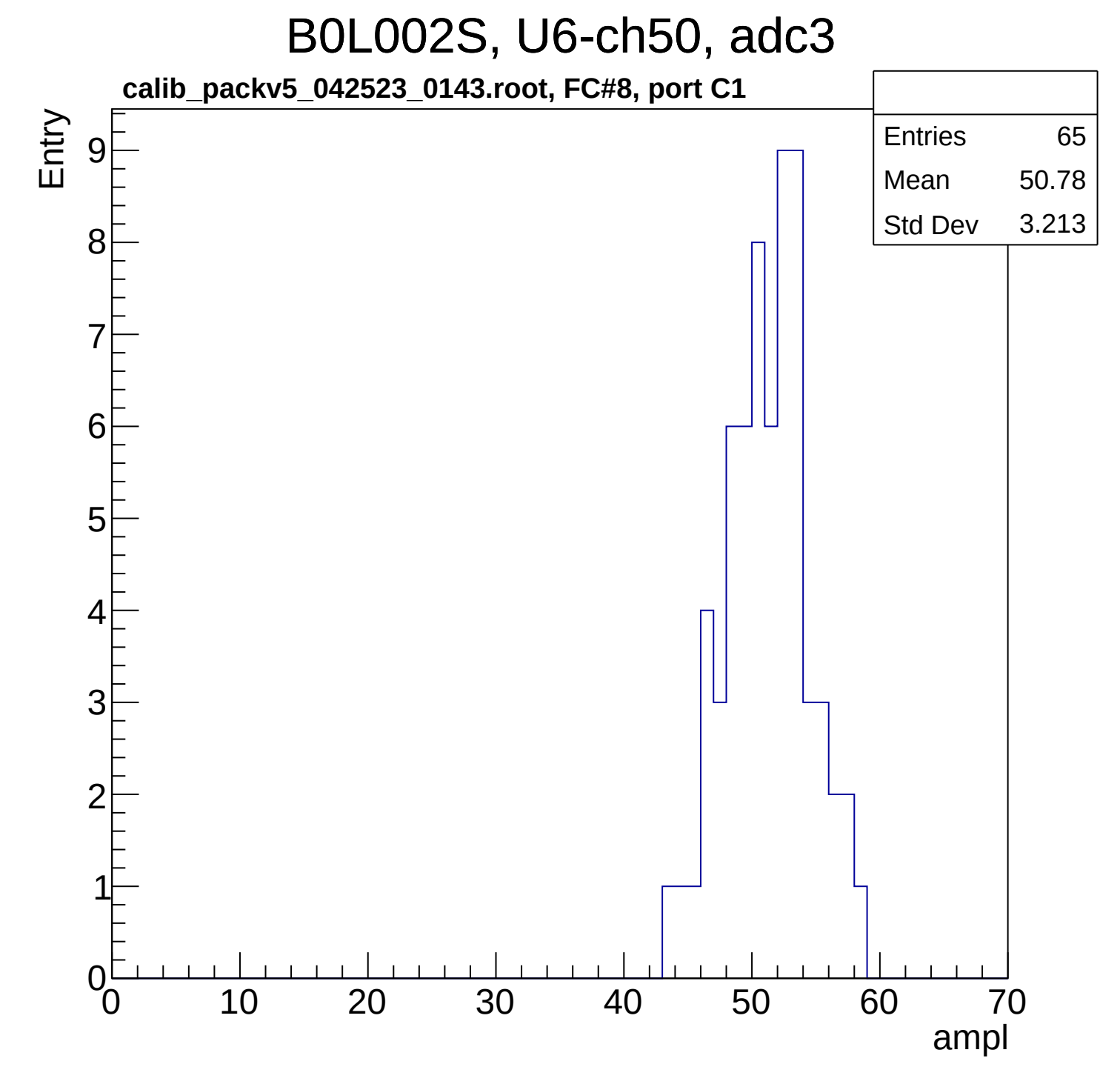

B0L002S, U6-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

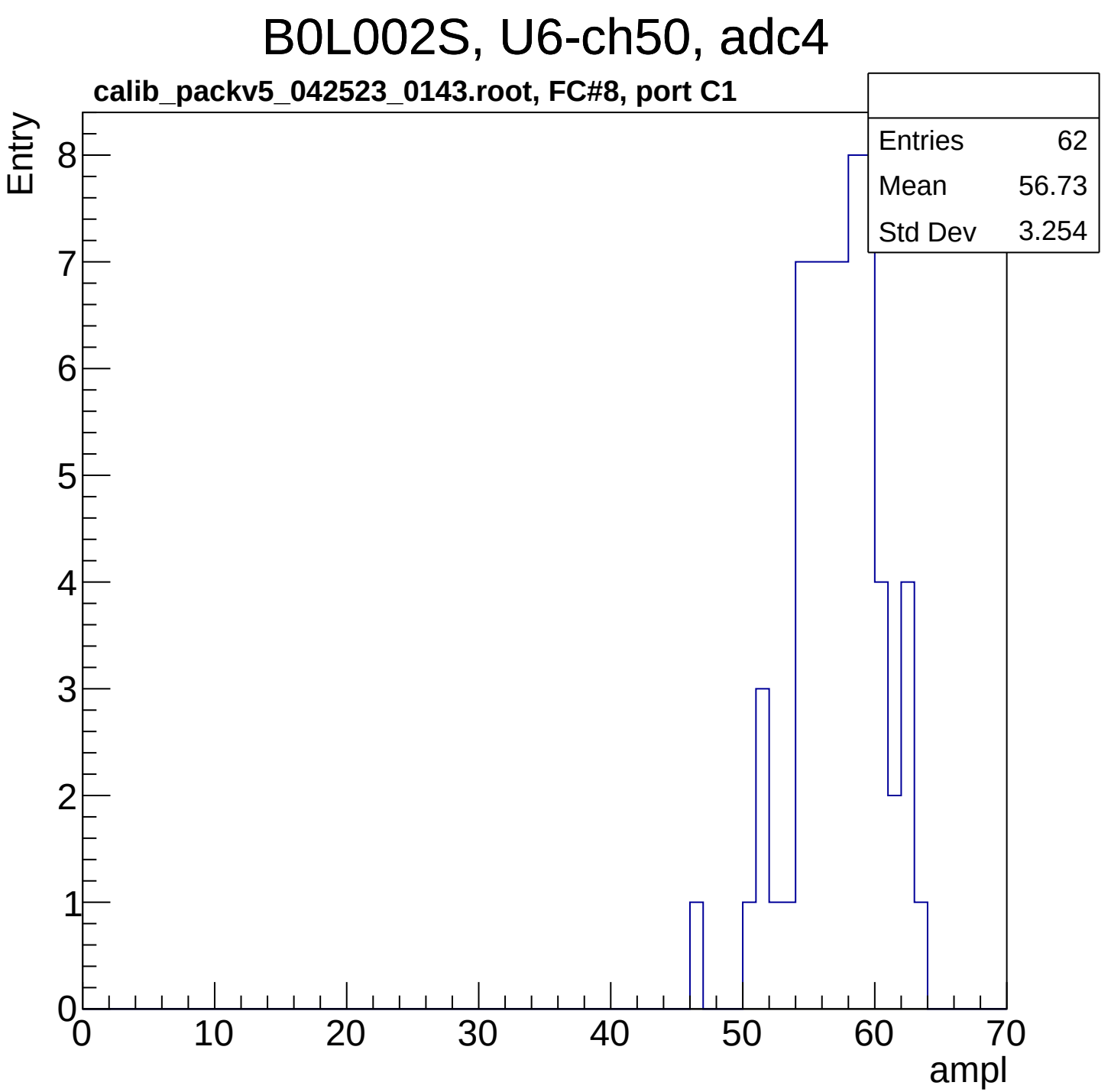

B0L002S, U6-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

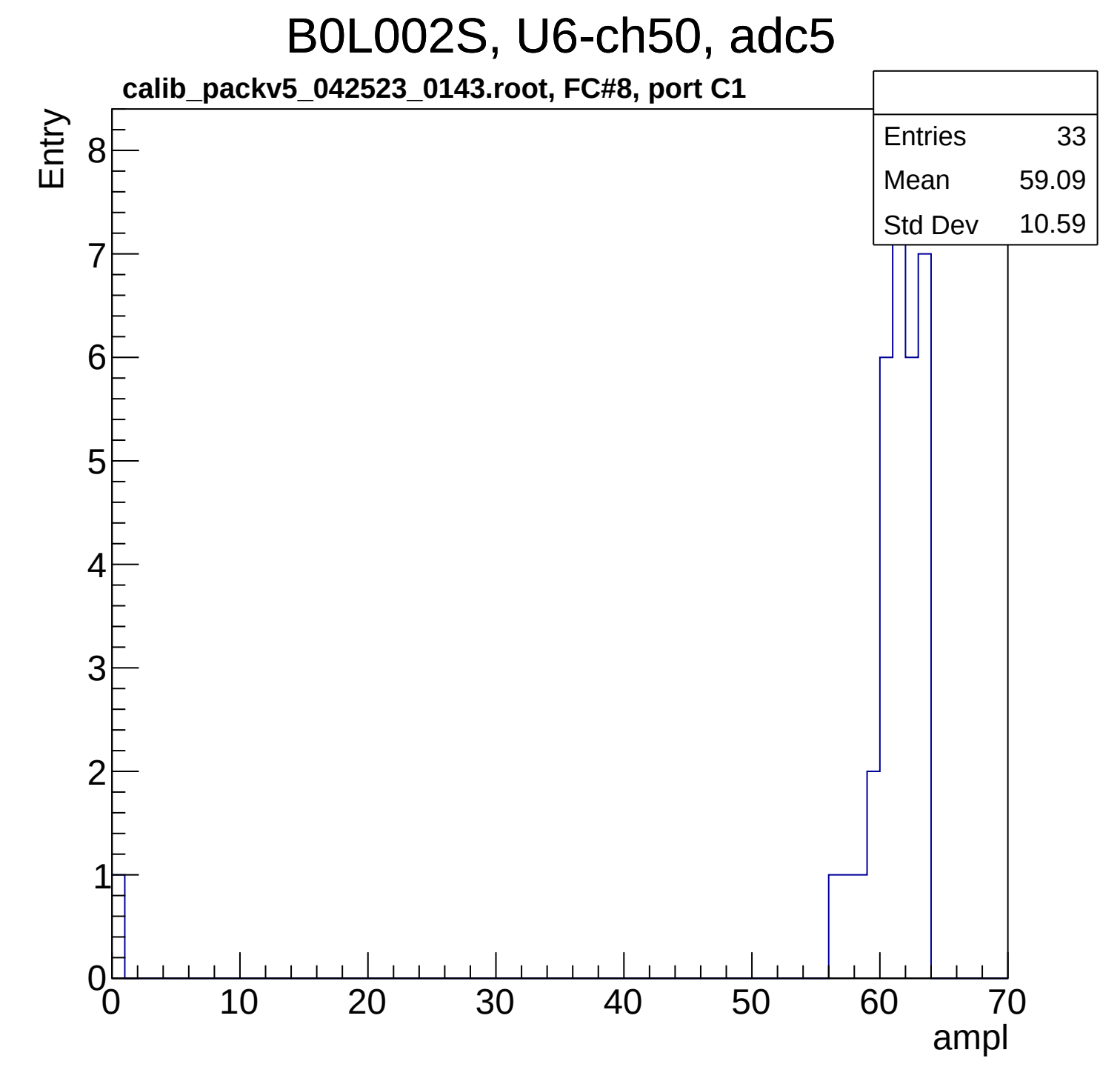

B0L002S, U6-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.46
Std Dev 3.263
Gaus mean : 30.6096
Gaus Width: 2.7689
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

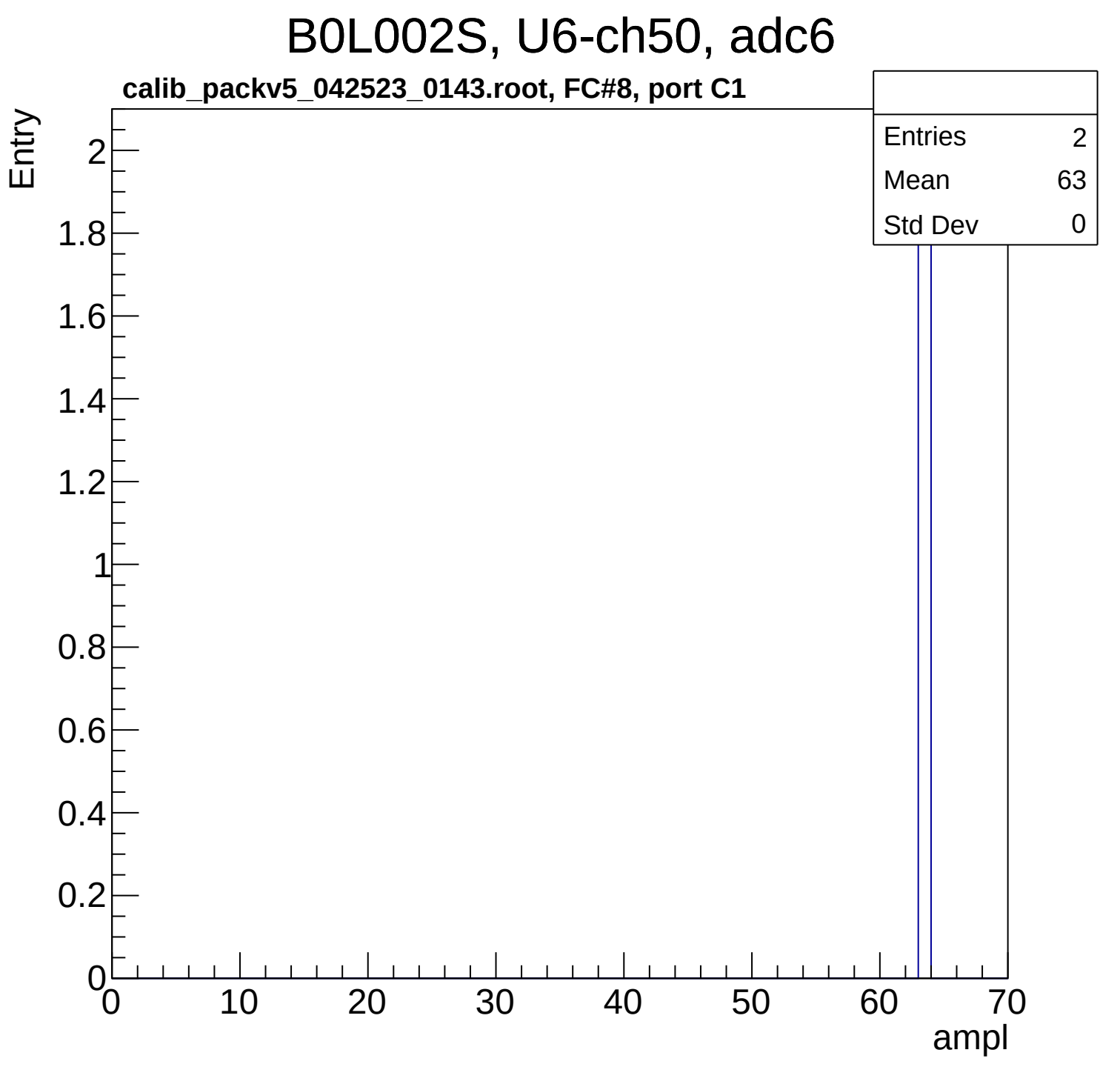

B0L002S, U6-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.22
Std Dev 2.913
Gaus mean : 37.5434
Gaus Width: 3.7234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

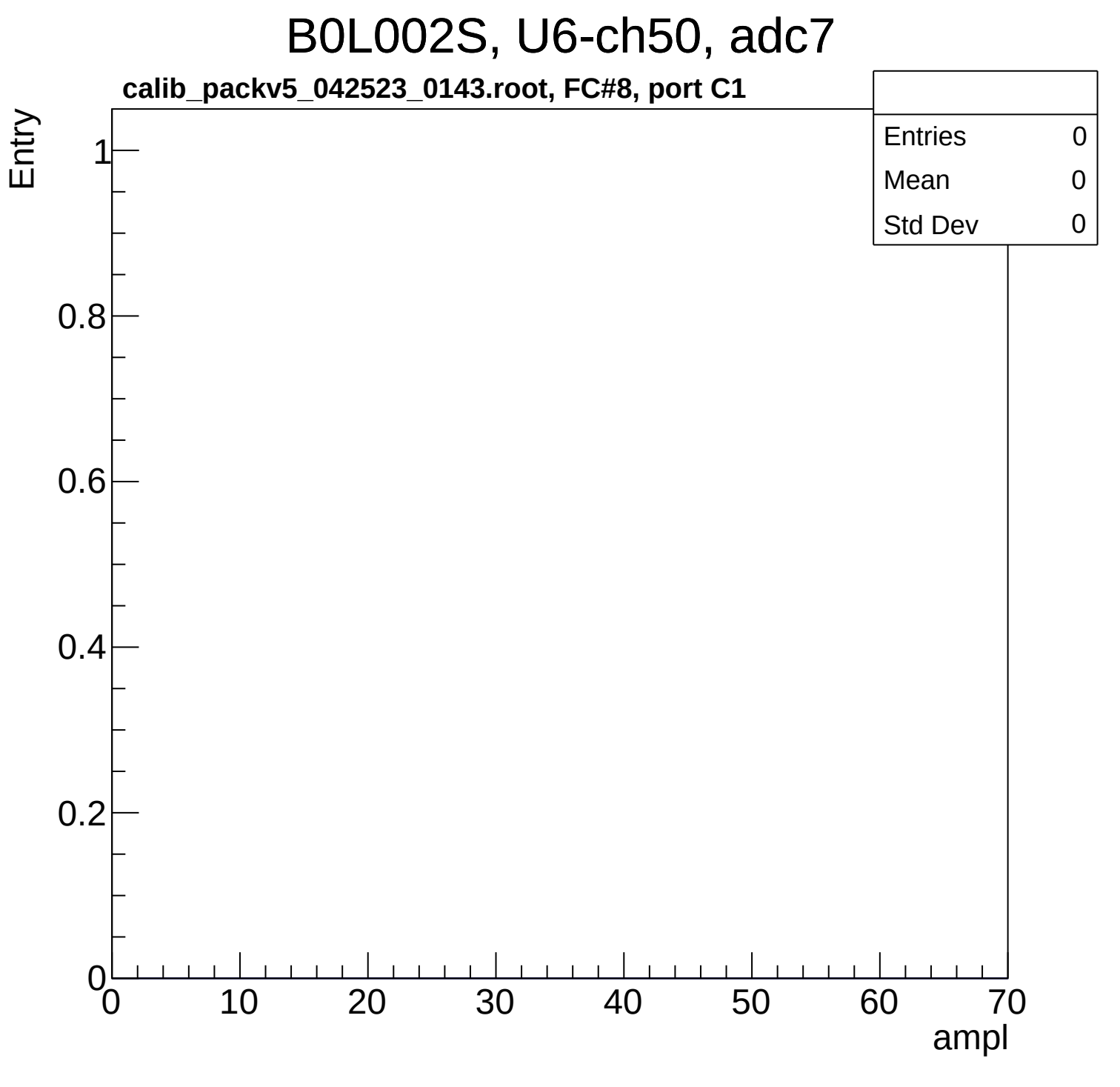

B0L002S, U6-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.8
Std Dev 3.334
Gaus mean : 44.2359
Gaus Width: 3.6358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

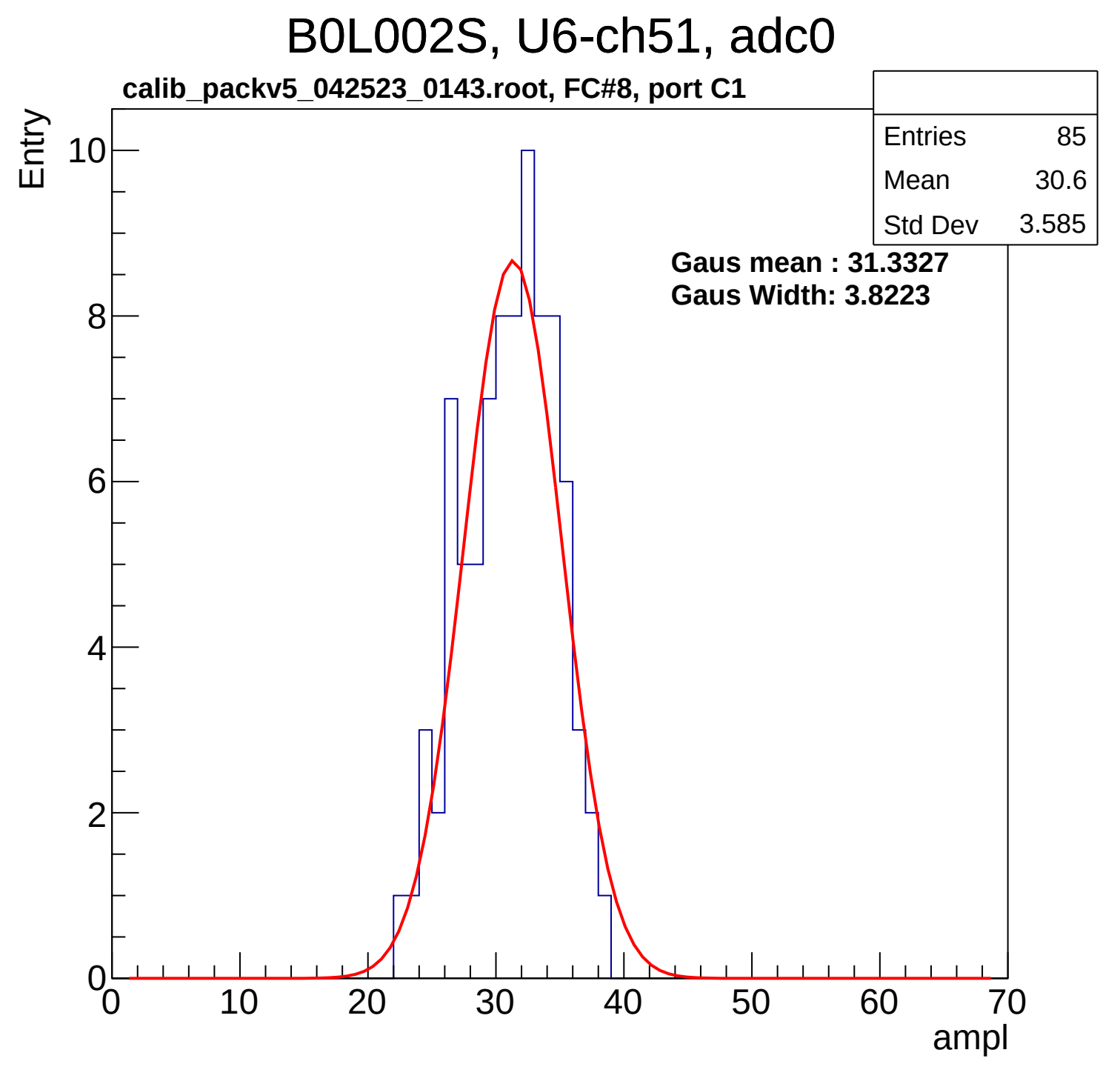

B0L002S, U6-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.78
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

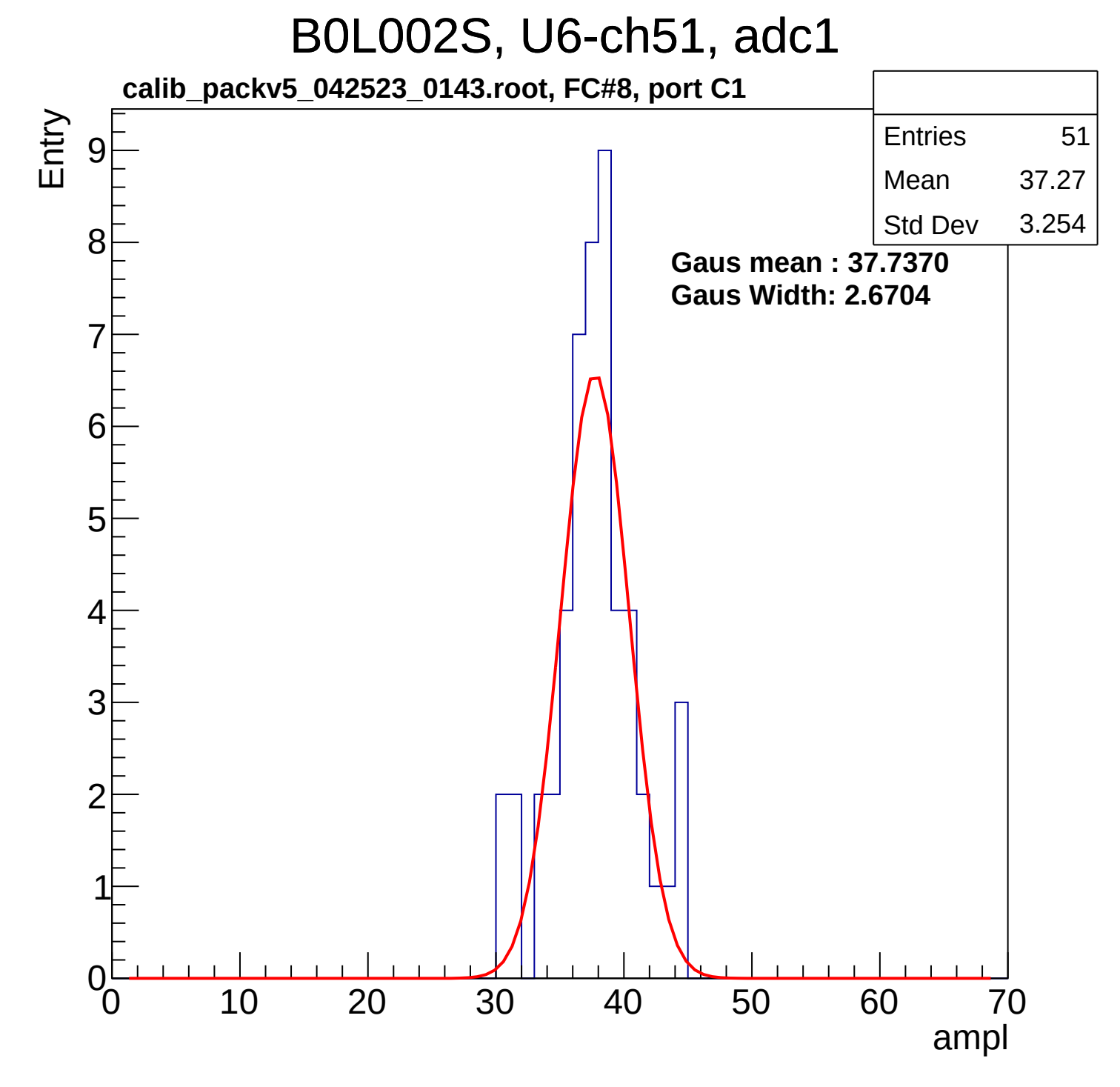

B0L002S, U6-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.73
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

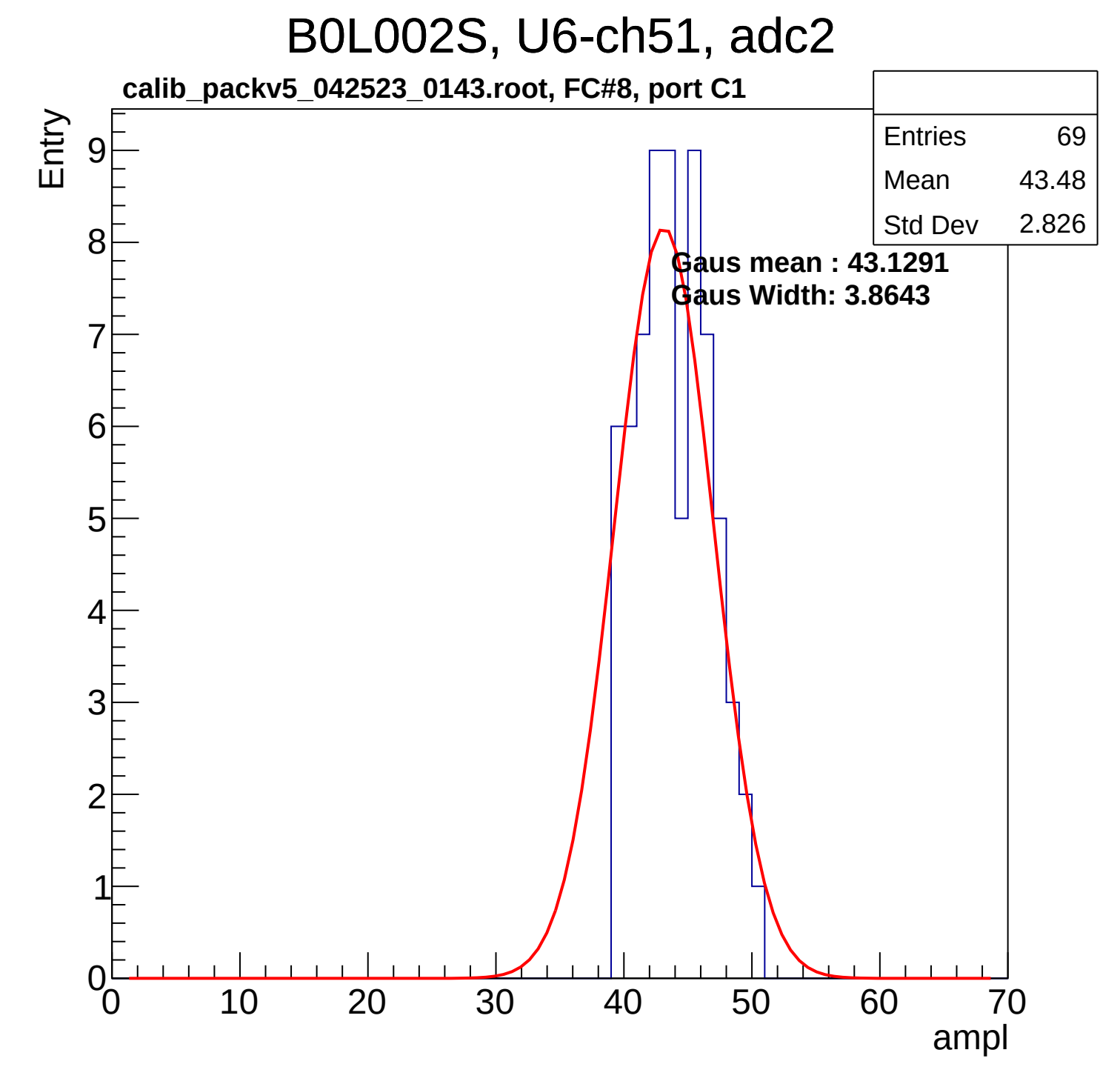

B0L002S, U6-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.09
Std Dev 10.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

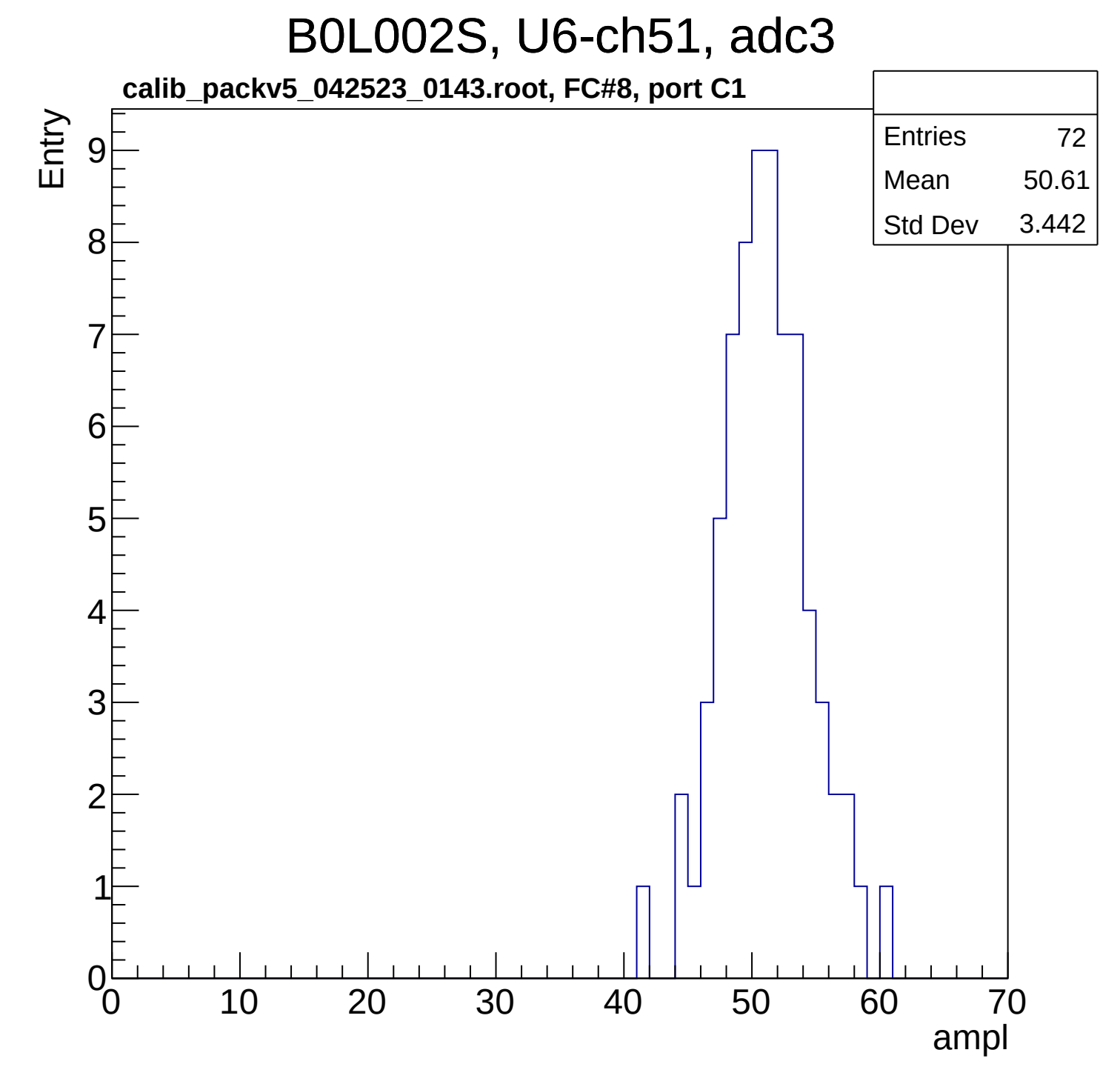

B0L002S, U6-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

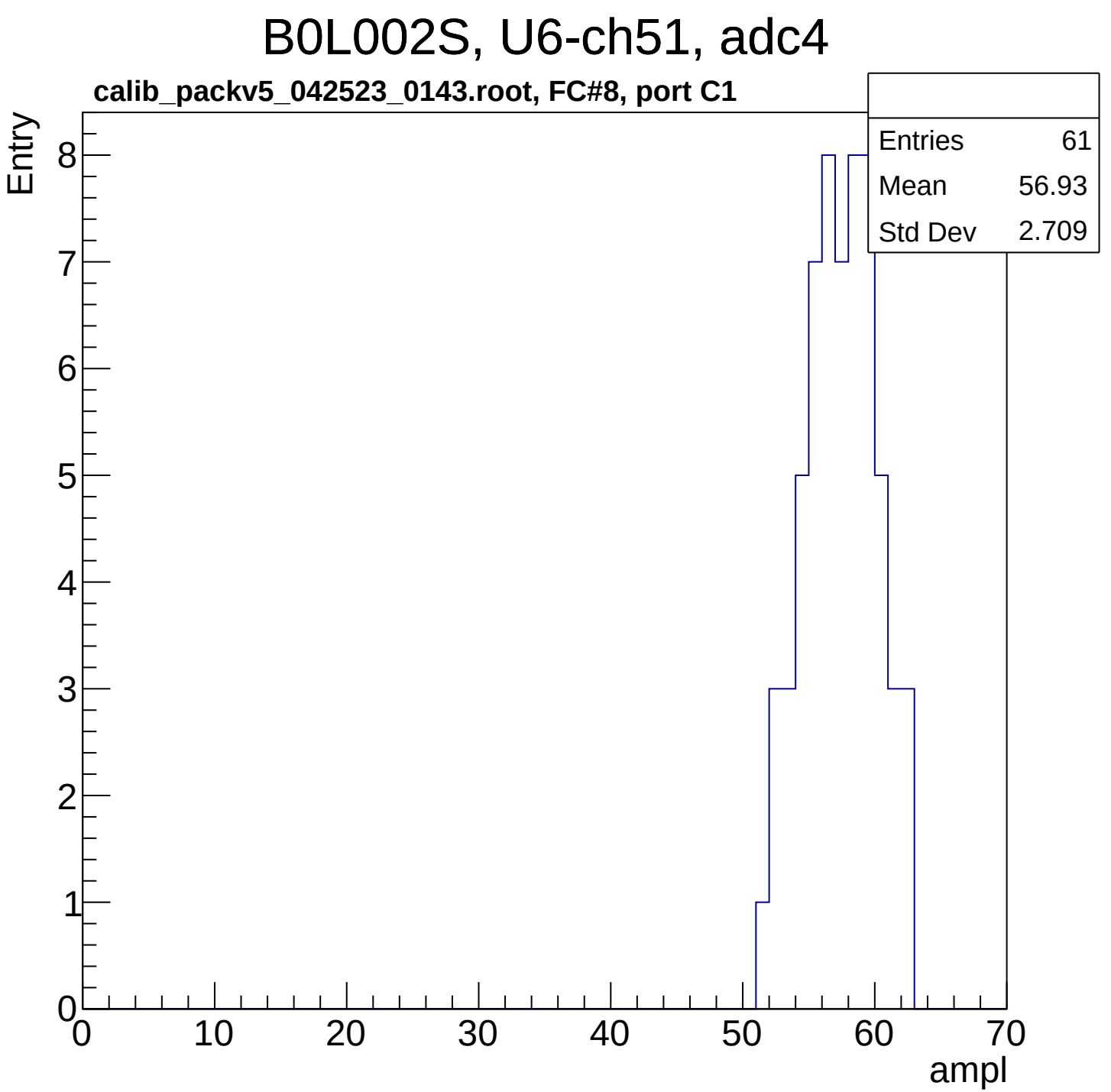

B0L002S, U6-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

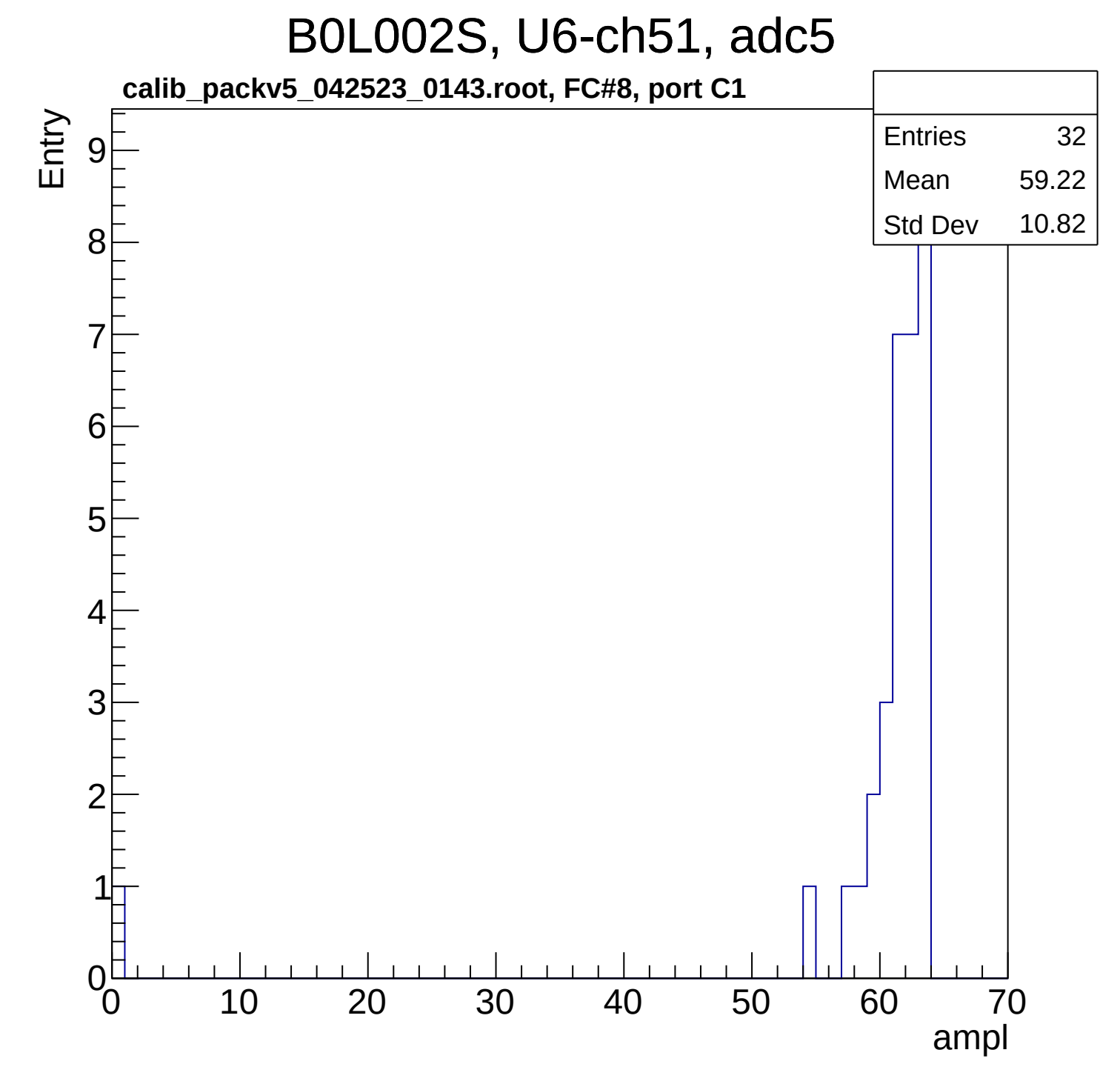

B0L002S, U6-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 30.6
Std Dev 3.585
Gaus mean : 31.3327
Gaus Width: 3.8223
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

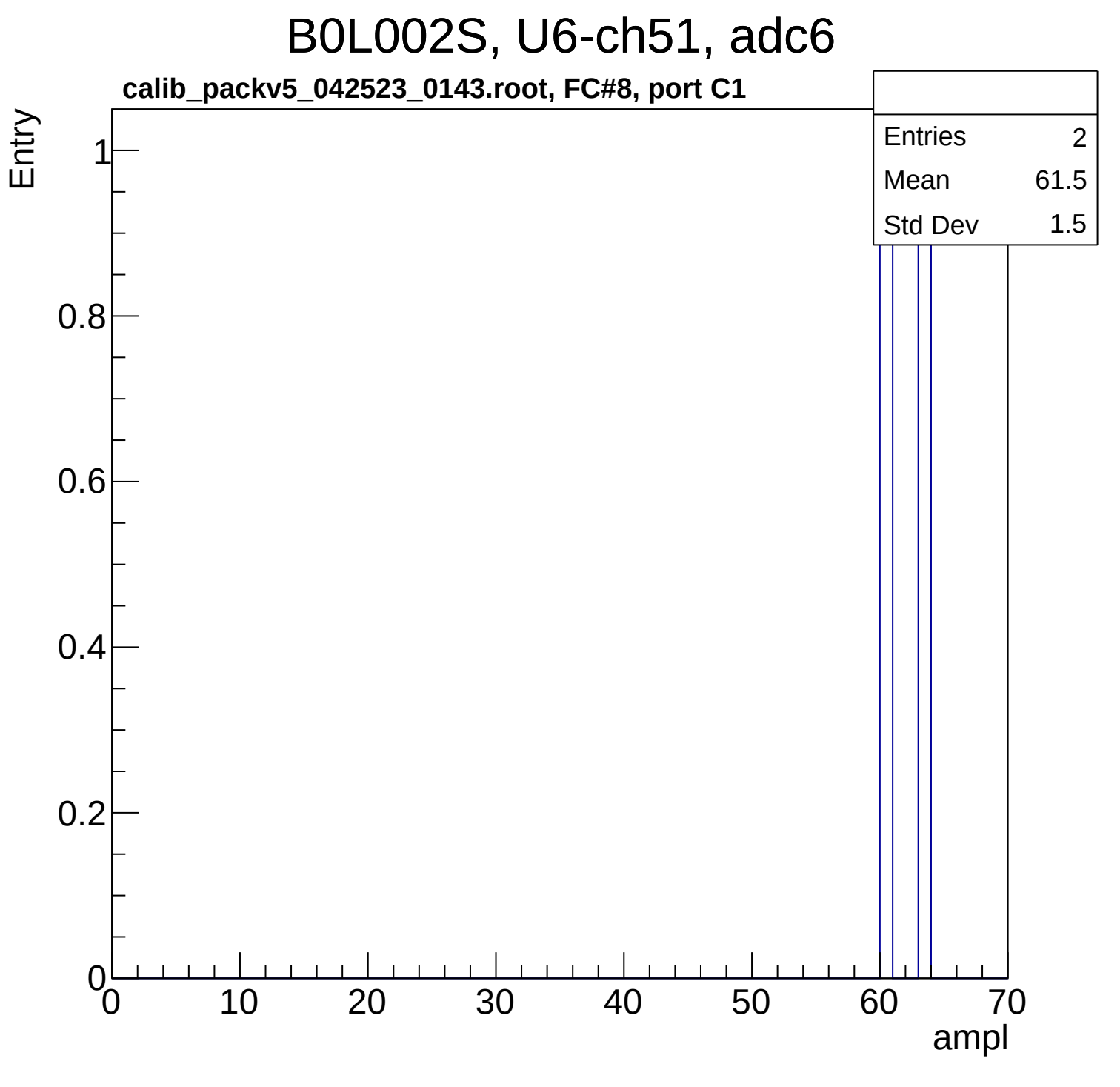

B0L002S, U6-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 37.27
Std Dev 3.254
Gaus mean : 37.7370
Gaus Width: 2.6704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

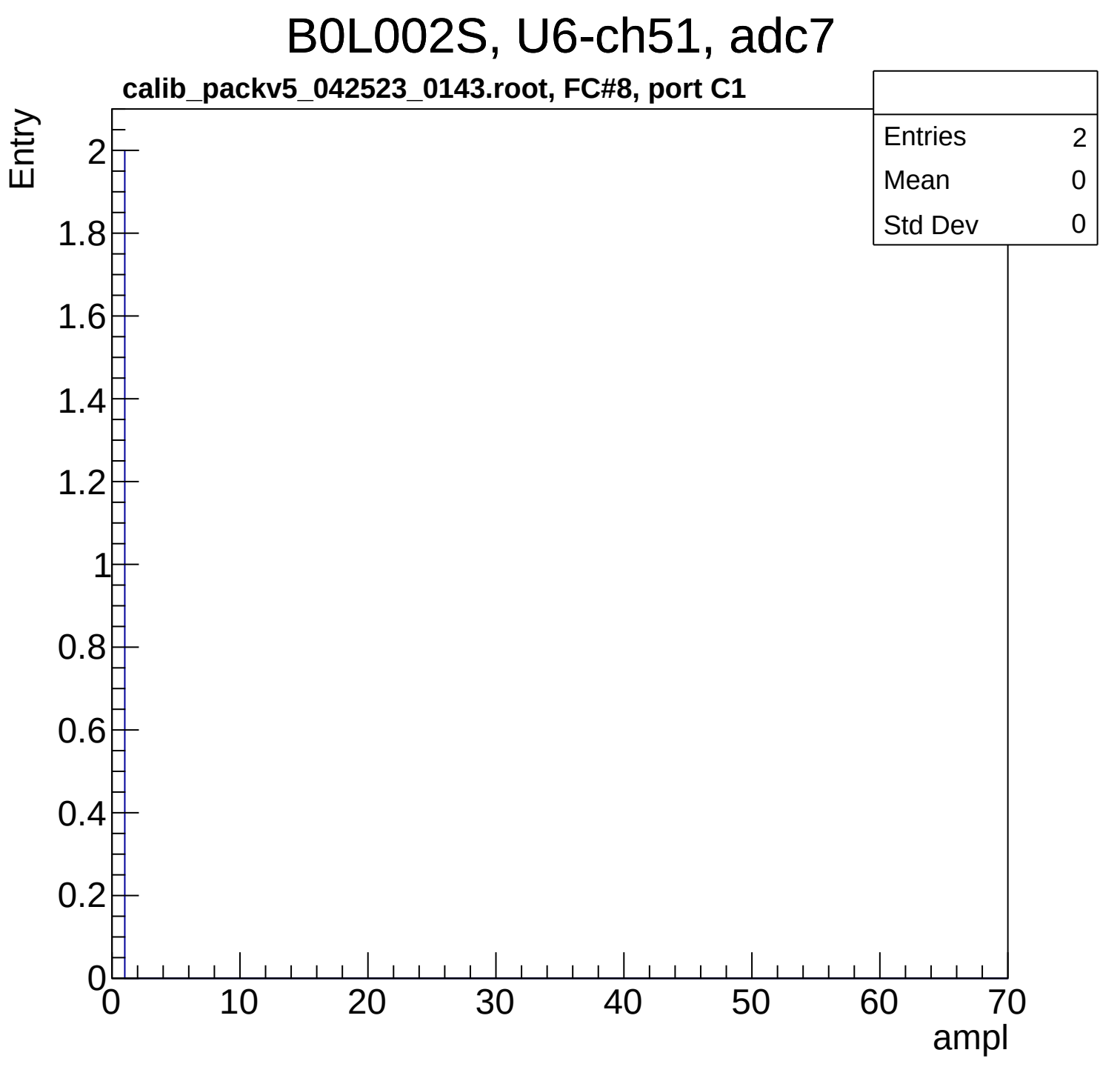

B0L002S, U6-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.48
Std Dev 2.826
Gaus mean : 43.1291
Gaus Width: 3.8643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

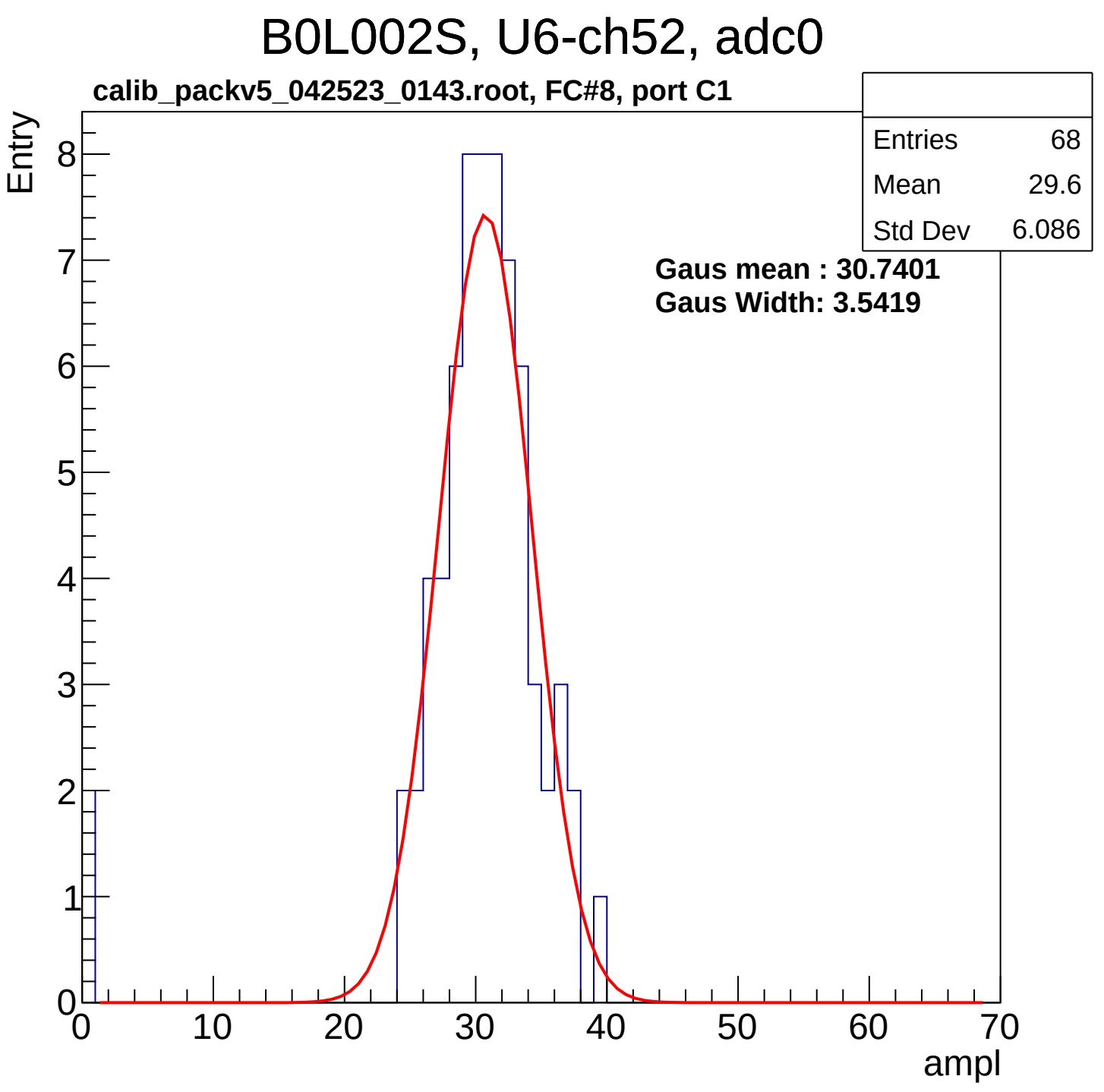

B0L002S, U6-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.61
Std Dev 3.442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

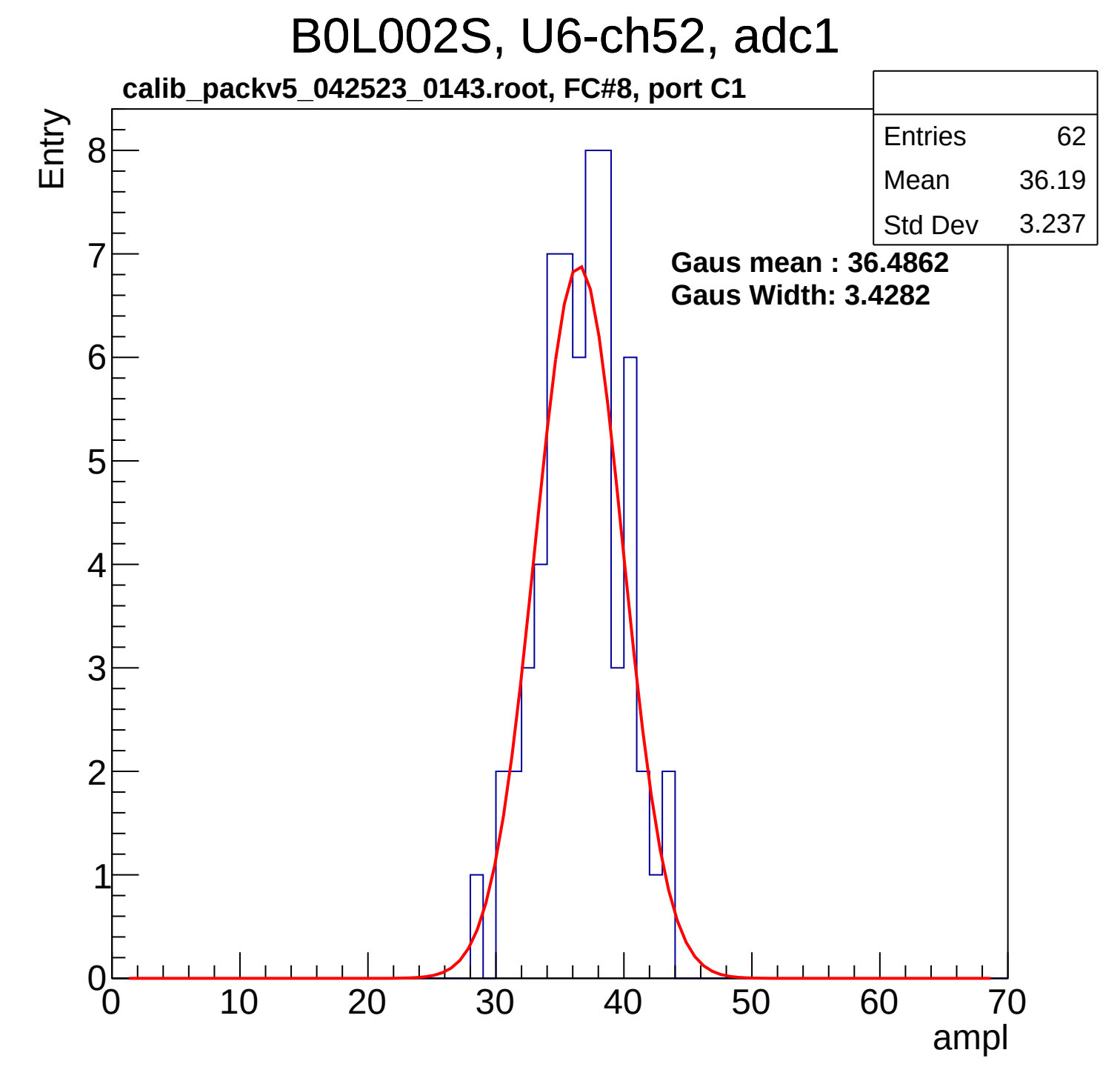

B0L002S, U6-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.93
Std Dev 2.709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

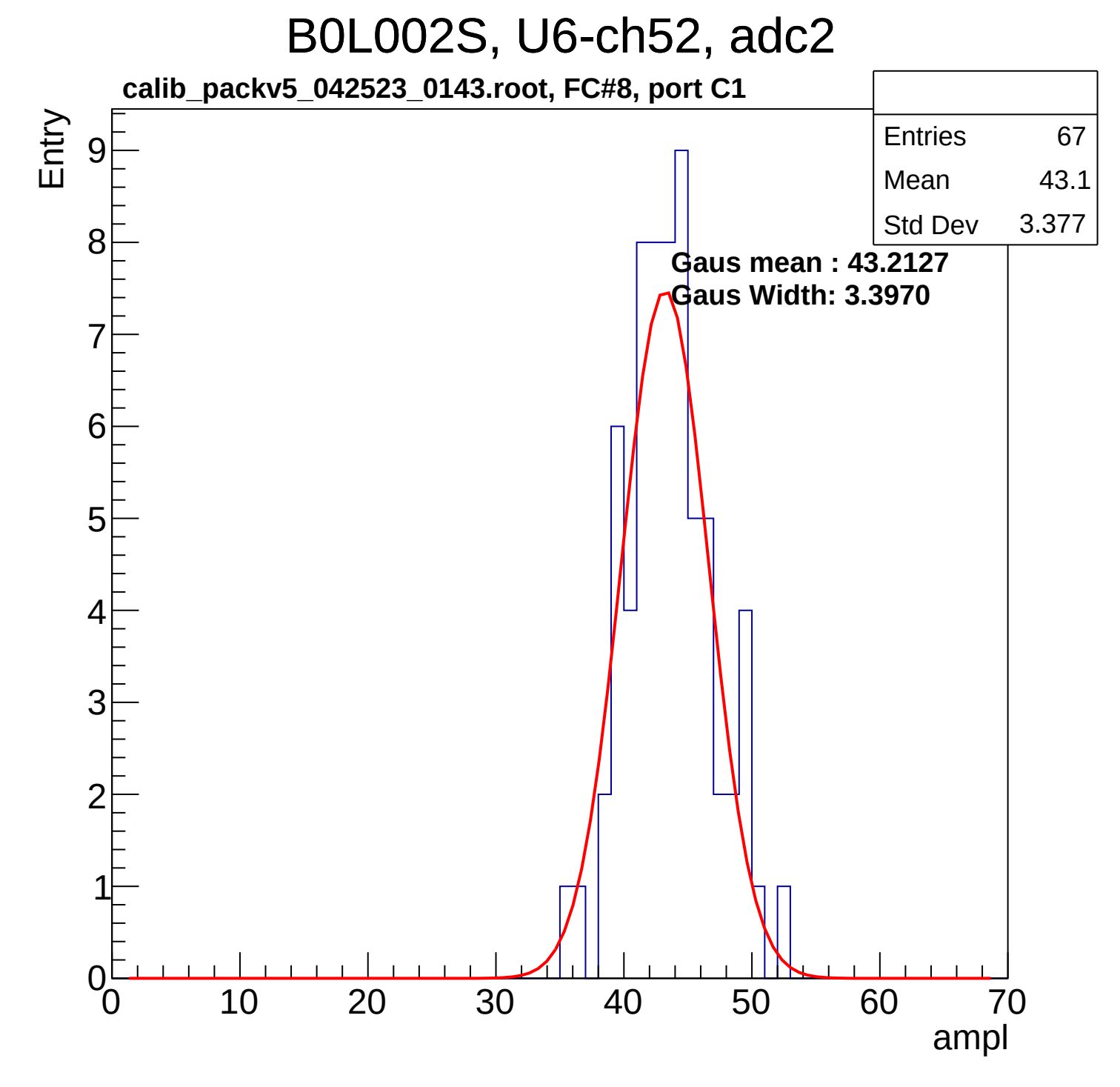

B0L002S, U6-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.22
Std Dev 10.82
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

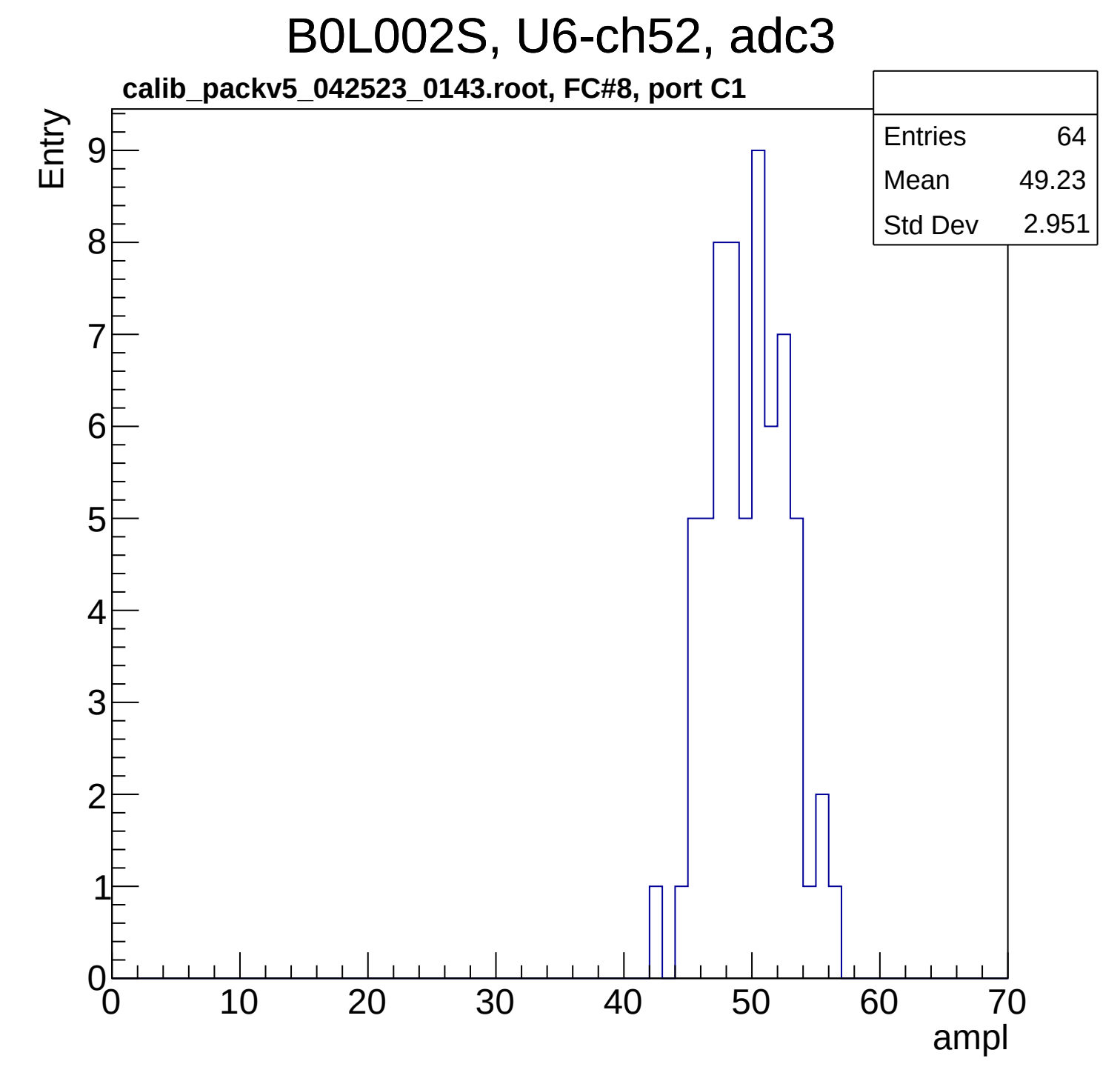

B0L002S, U6-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

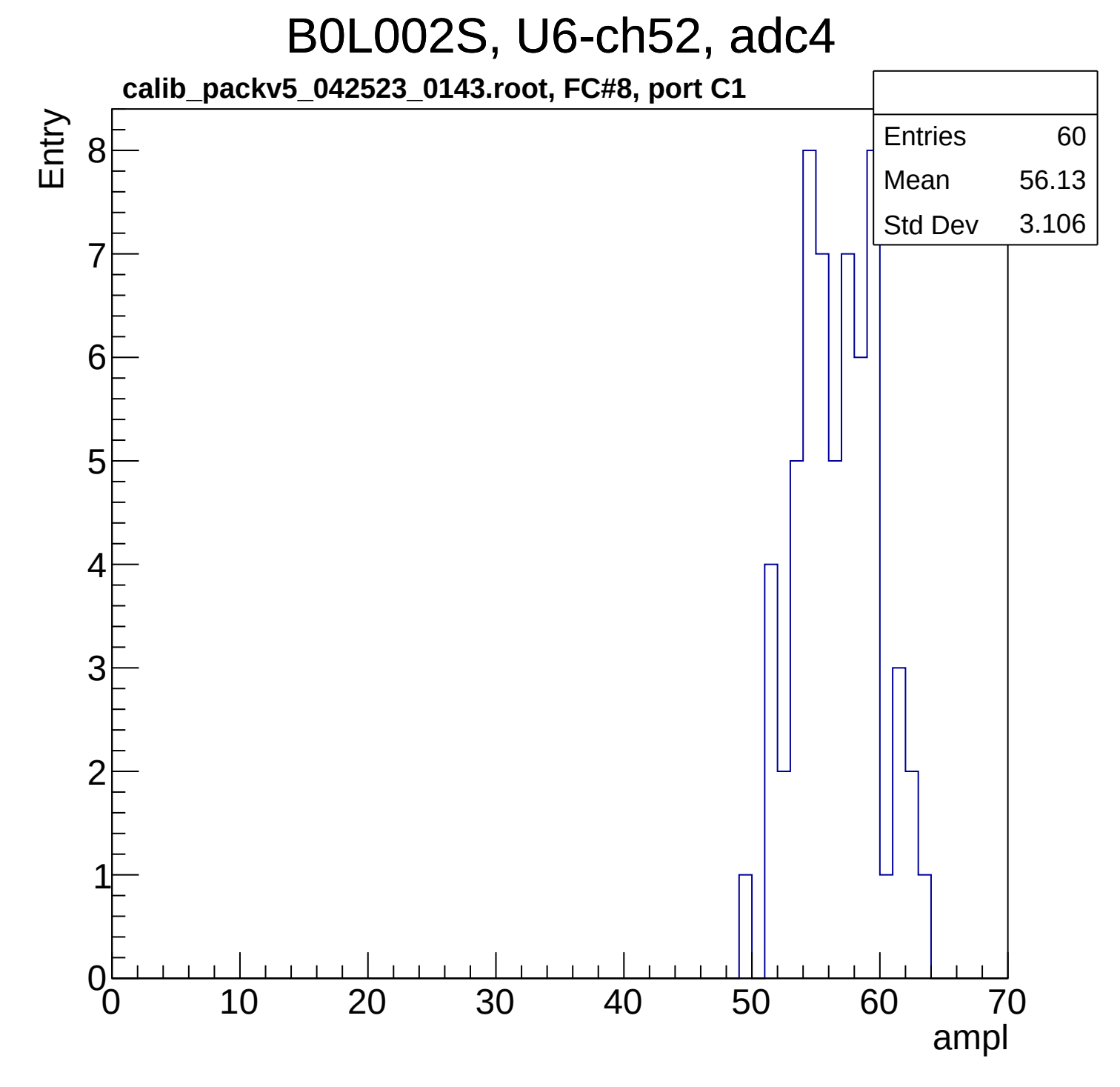

B0L002S, U6-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.6
Std Dev 6.086
Gaus mean : 30.7401
Gaus Width: 3.5419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 36.19
Std Dev 3.237
Gaus mean : 36.4862
Gaus Width: 3.4282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.1
Std Dev 3.377
Gaus mean : 43.2127
Gaus Width: 3.3970
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.23
Std Dev 2.951
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.13
Std Dev 3.106
Entry
ampl
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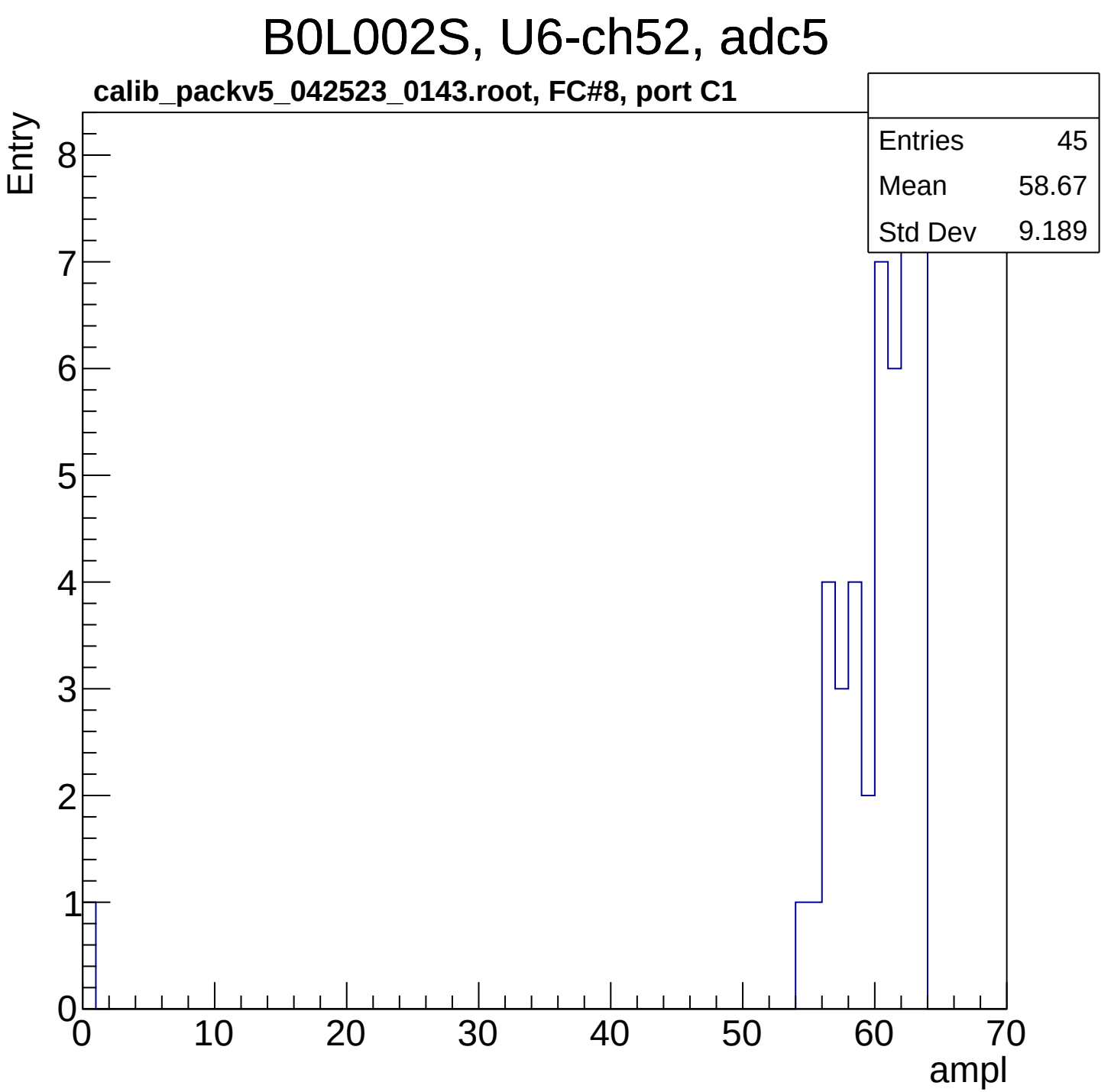
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.67
Std Dev 9.189
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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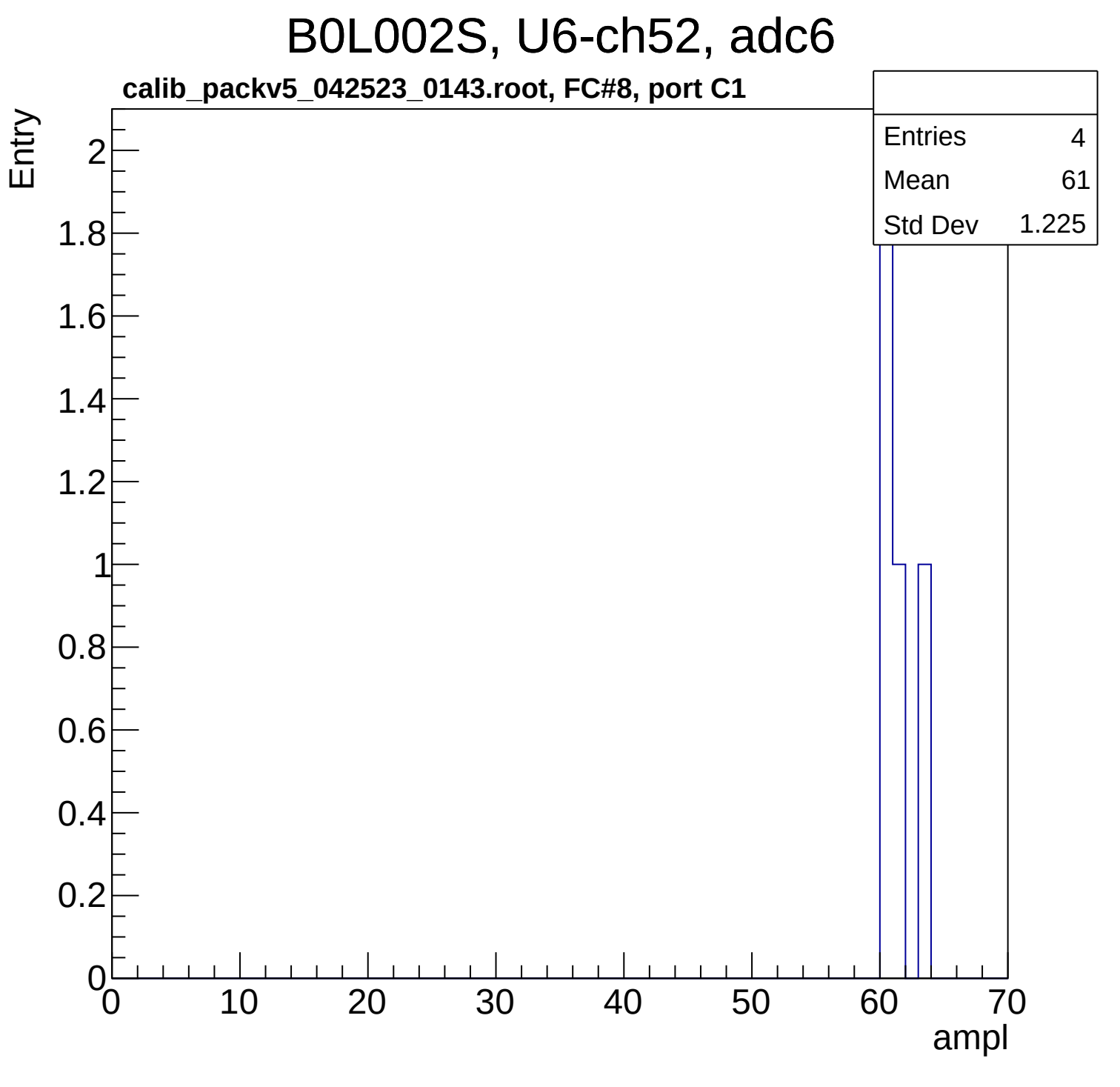
60
70


B0L002S, U6-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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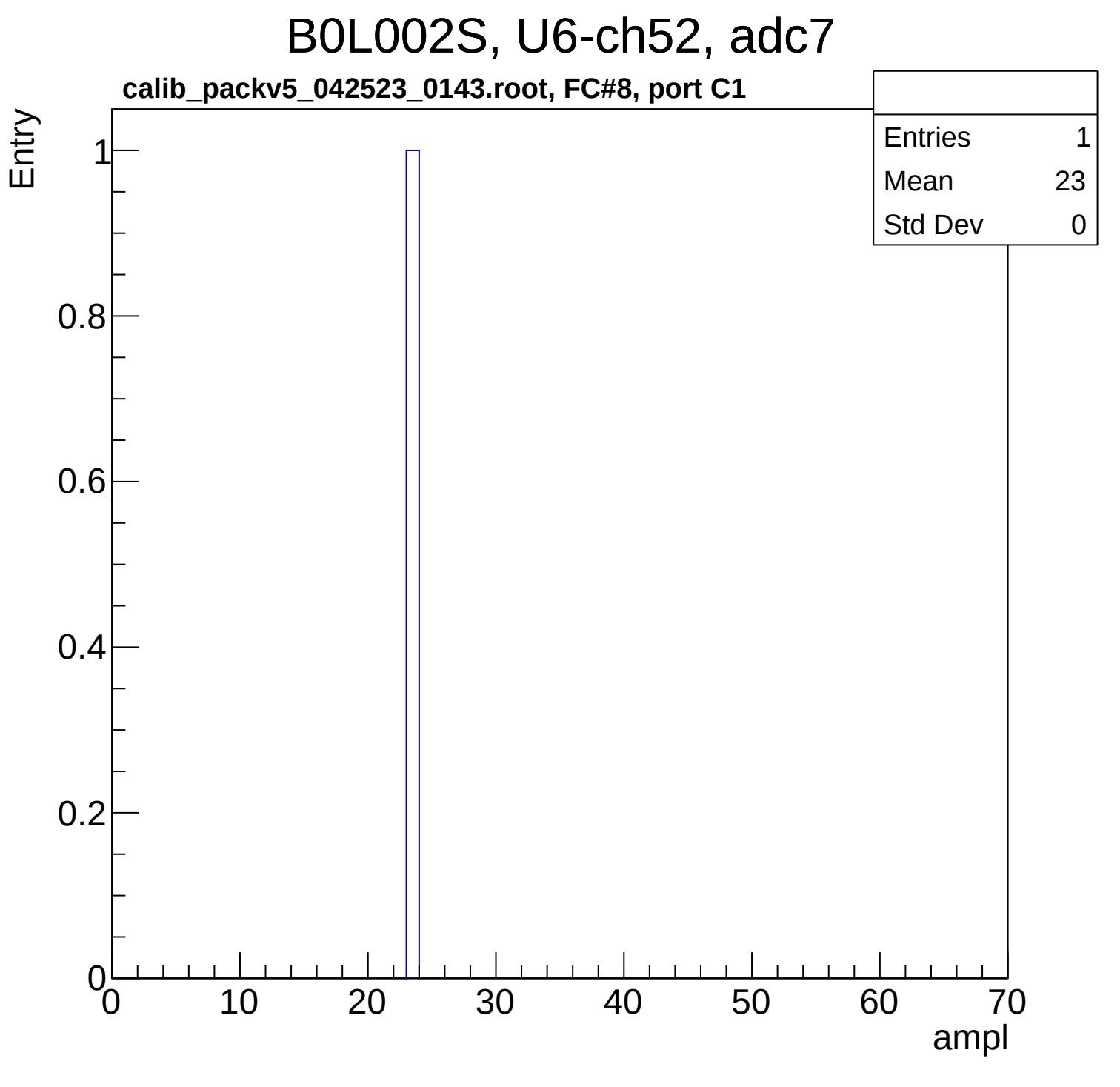
20
30
40
50
60
70


B0L002S, U6-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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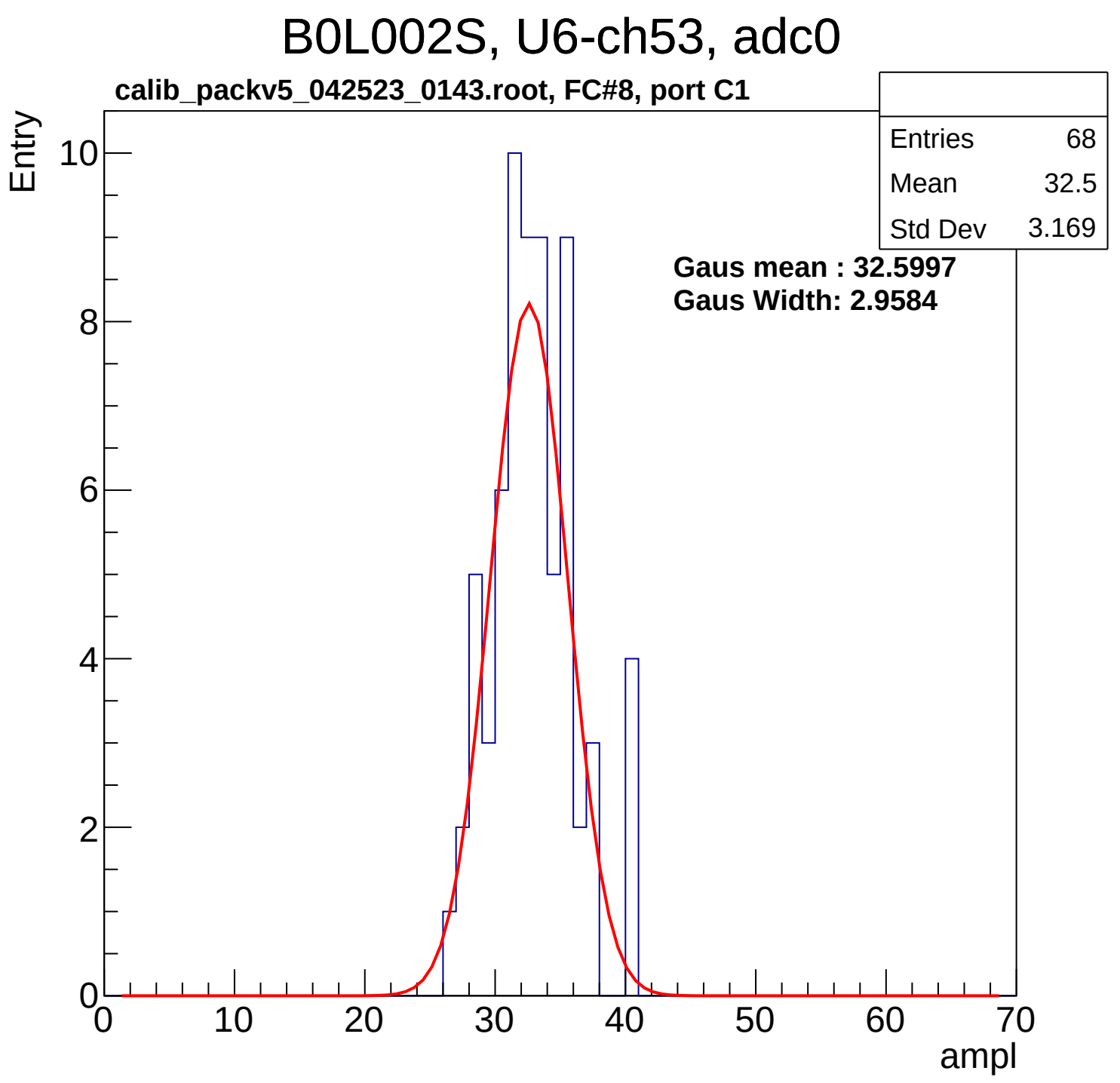
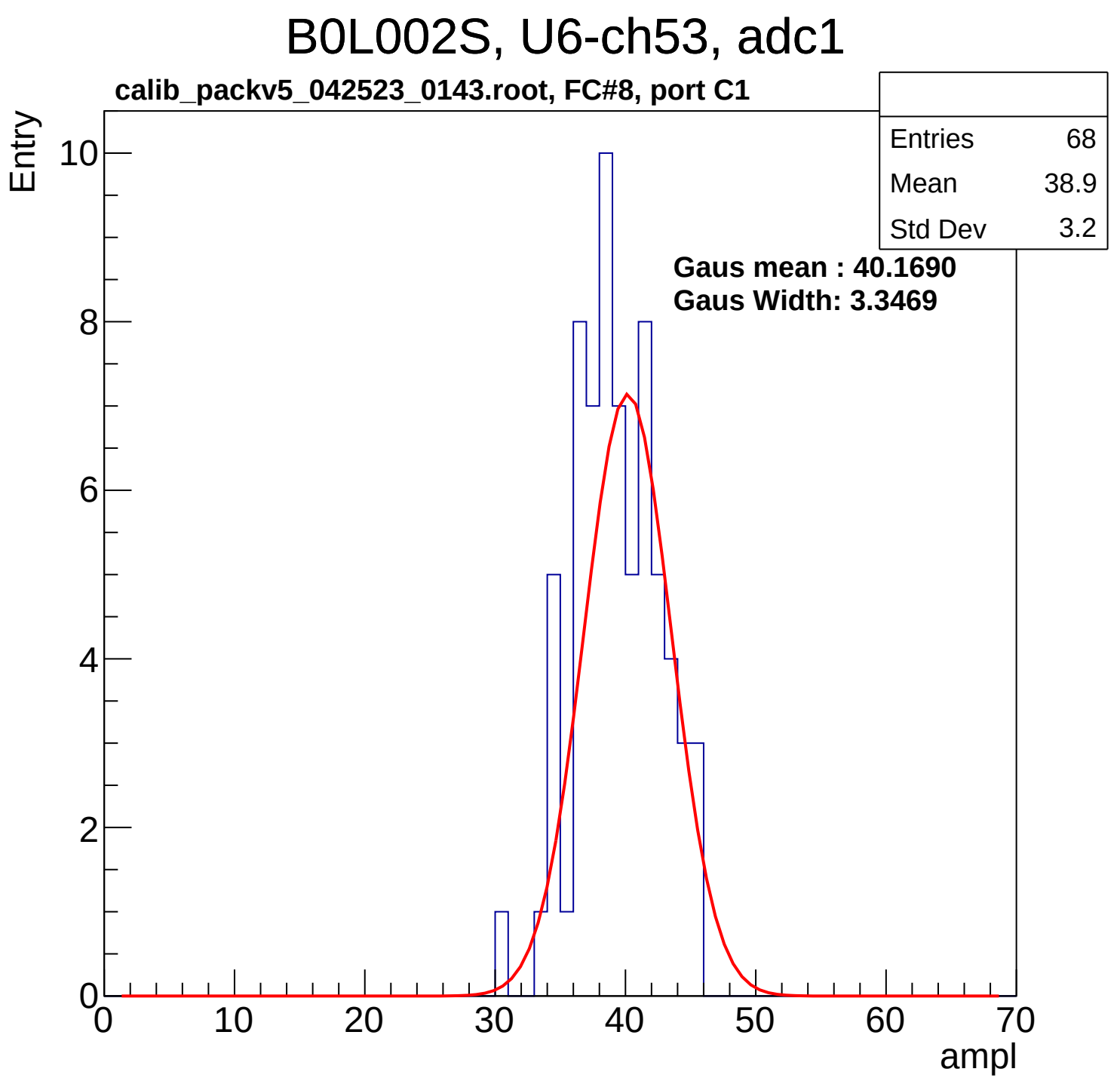
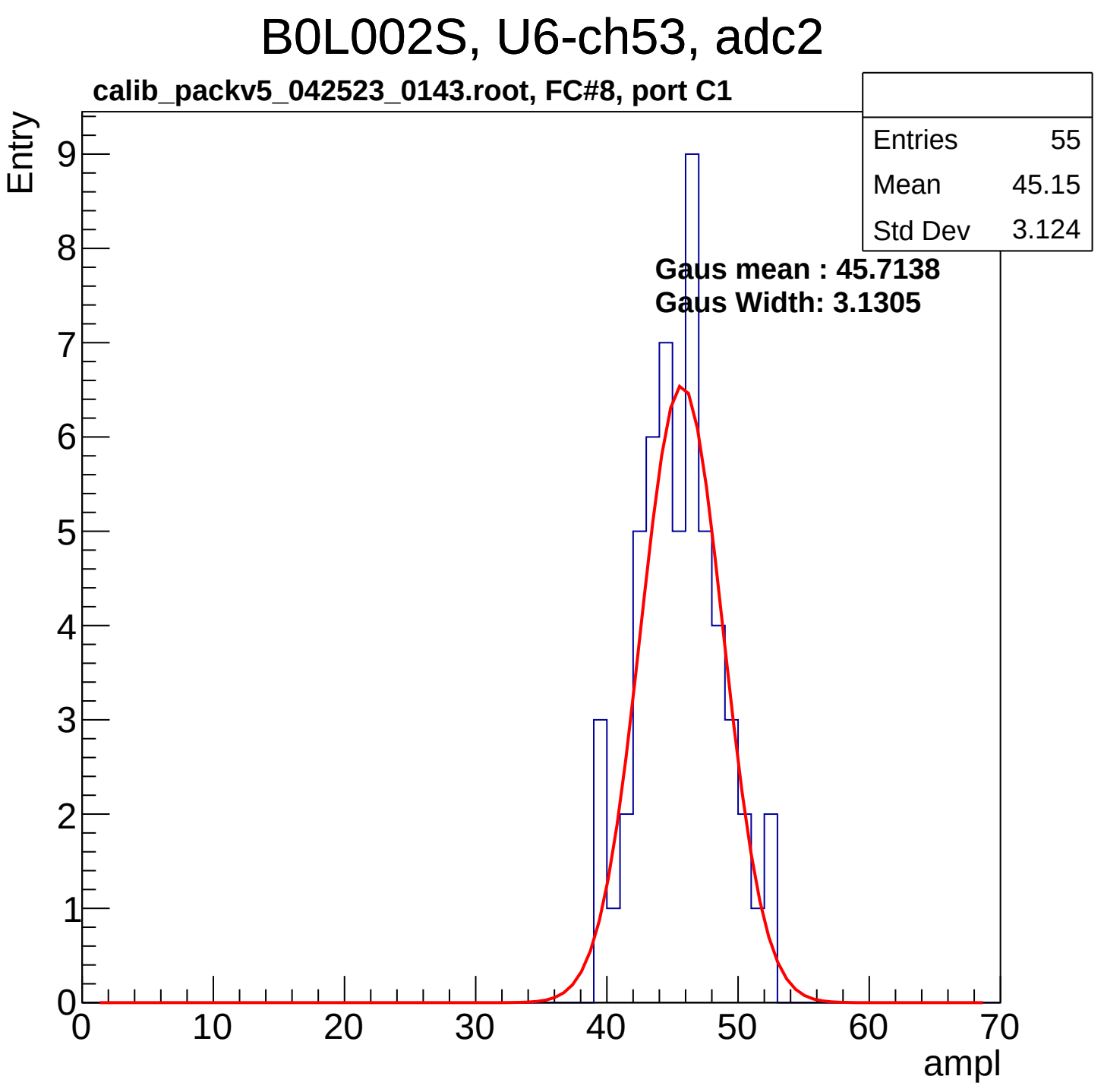
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 32.5
Std Dev 3.169
Gaus mean : 32.5997
Gaus Width: 2.9584
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.9
Std Dev 3.2
Gaus mean : 40.1690
Gaus Width: 3.3469
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.15
Std Dev 3.124
Gaus mean : 45.7138
Gaus Width: 3.1305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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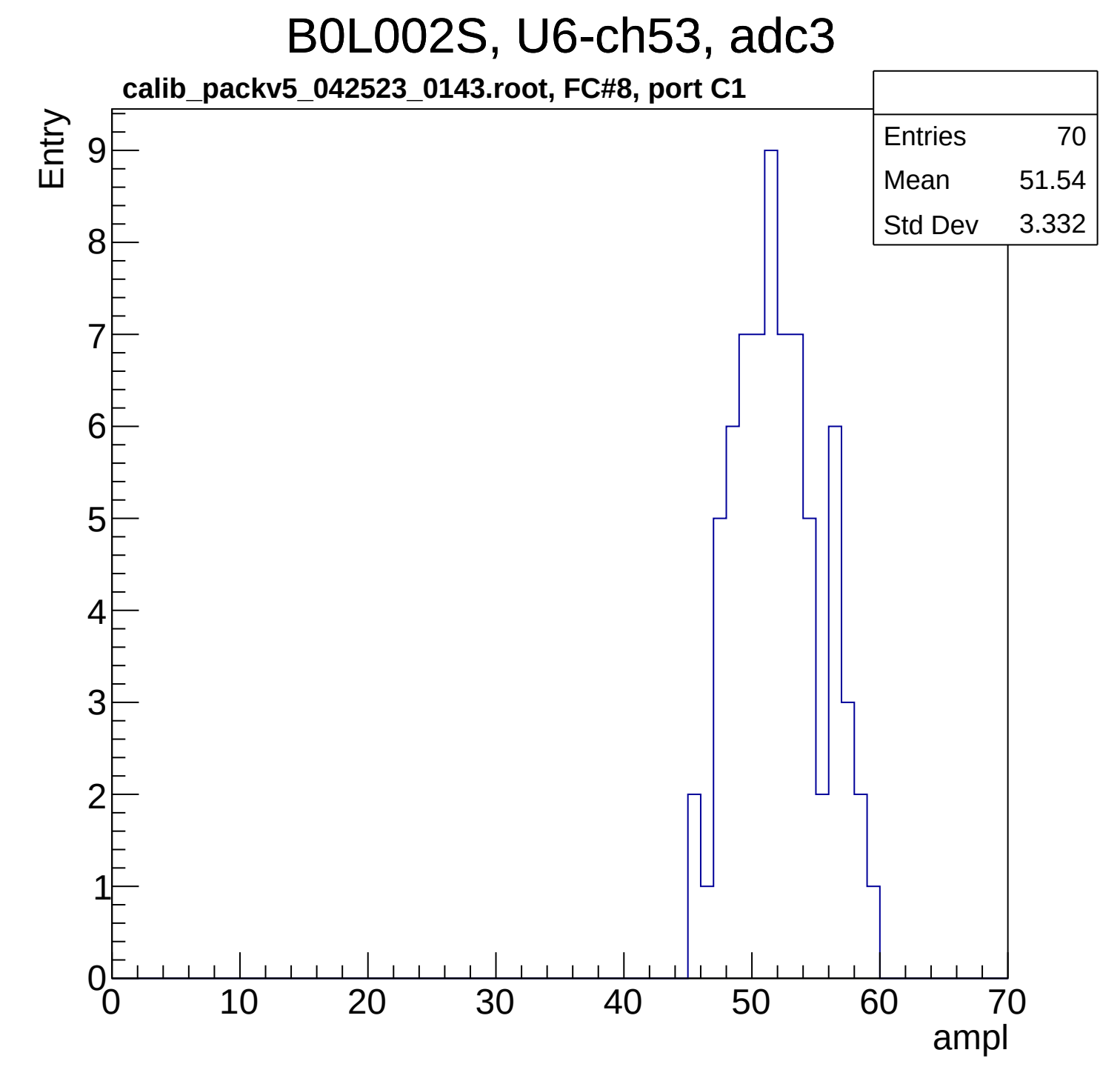
B0L002S, U6-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.54
Std Dev 3.332
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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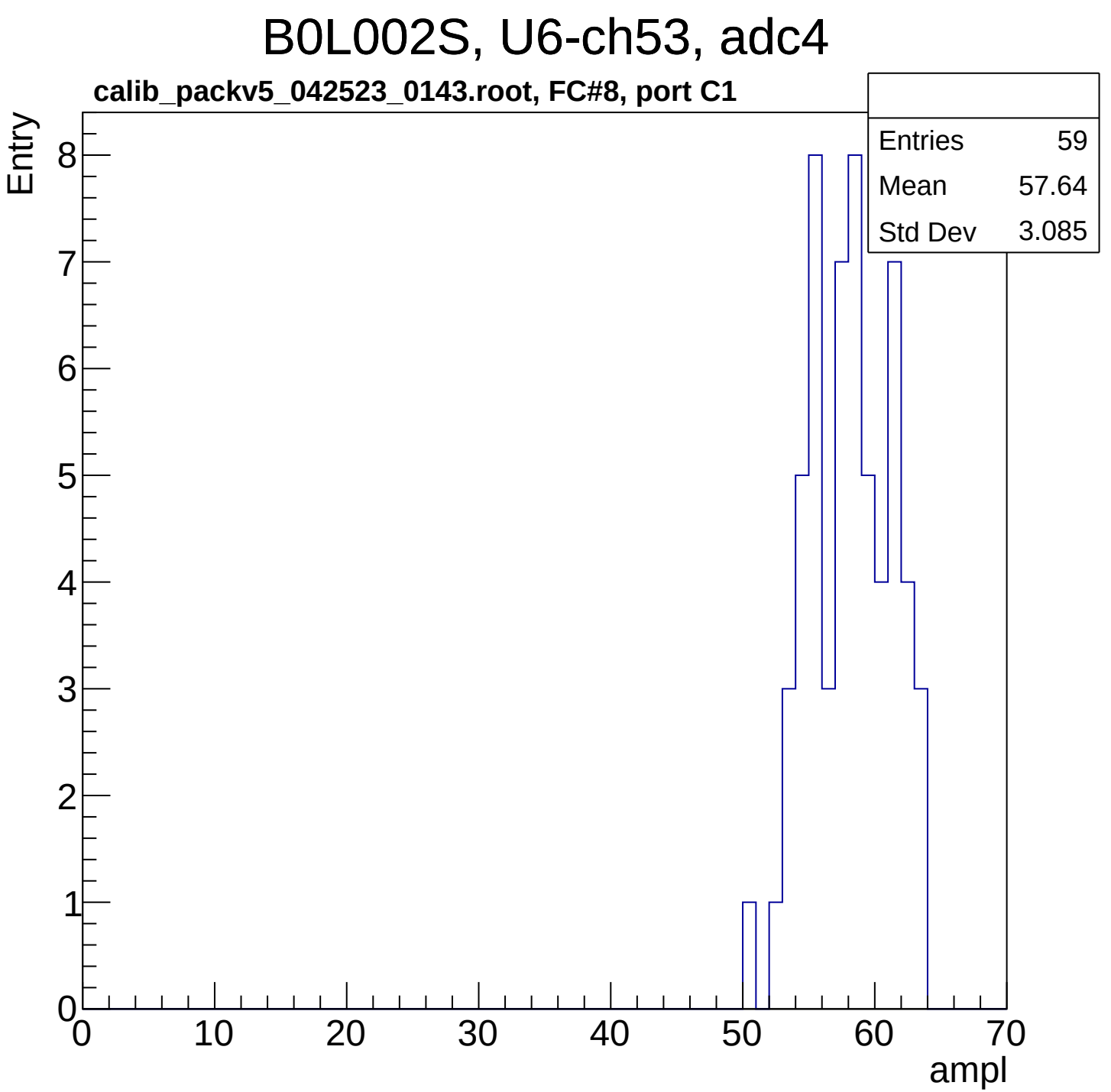
60
70


B0L002S, U6-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.64
Std Dev 3.085
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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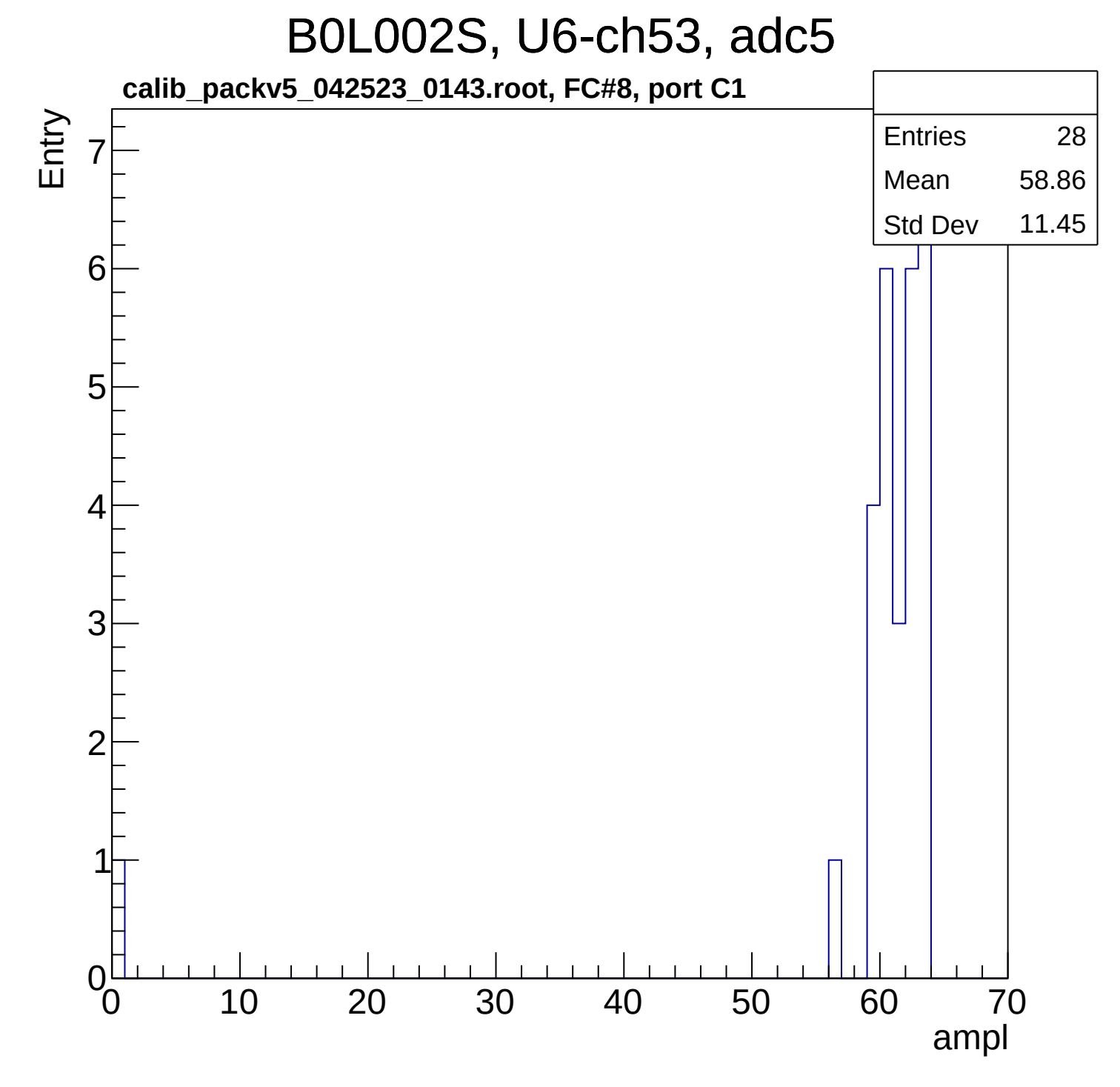
40
50
60
70


B0L002S, U6-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.86
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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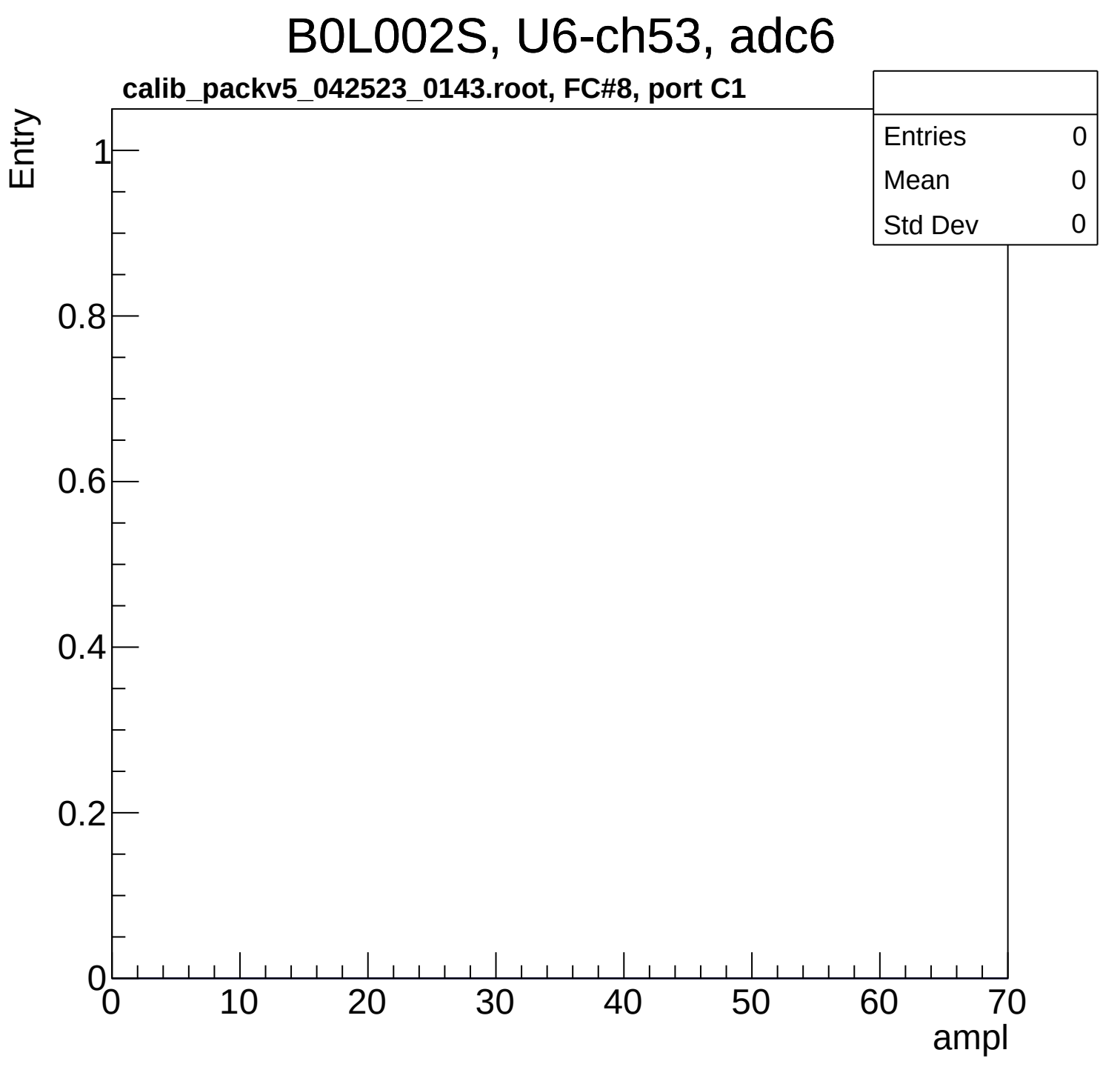
B0L002S, U6-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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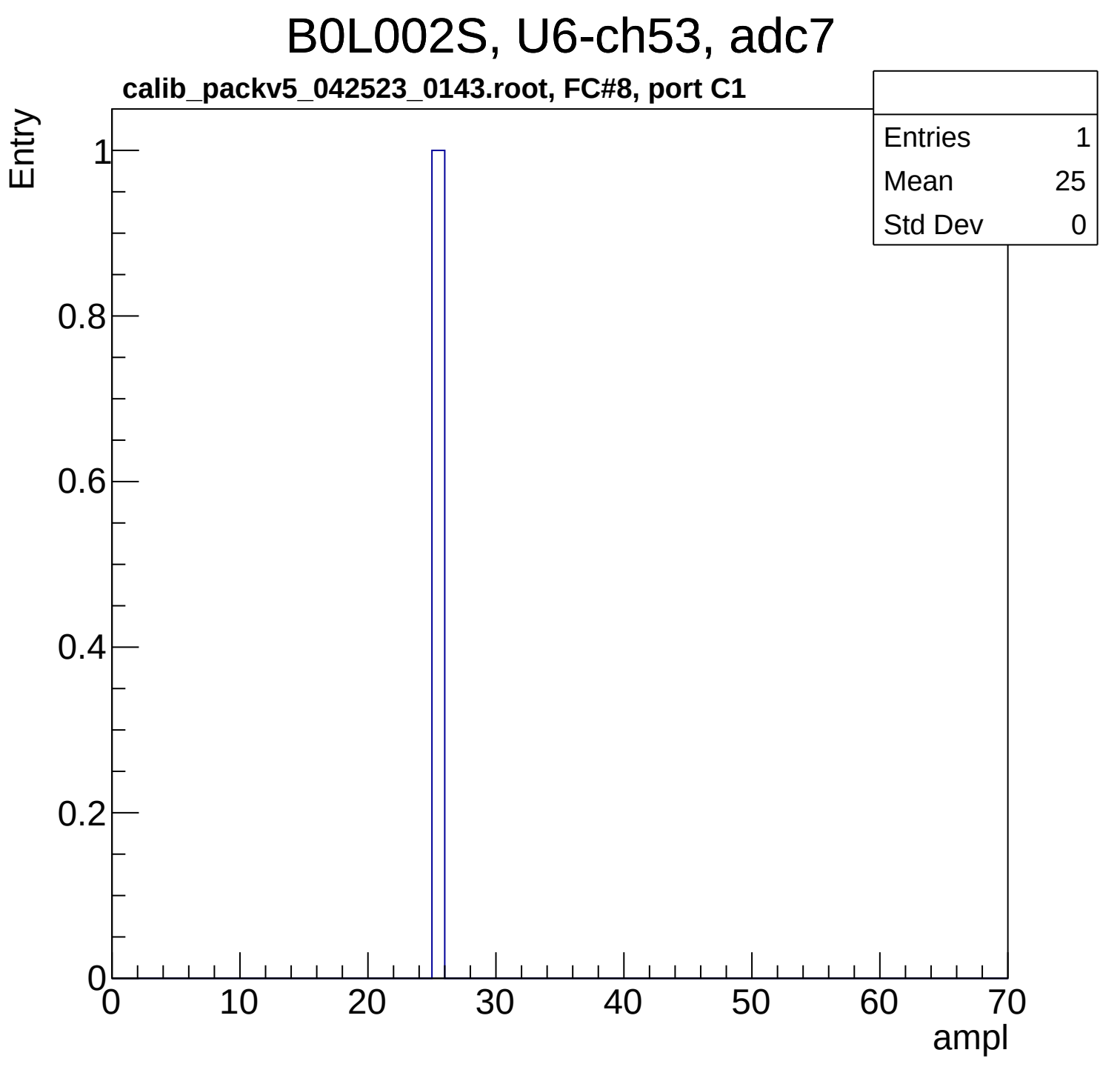
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
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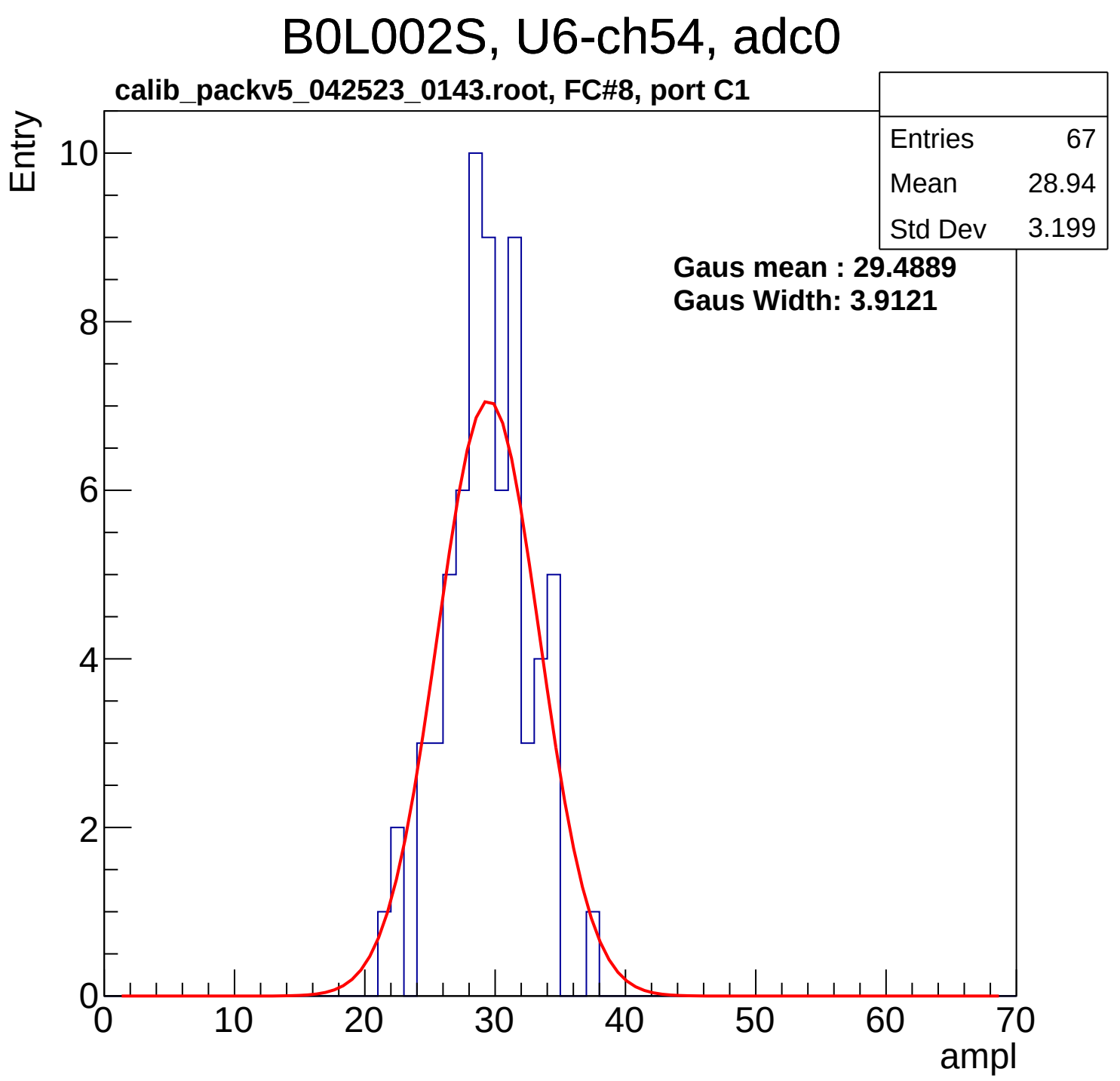
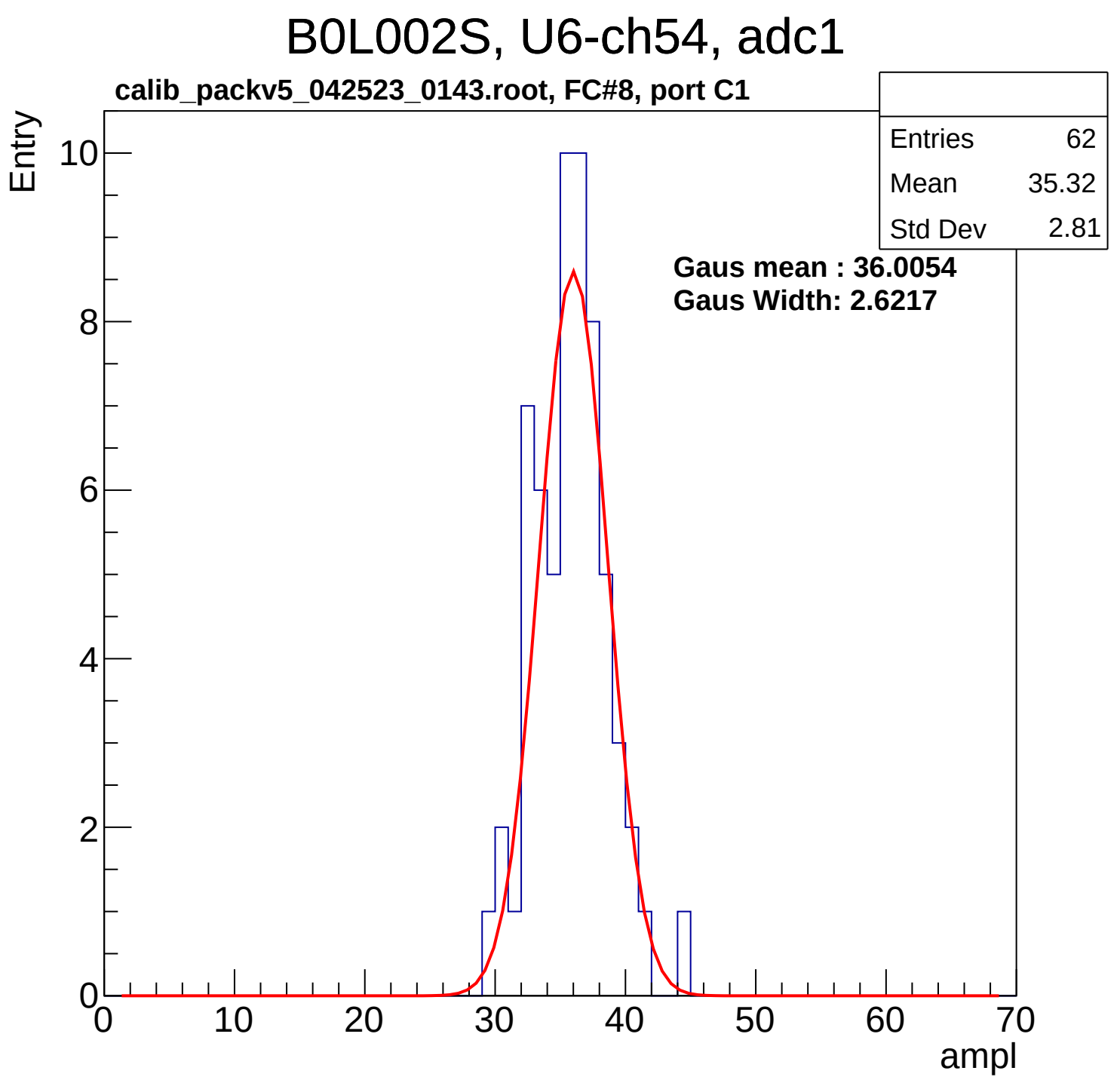
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 28.94
Std Dev 3.199
Gaus mean : 29.4889
Gaus Width: 3.9121
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 35.32
Std Dev 2.81
Gaus mean : 36.0054
Gaus Width: 2.6217
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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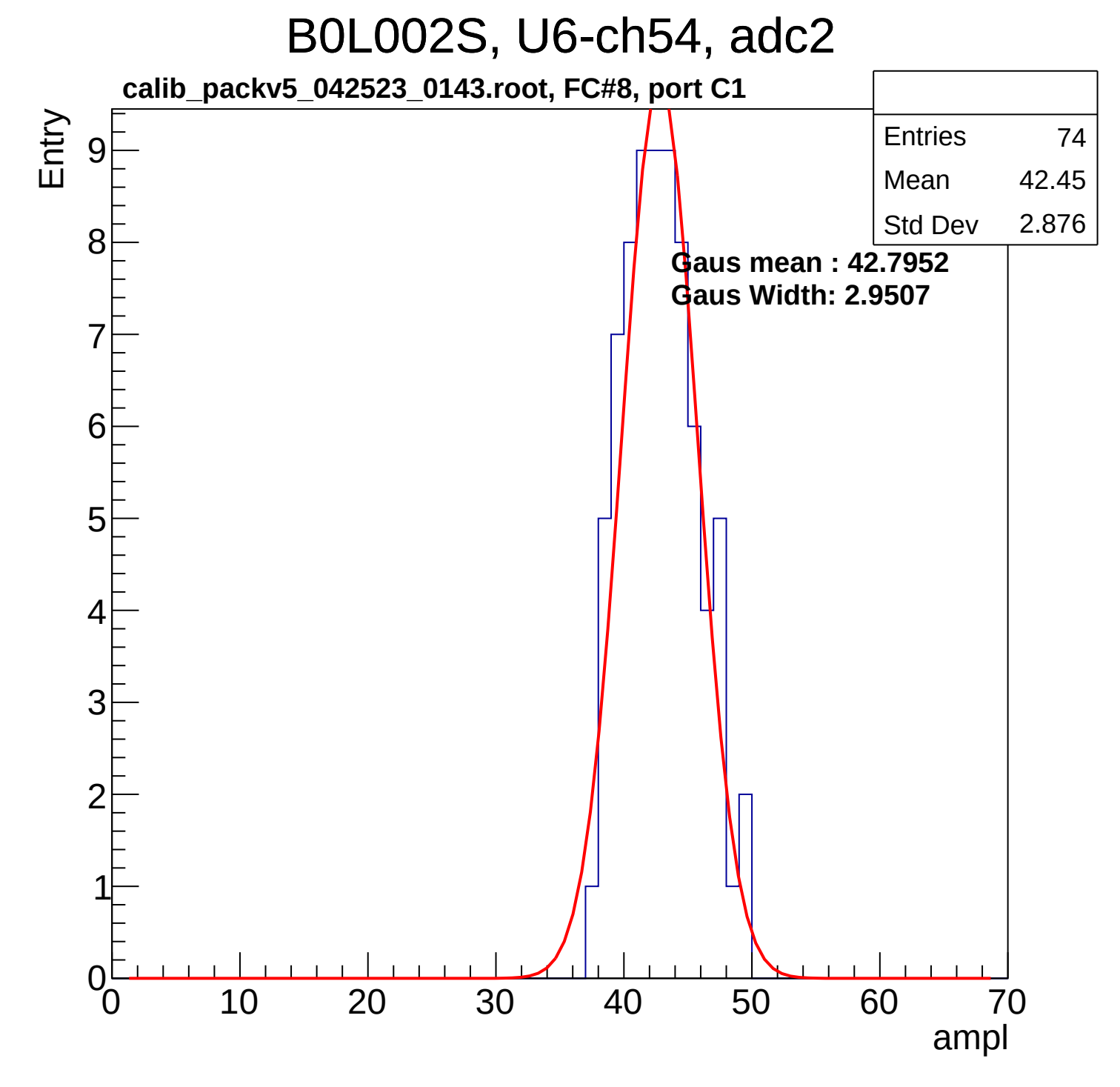
B0L002S, U6-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 42.45
Std Dev 2.876
Gaus mean : 42.7952
Gaus Width: 2.9507
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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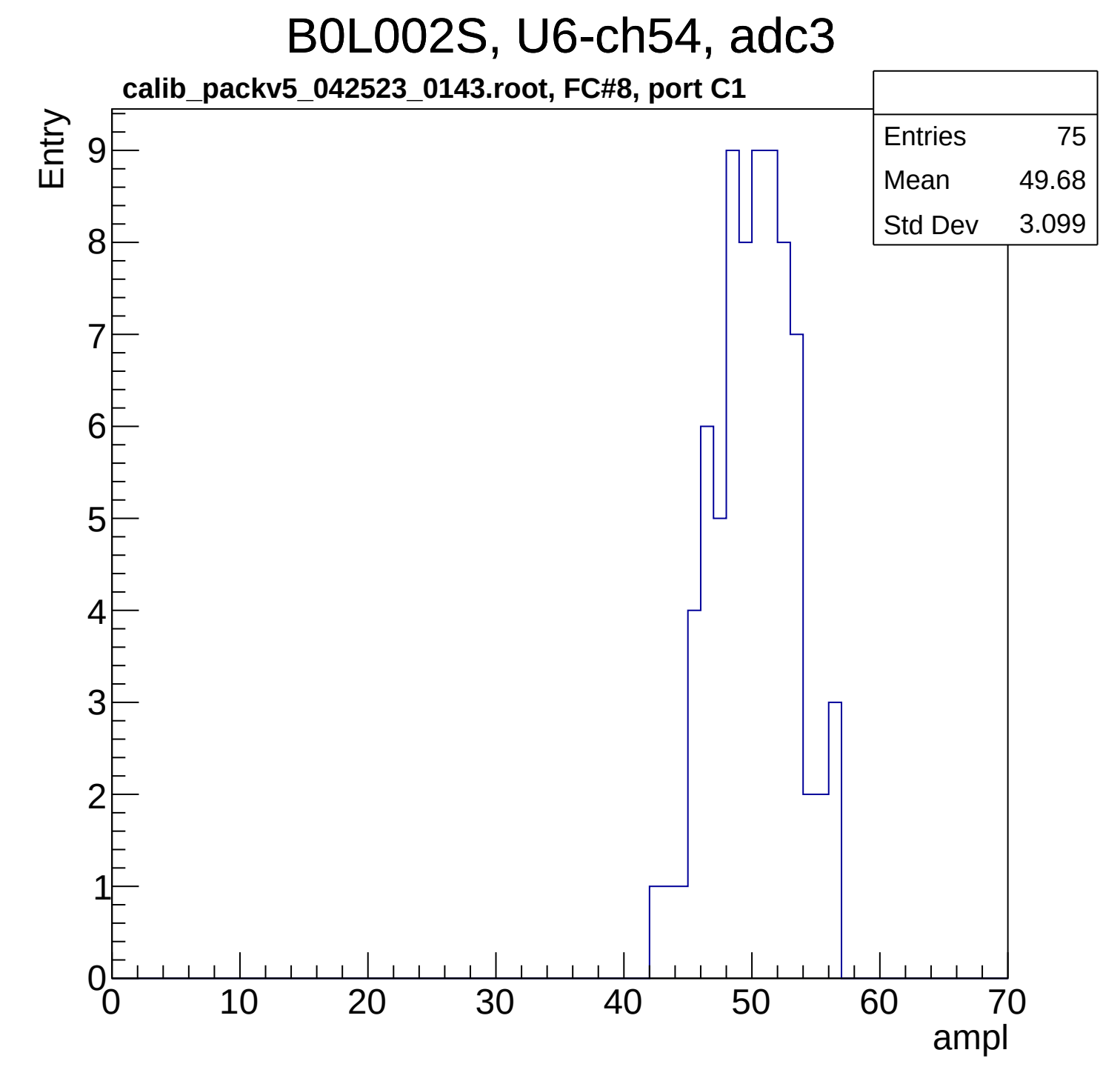
B0L002S, U6-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 49.68
Std Dev 3.099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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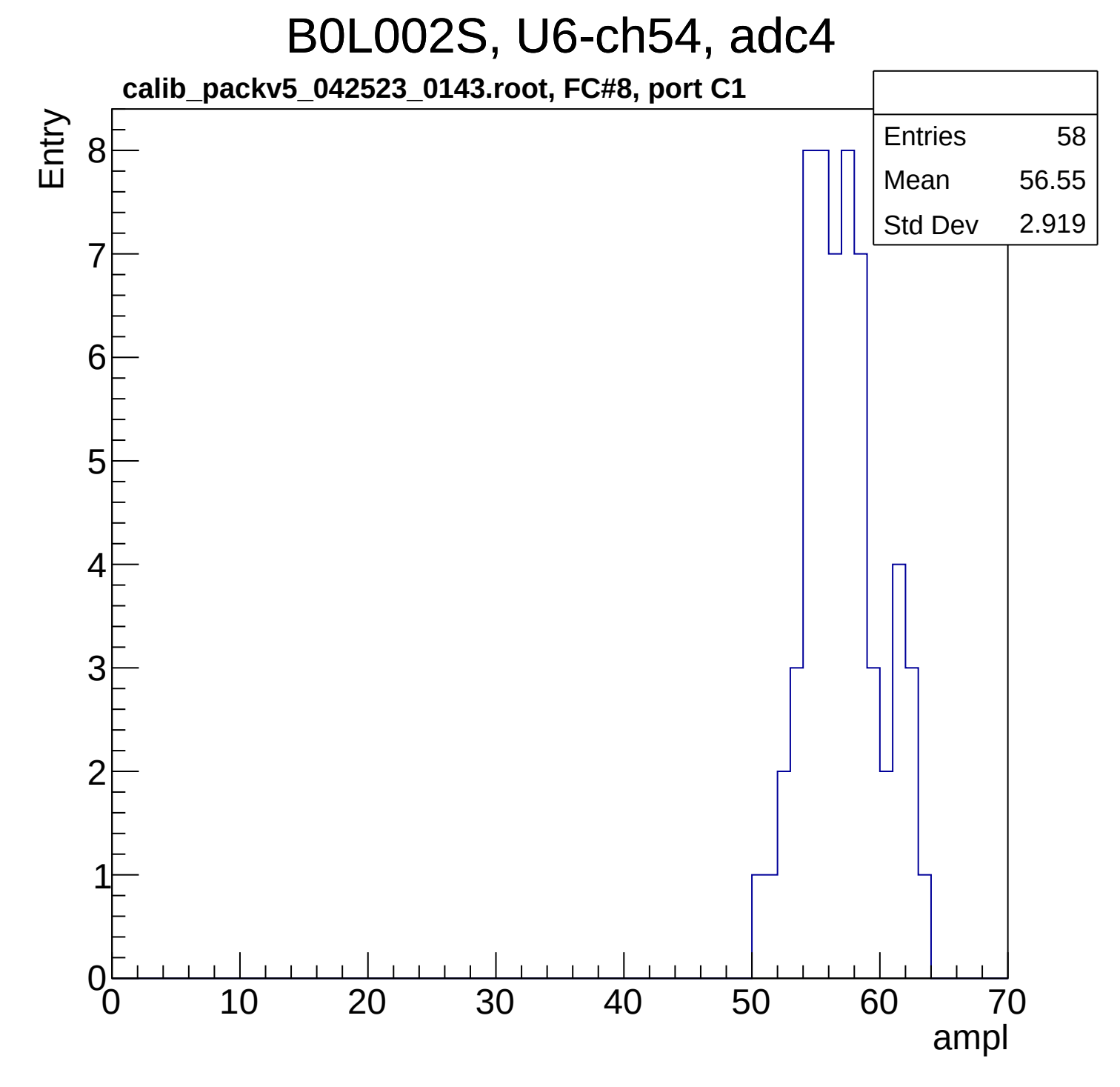
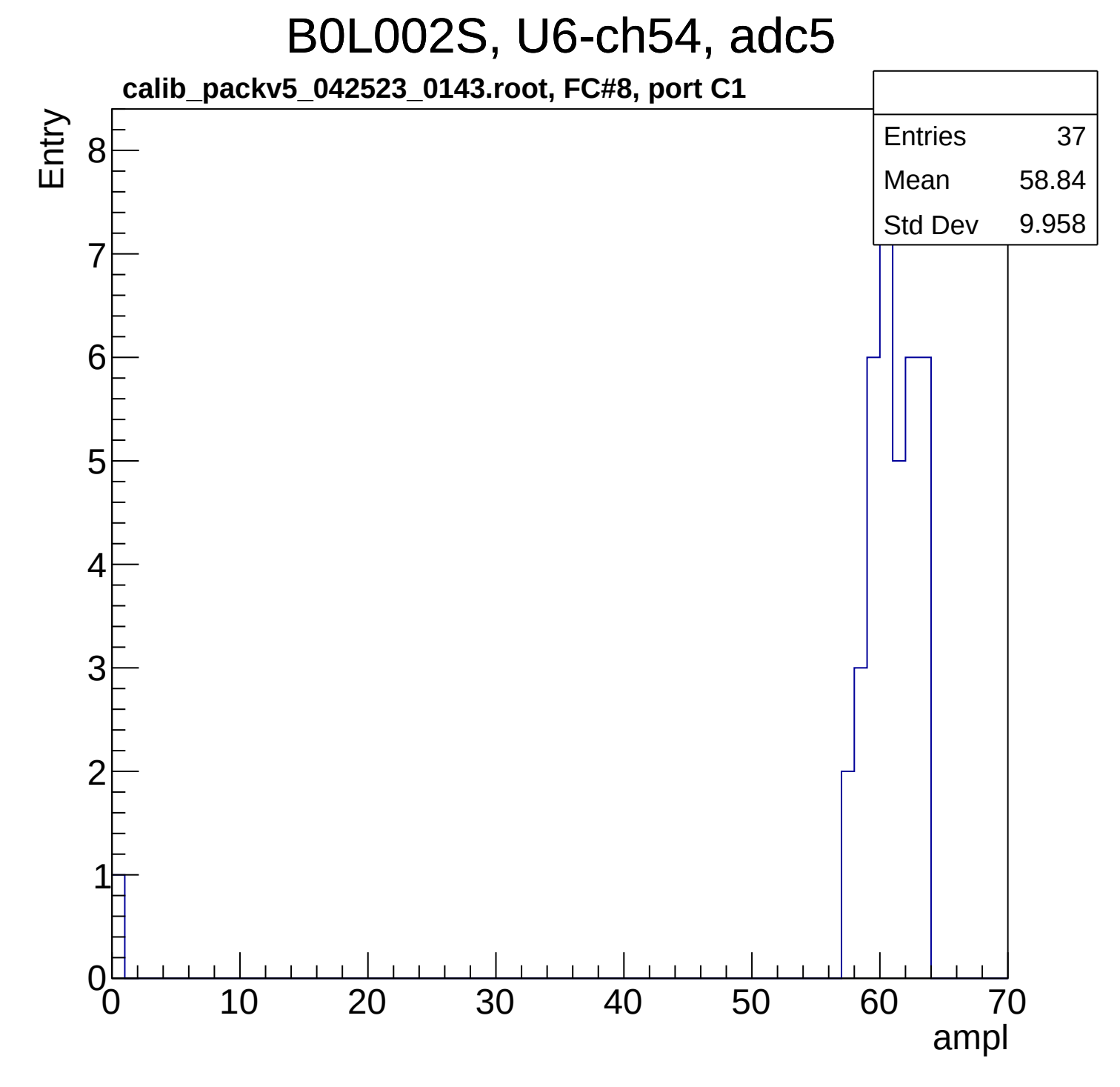

B0L002S, U6-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.55
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.84
Std Dev 9.958
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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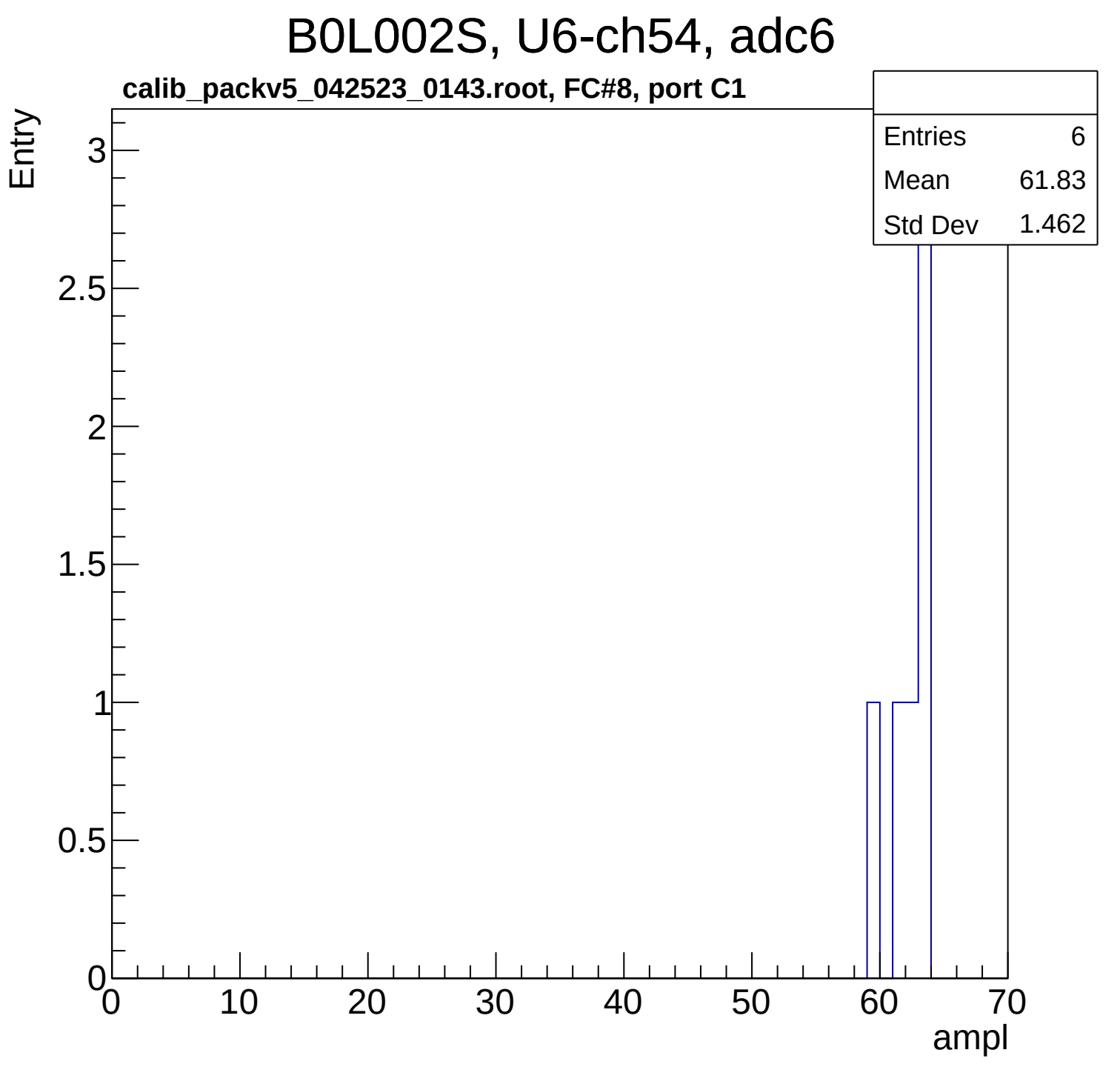
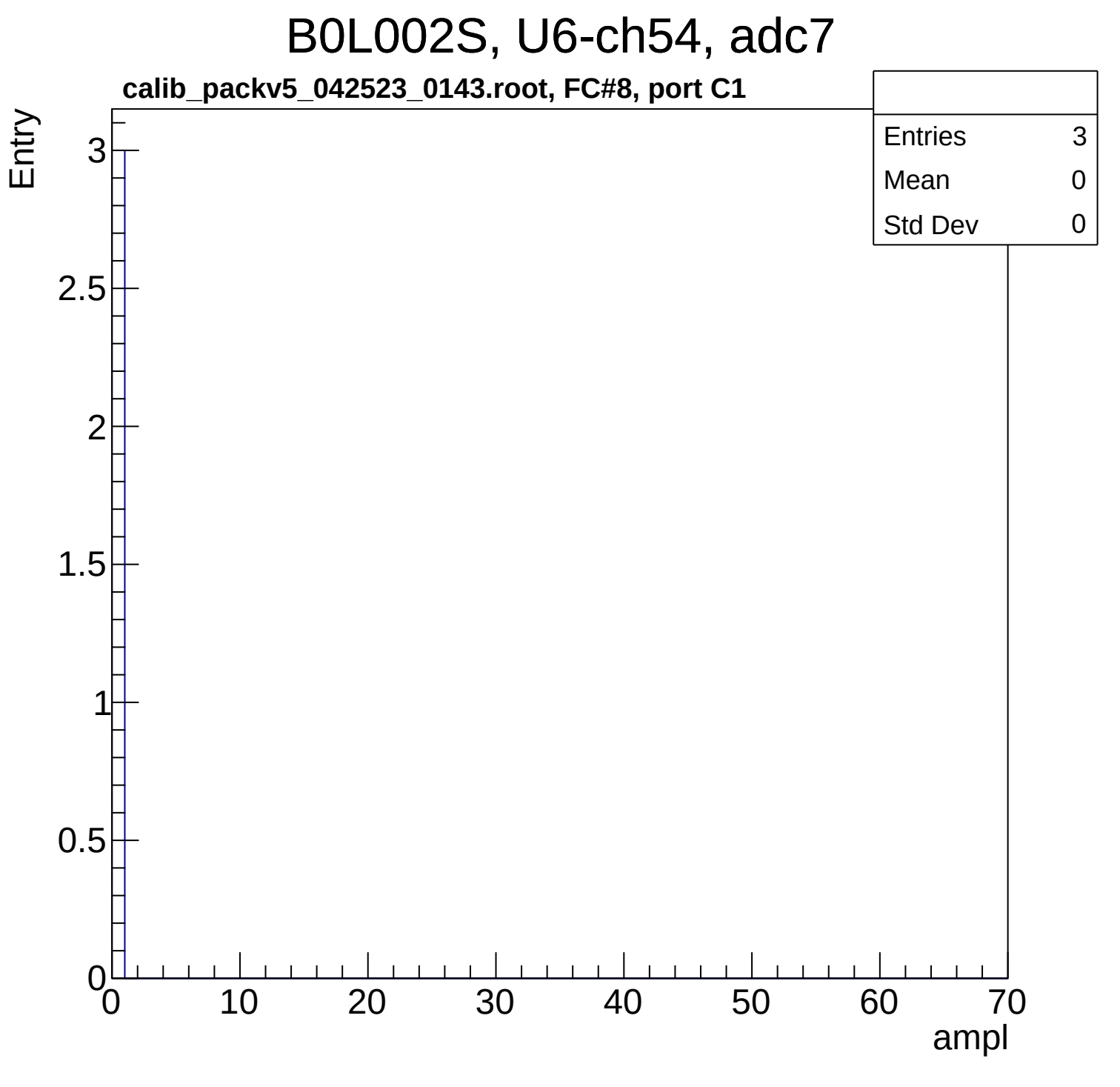
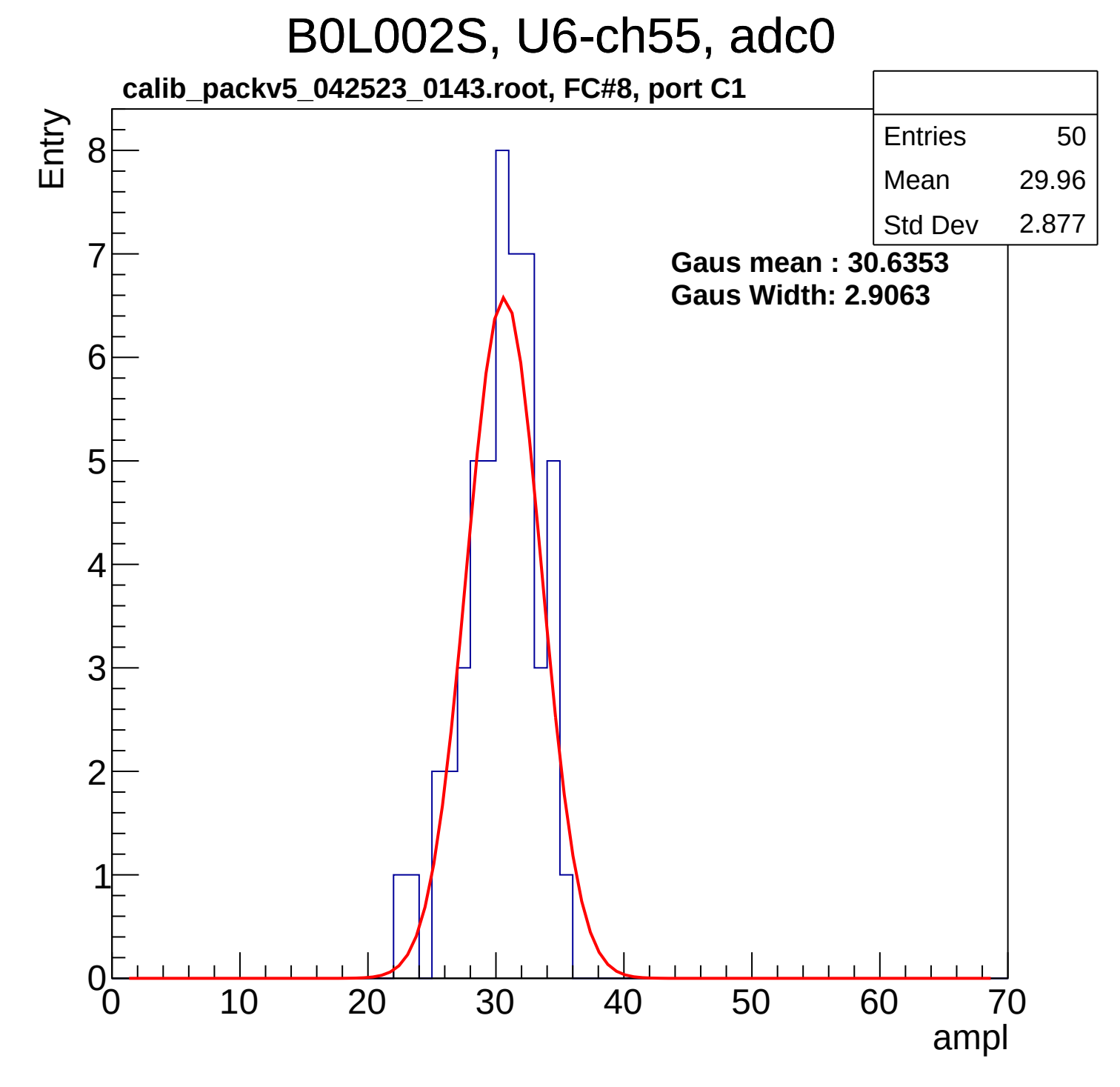
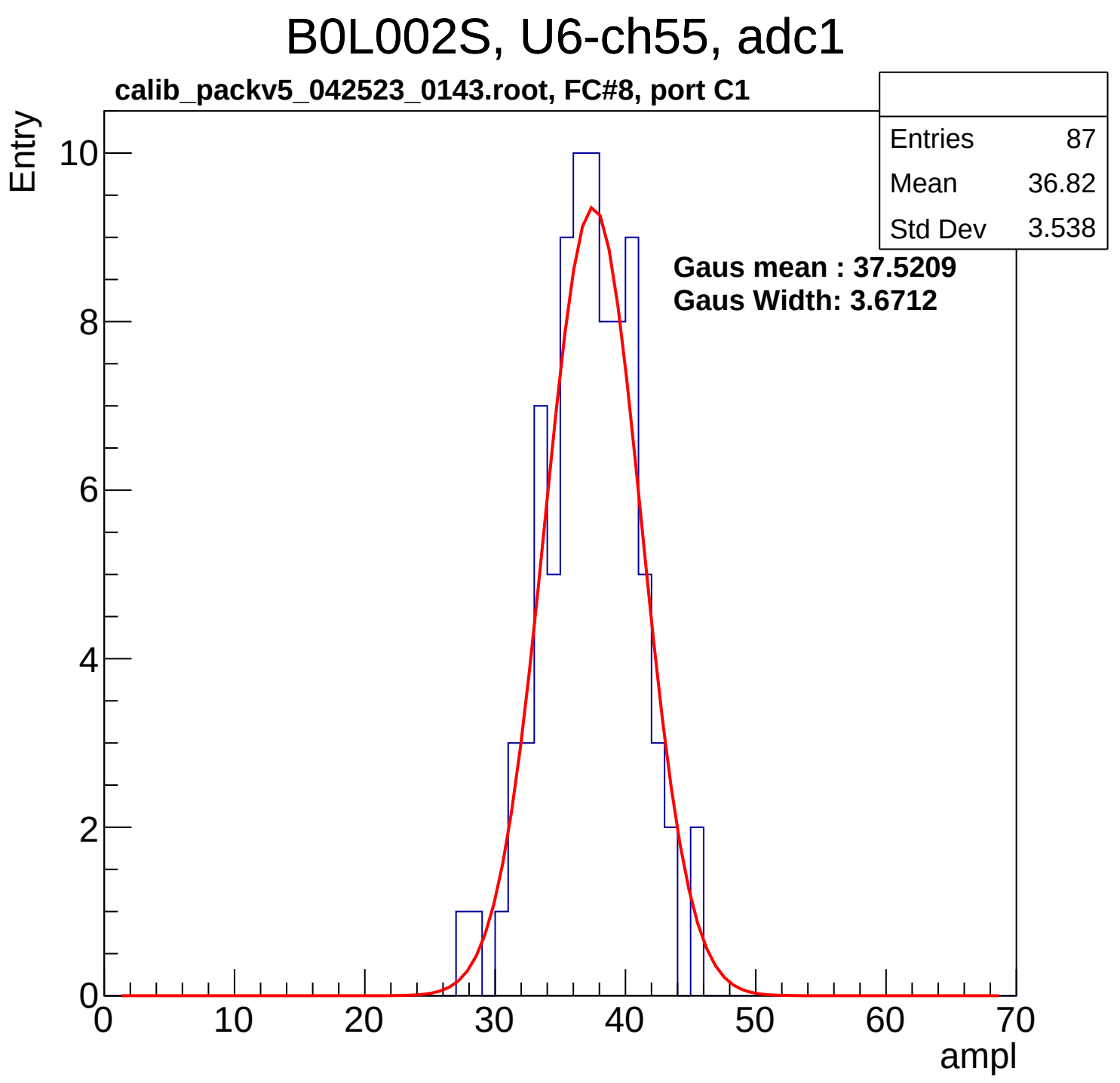
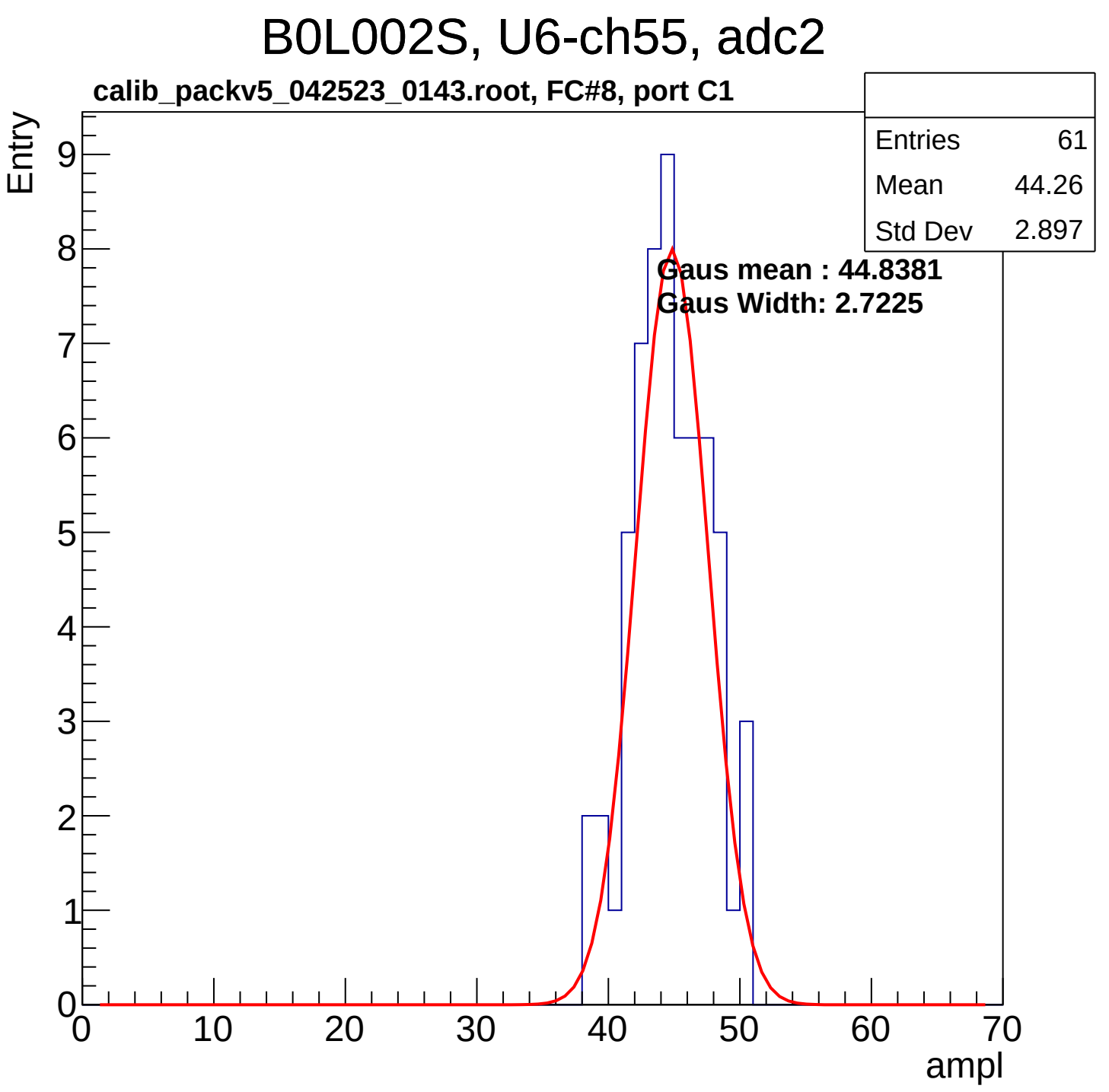

B0L002S, U6-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 29.96
Std Dev 2.877
Gaus mean : 30.6353
Gaus Width: 2.9063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 36.82
Std Dev 3.538
Gaus mean : 37.5209
Gaus Width: 3.6712
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.26
Std Dev 2.897
Gaus mean : 44.8381
Gaus Width: 2.7225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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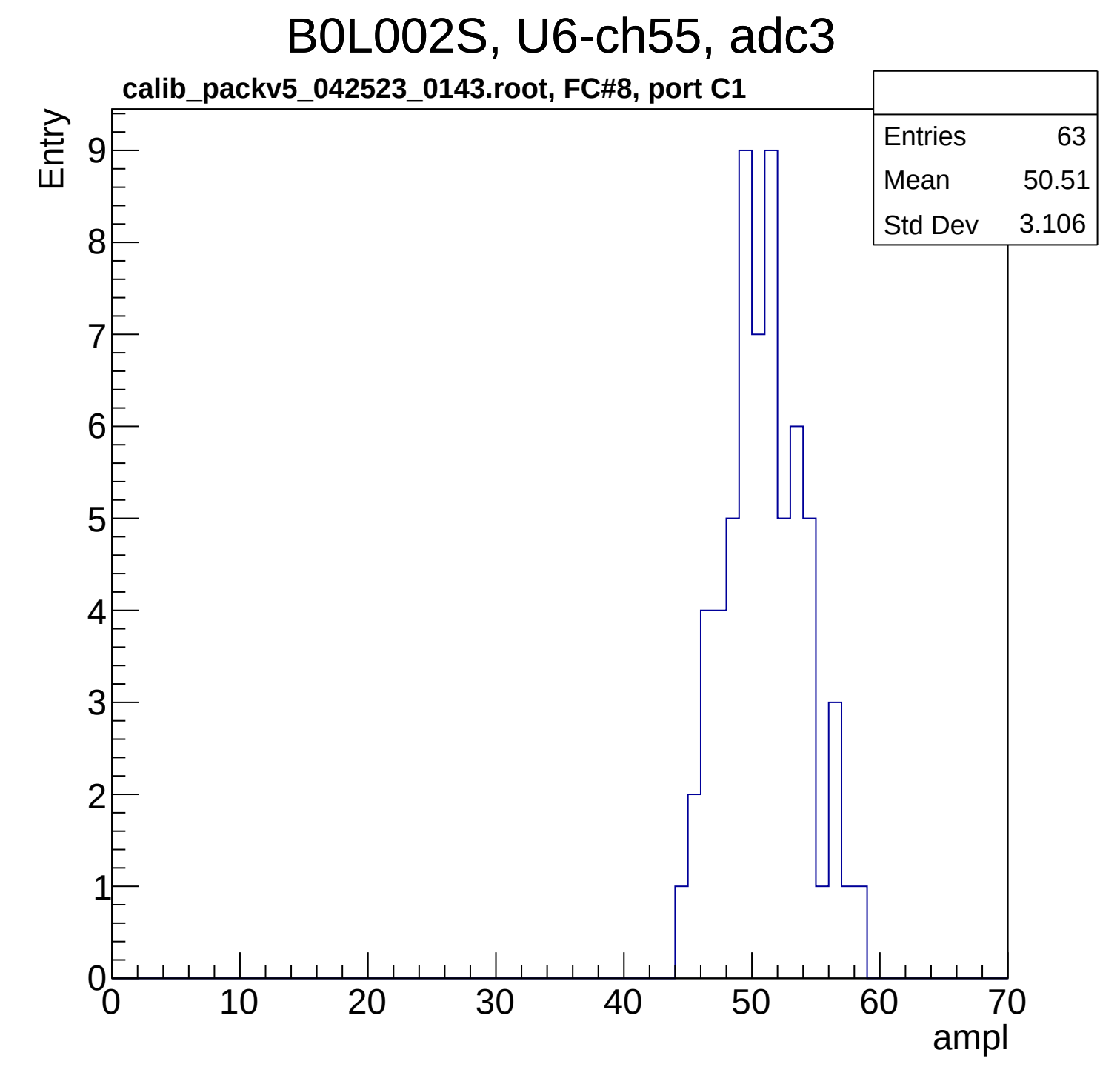
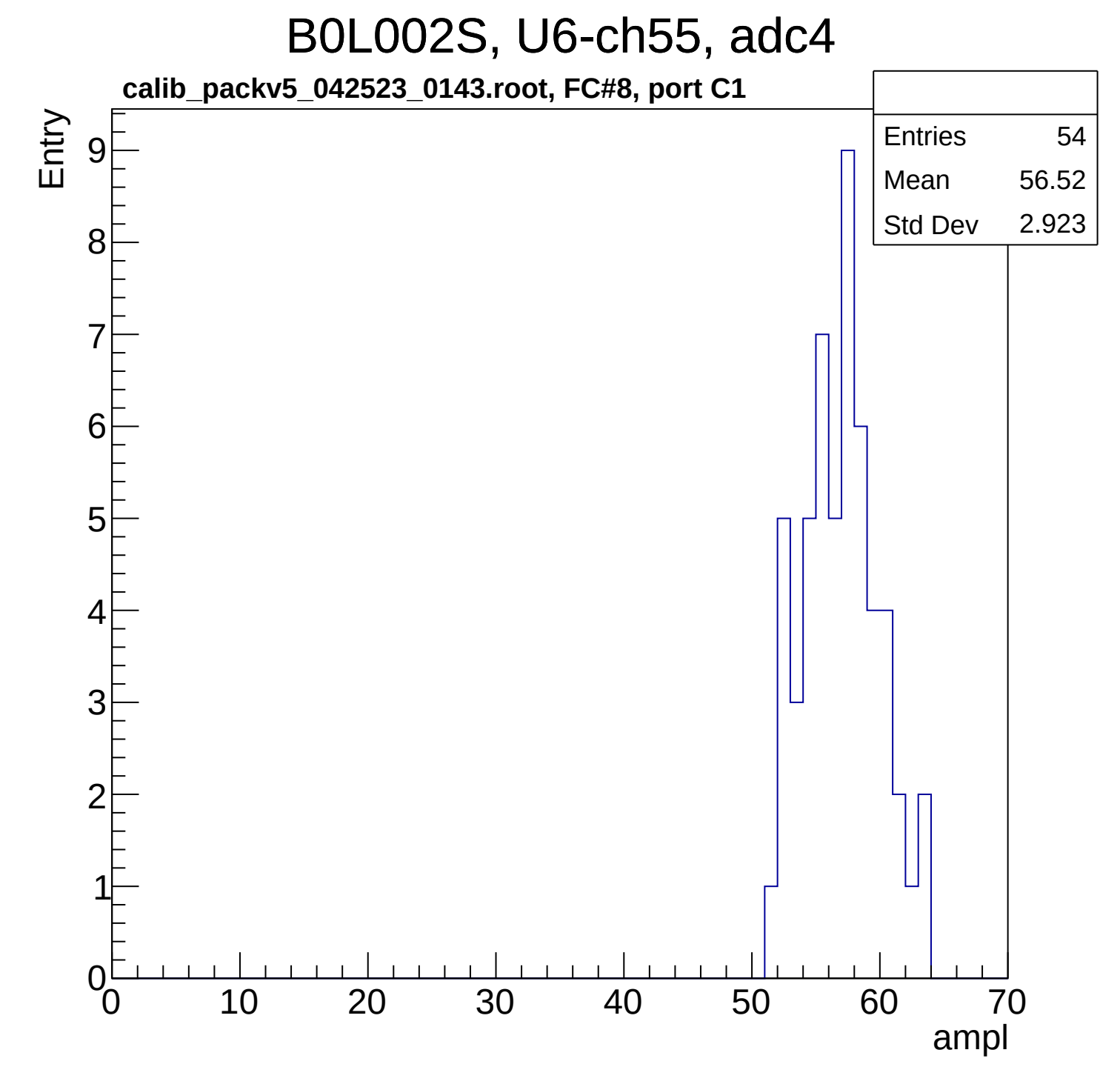

B0L002S, U6-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.51
Std Dev 3.106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.52
Std Dev 2.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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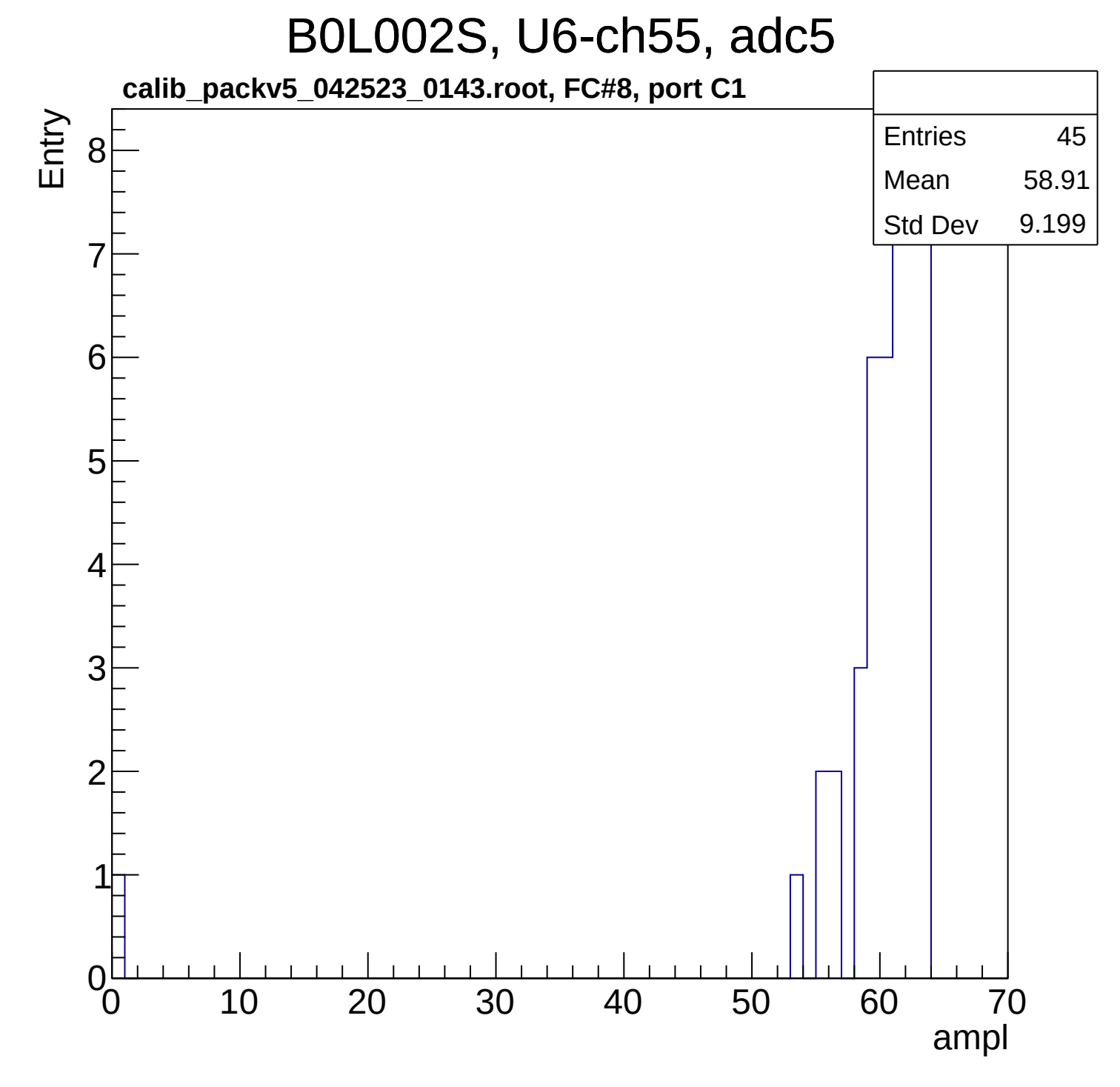
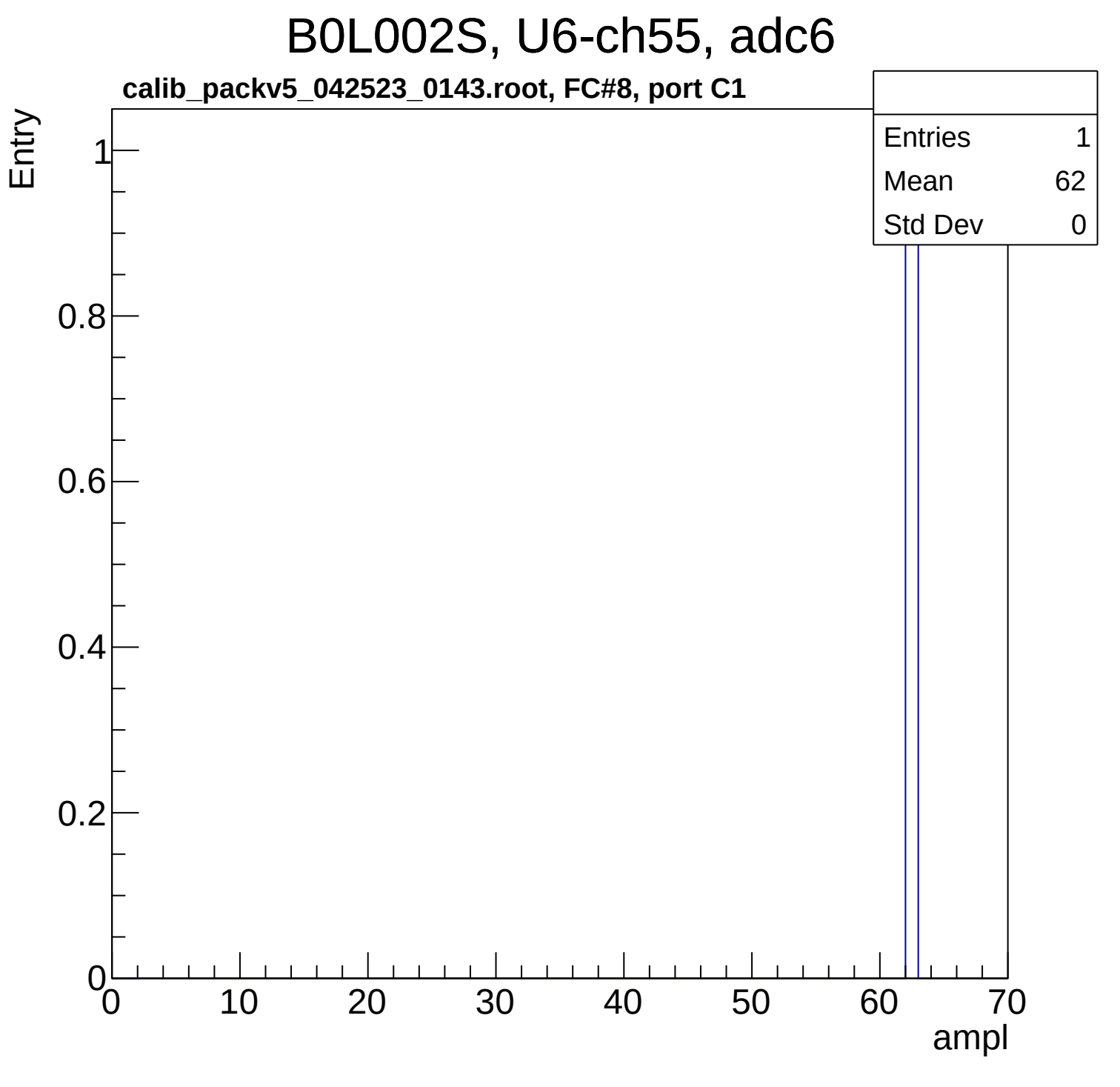
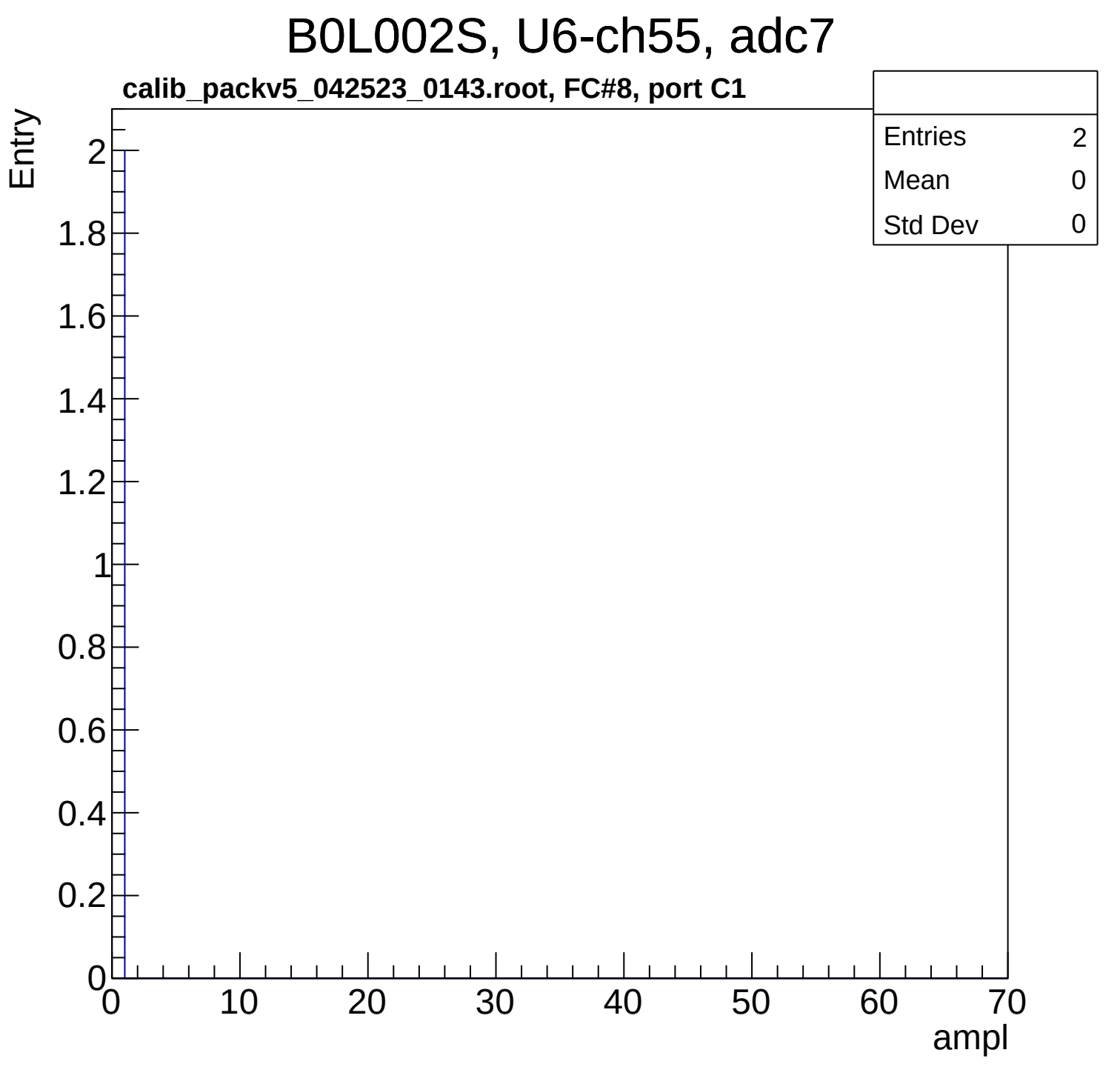
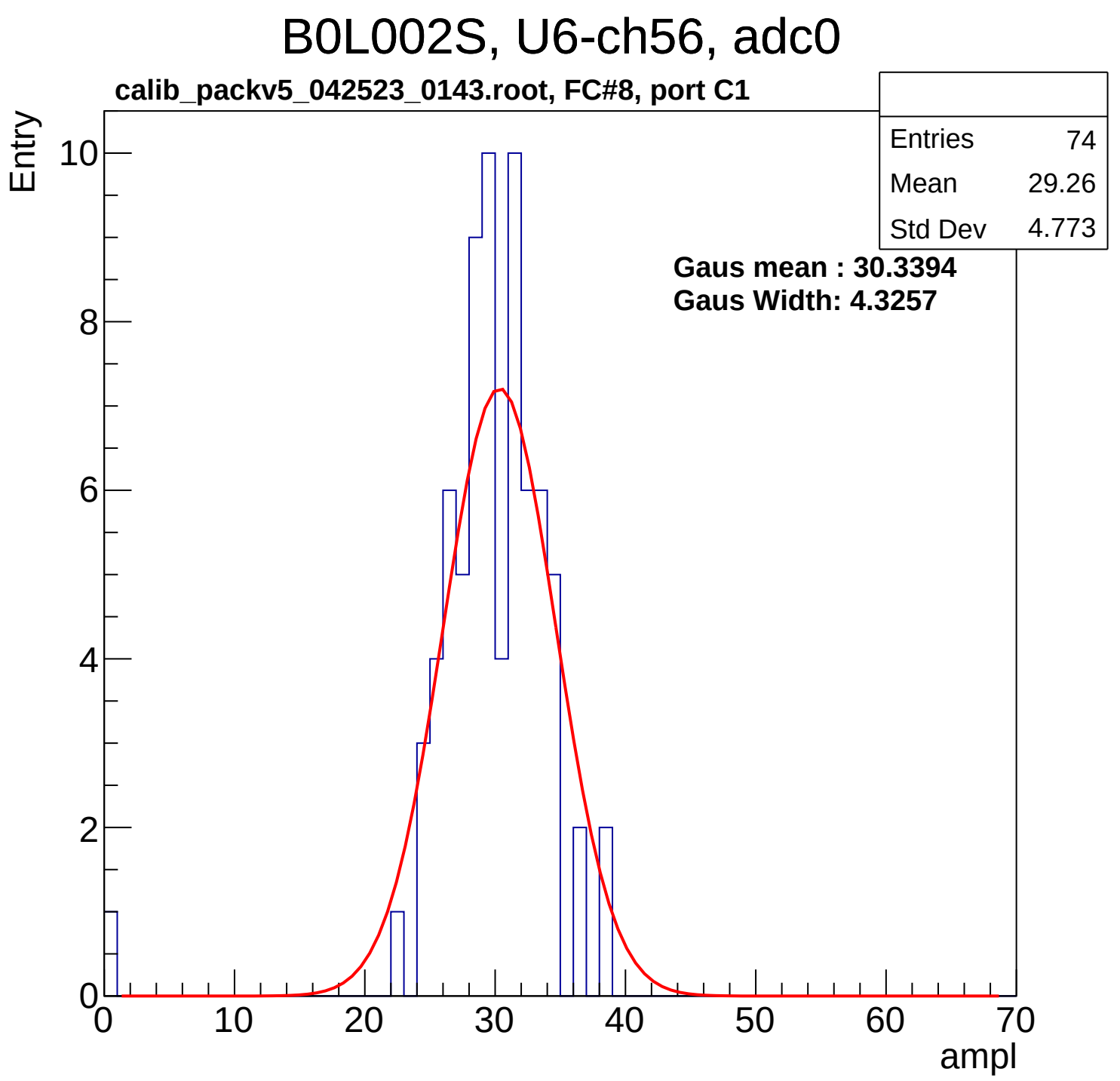
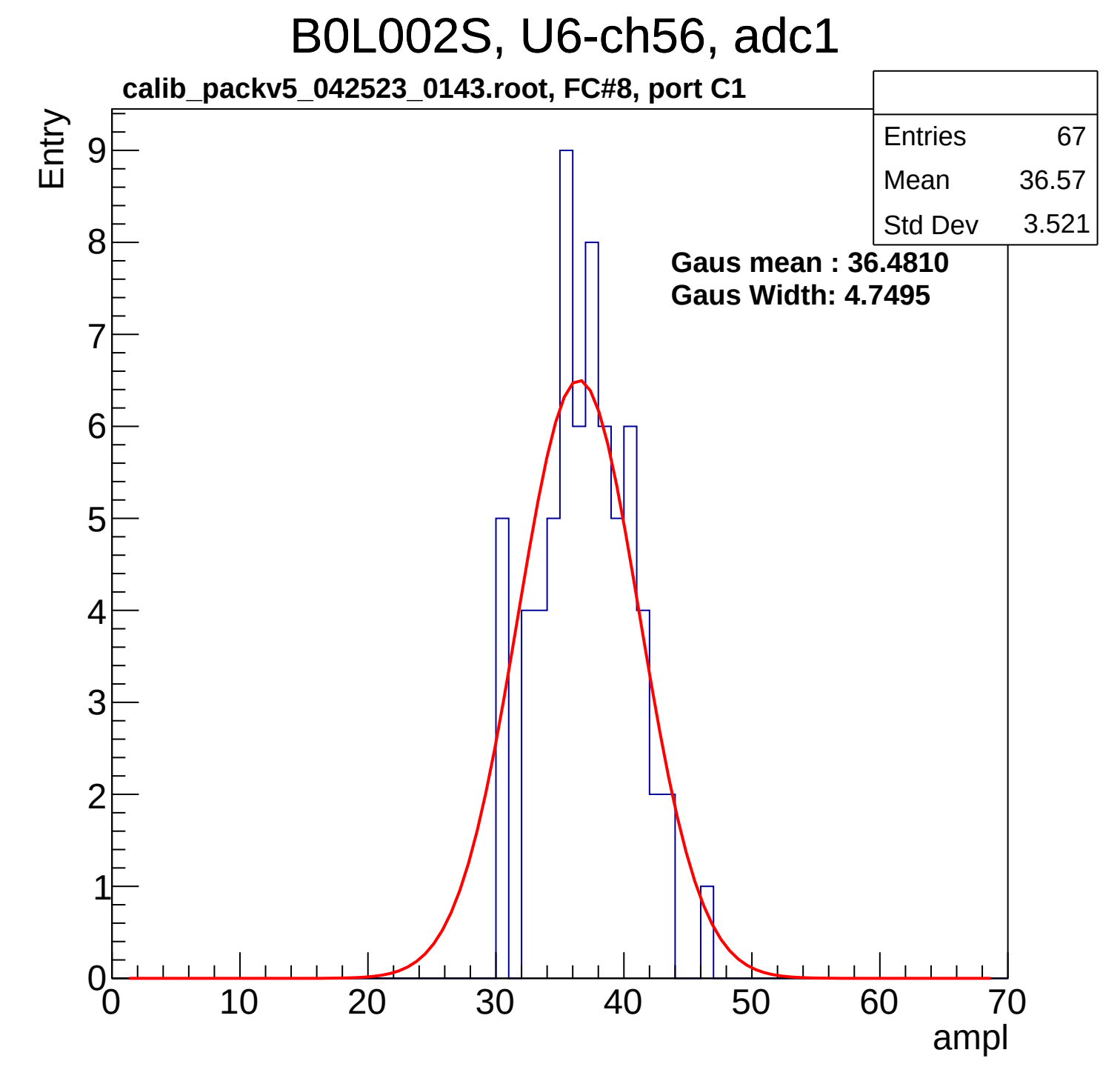
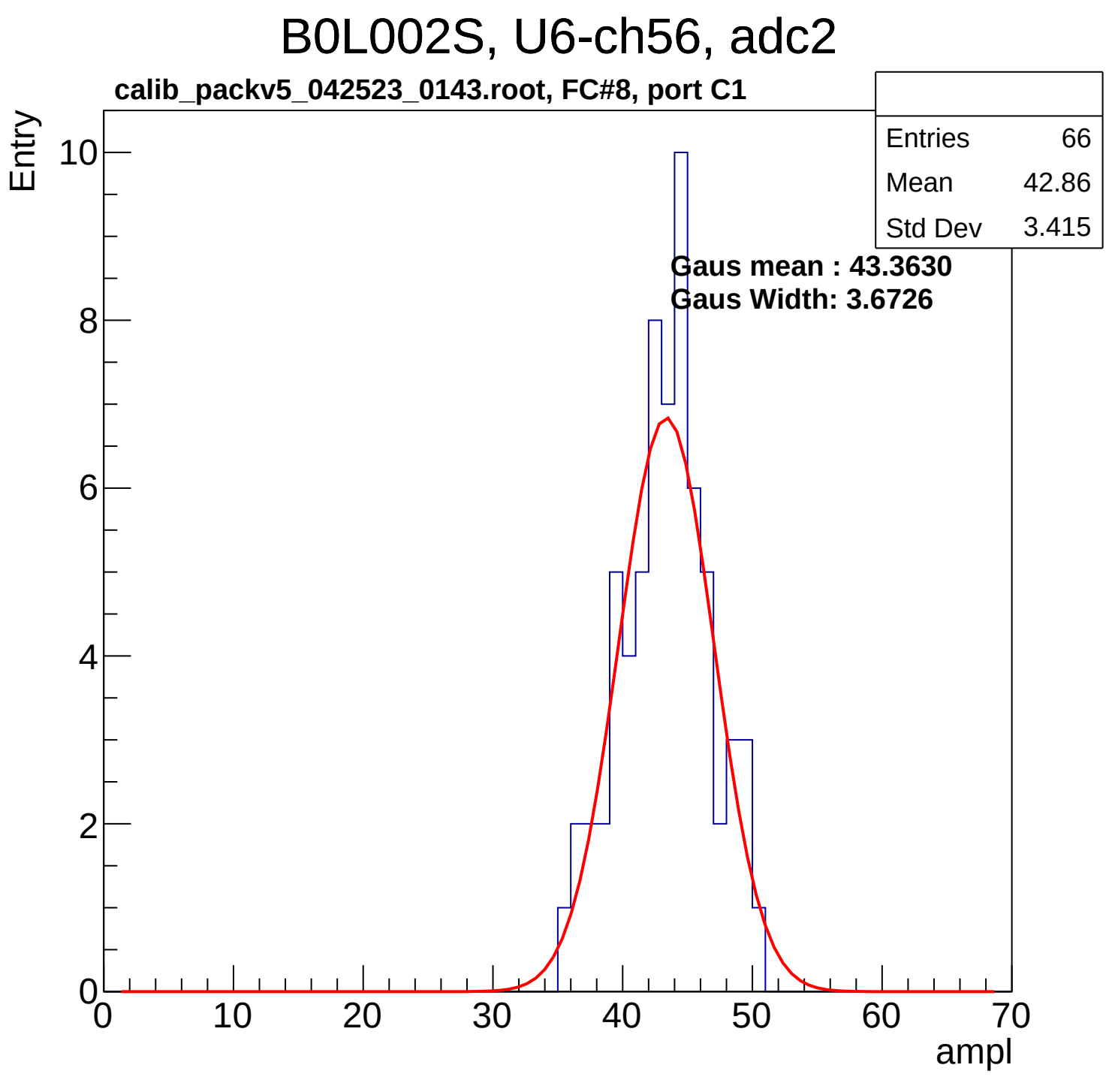

B0L002S, U6-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.91
Std Dev 9.199
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

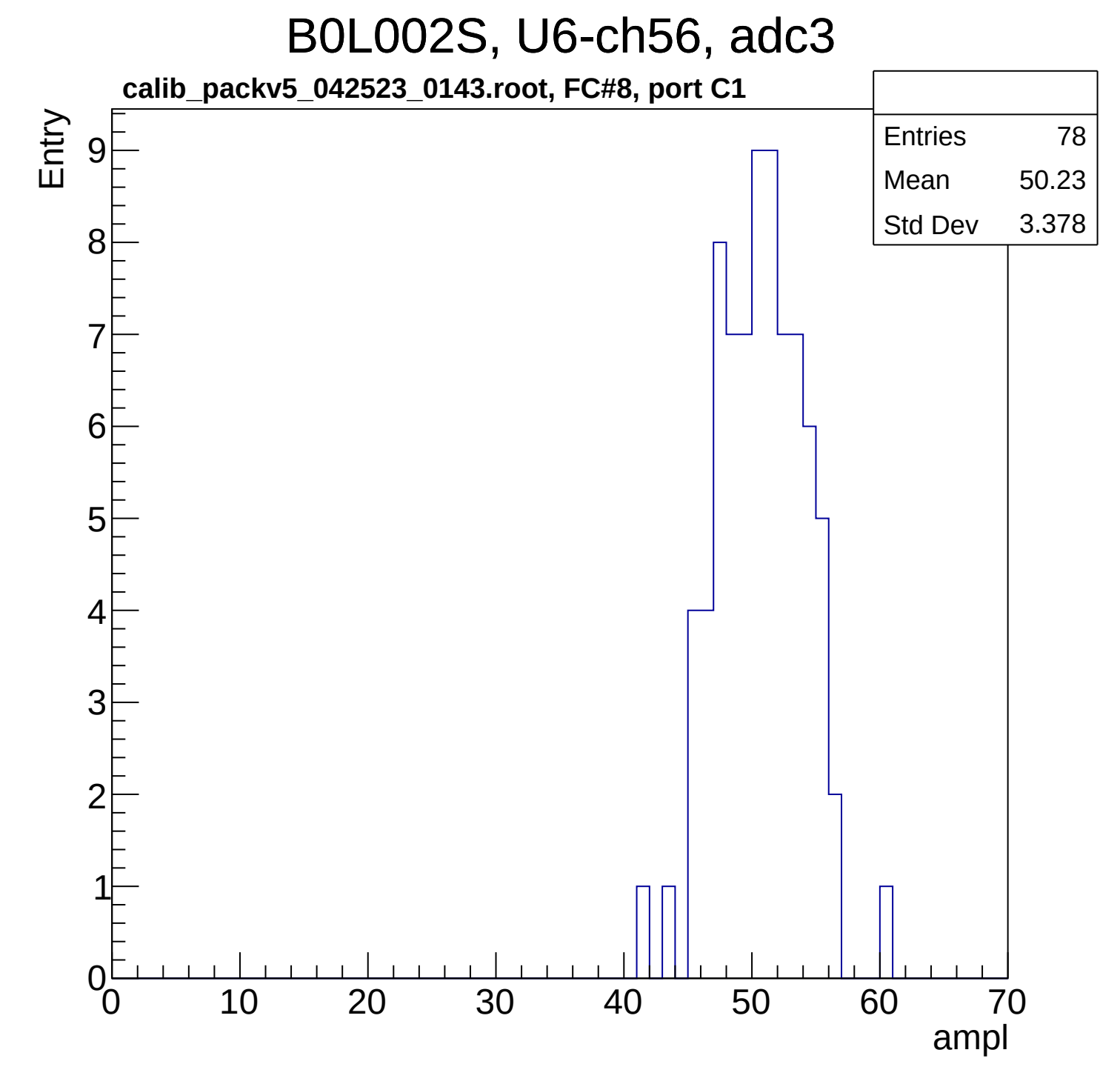

B0L002S, U6-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.26
Std Dev 4.773
Gaus mean : 30.3394
Gaus Width: 4.3257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.57
Std Dev 3.521
Gaus mean : 36.4810
Gaus Width: 4.7495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.86
Std Dev 3.415
Gaus mean : 43.3630
Gaus Width: 3.6726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 50.23
Std Dev 3.378
Entry
ampl
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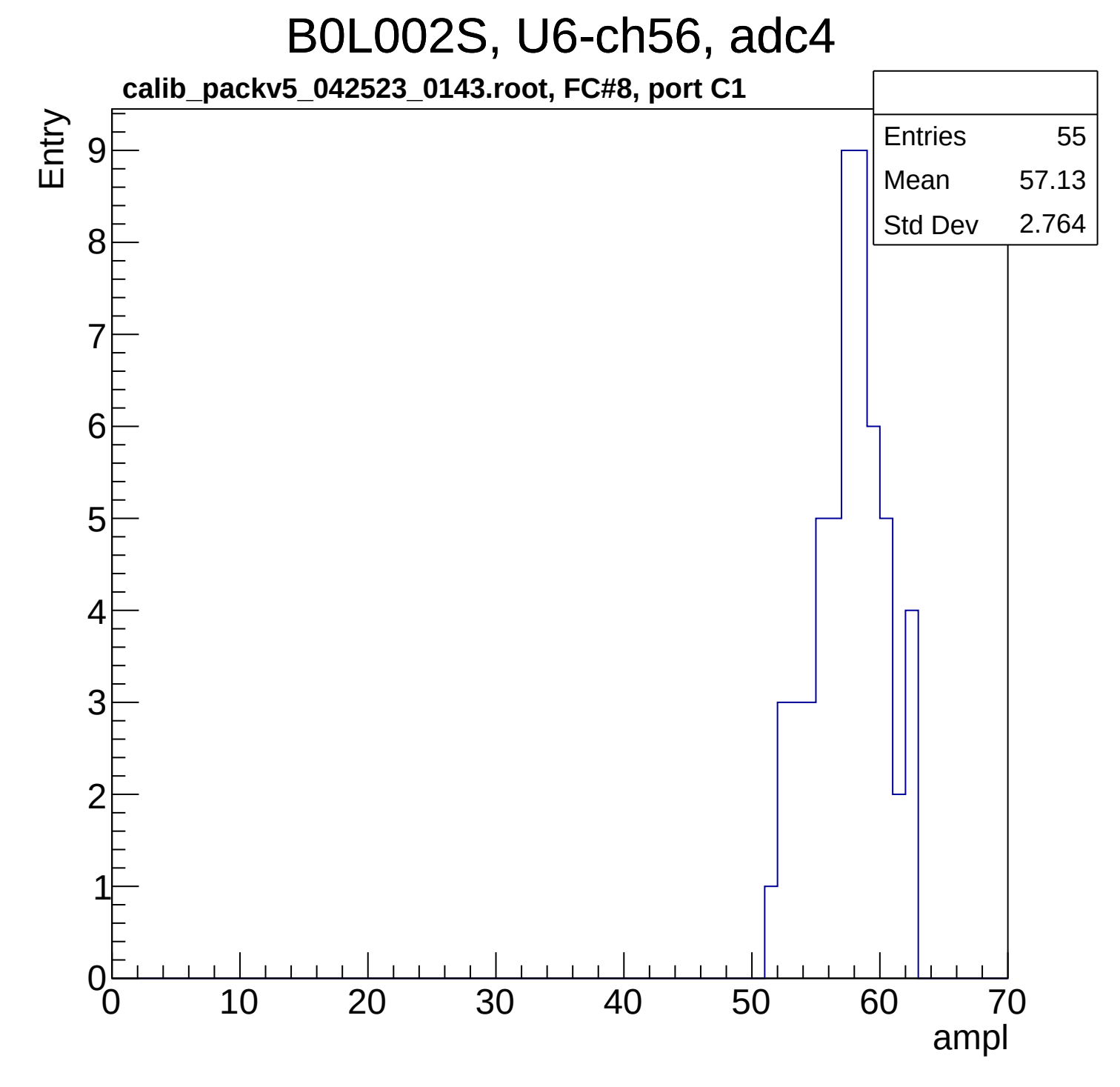
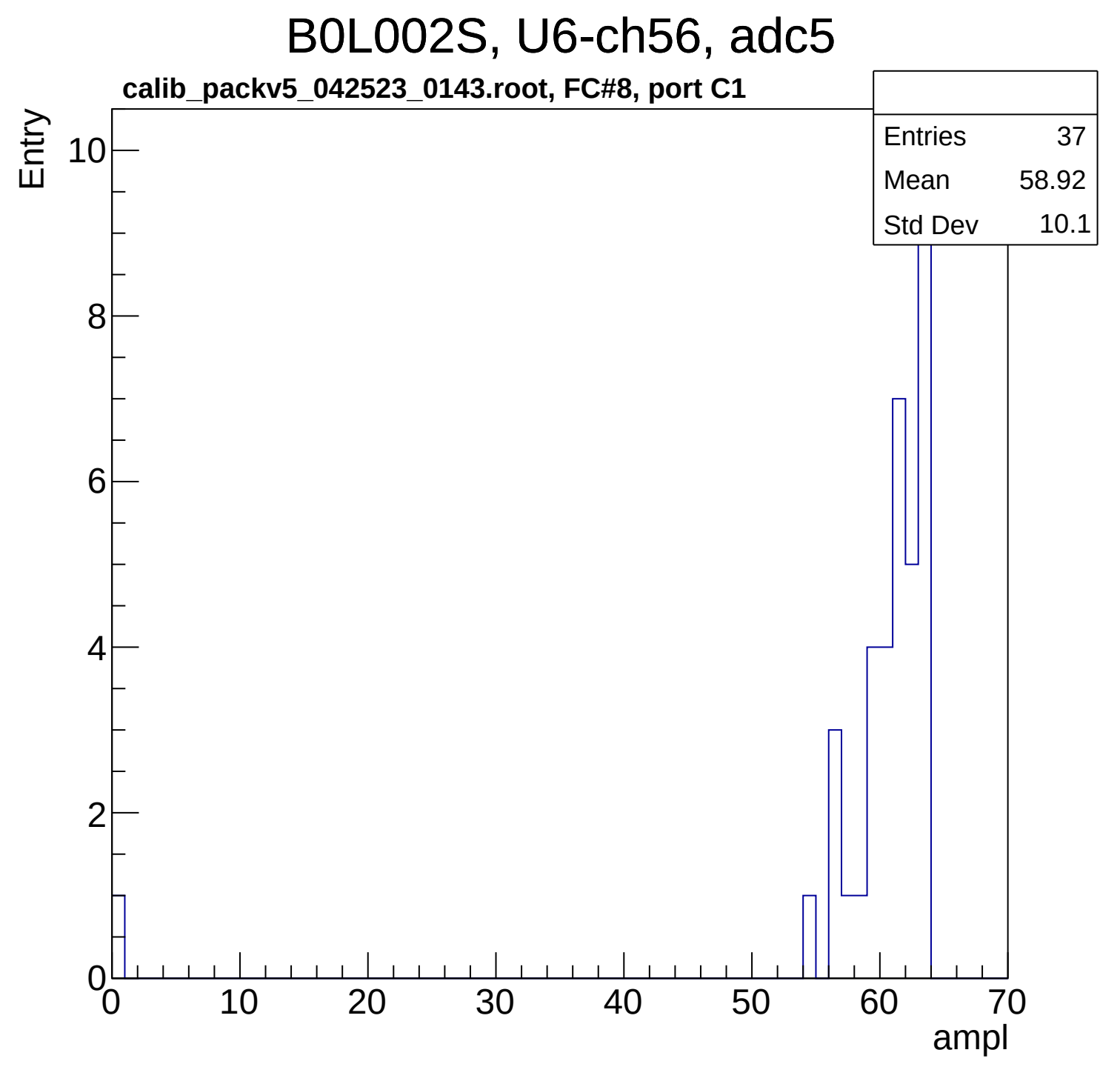
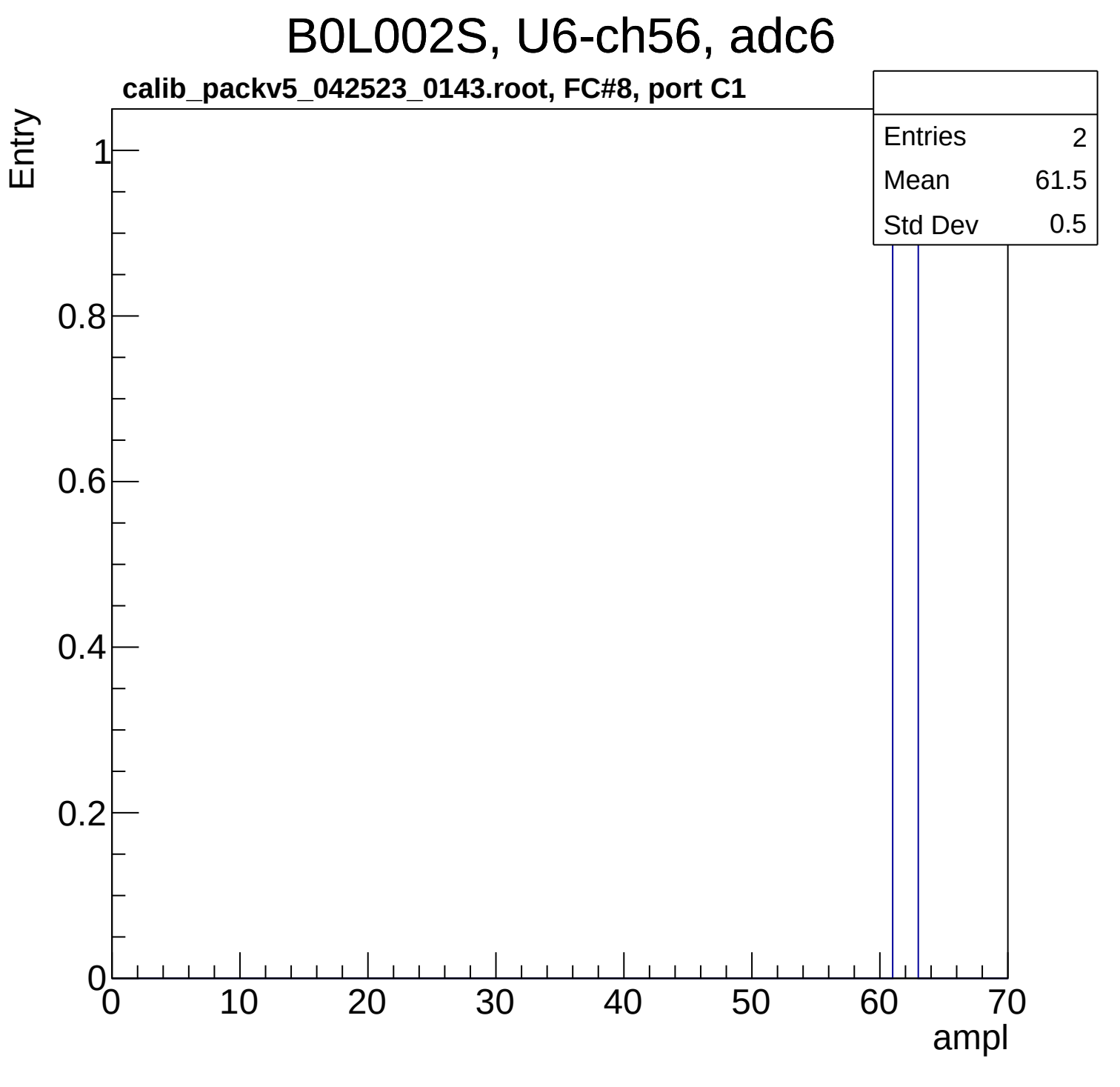
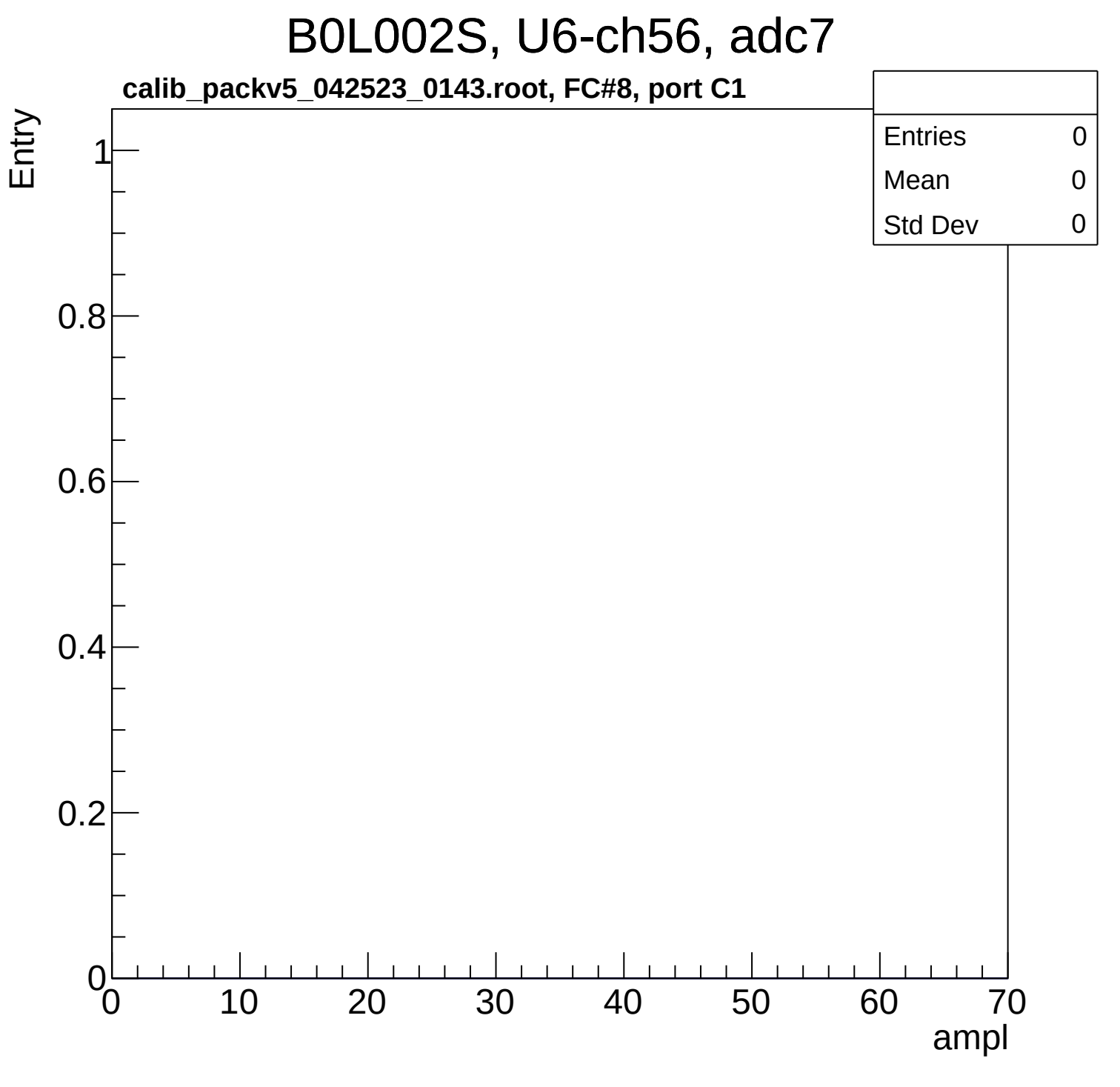
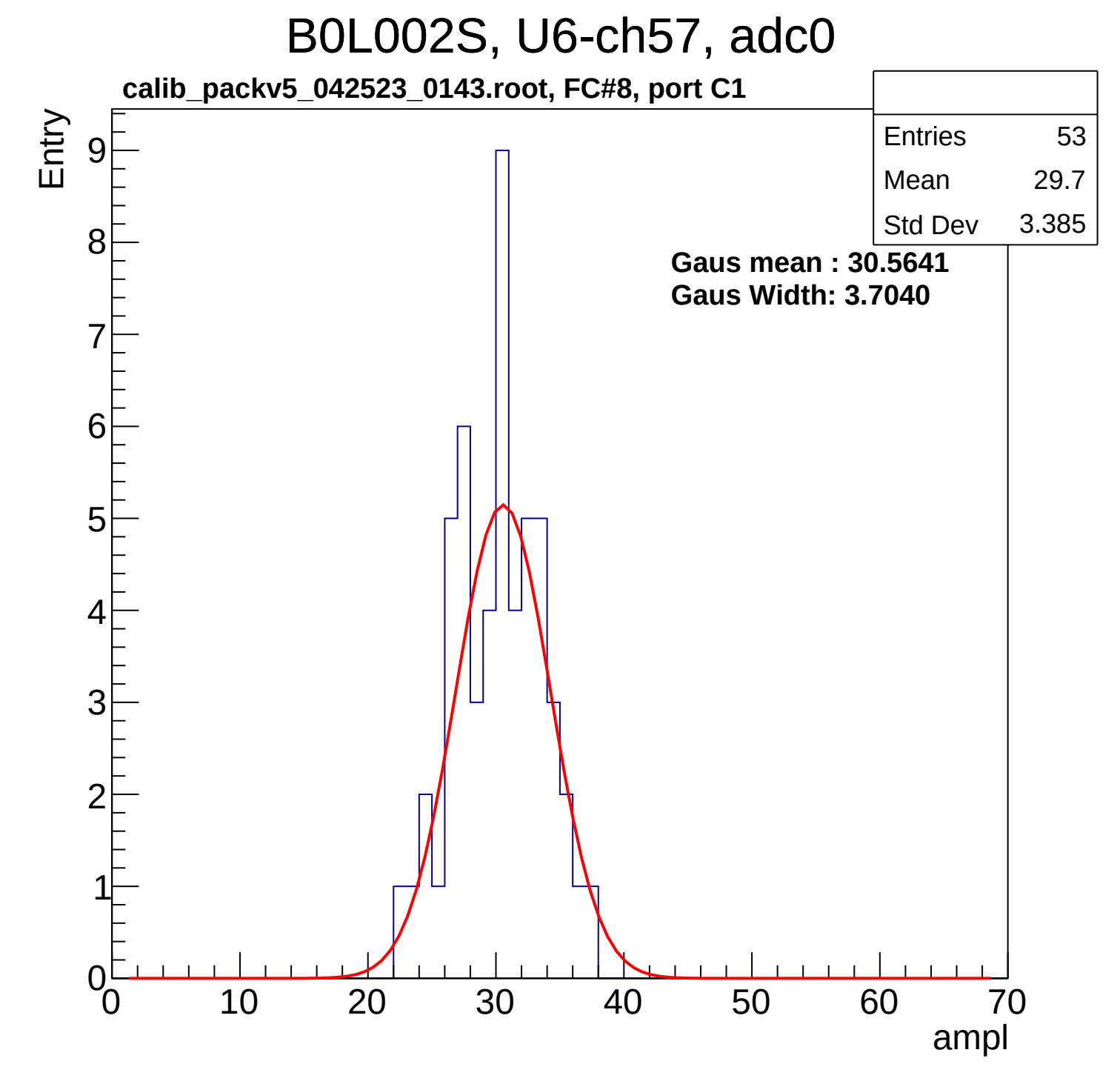
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

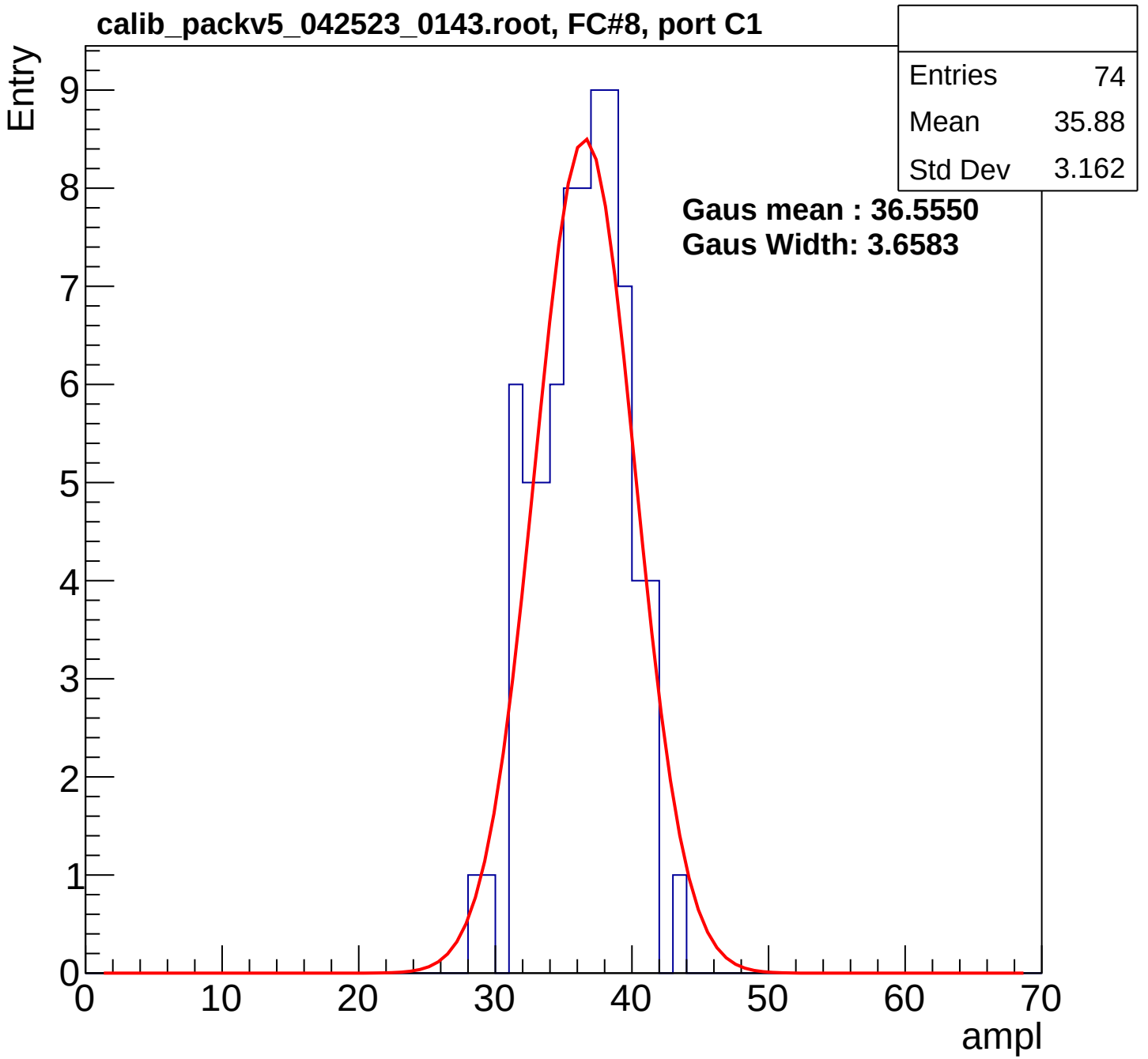

B0L002S, U6-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.13
Std Dev 2.764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

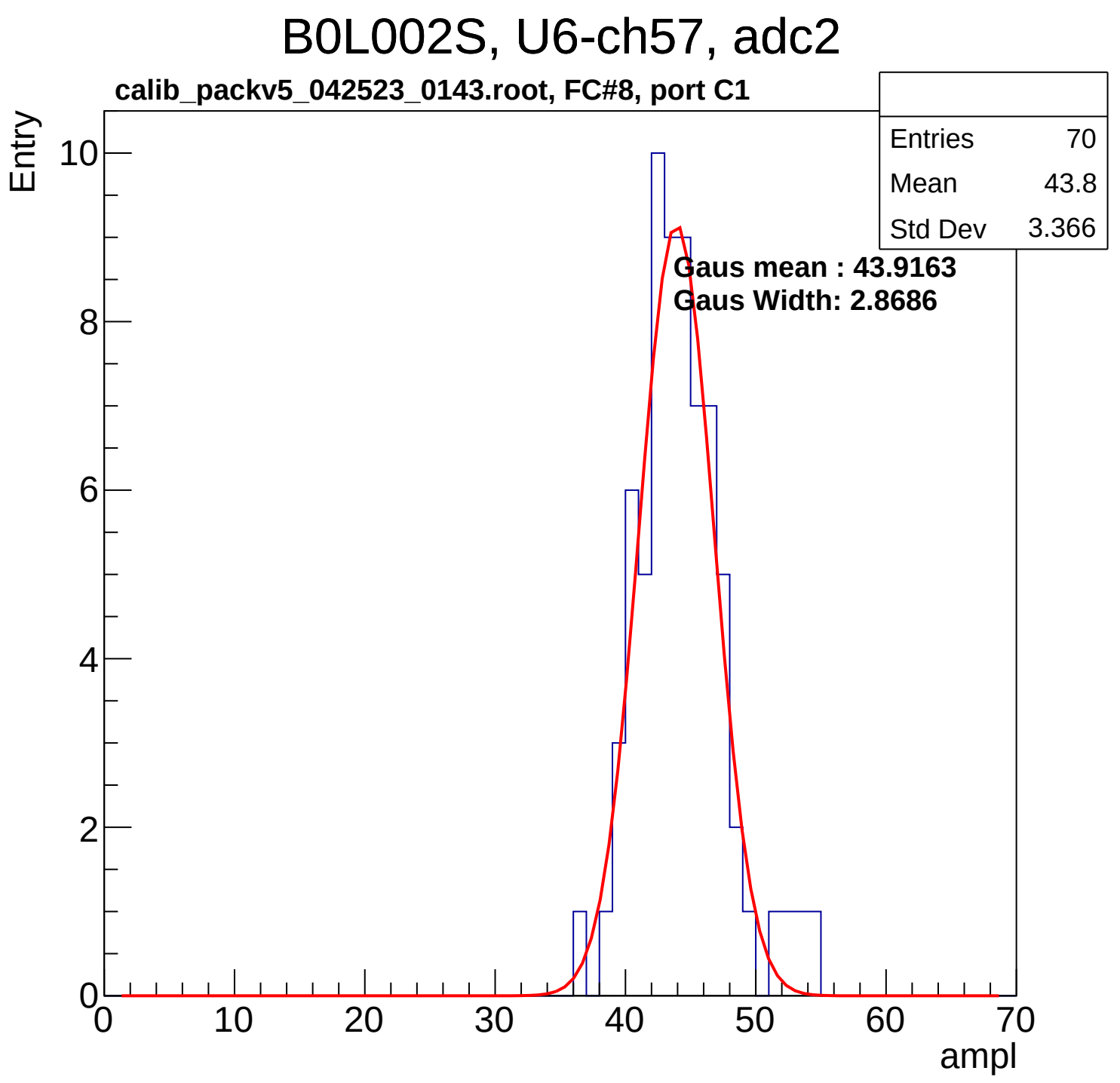

B0L002S, U6-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.92
Std Dev 10.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

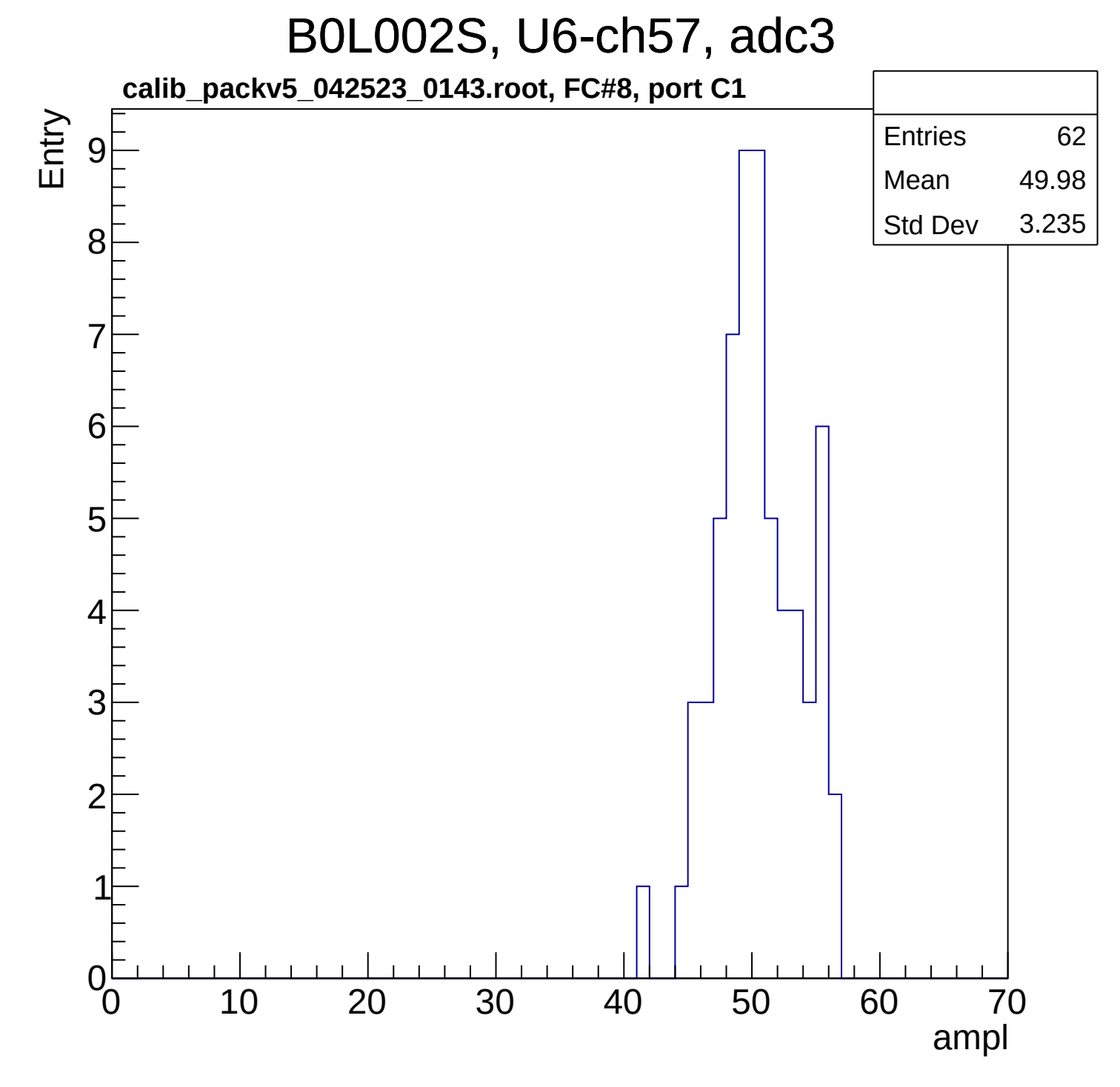

B0L002S, U6-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 29.7
Std Dev 3.385
Gaus mean : 30.5641
Gaus Width: 3.7040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 35.88
Std Dev 3.162
Gaus mean : 36.5550
Gaus Width: 3.6583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.8
Std Dev 3.366
Gaus mean : 43.9163
Gaus Width: 2.8686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 49.98
Std Dev 3.235
Entry
ampl
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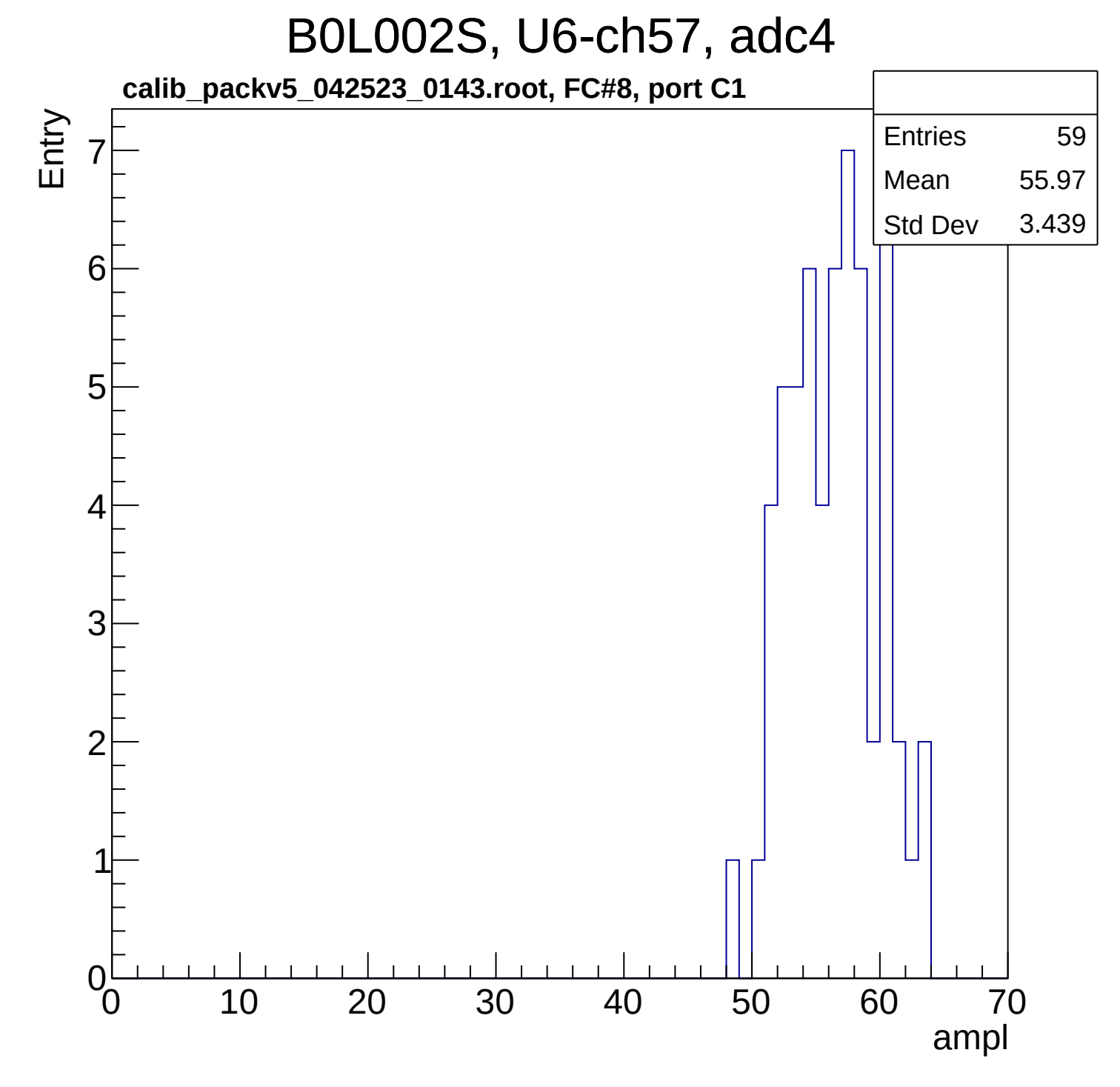
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.97
Std Dev 3.439
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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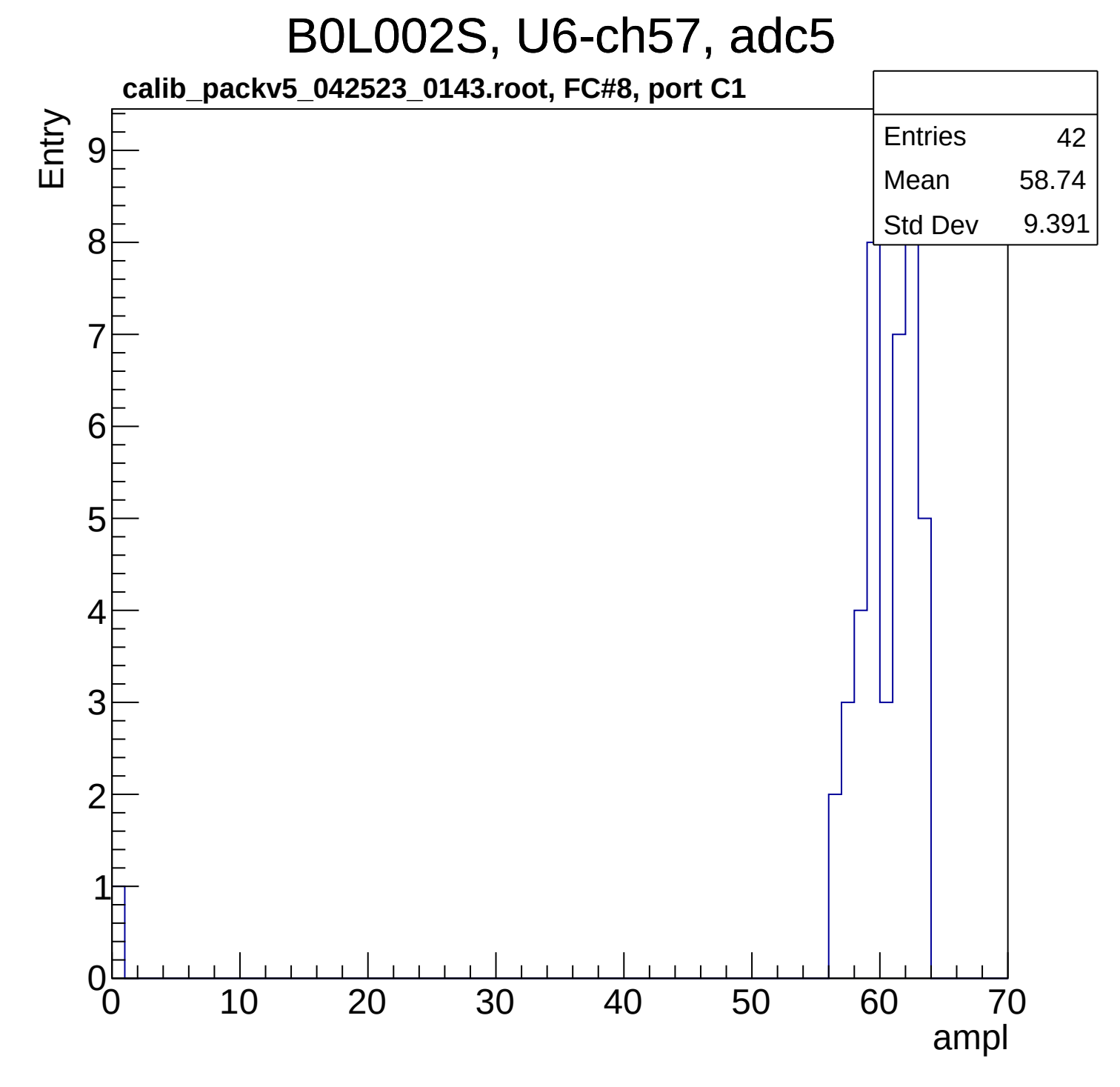
B0L002S, U6-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.74
Std Dev 9.391
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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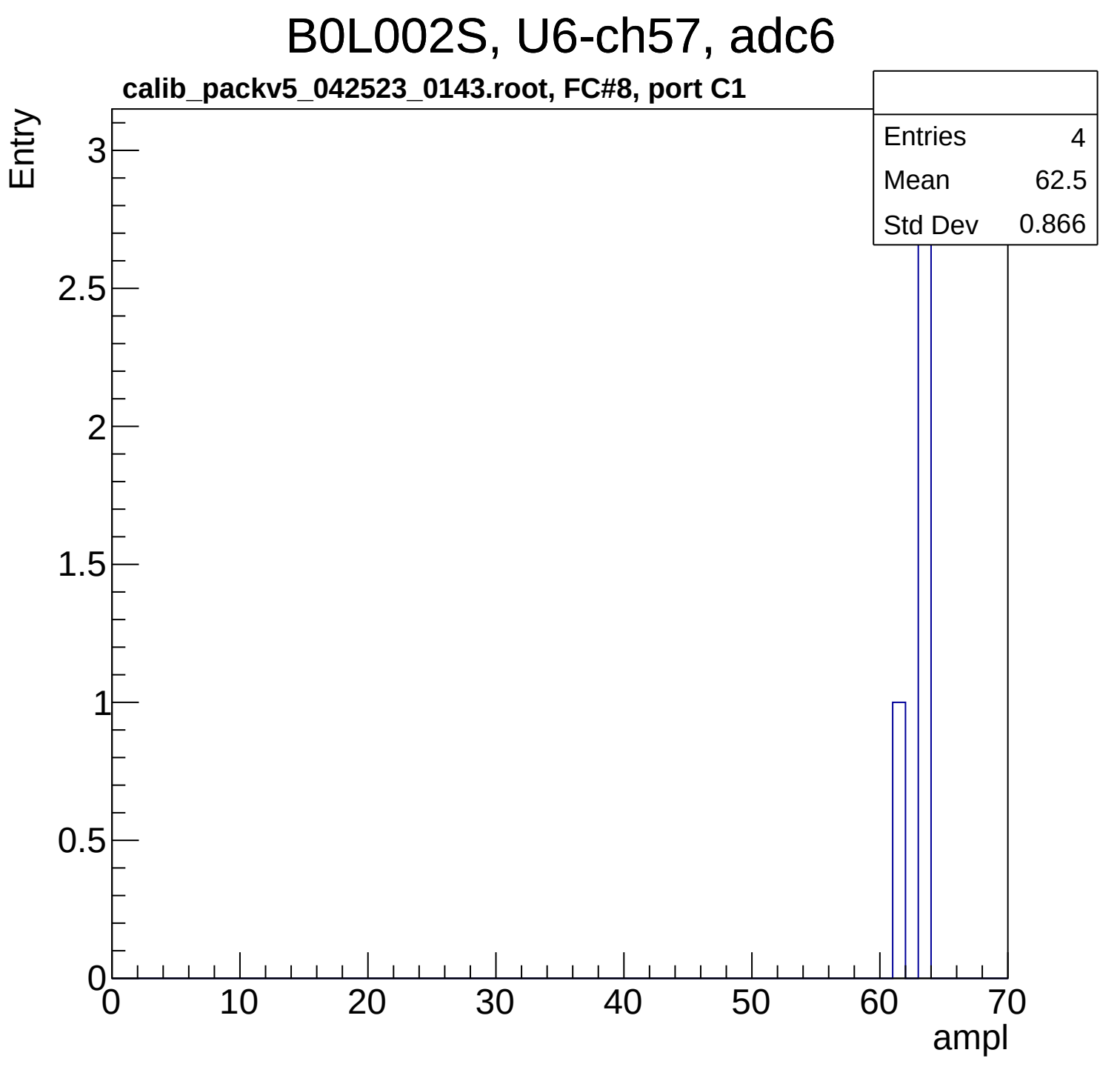
B0L002S, U6-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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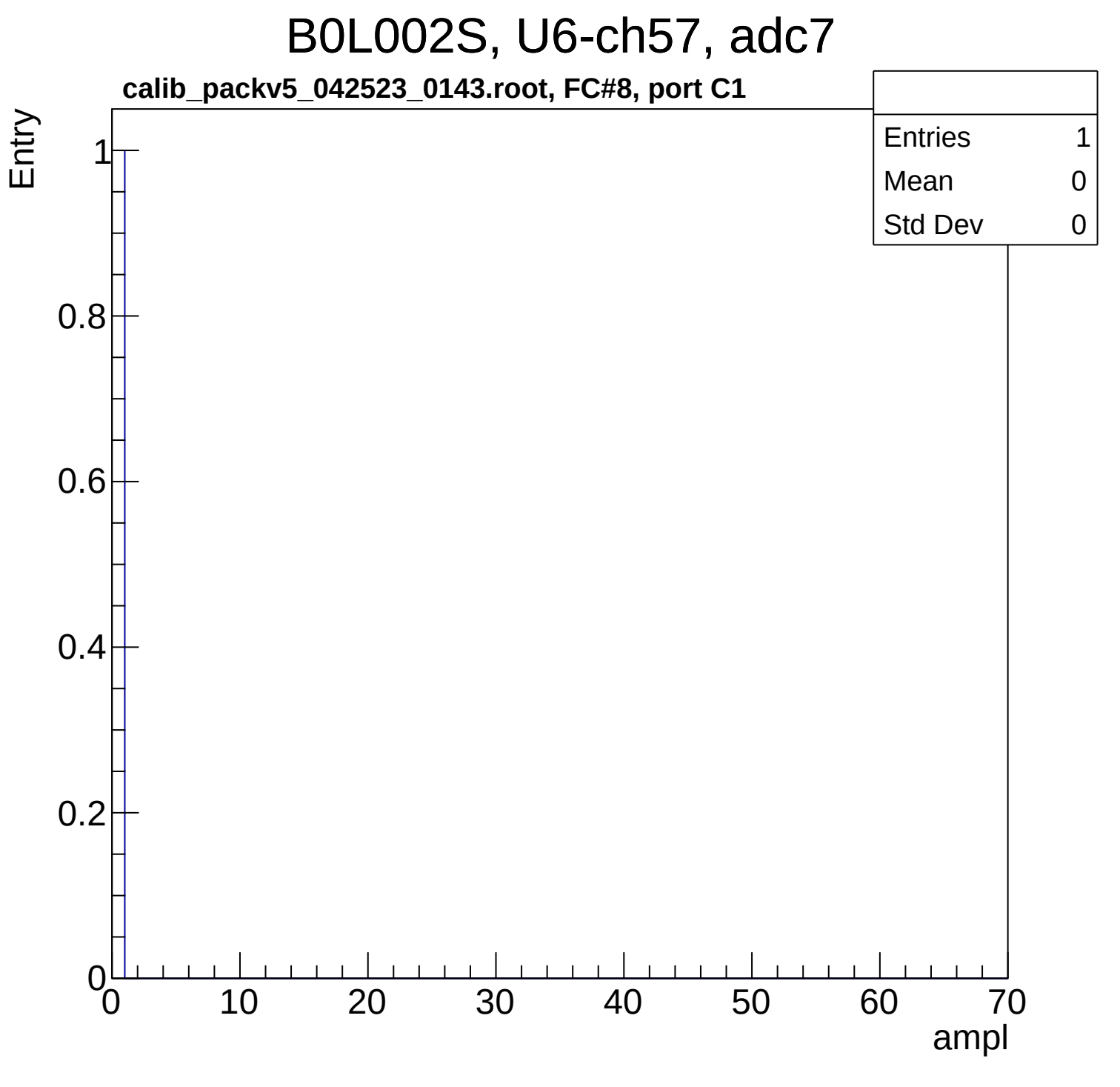
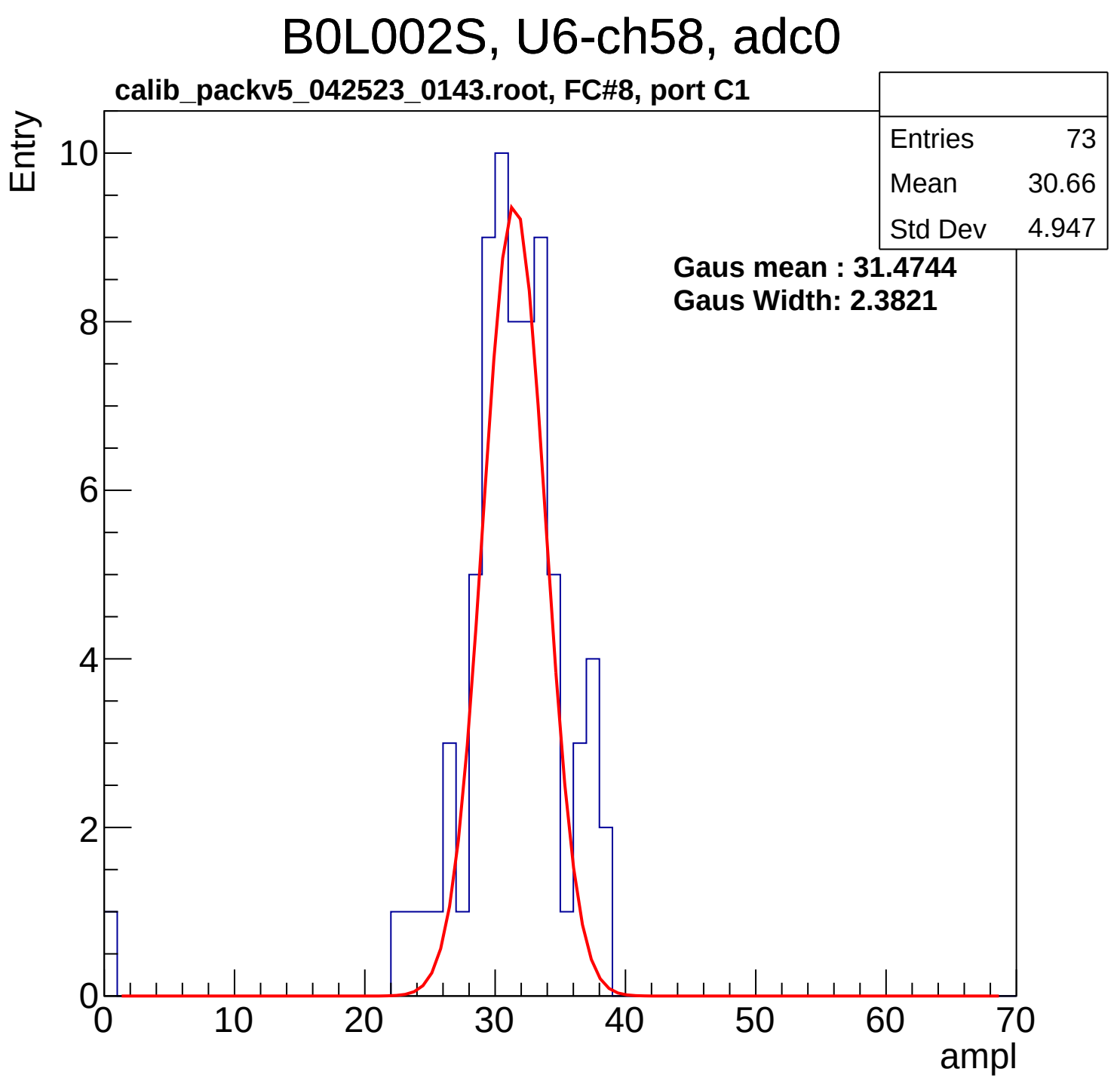
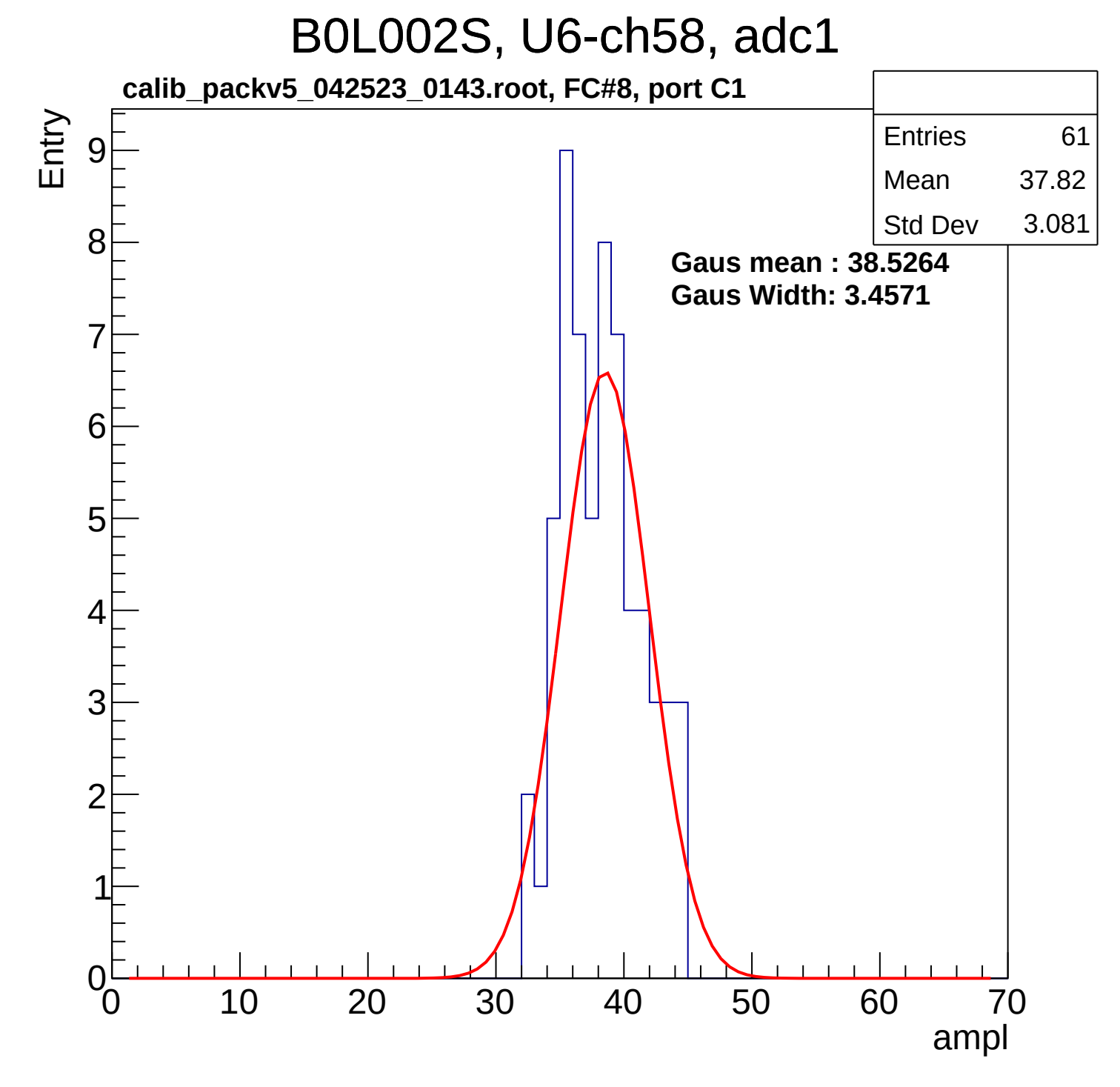
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.66
Std Dev 4.947
Gaus mean : 31.4744
Gaus Width: 2.3821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.82
Std Dev 3.081
Gaus mean : 38.5264
Gaus Width: 3.4571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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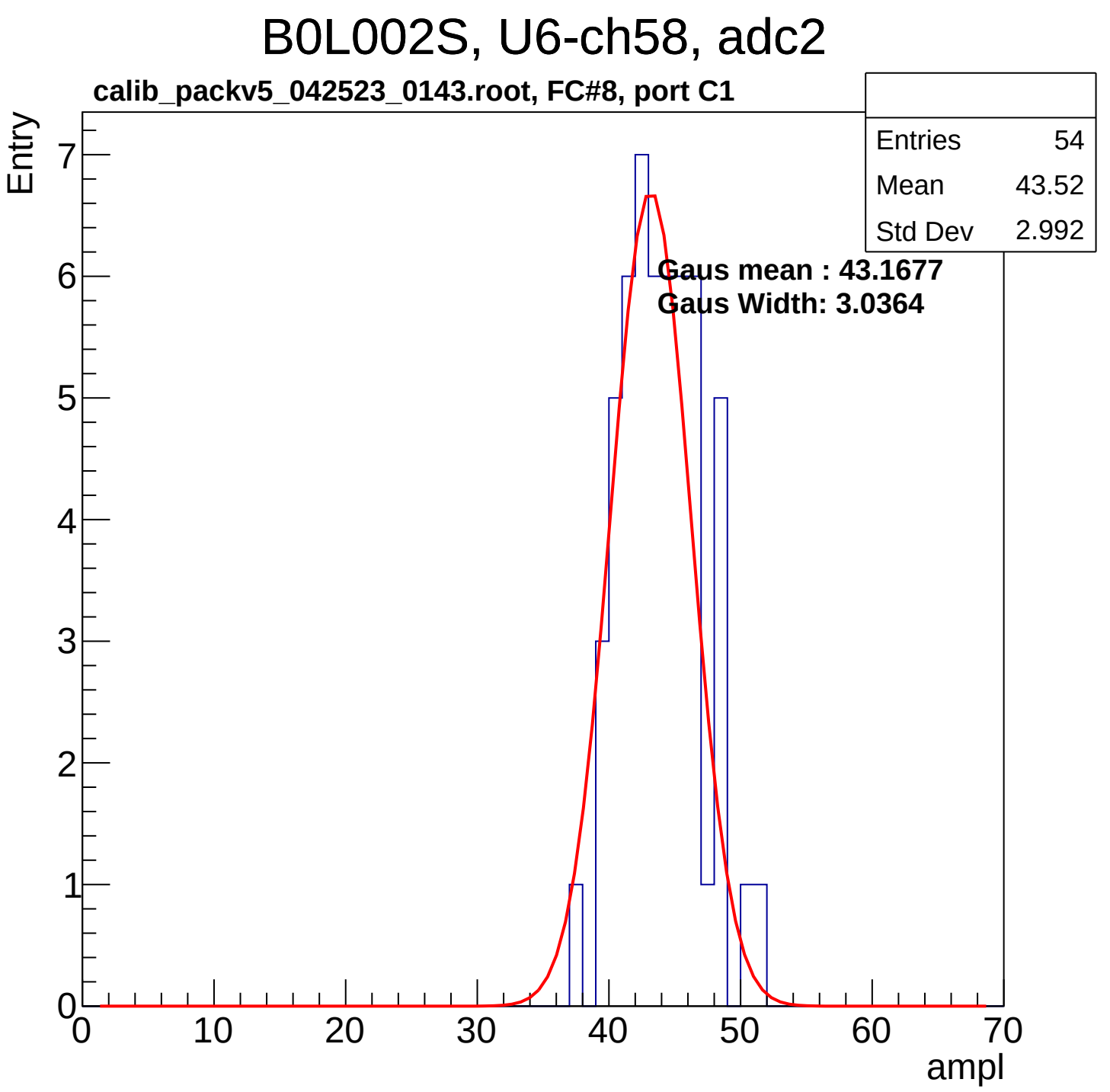
B0L002S, U6-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 43.52
Std Dev 2.992
Gaus mean : 43.1677
Gaus Width: 3.0364
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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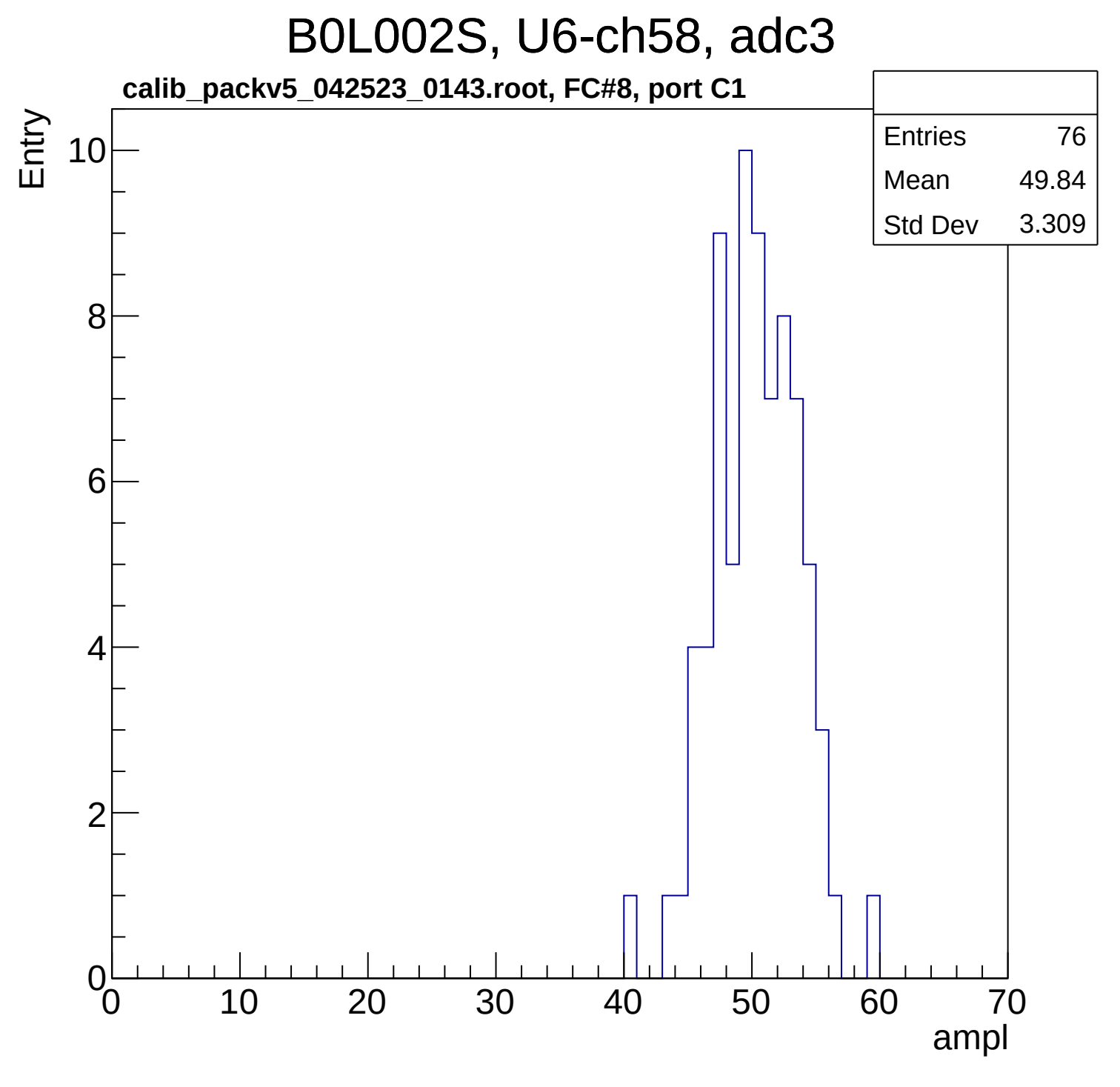
B0L002S, U6-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 49.84
Std Dev 3.309
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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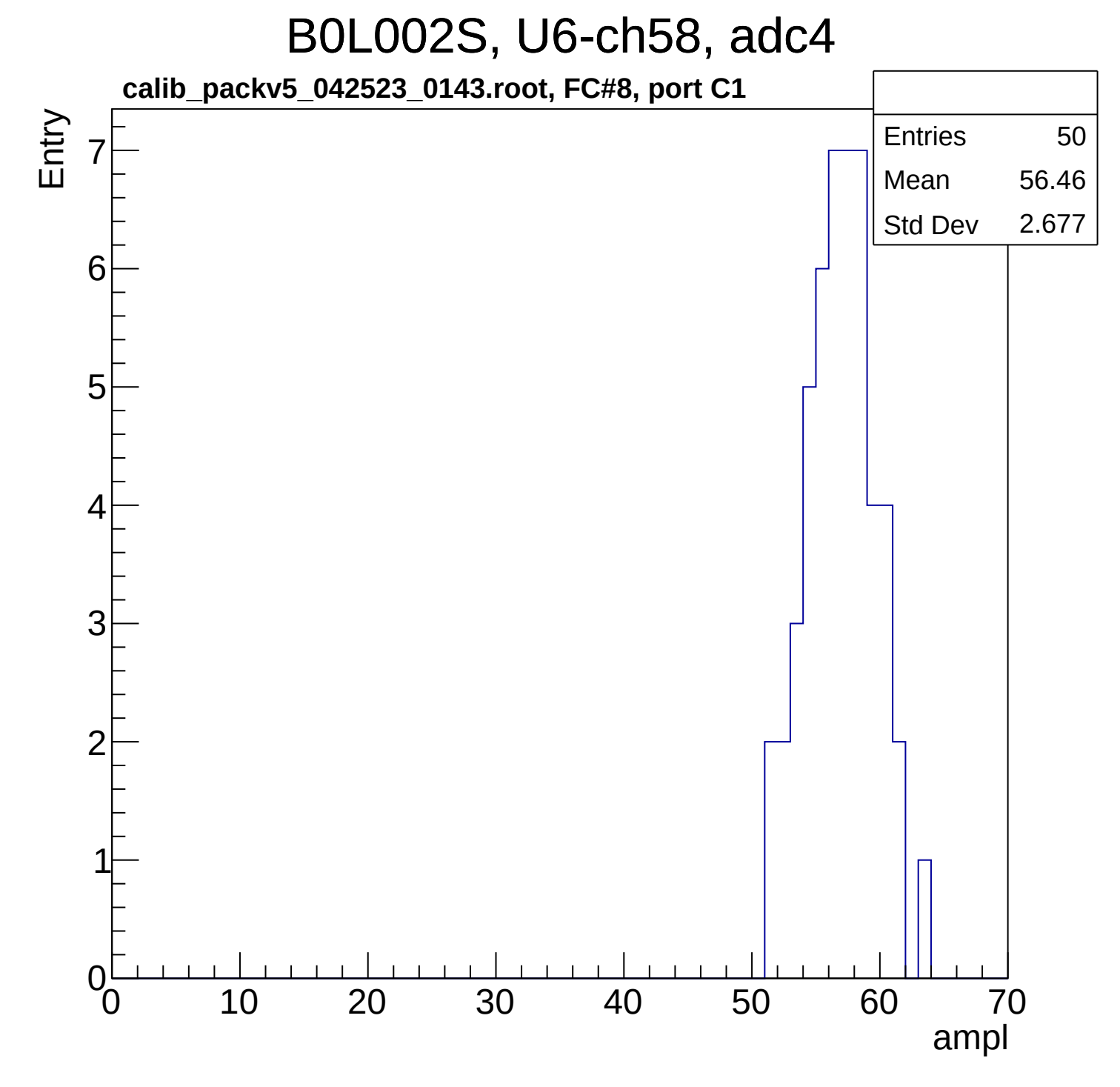
B0L002S, U6-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.46
Std Dev 2.677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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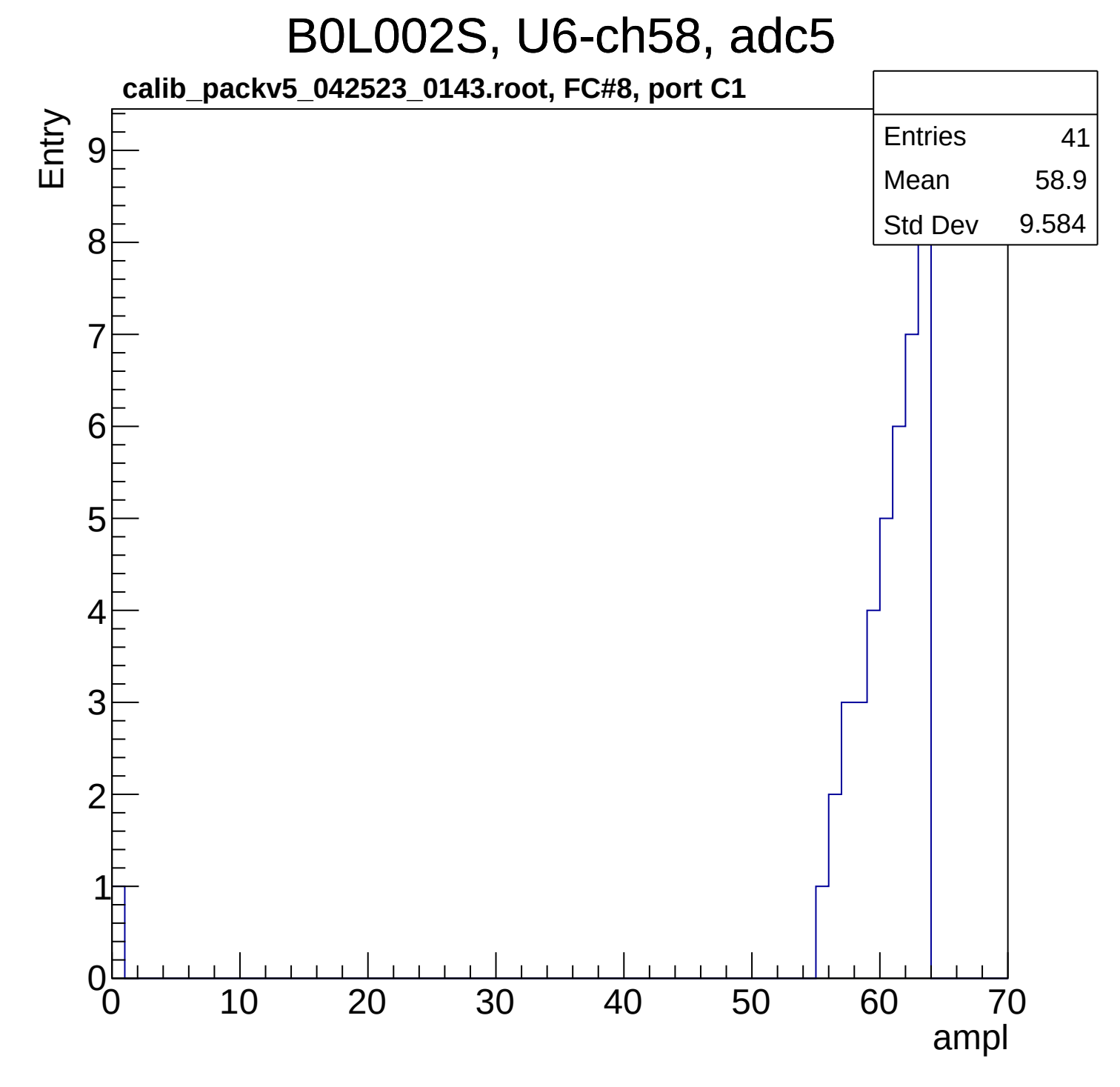
B0L002S, U6-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.9
Std Dev 9.584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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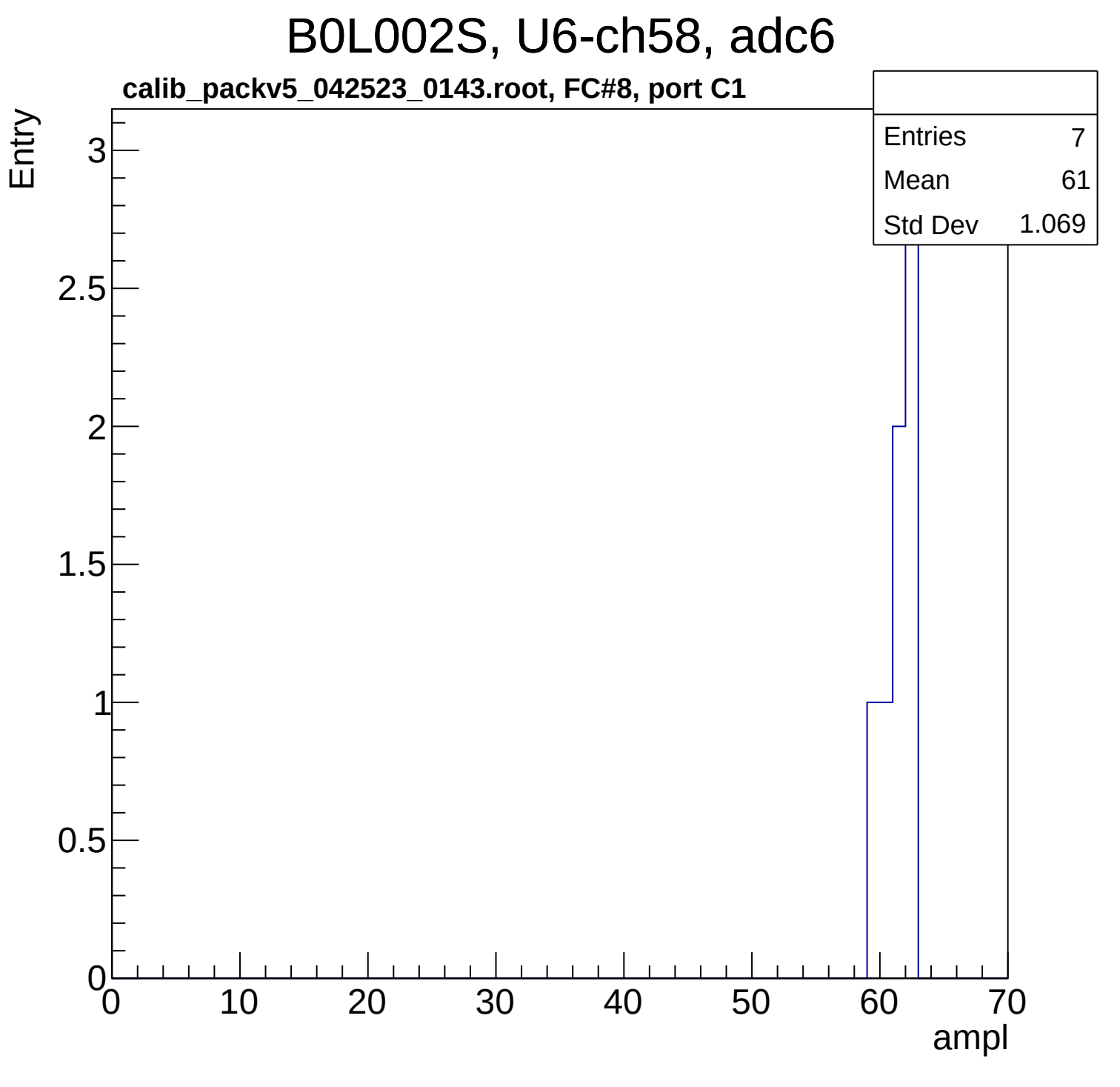
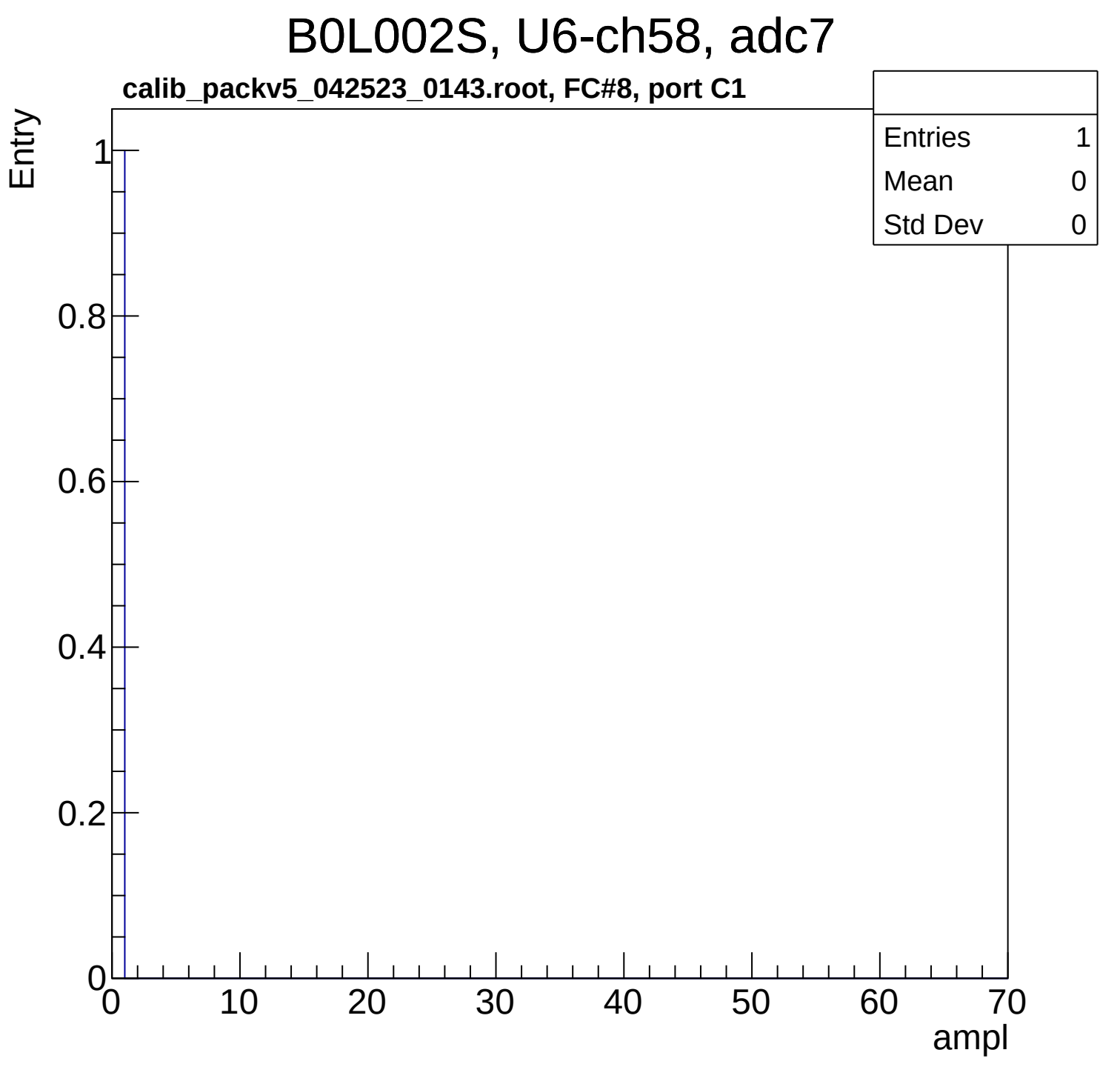
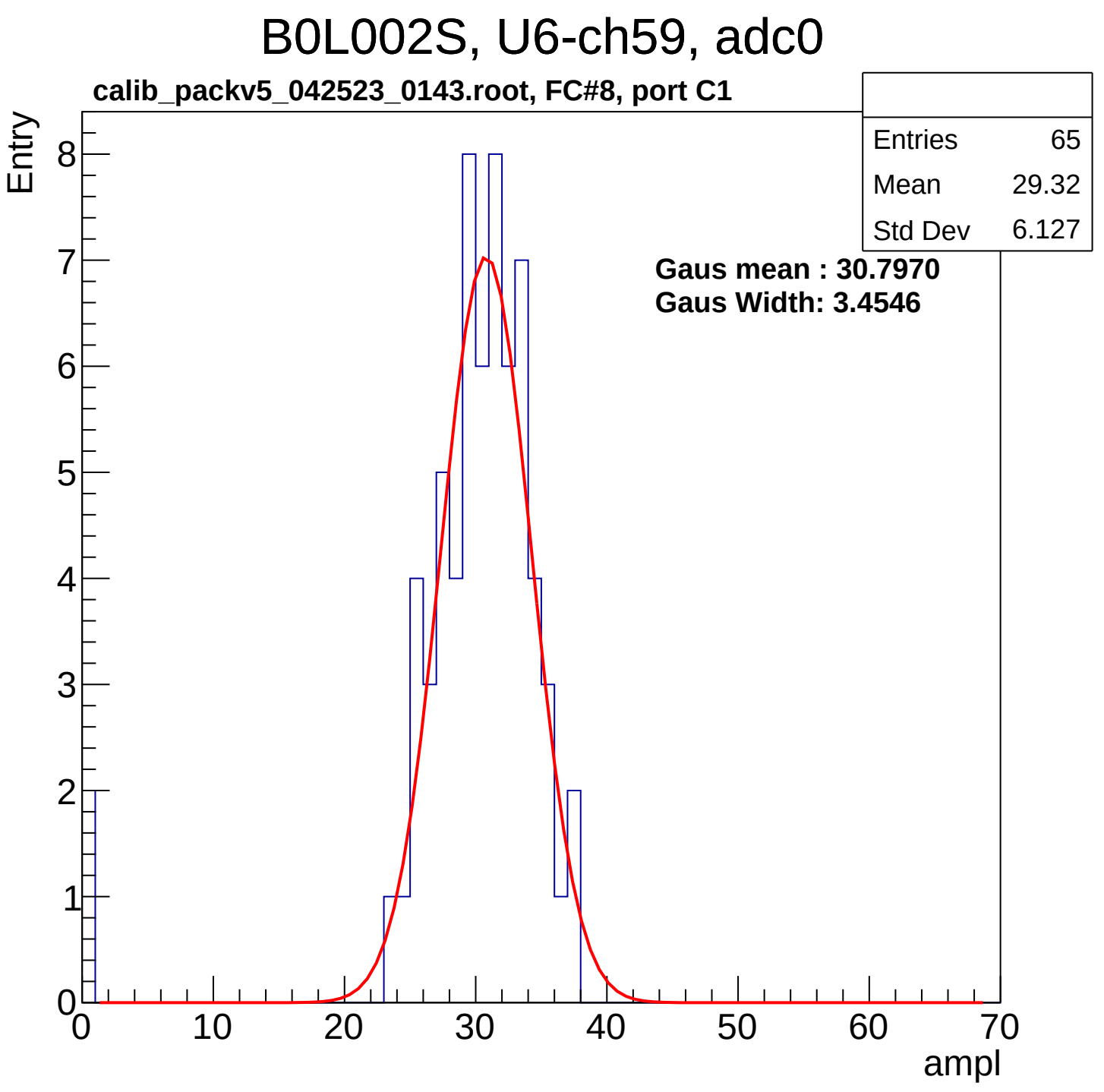
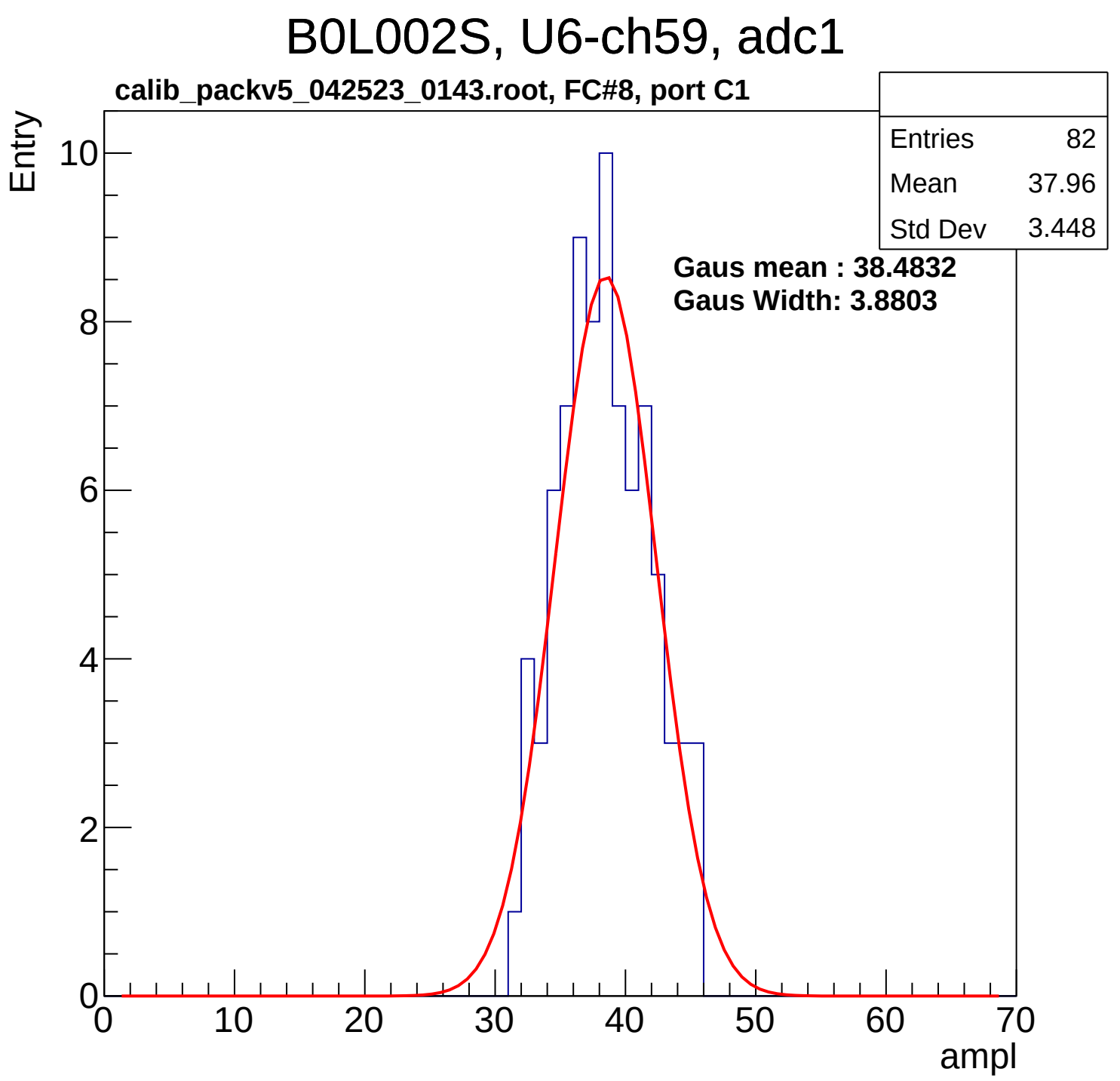
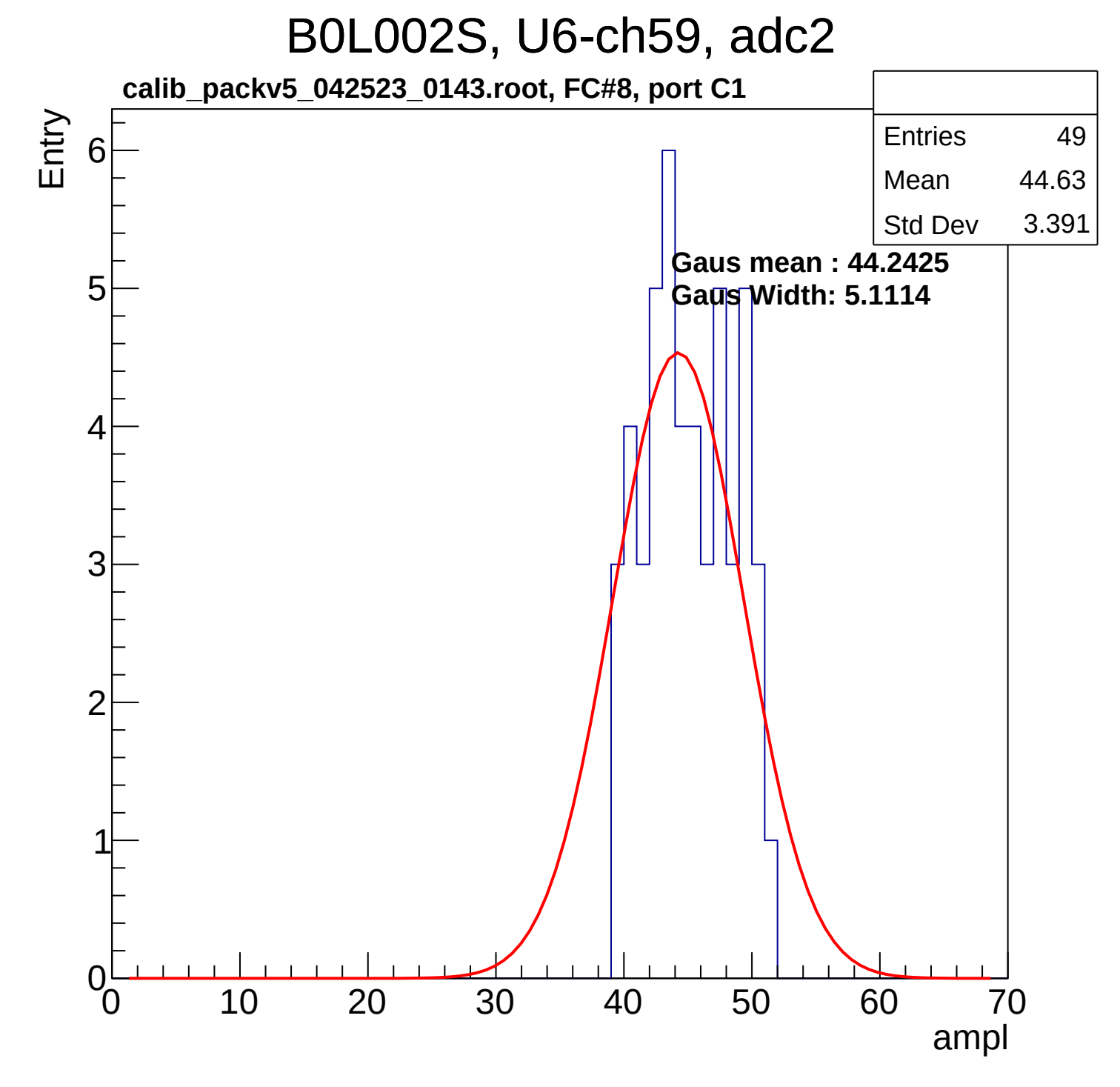
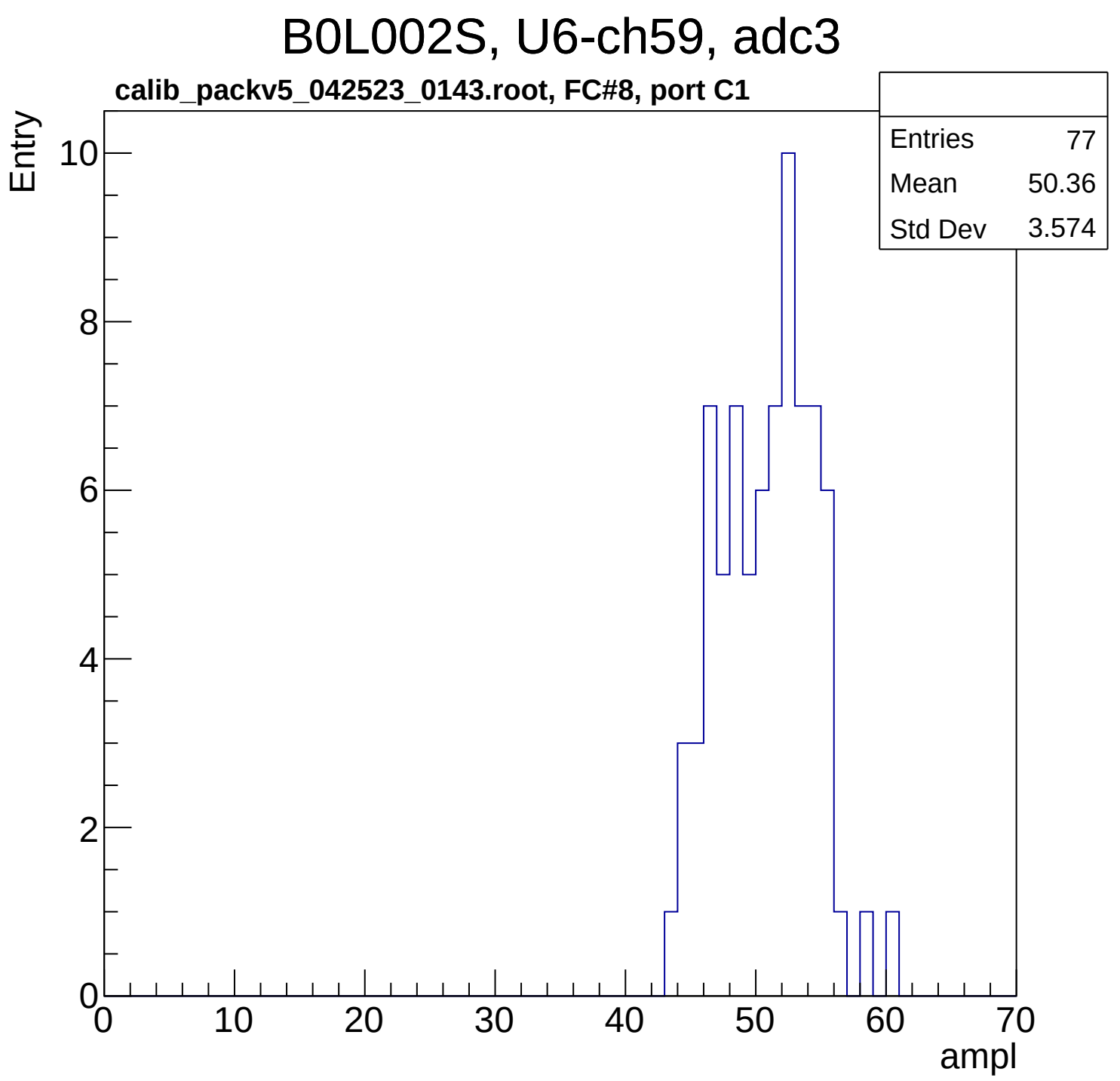

B0L002S, U6-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 61
Std Dev 1.069
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

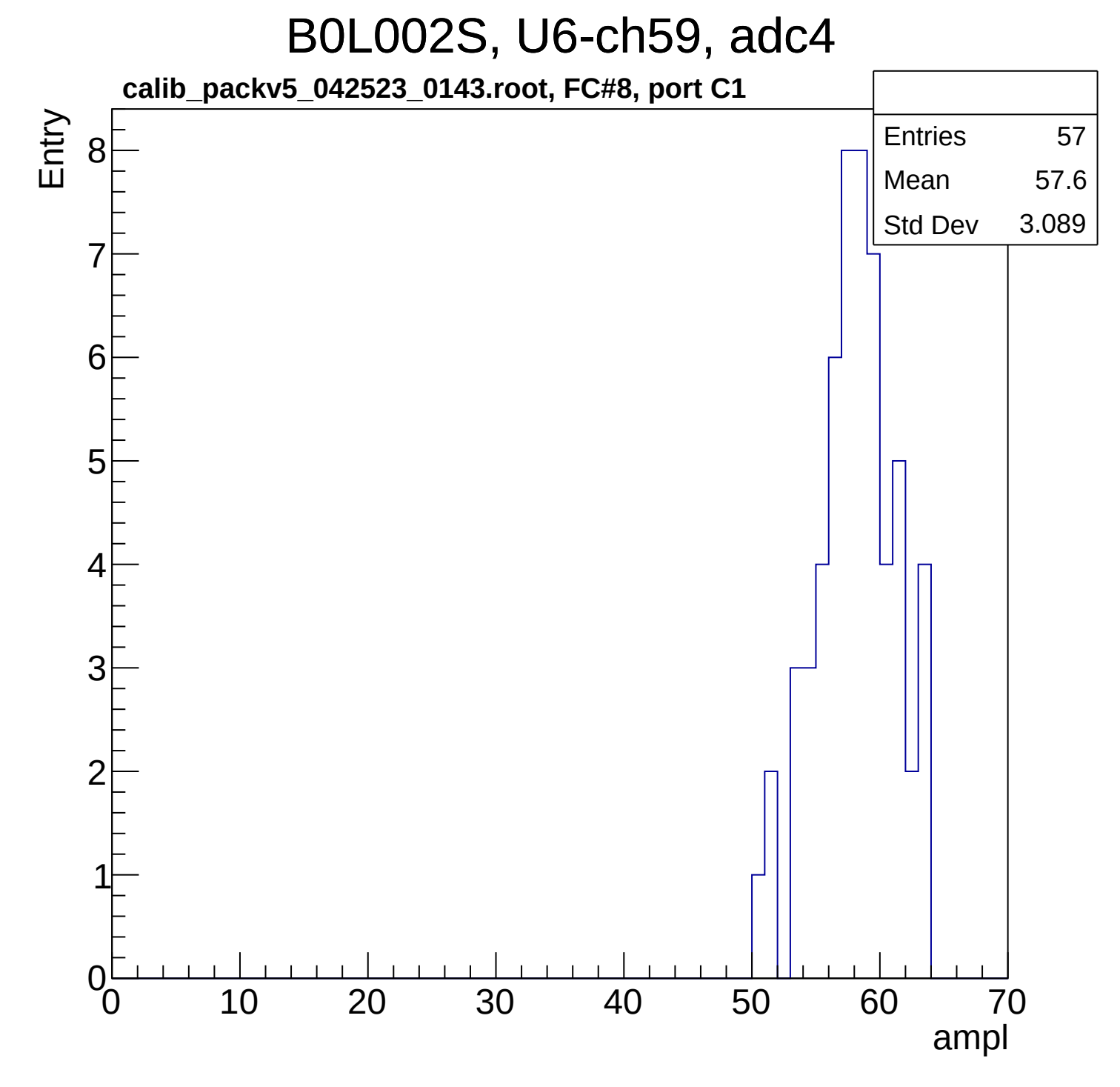

B0L002S, U6-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

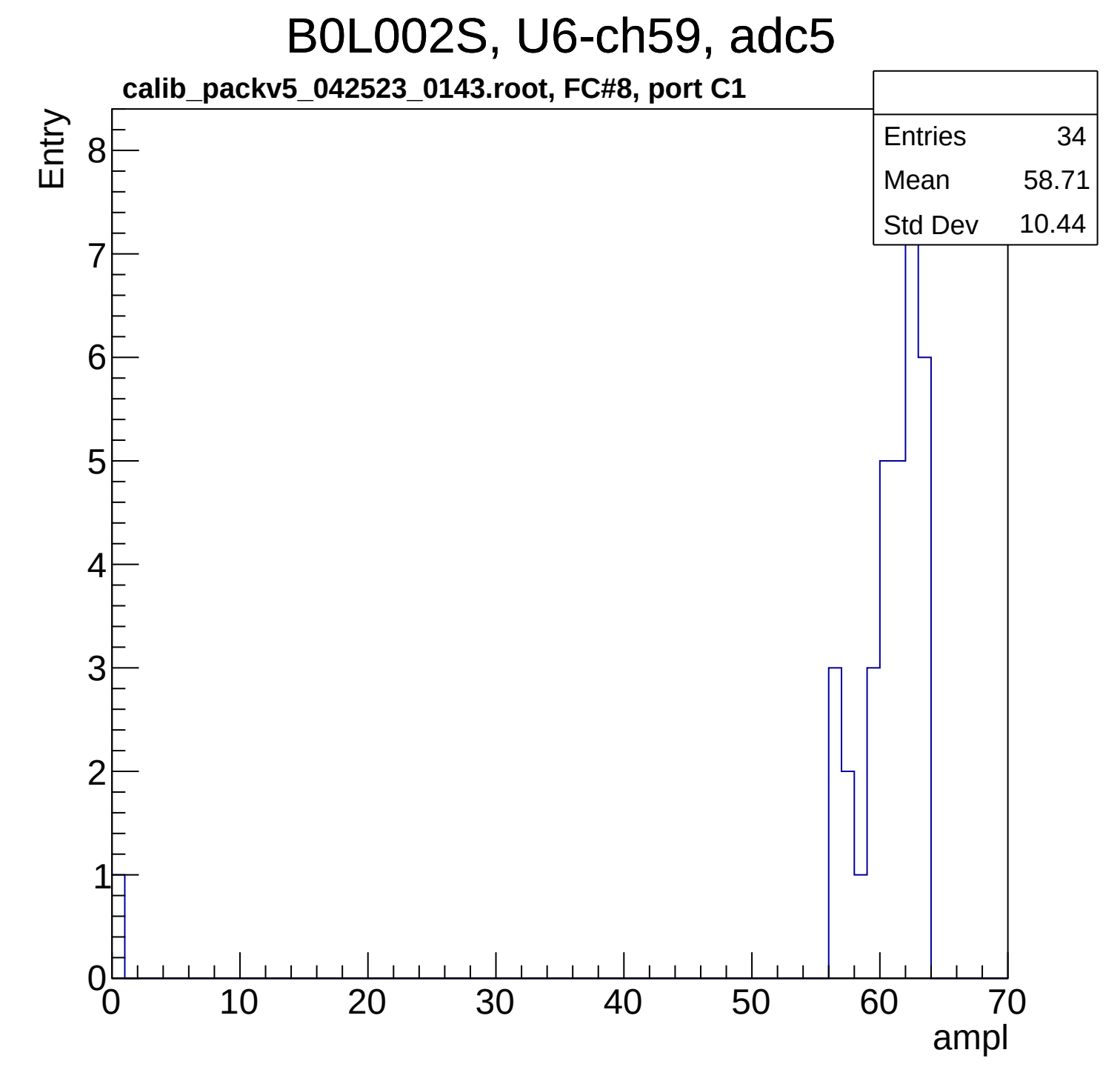

B0L002S, U6-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.32
Std Dev 6.127
Gaus mean : 30.7970
Gaus Width: 3.4546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

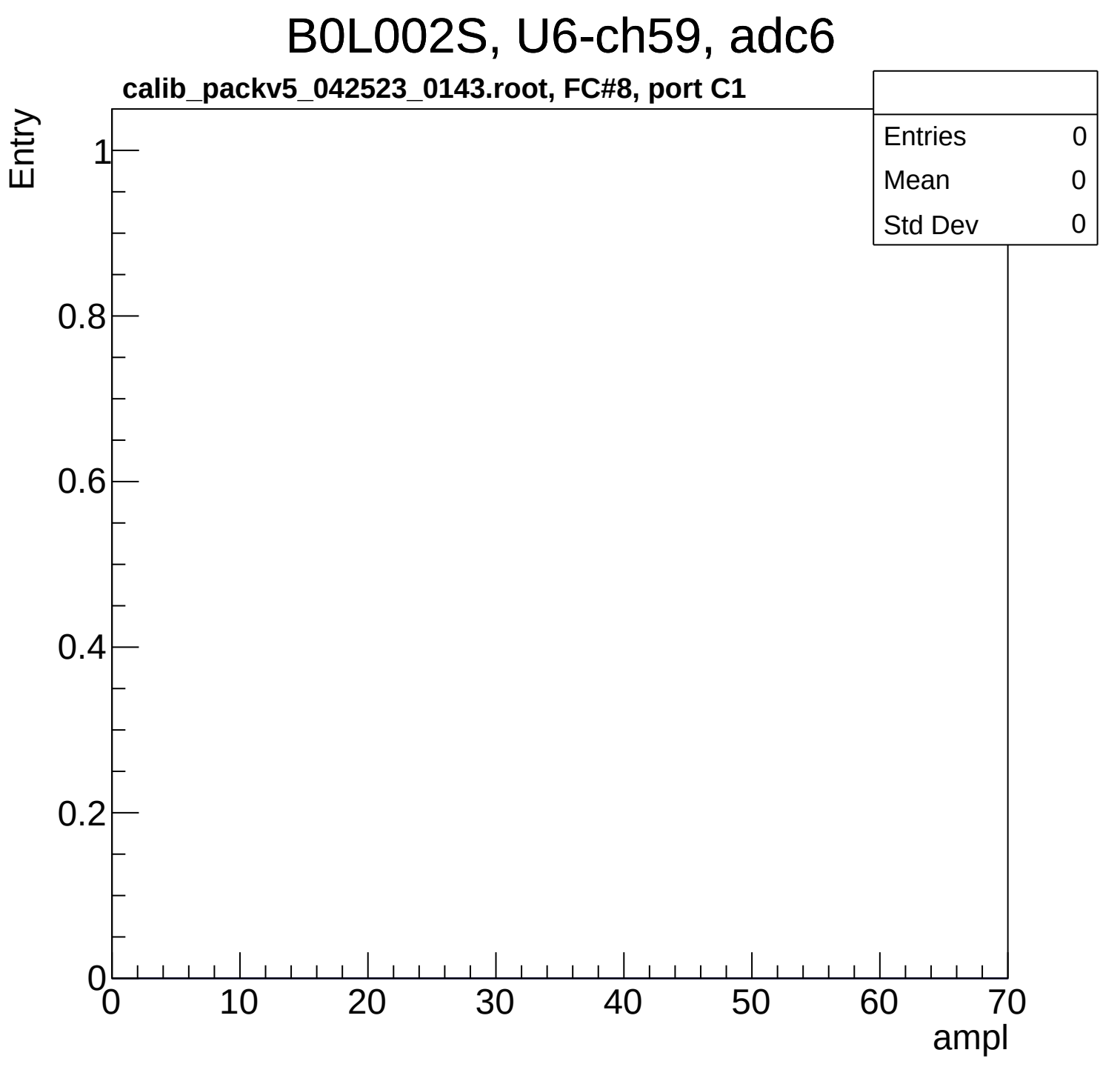

B0L002S, U6-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 37.96
Std Dev 3.448
Gaus mean : 38.4832
Gaus Width: 3.8803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

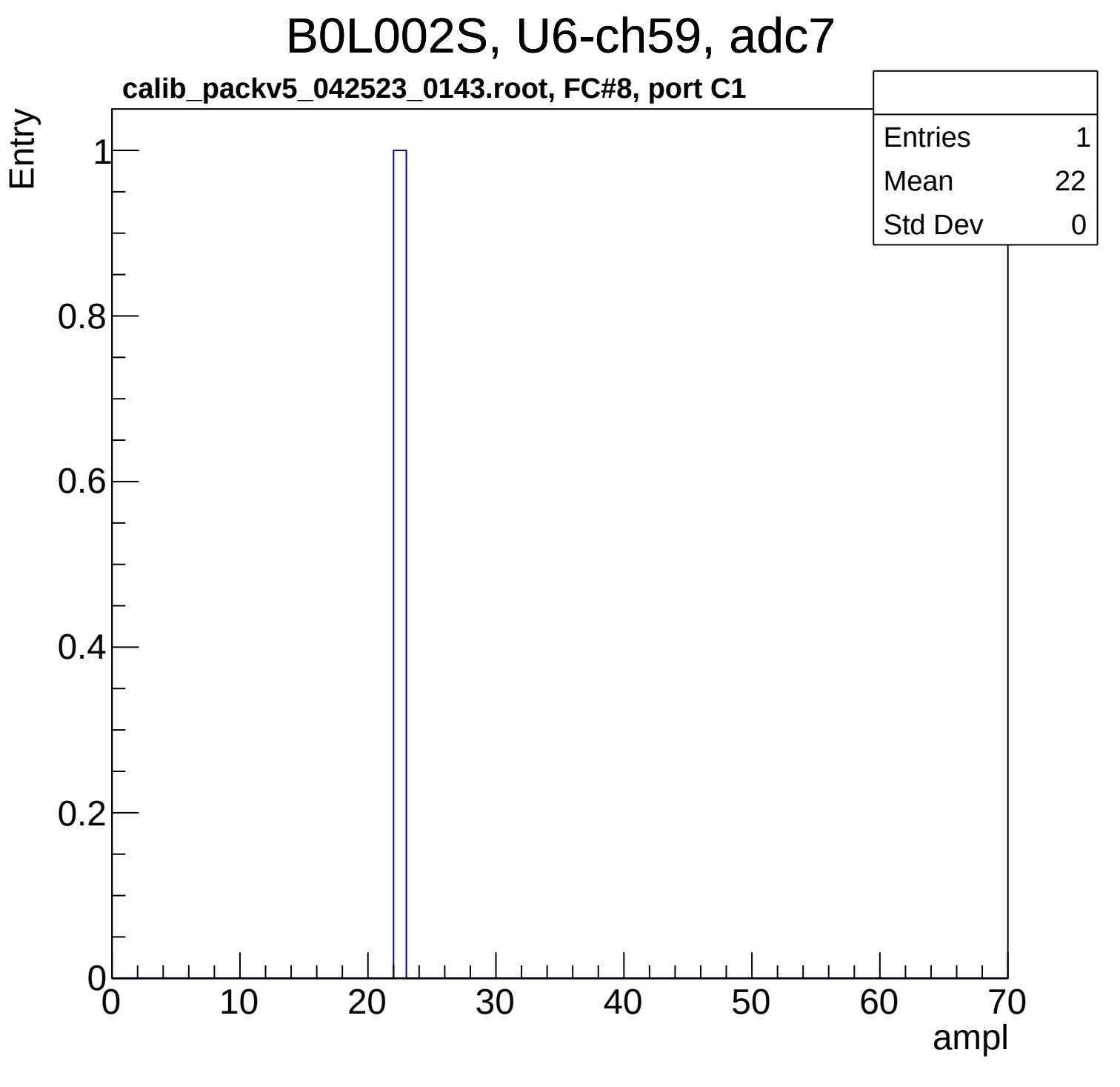

B0L002S, U6-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 44.63
Std Dev 3.391
Gaus mean : 44.2425
Gaus Width: 5.1114
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U6-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 50.36
Std Dev 3.574
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.6
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.71
Std Dev 10.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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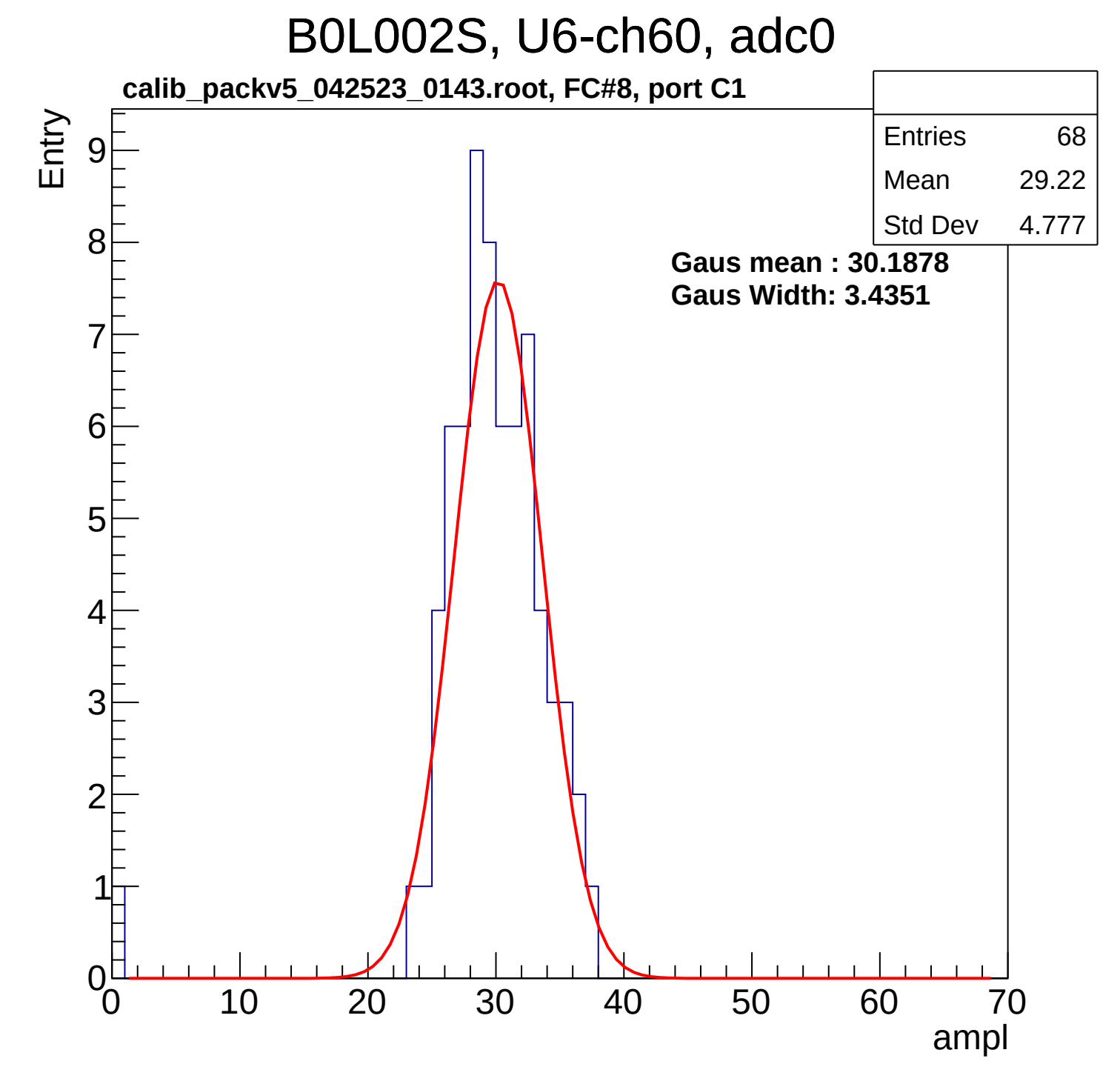
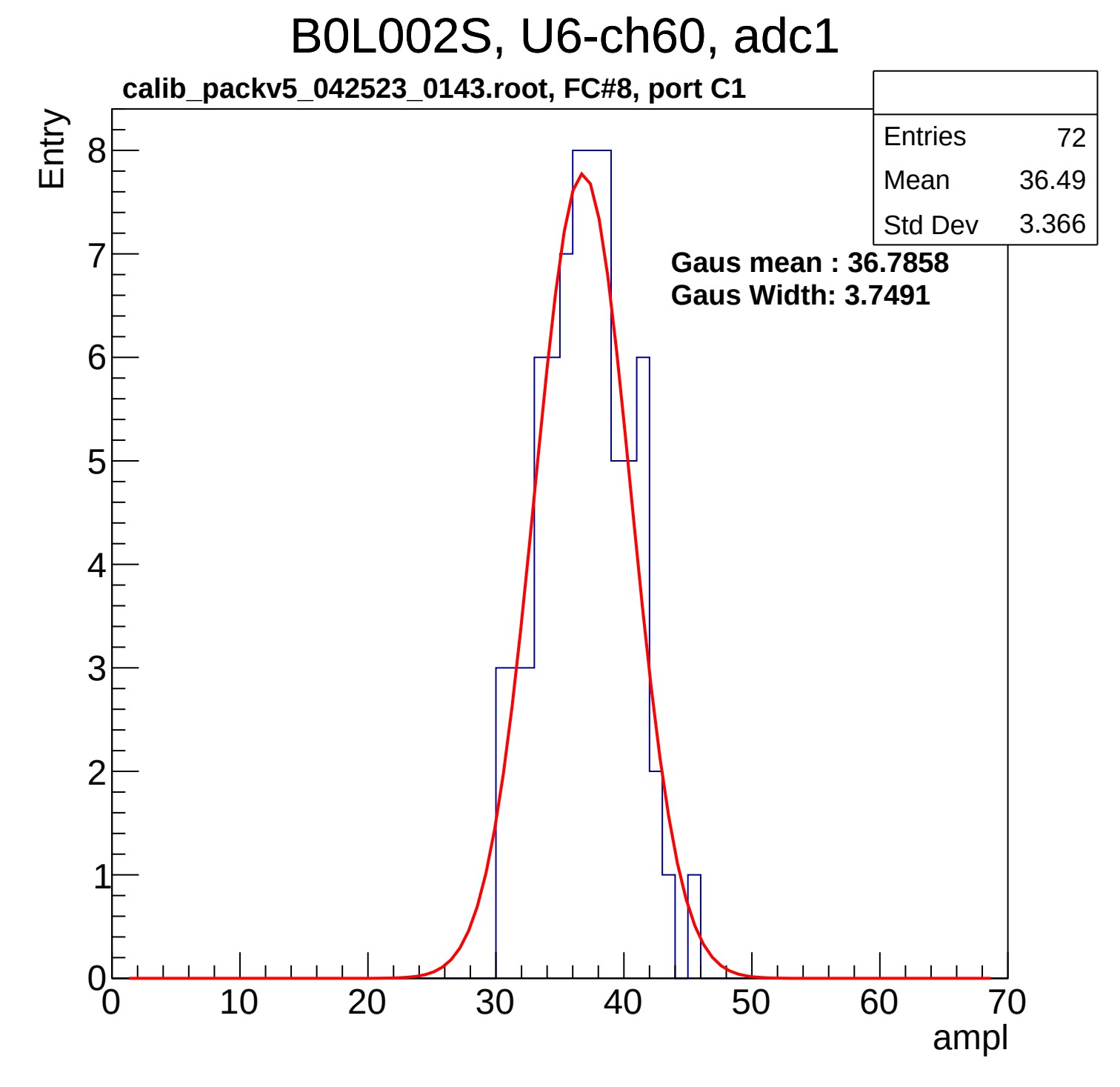
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.22
Std Dev 4.777
Gaus mean : 30.1878
Gaus Width: 3.4351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.49
Std Dev 3.366
Gaus mean : 36.7858
Gaus Width: 3.7491
Entry
ampl
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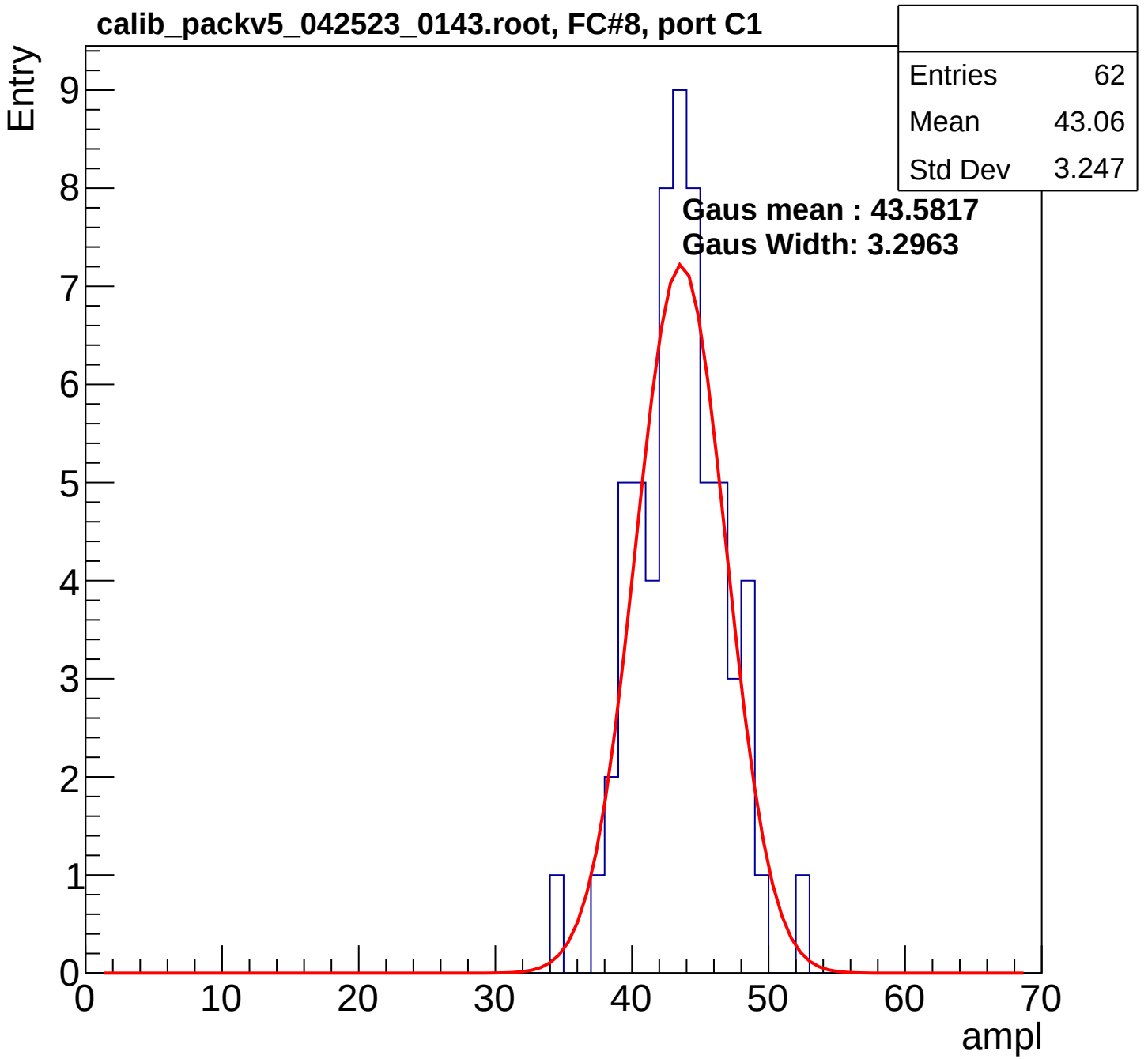
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 43.06
Std Dev 3.247
Gaus mean : 43.5817
Gaus Width: 3.2963
Entry
ampl
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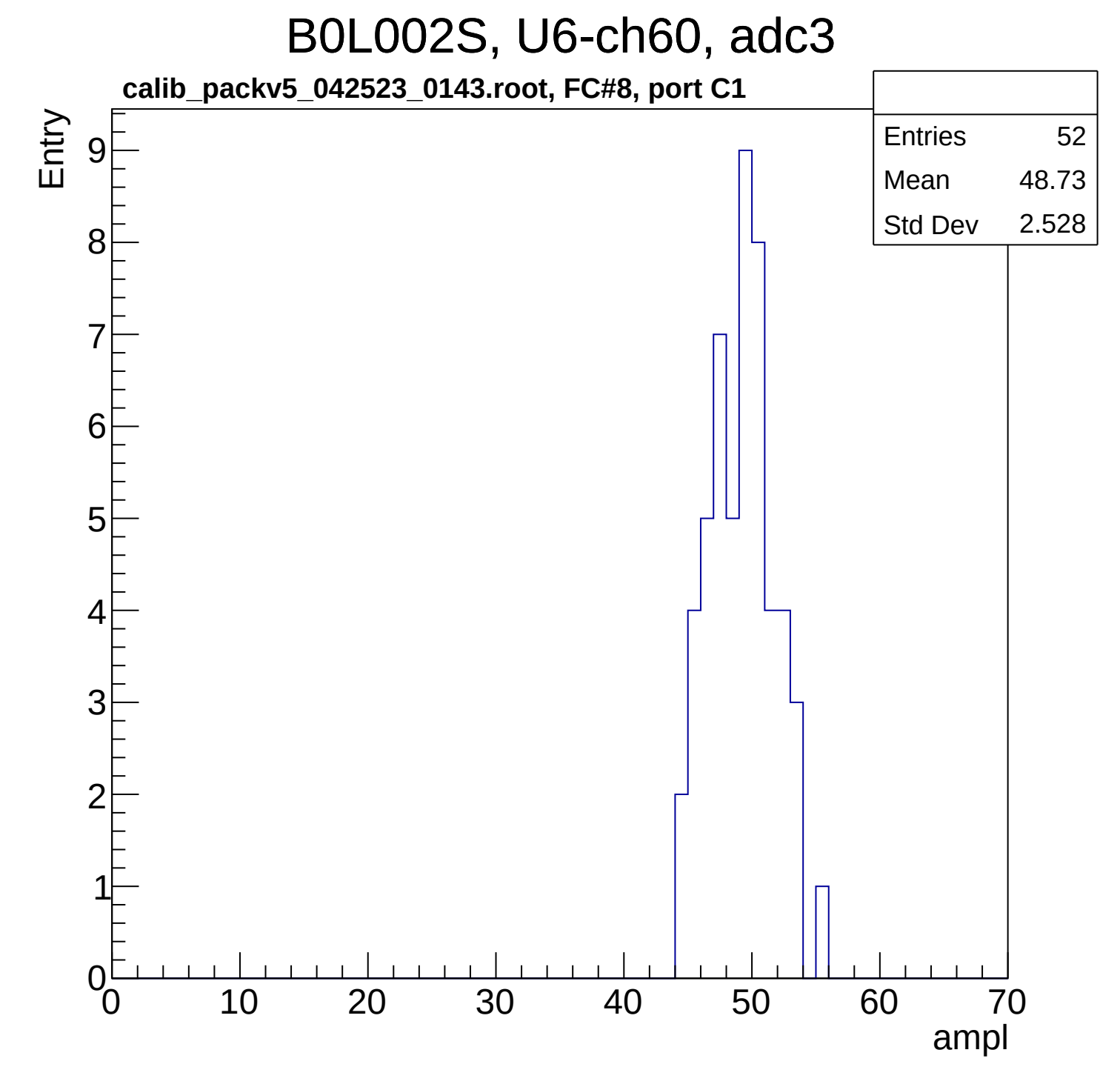
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 48.73
Std Dev 2.528
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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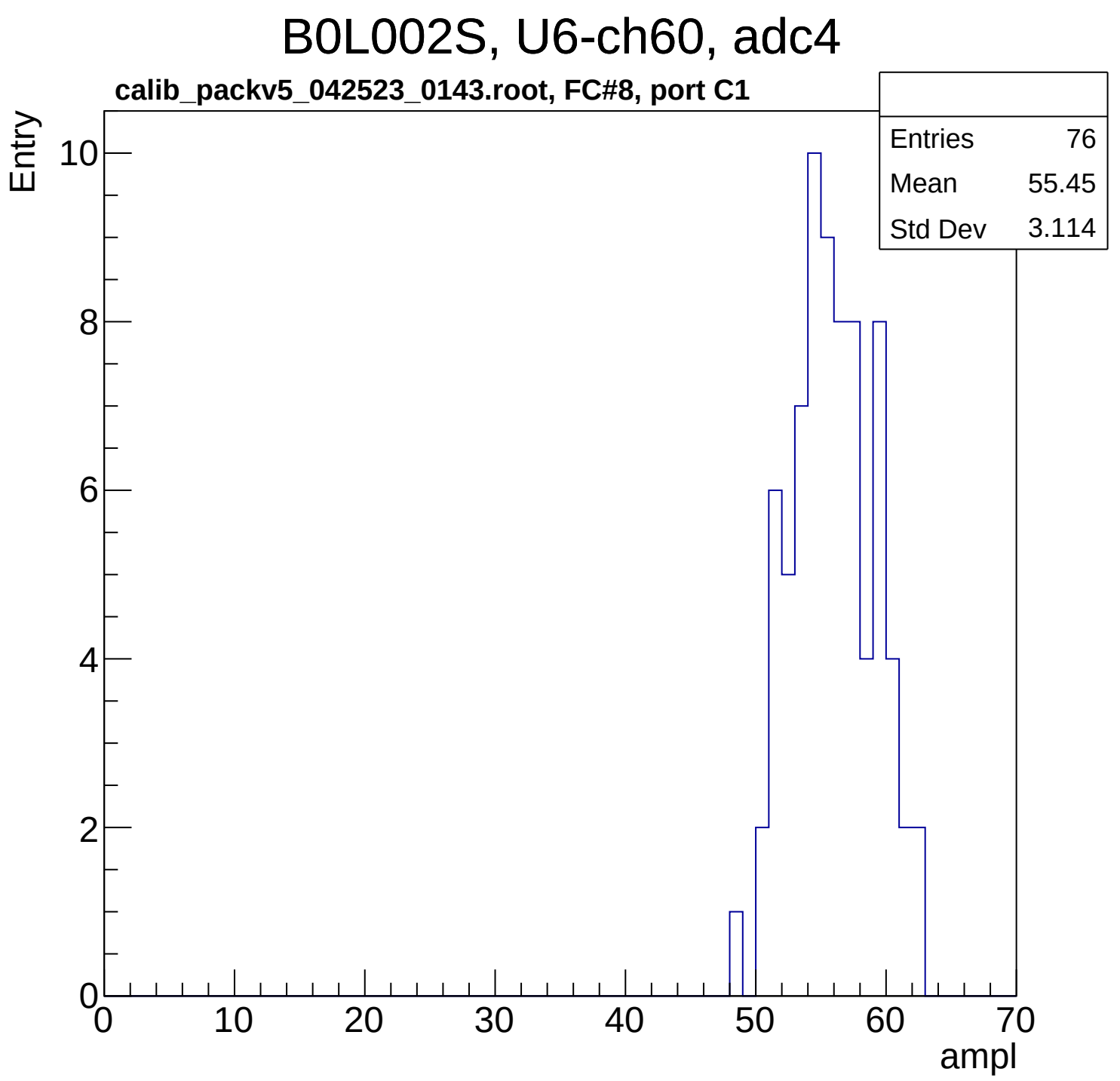
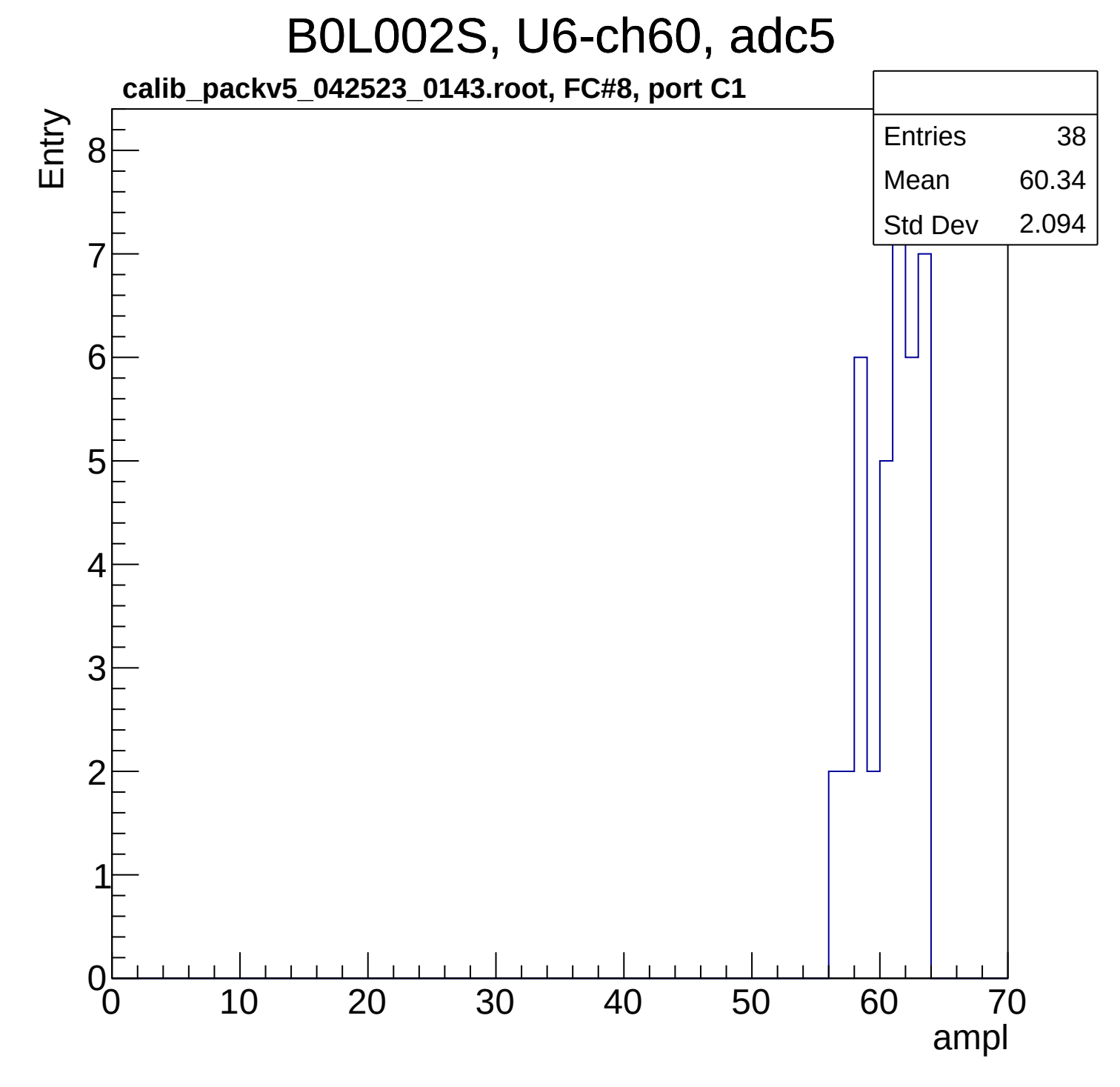
40
50
60
70


B0L002S, U6-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 55.45
Std Dev 3.114
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.34
Std Dev 2.094
Entry
ampl
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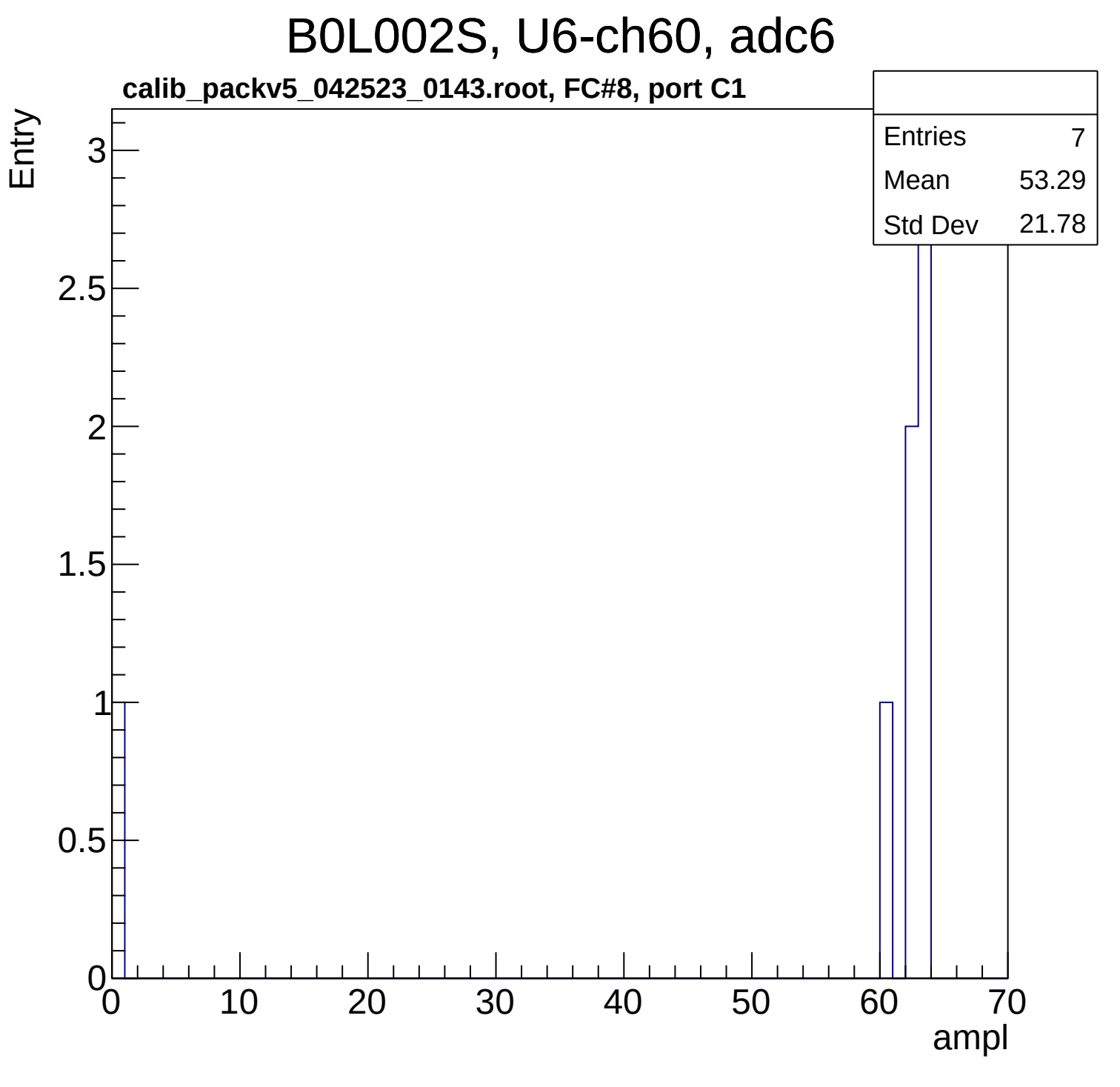
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.29
Std Dev 21.78
Entry
ampl
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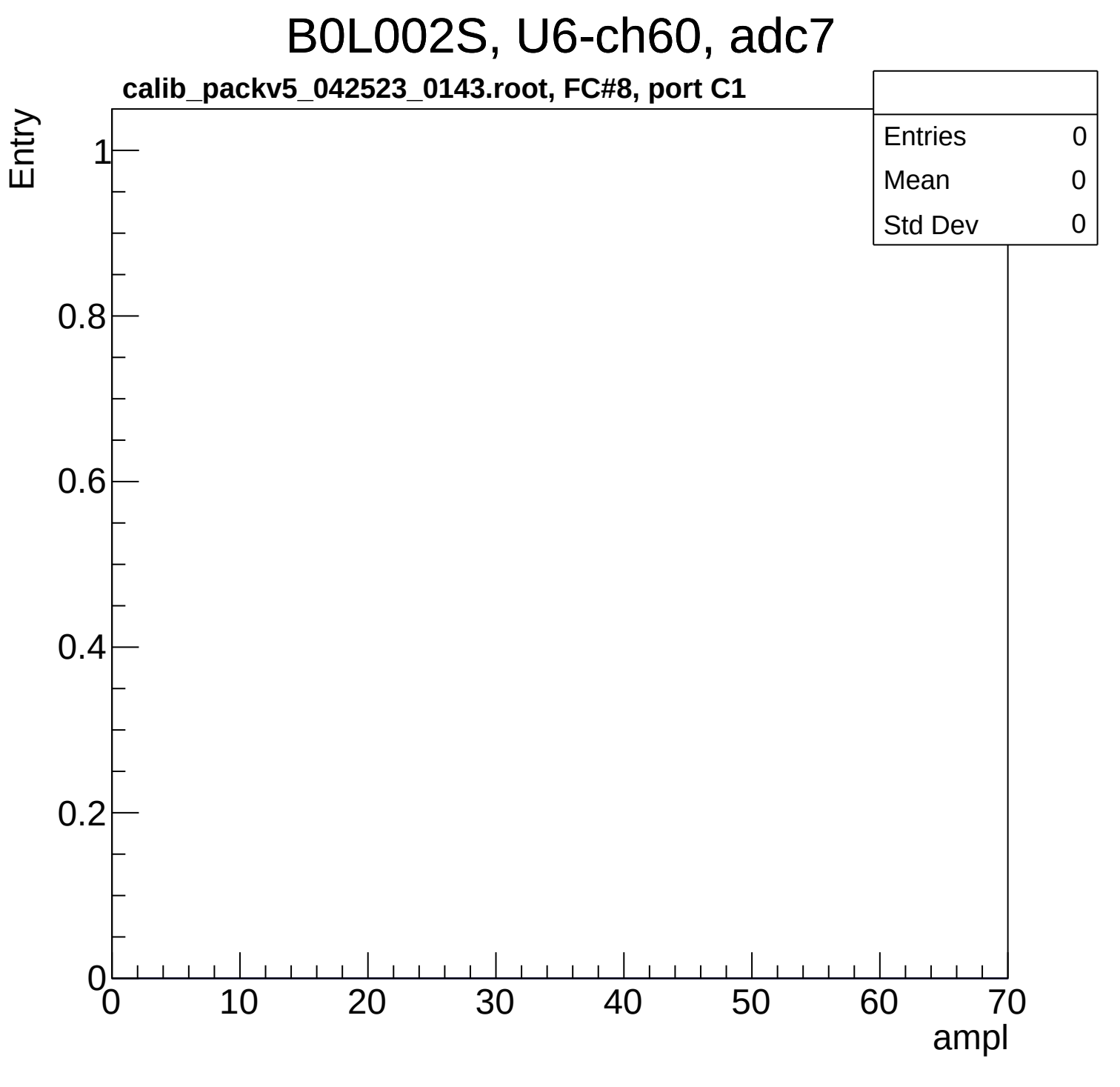
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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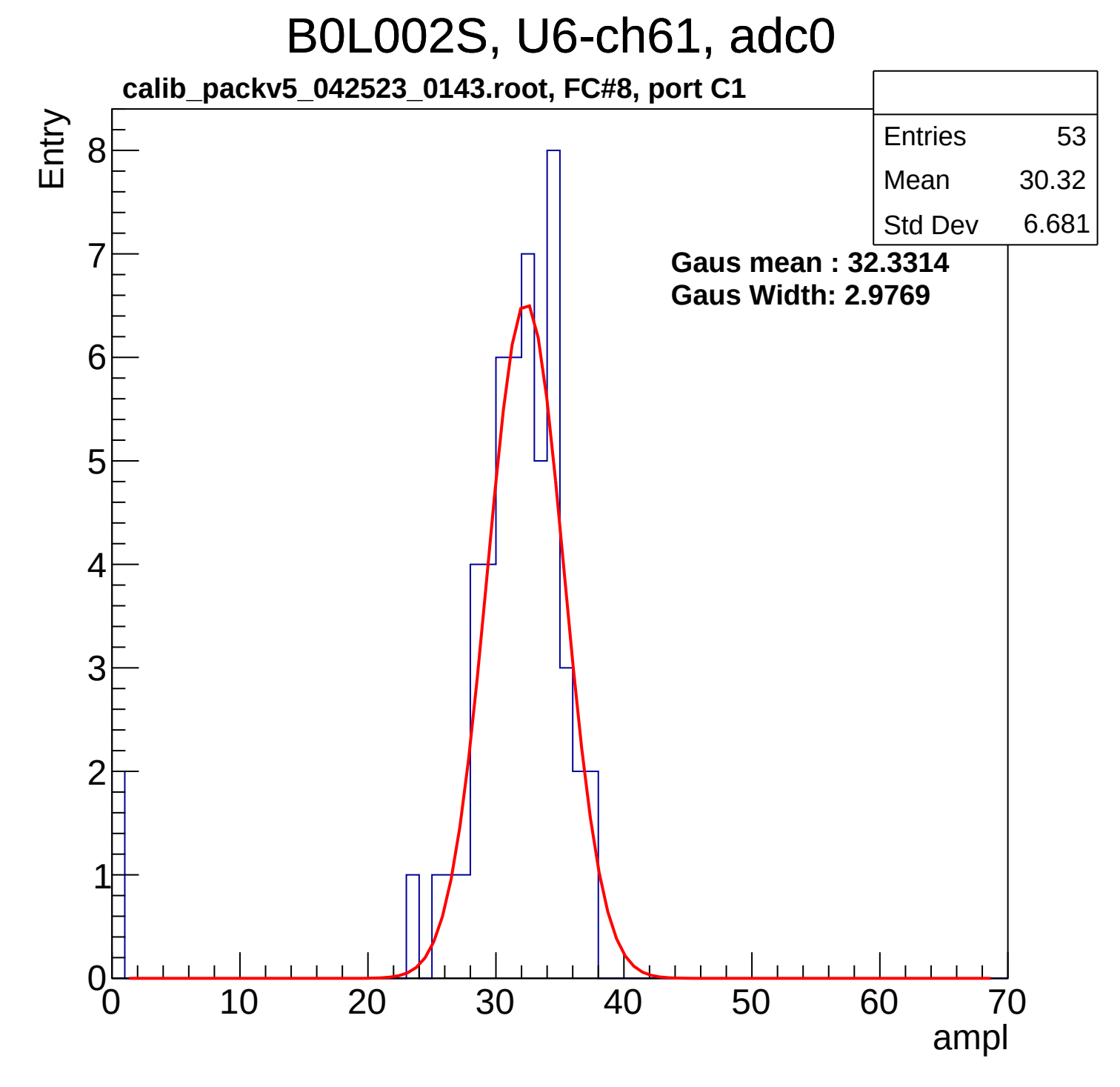
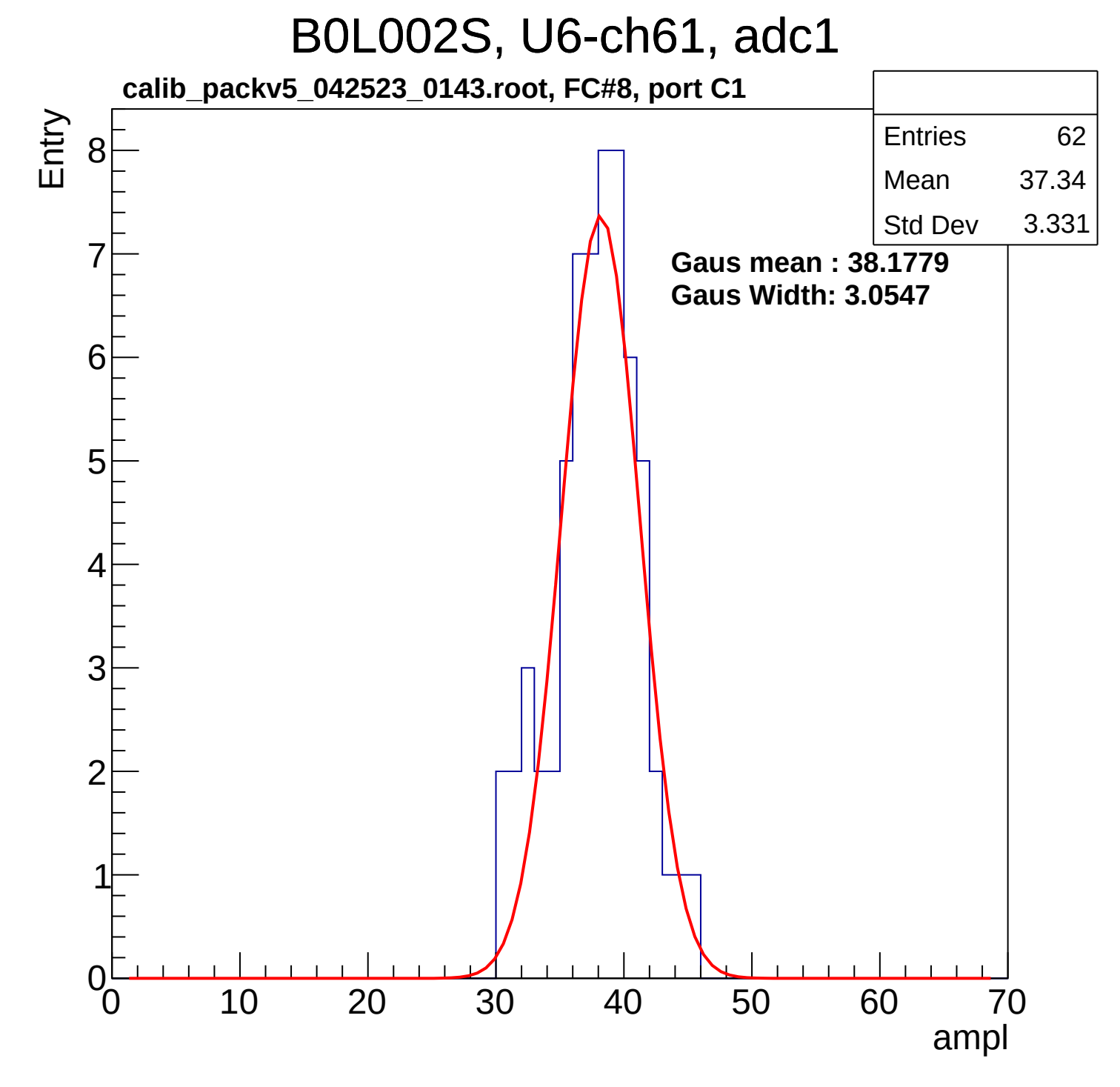
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 30.32
Std Dev 6.681
Gaus mean : 32.3314
Gaus Width: 2.9769
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.34
Std Dev 3.331
Gaus mean : 38.1779
Gaus Width: 3.0547
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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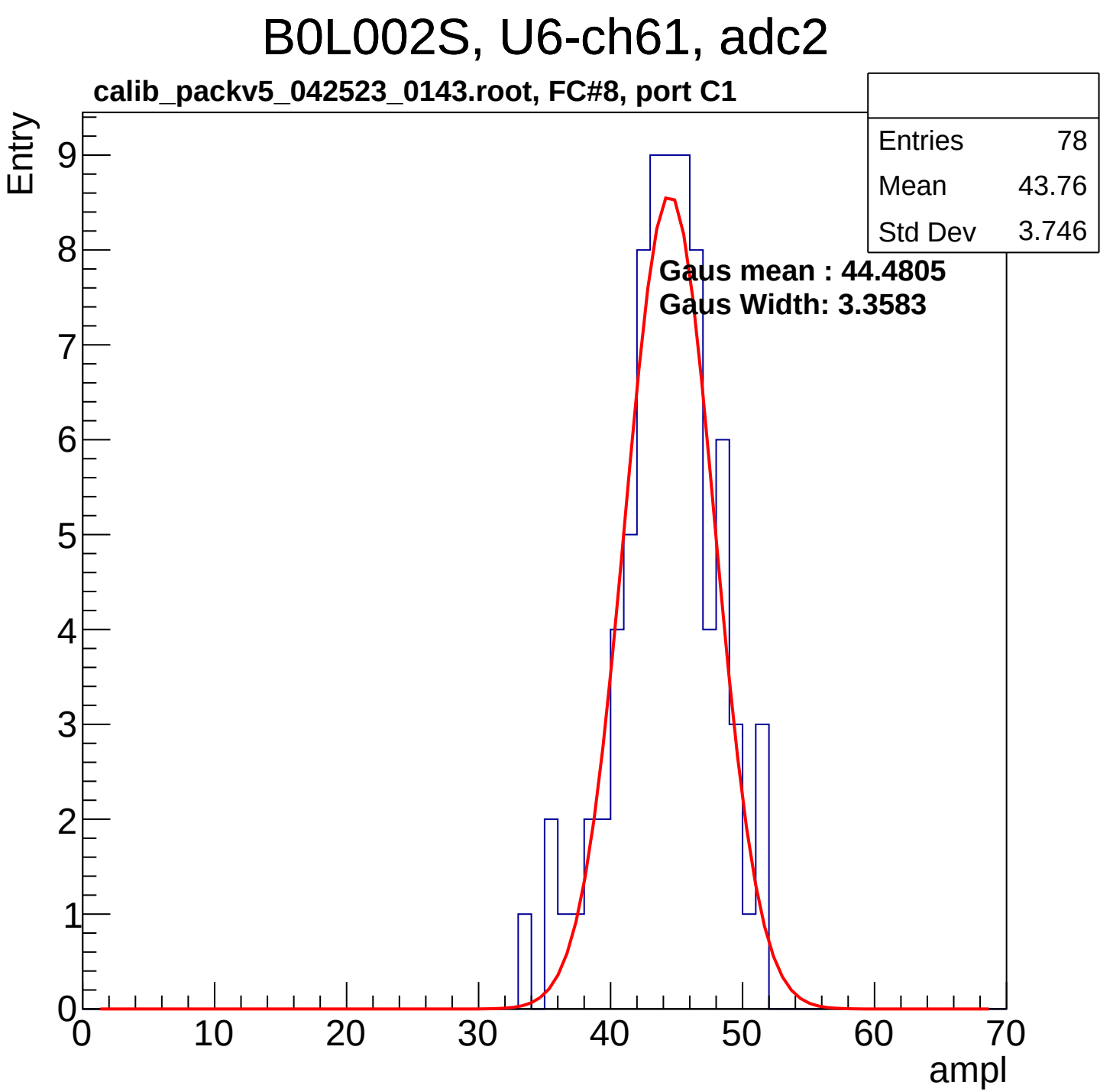
B0L002S, U6-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 43.76
Std Dev 3.746
Gaus mean : 44.4805
Gaus Width: 3.3583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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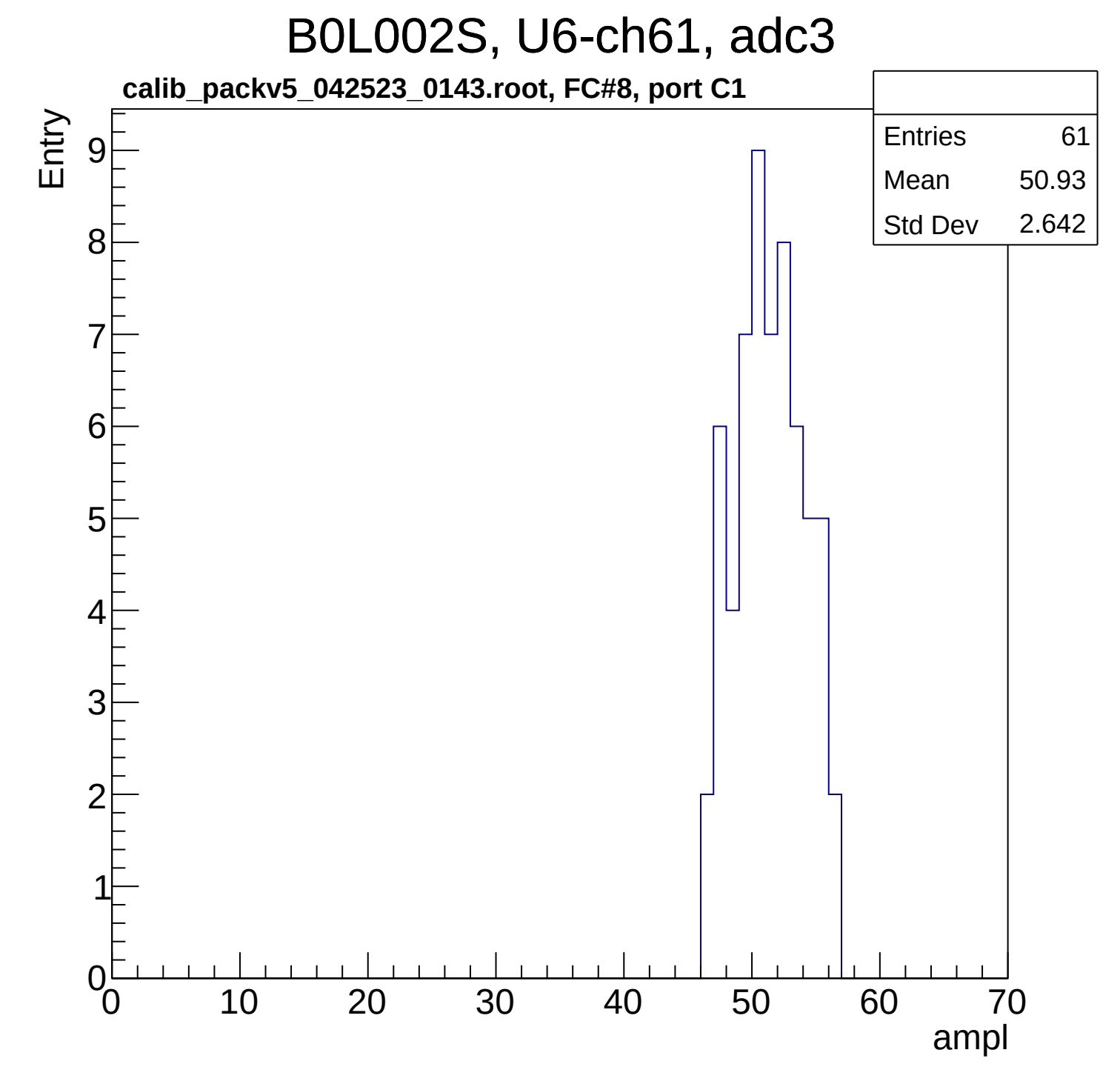
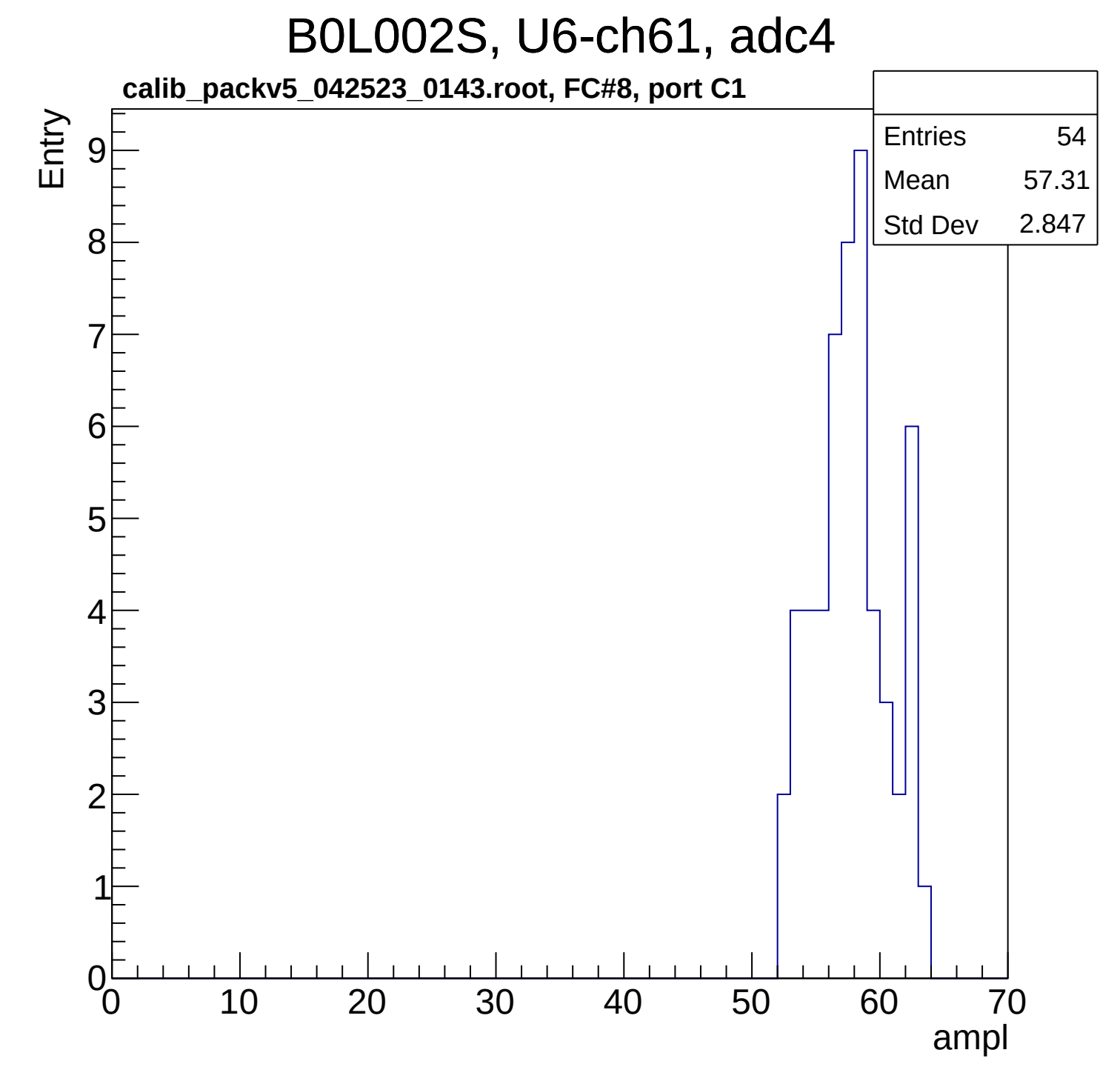

B0L002S, U6-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.93
Std Dev 2.642
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.31
Std Dev 2.847
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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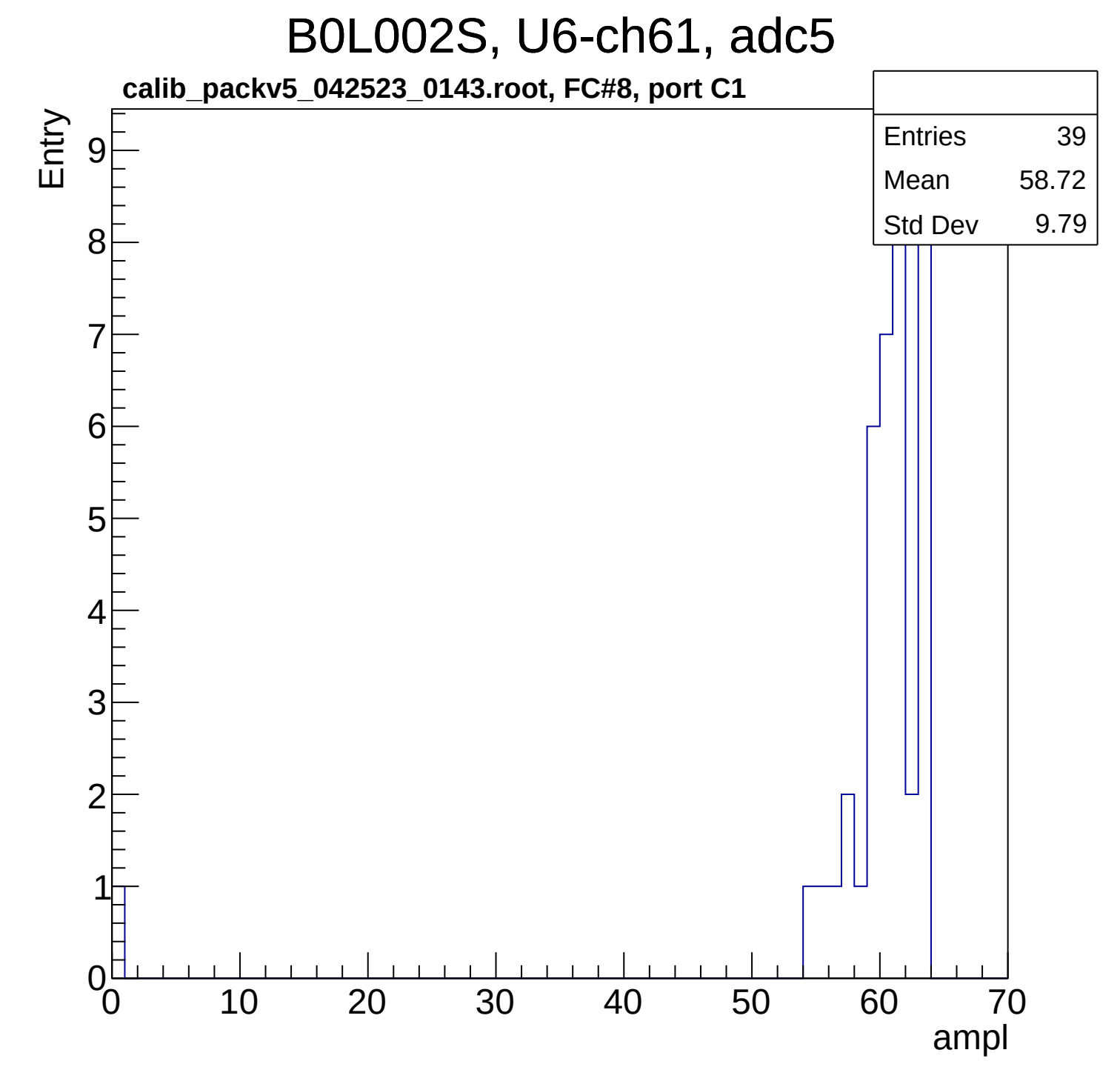
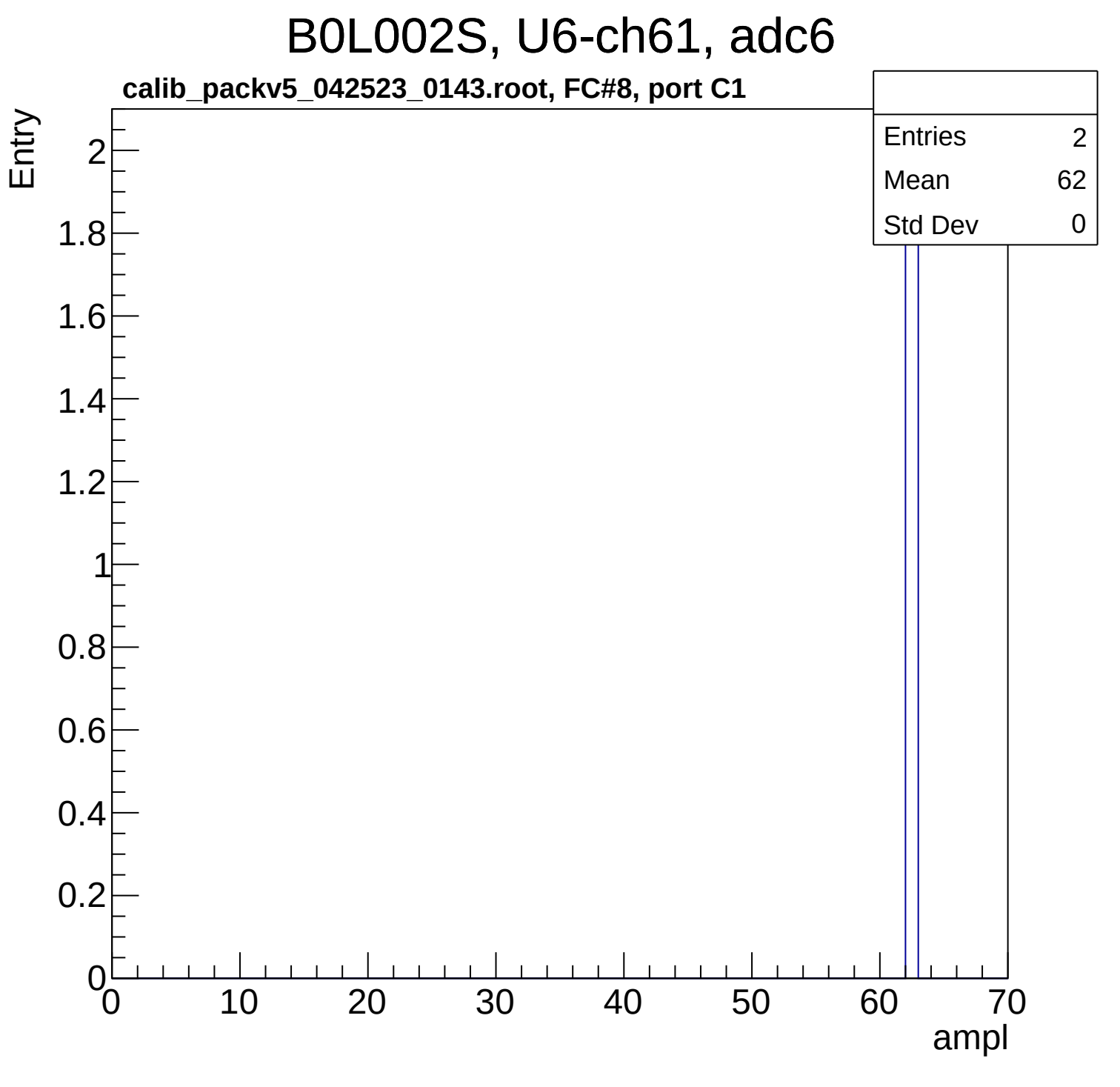

B0L002S, U6-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.72
Std Dev 9.79
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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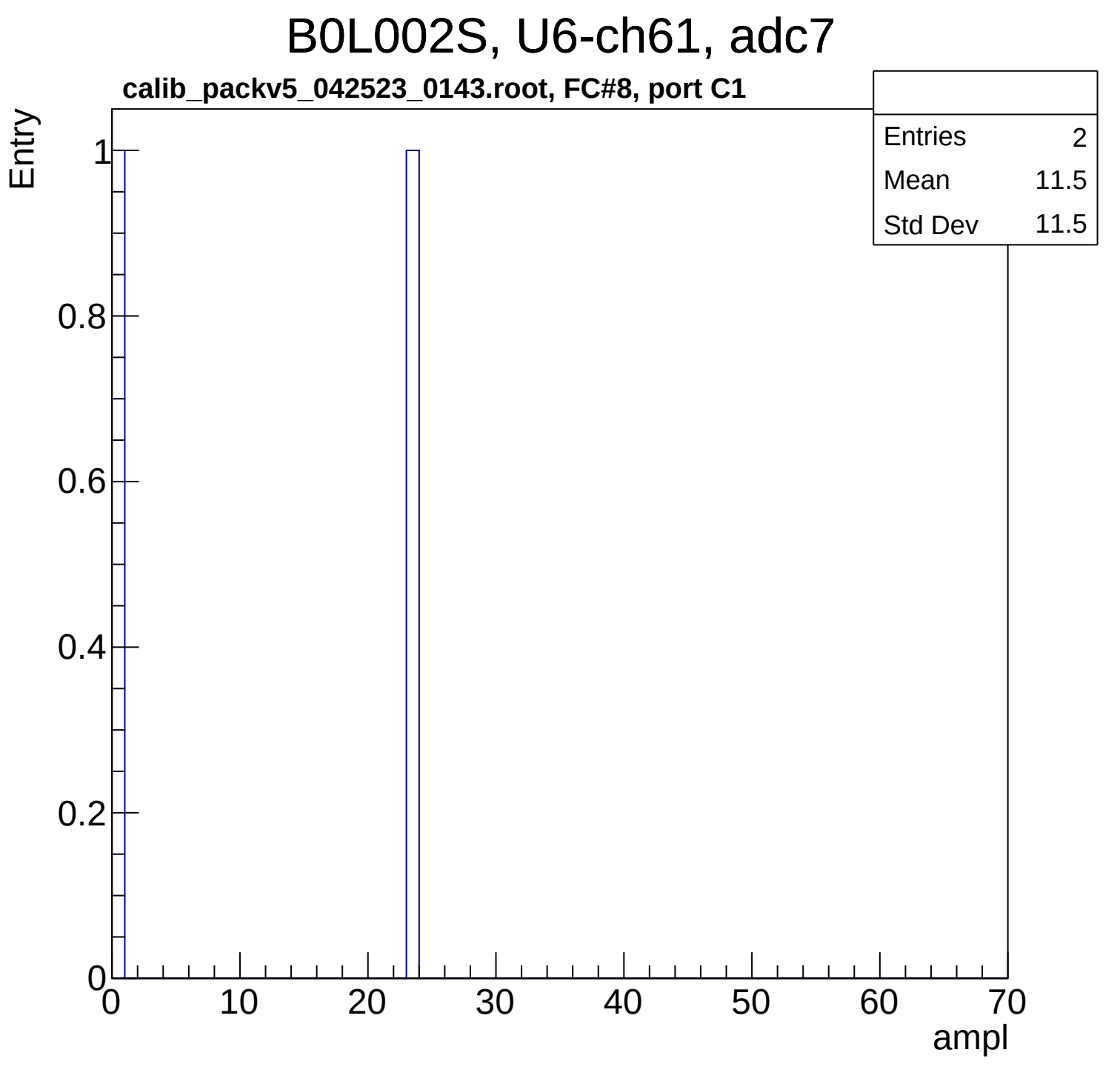
B0L002S, U6-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
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50
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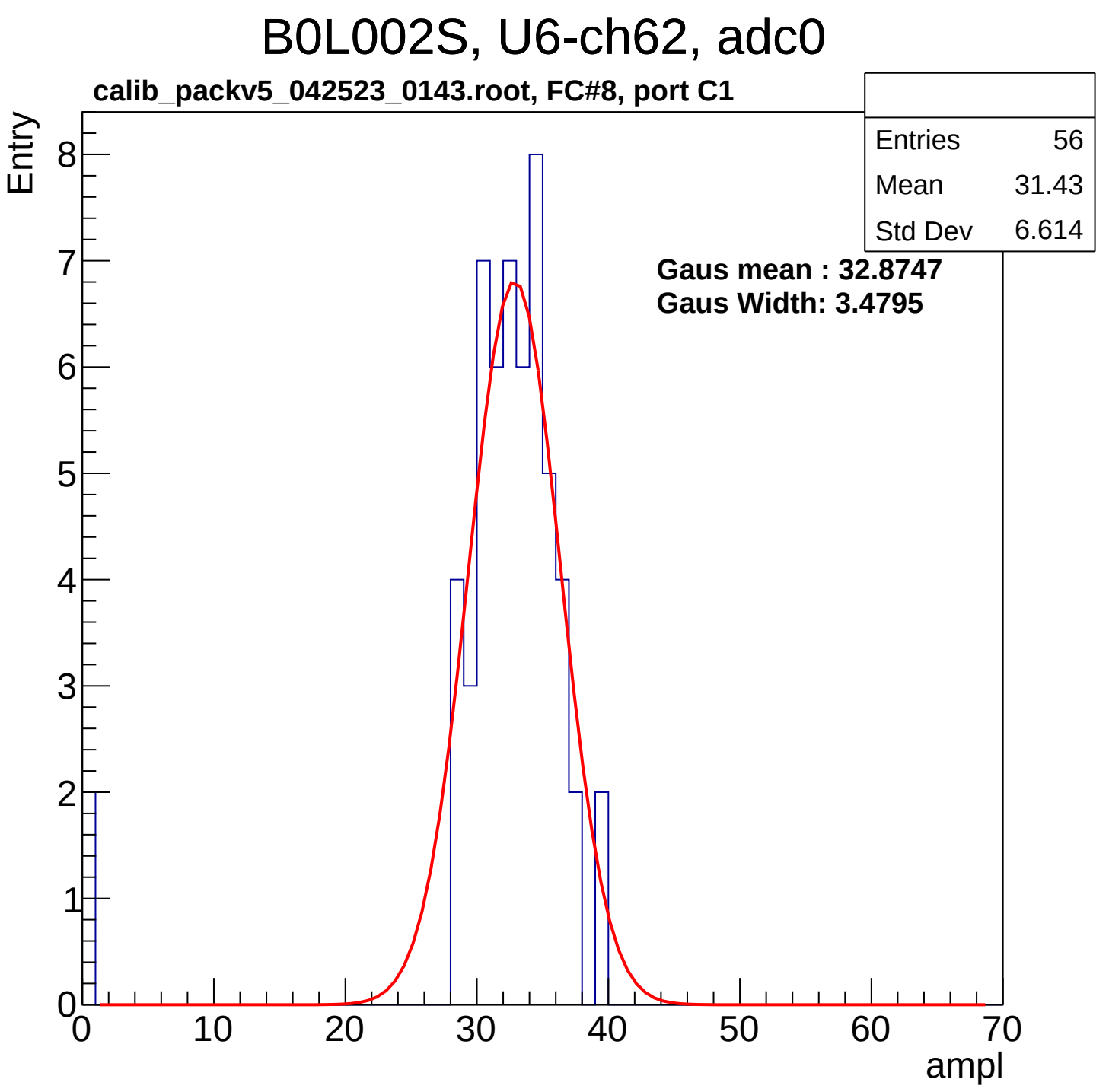
70
ampl


B0L002S, U6-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.43
Std Dev 6.614
Gaus mean : 32.8747
Gaus Width: 3.4795
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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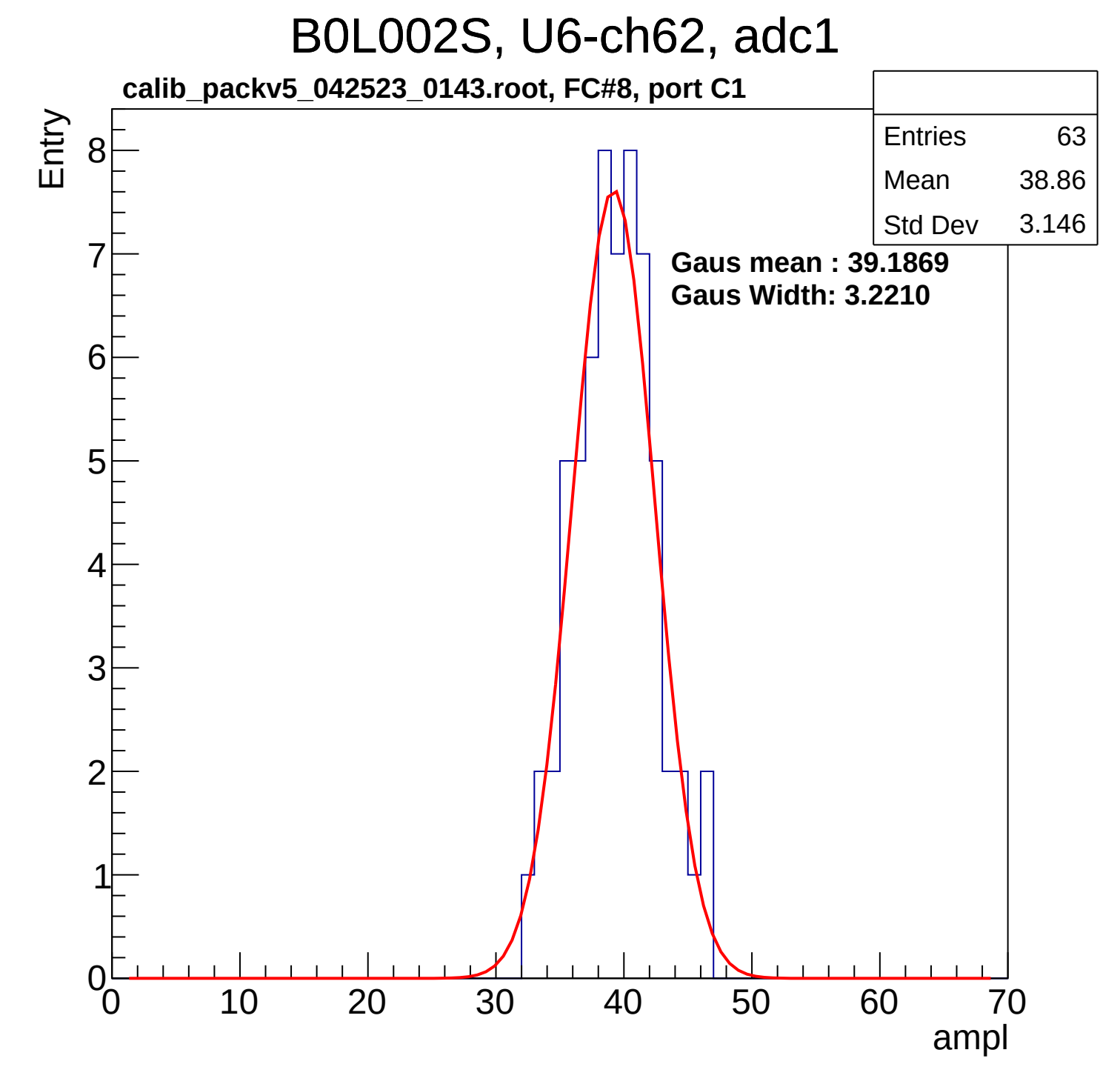
B0L002S, U6-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.86
Std Dev 3.146
Gaus mean : 39.1869
Gaus Width: 3.2210
Entry
ampl
0
1
2
3
4
5
6
7
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0
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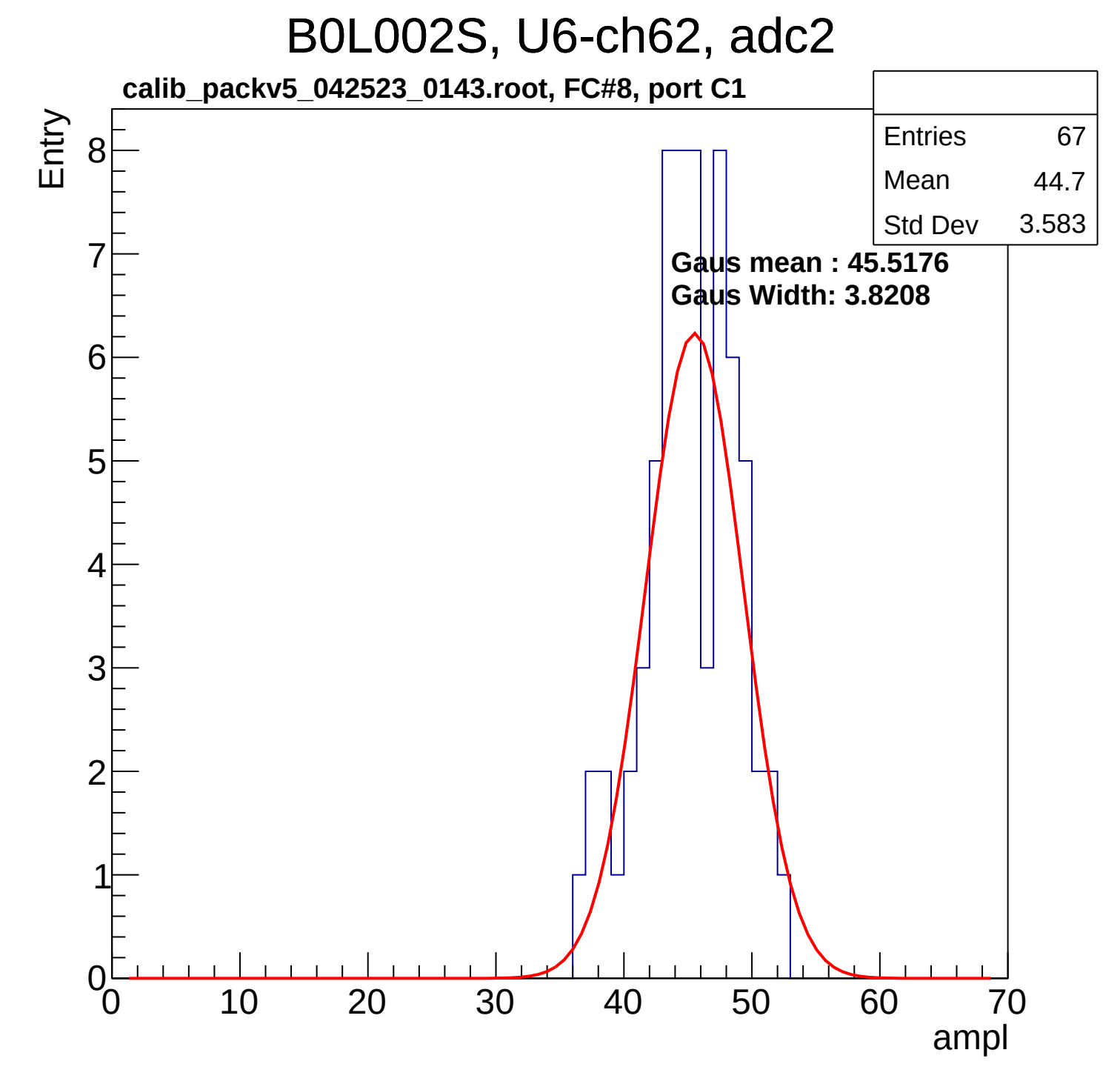
B0L002S, U6-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.7
Std Dev 3.583
Gaus mean : 45.5176
Gaus Width: 3.8208
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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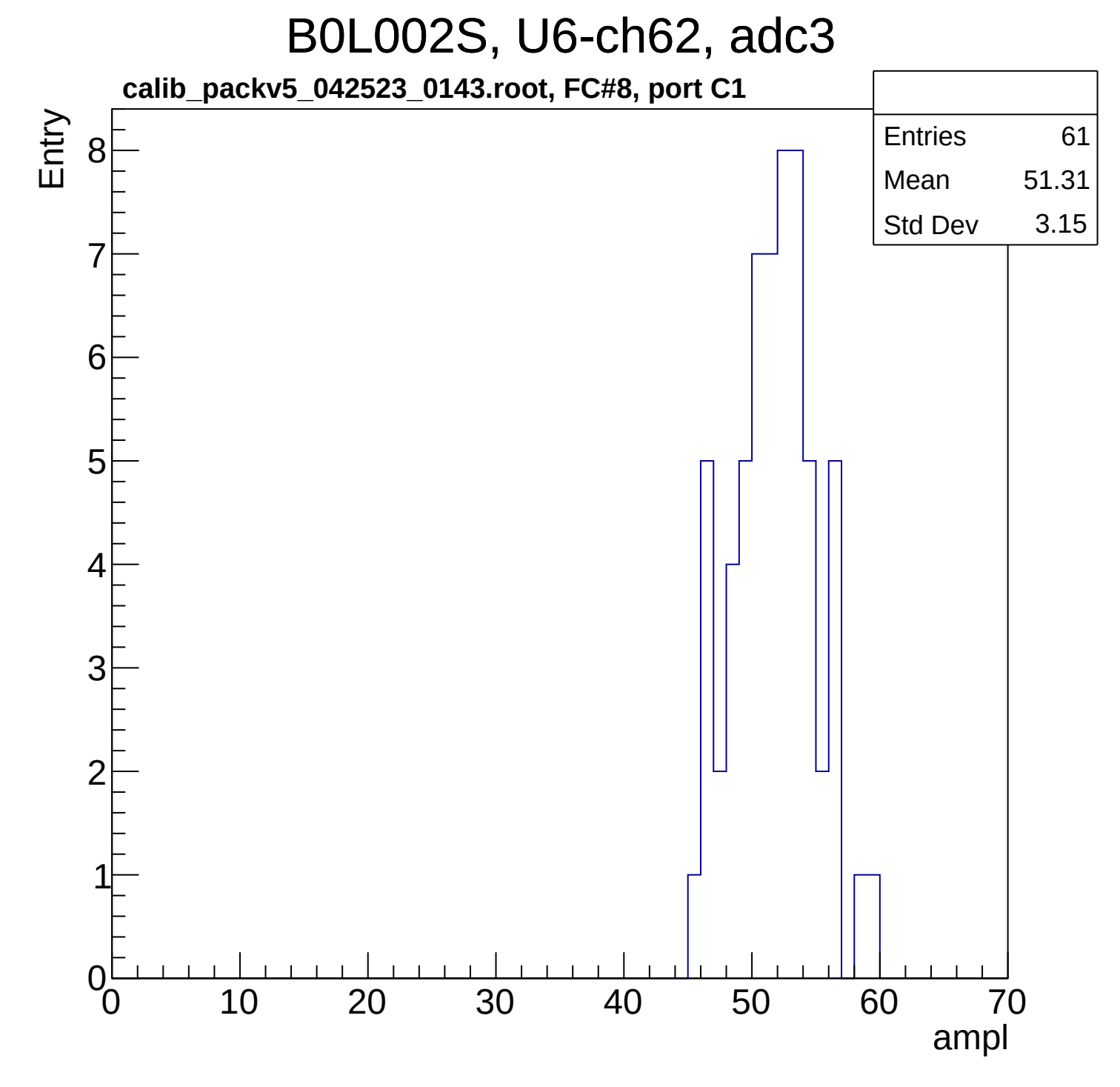
B0L002S, U6-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.31
Std Dev 3.15
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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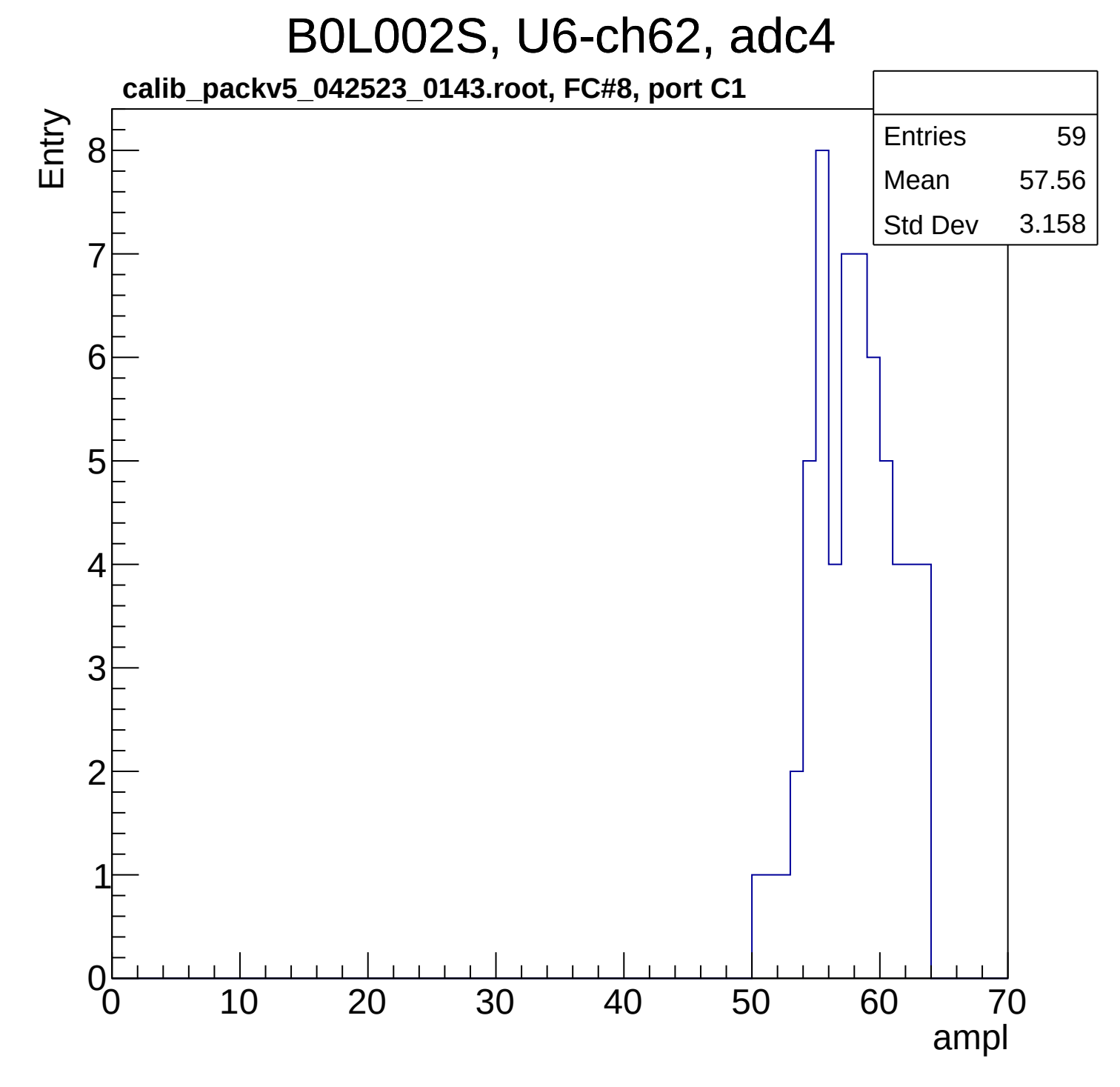
B0L002S, U6-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.56
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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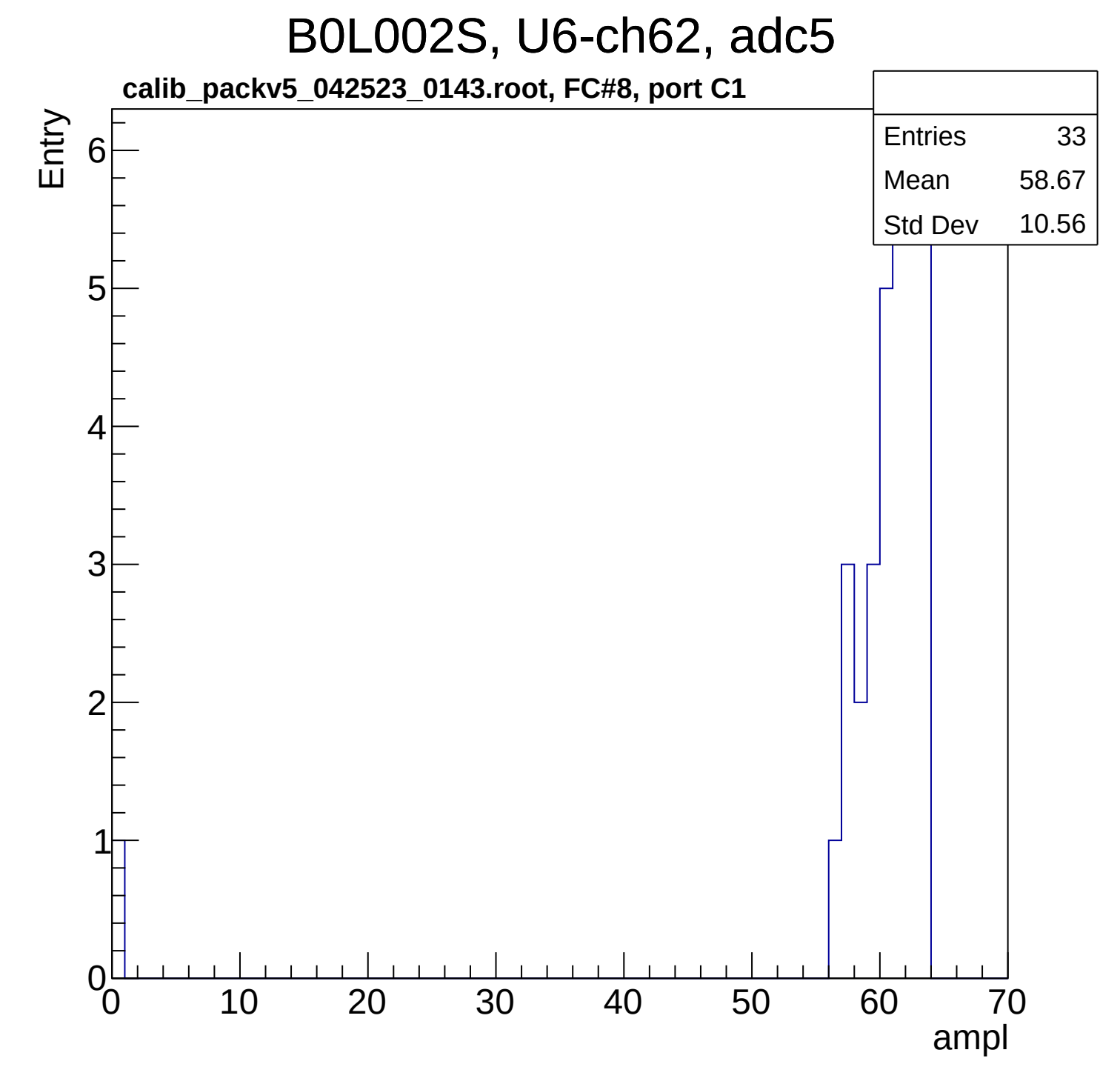
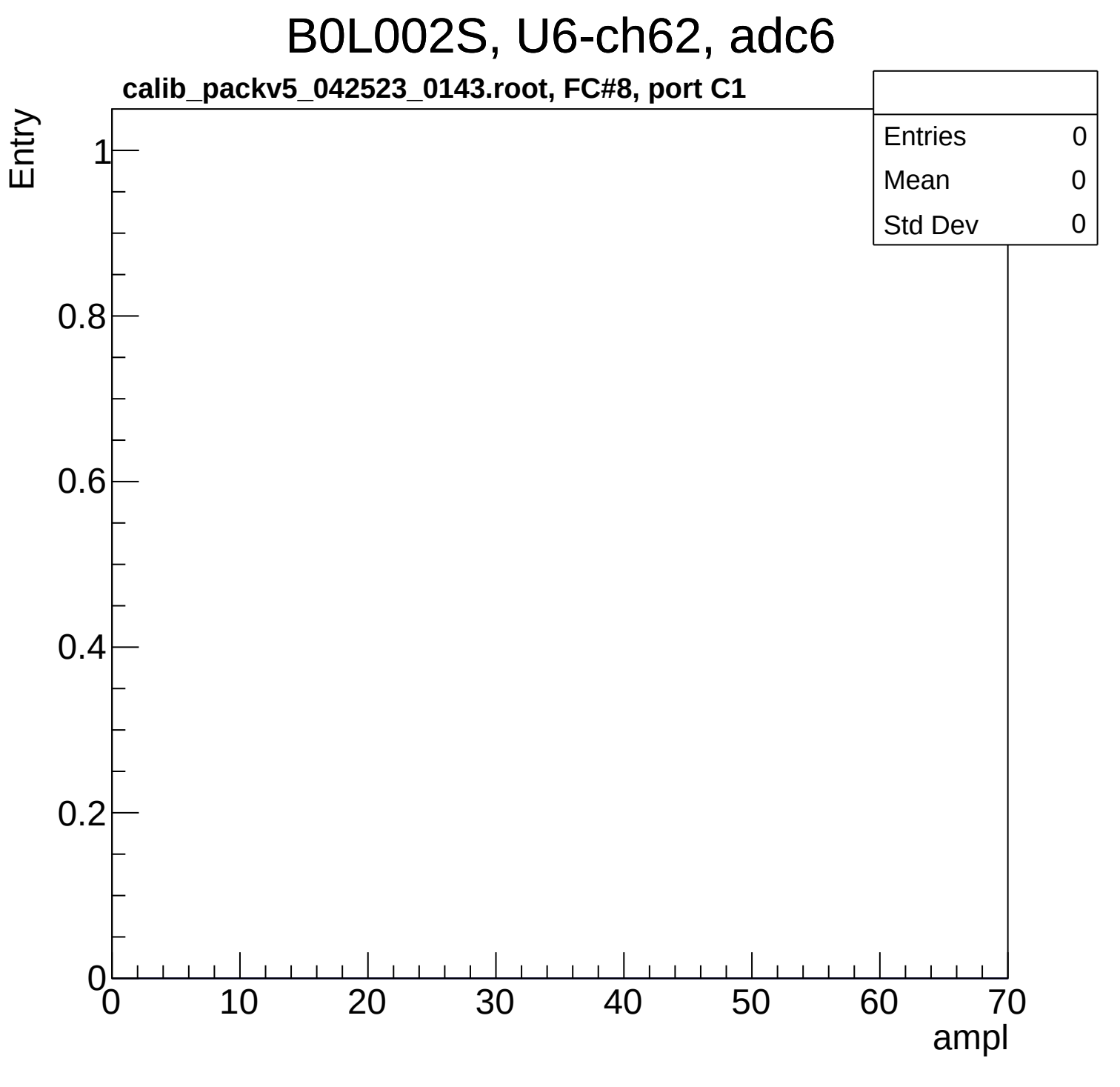
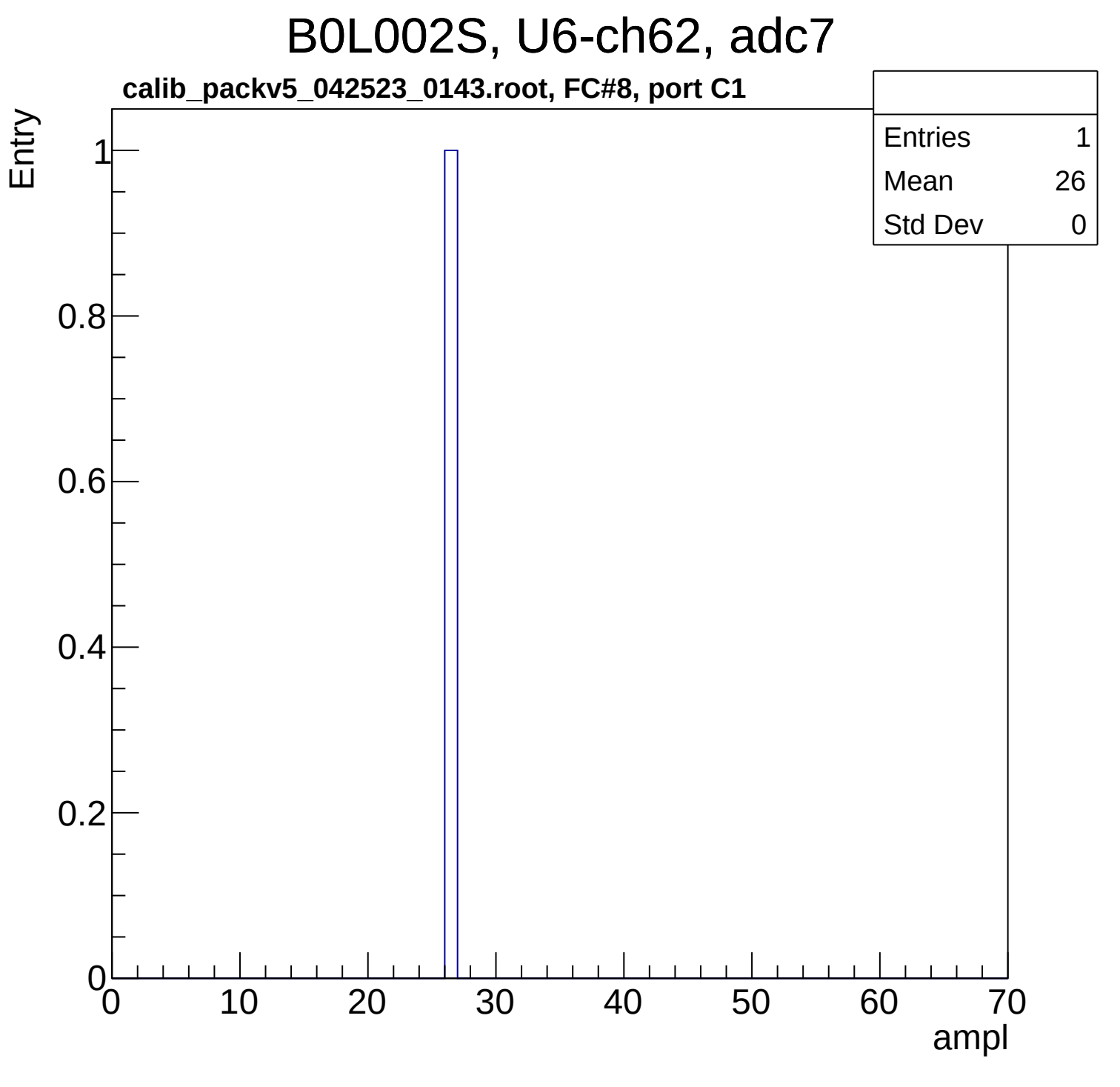

B0L002S, U6-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.67
Std Dev 10.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
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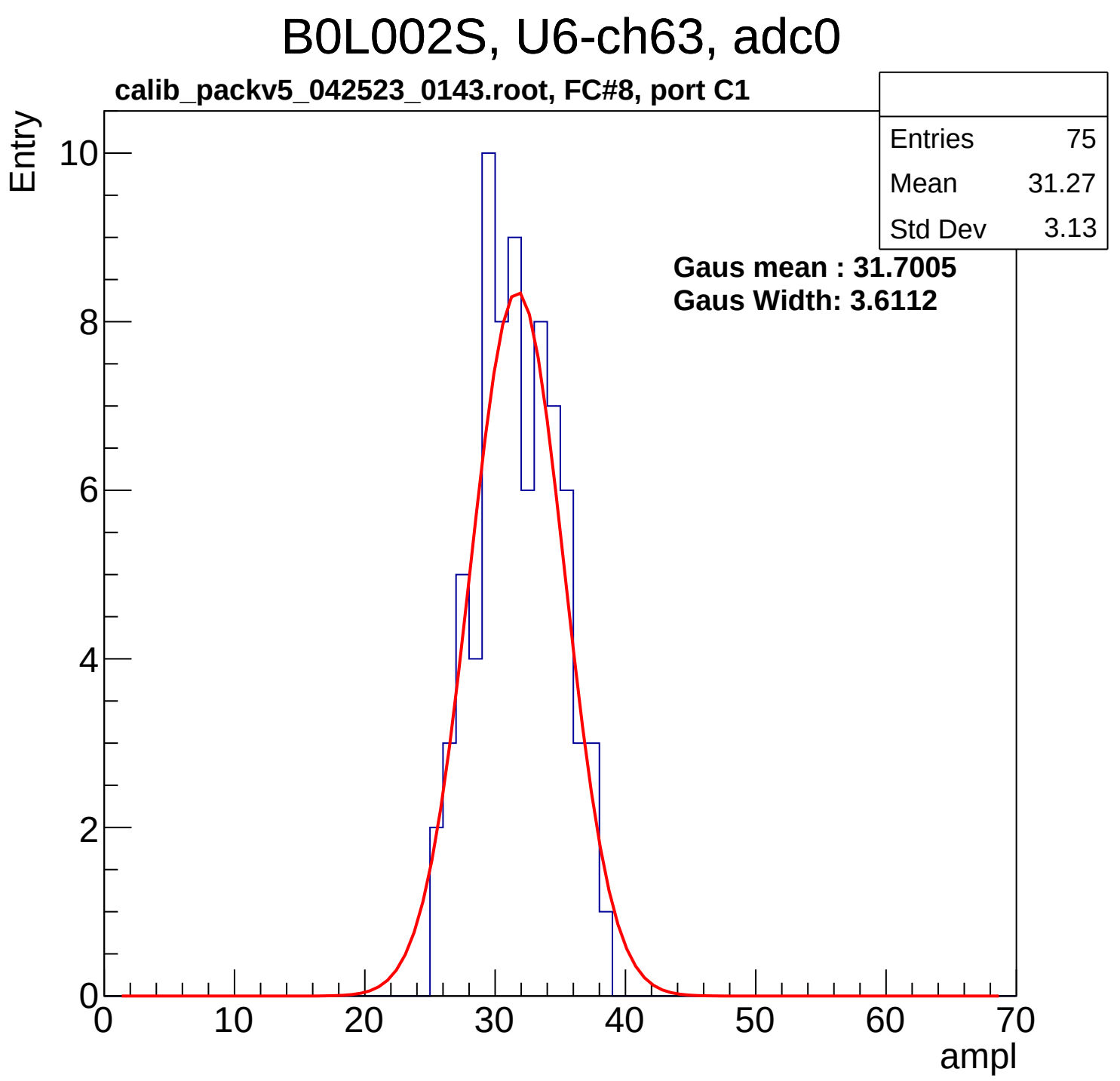
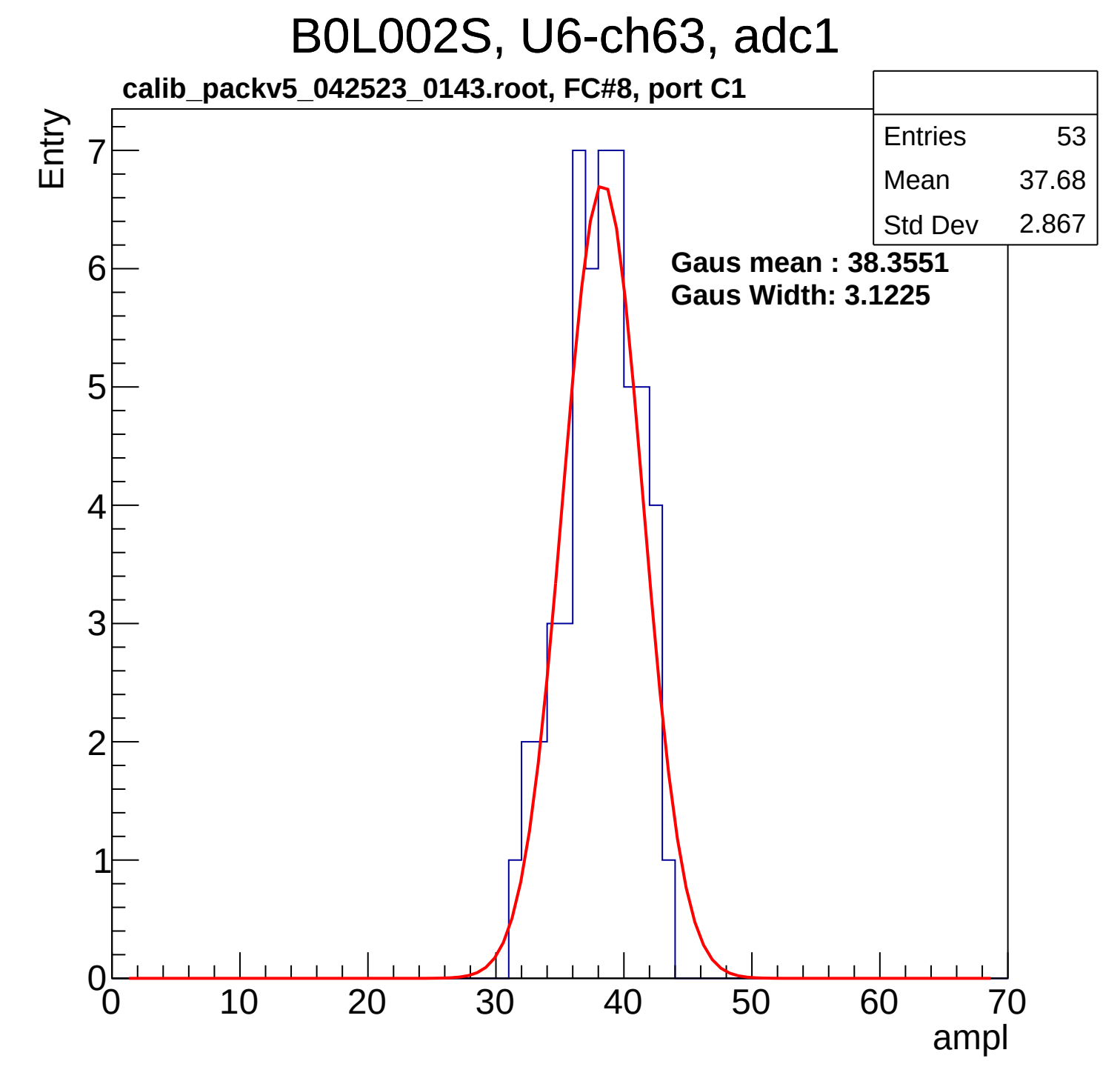
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.27
Std Dev 3.13
Gaus mean : 31.7005
Gaus Width: 3.6112
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 37.68
Std Dev 2.867
Gaus mean : 38.3551
Gaus Width: 3.1225
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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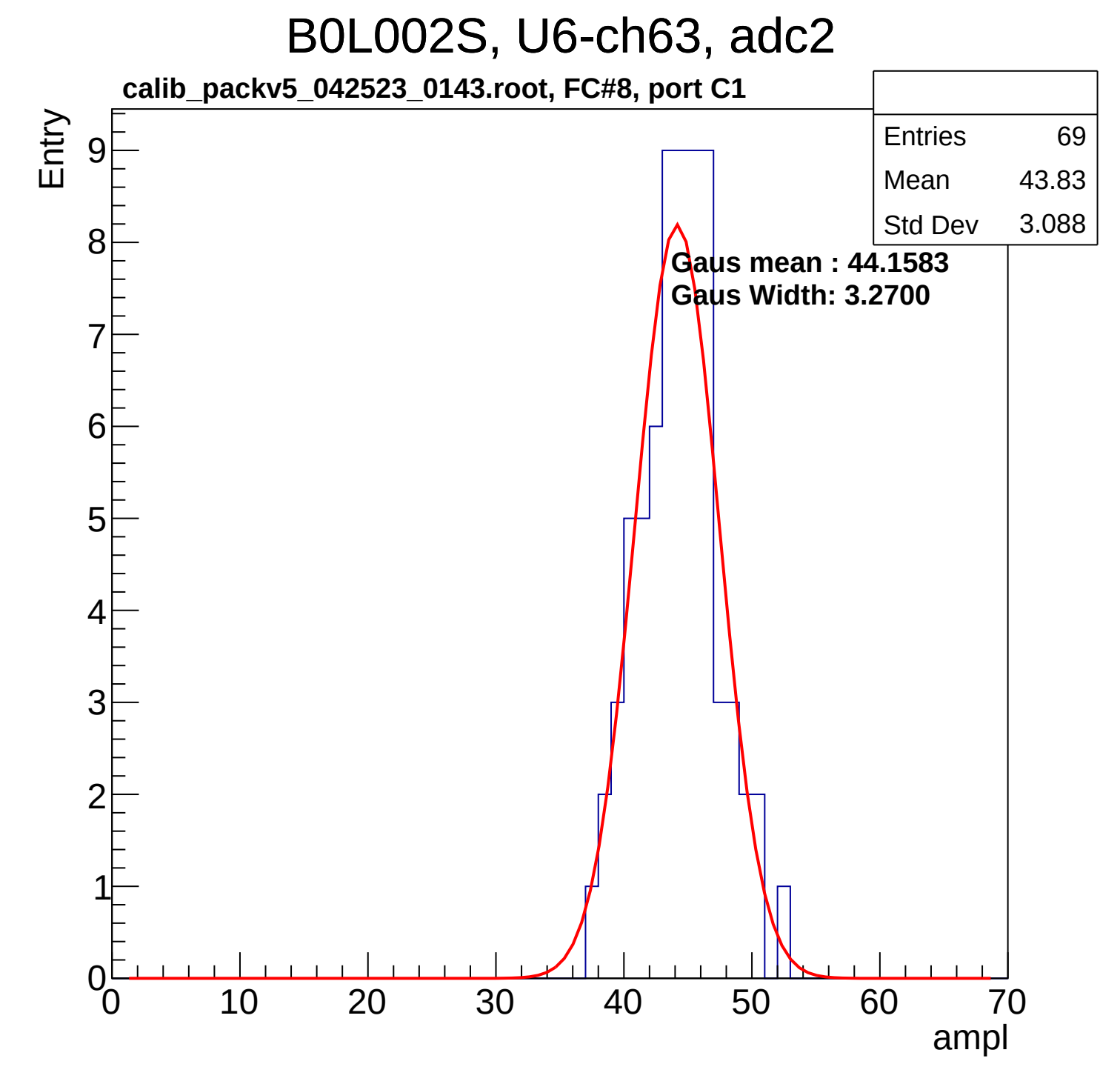
B0L002S, U6-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.83
Std Dev 3.088
Gaus mean : 44.1583
Gaus Width: 3.2700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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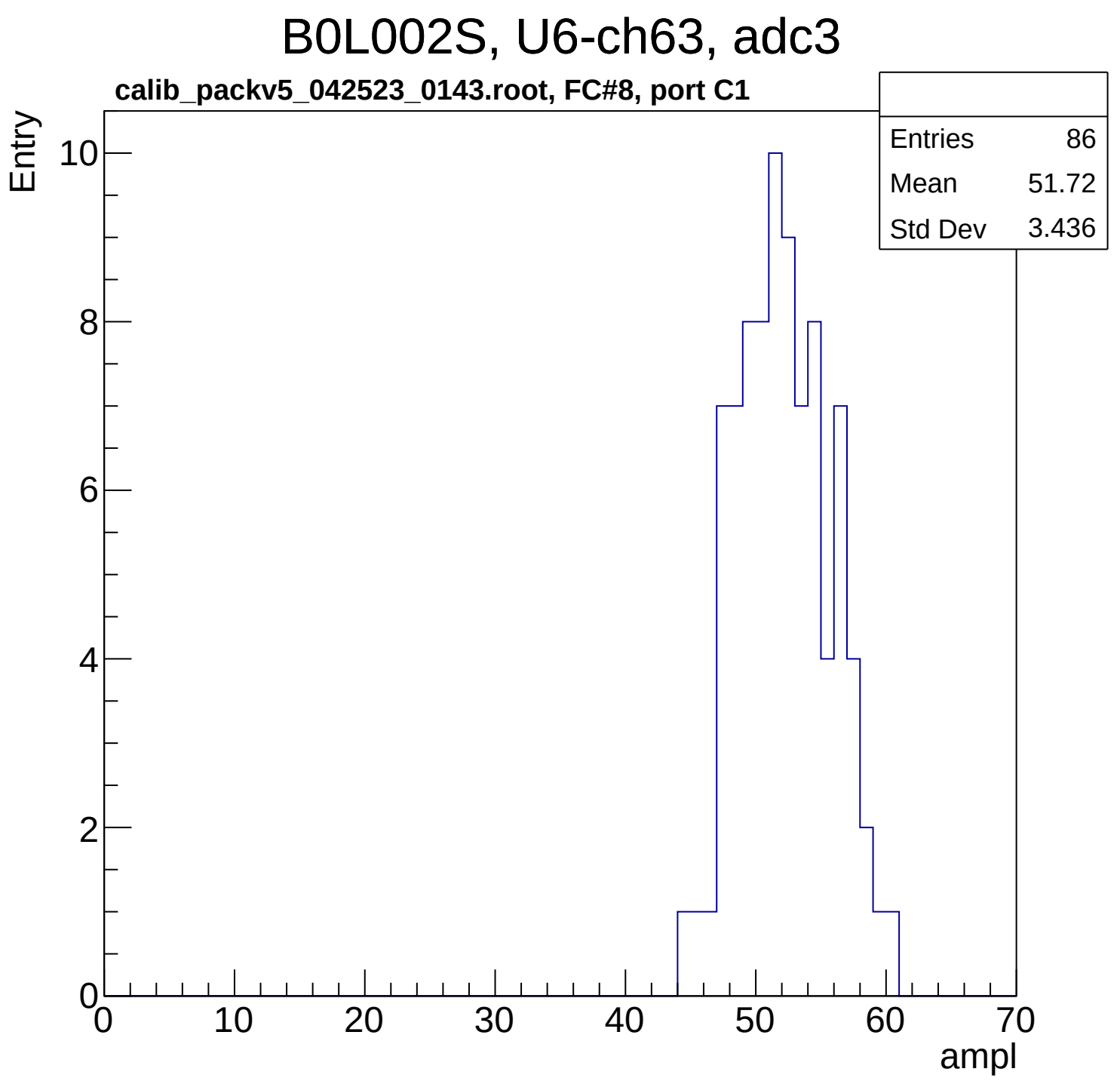
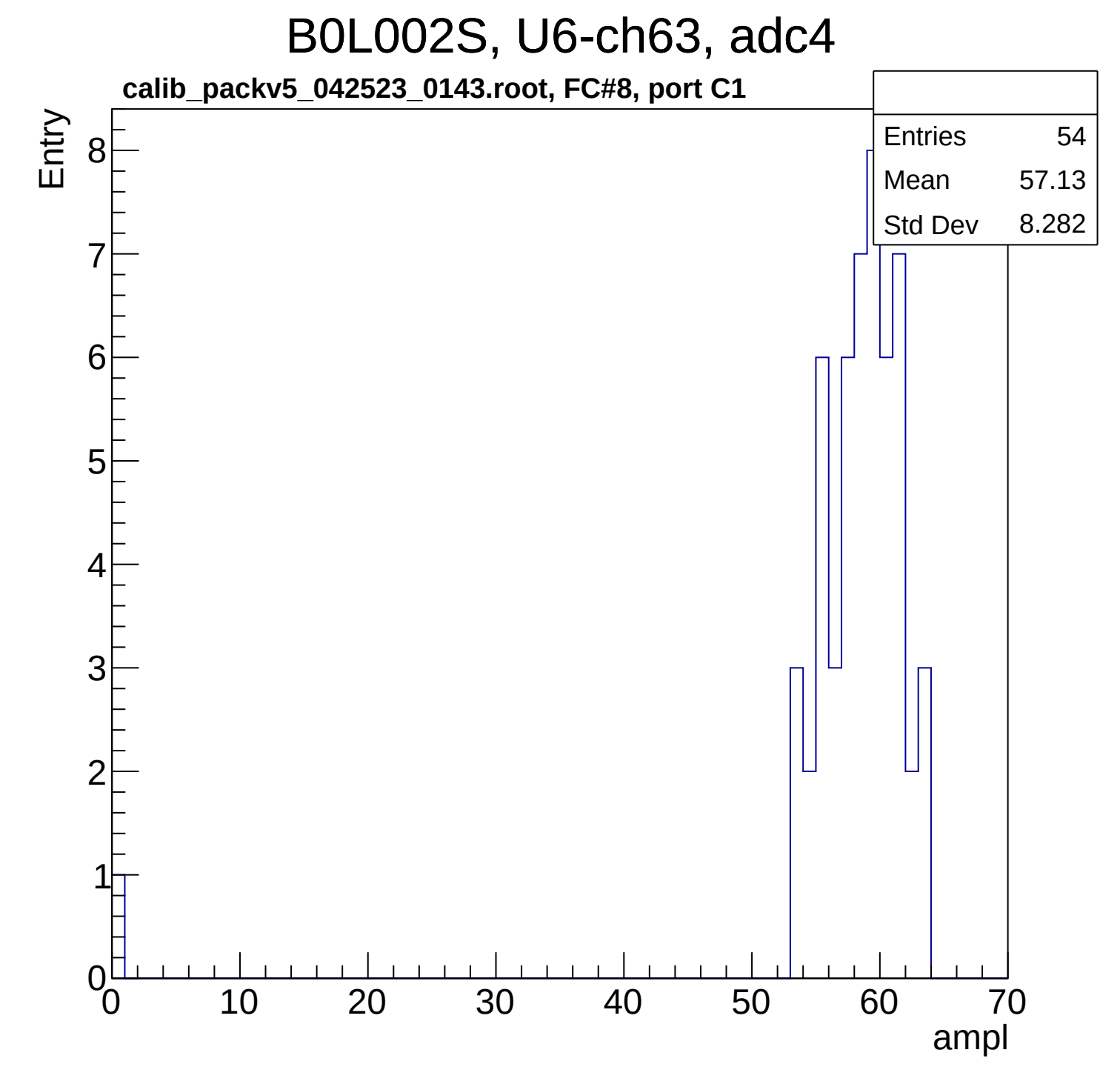
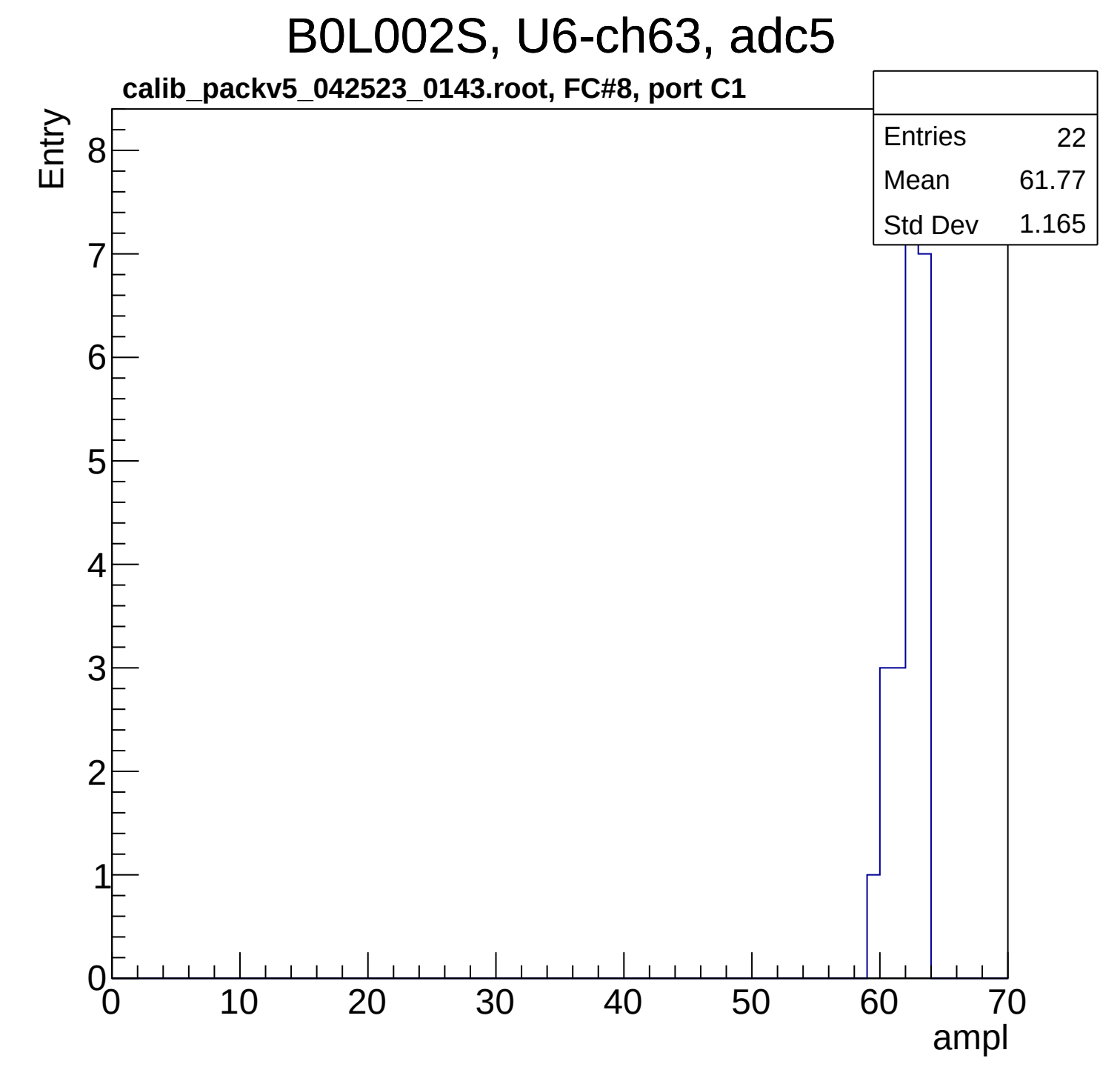

B0L002S, U6-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 51.72
Std Dev 3.436
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.13
Std Dev 8.282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.77
Std Dev 1.165
Entry
ampl
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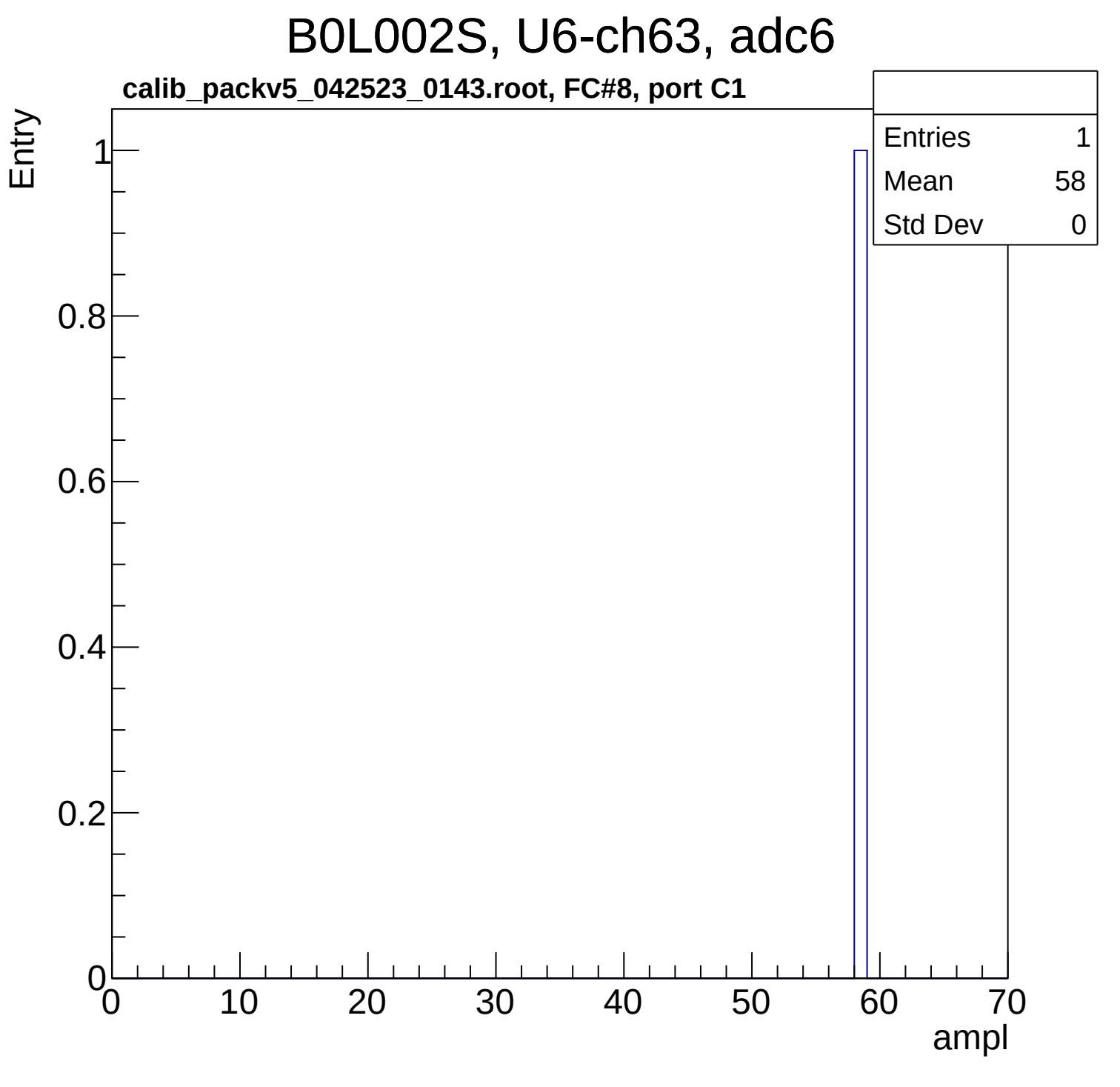
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 58
Std Dev 0
Entry
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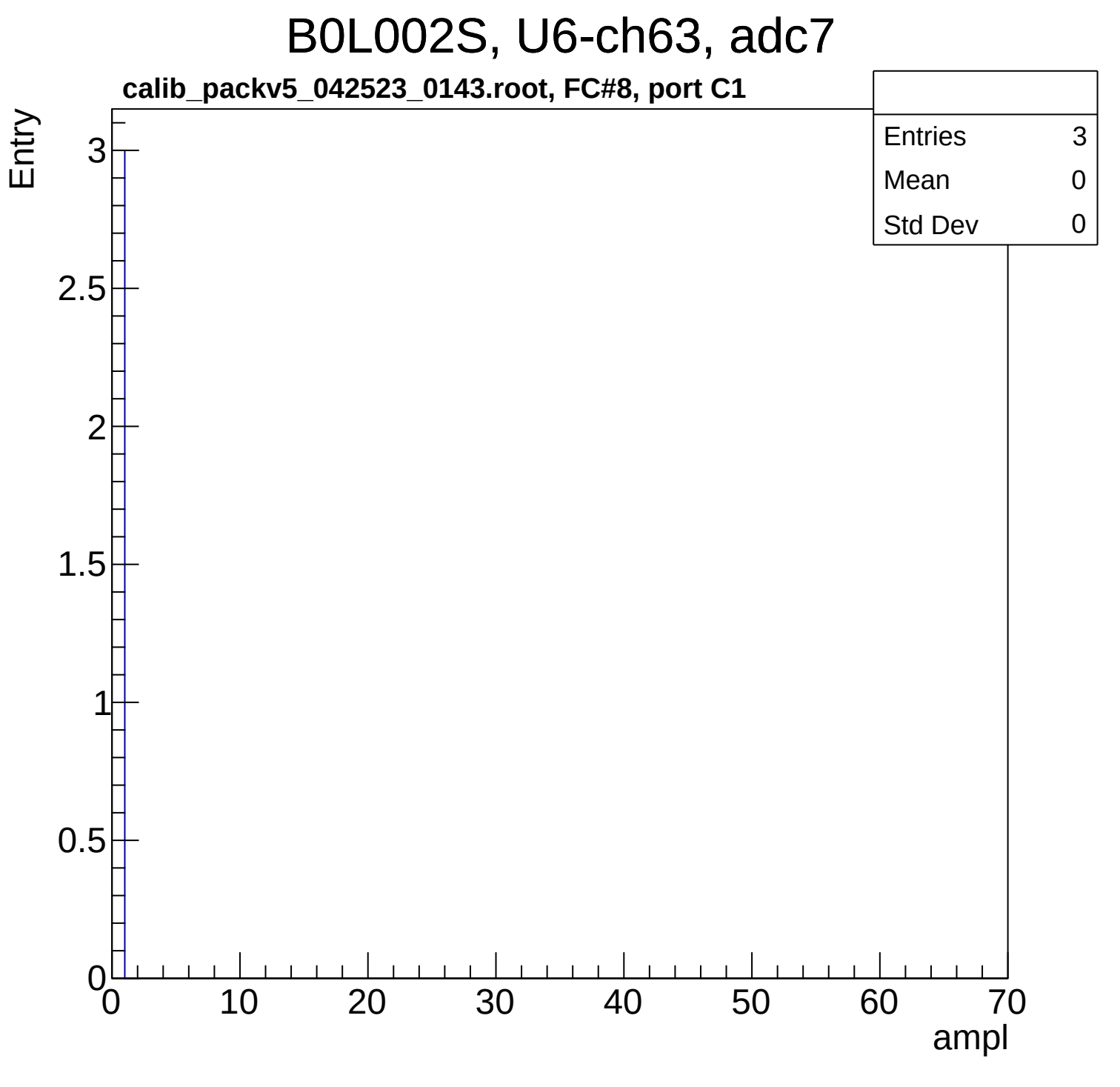
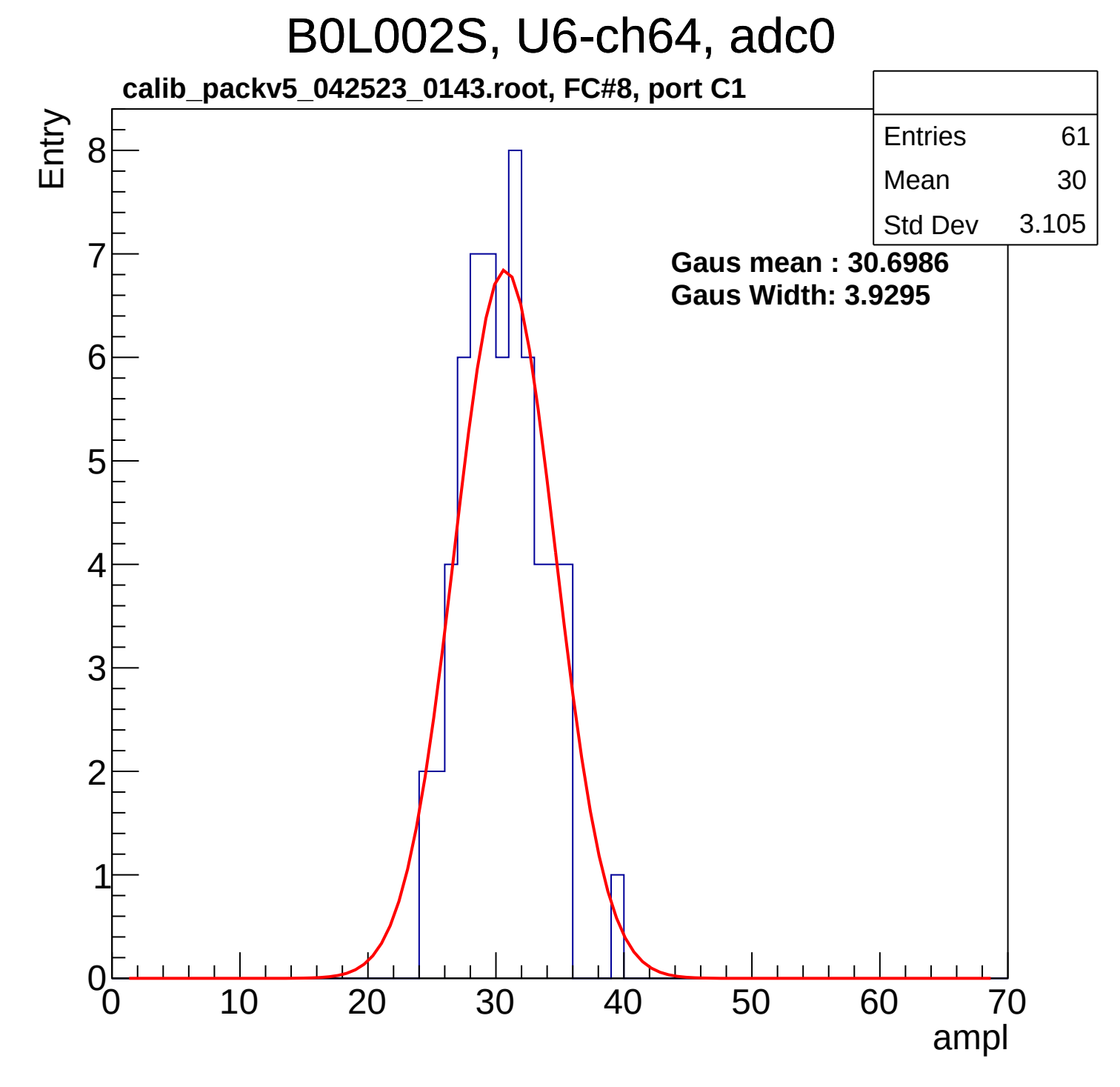
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30
Std Dev 3.105
Gaus mean : 30.6986
Gaus Width: 3.9295
Entry
ampl
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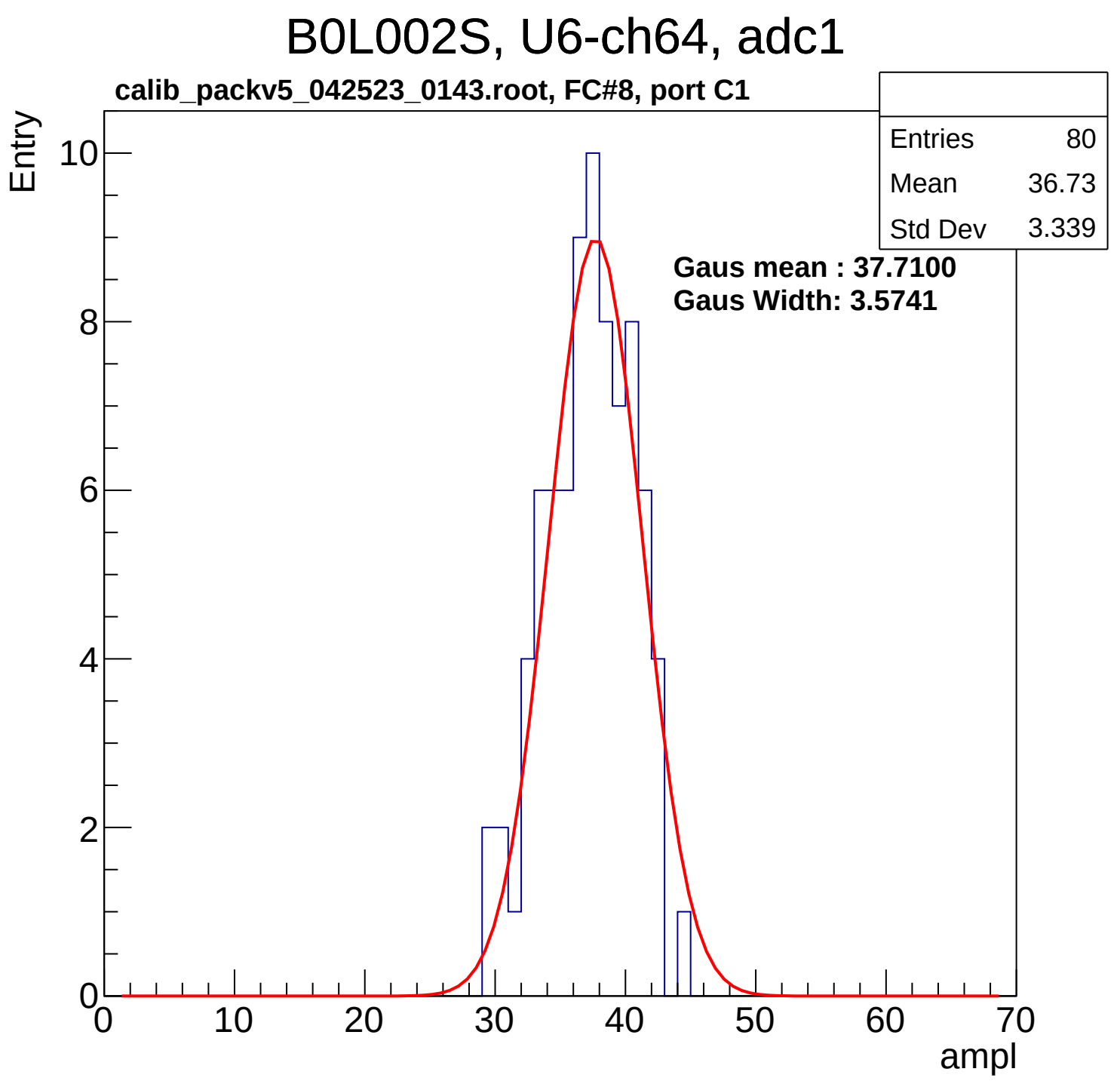
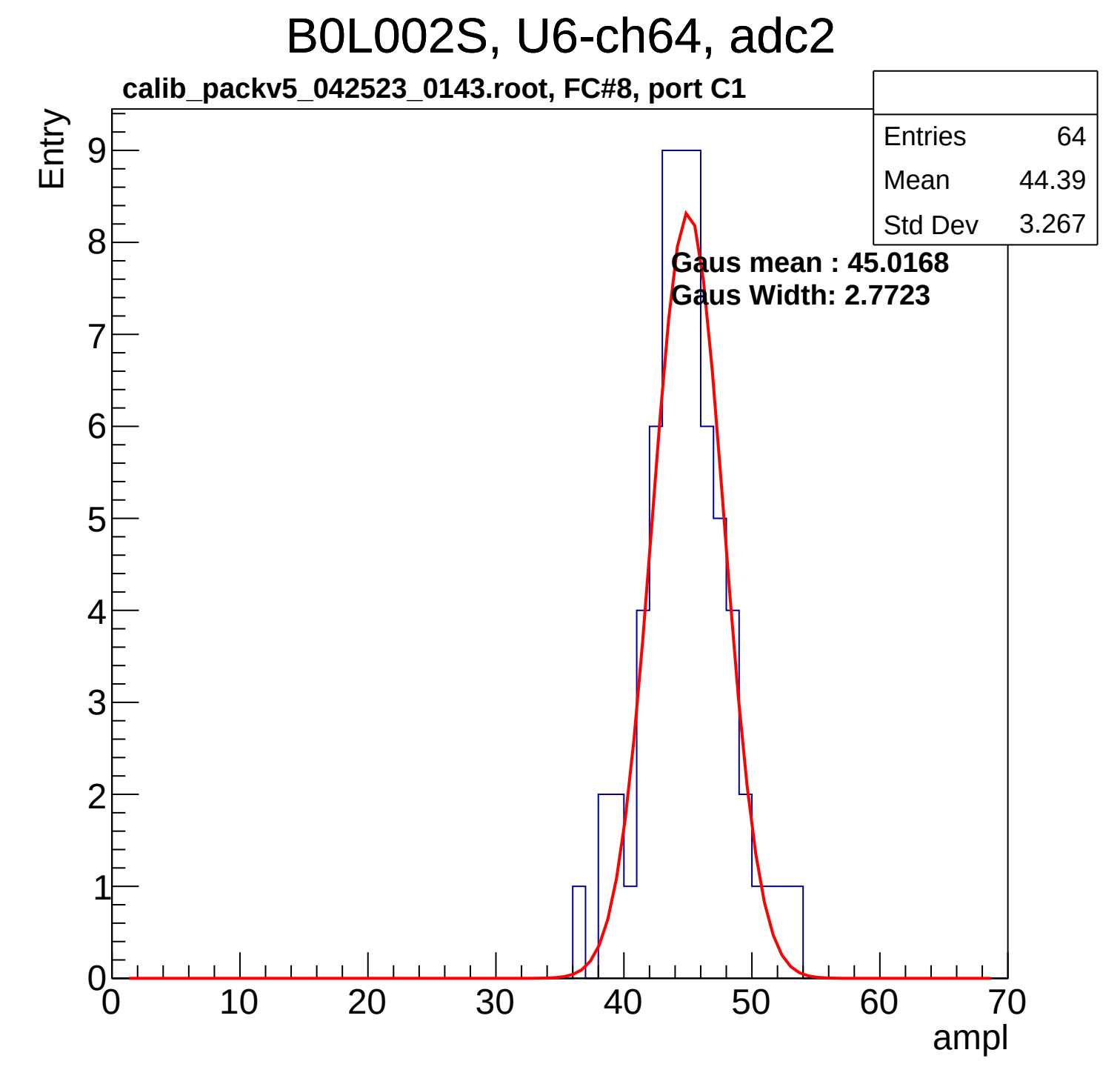
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 36.73
Std Dev 3.339
Gaus mean : 37.7100
Gaus Width: 3.5741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.39
Std Dev 3.267
Gaus mean : 45.0168
Gaus Width: 2.7723
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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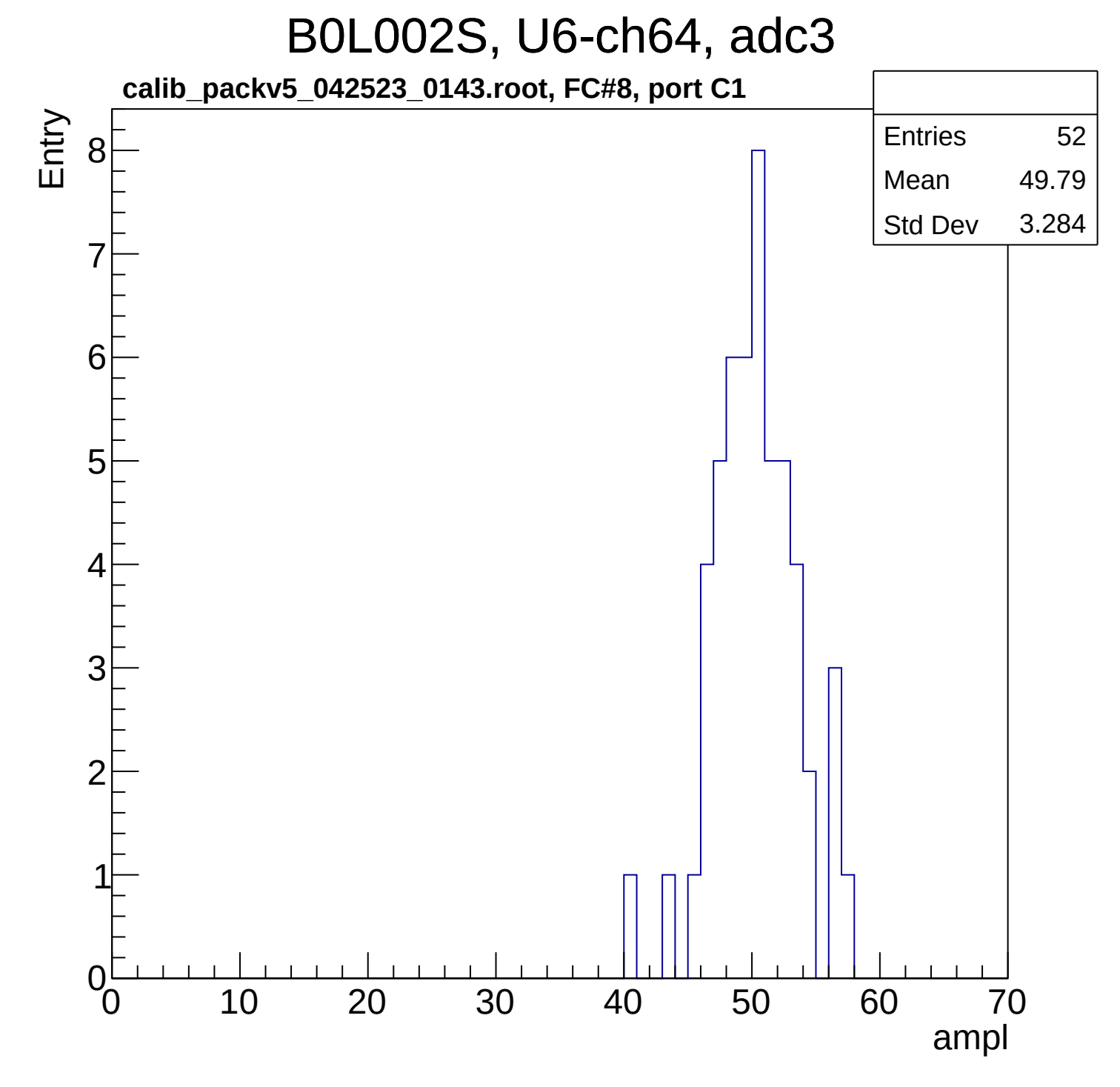
B0L002S, U6-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 49.79
Std Dev 3.284
Entry
ampl
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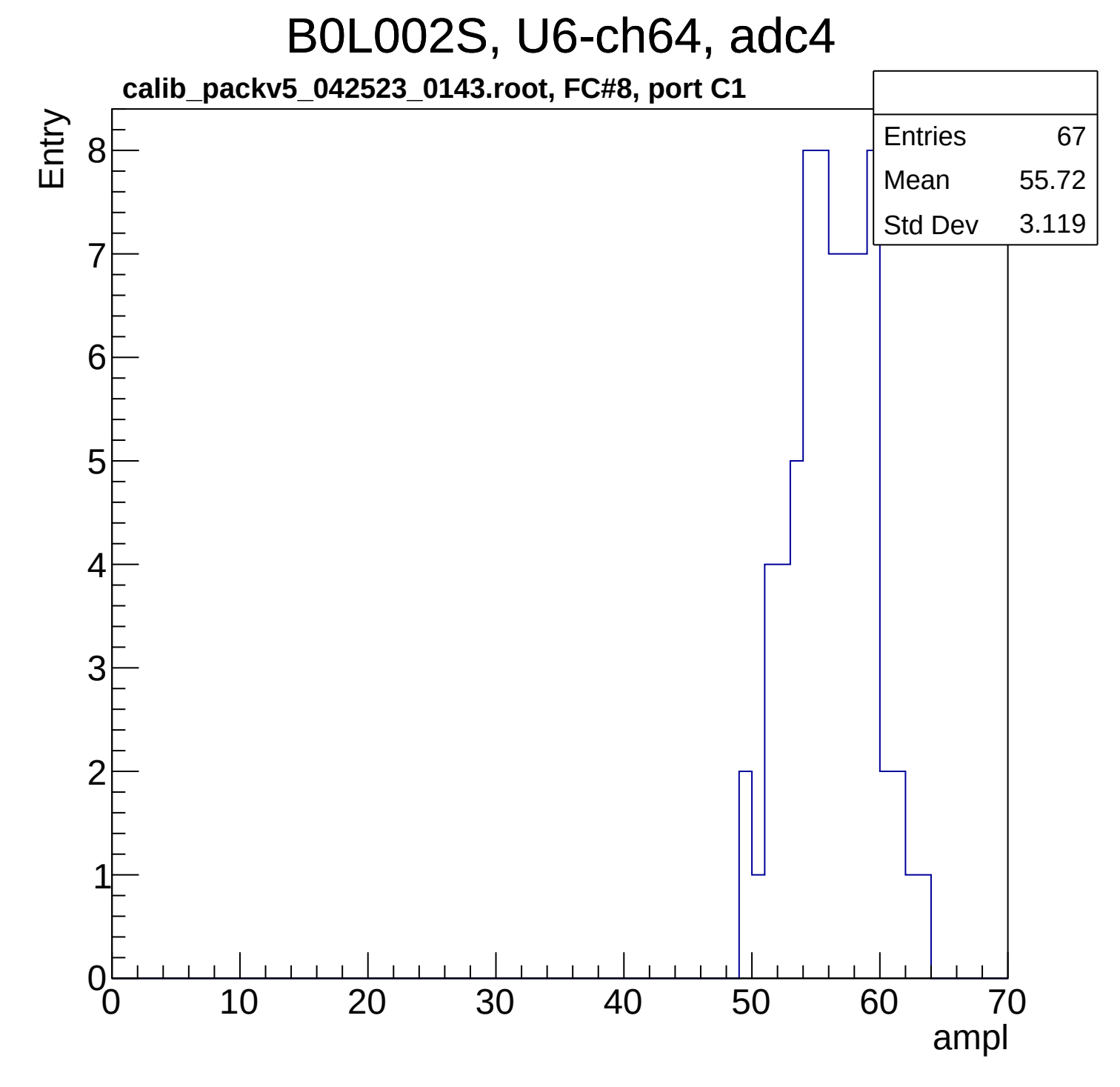
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 55.72
Std Dev 3.119
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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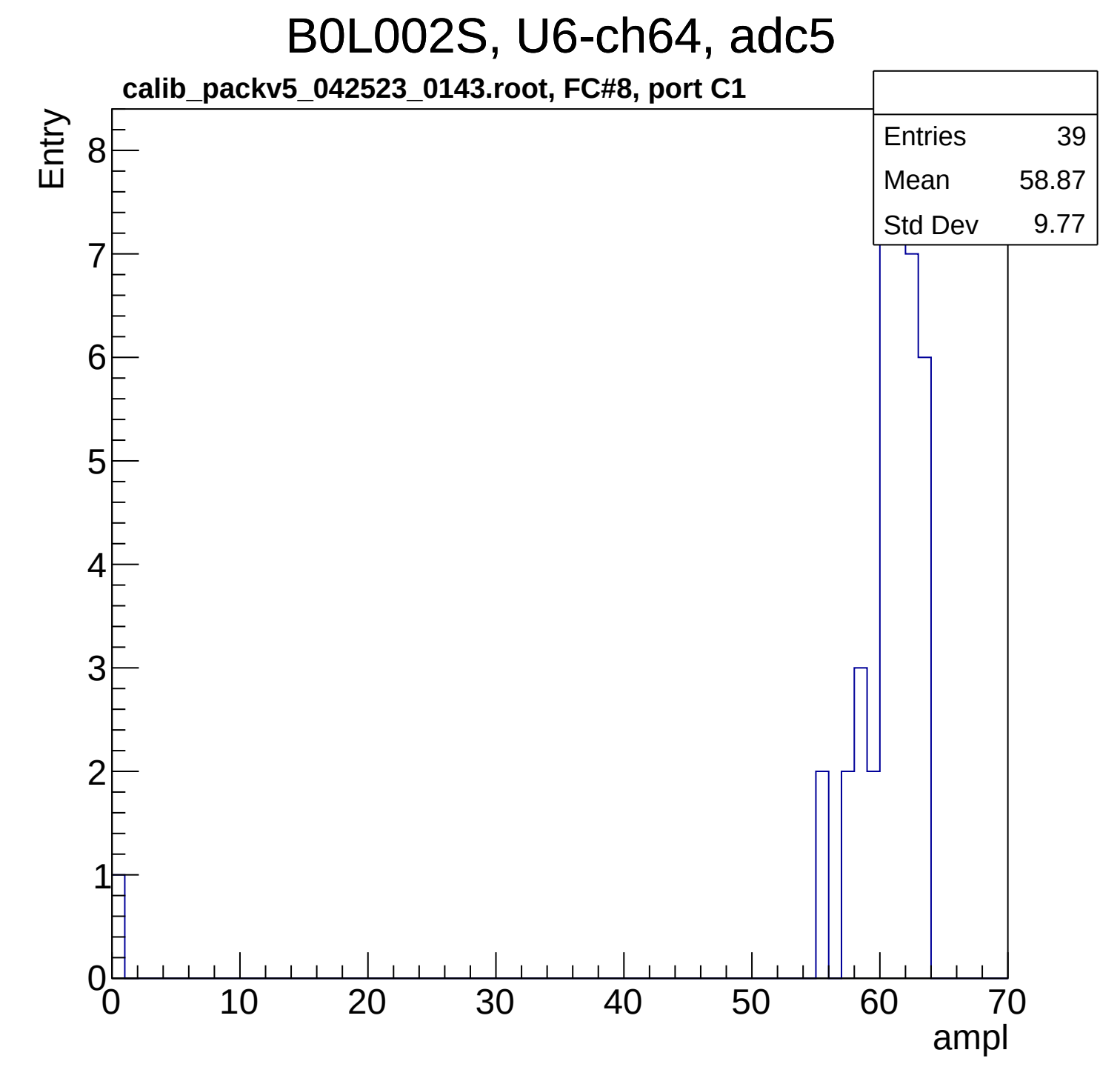
B0L002S, U6-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.87
Std Dev 9.77
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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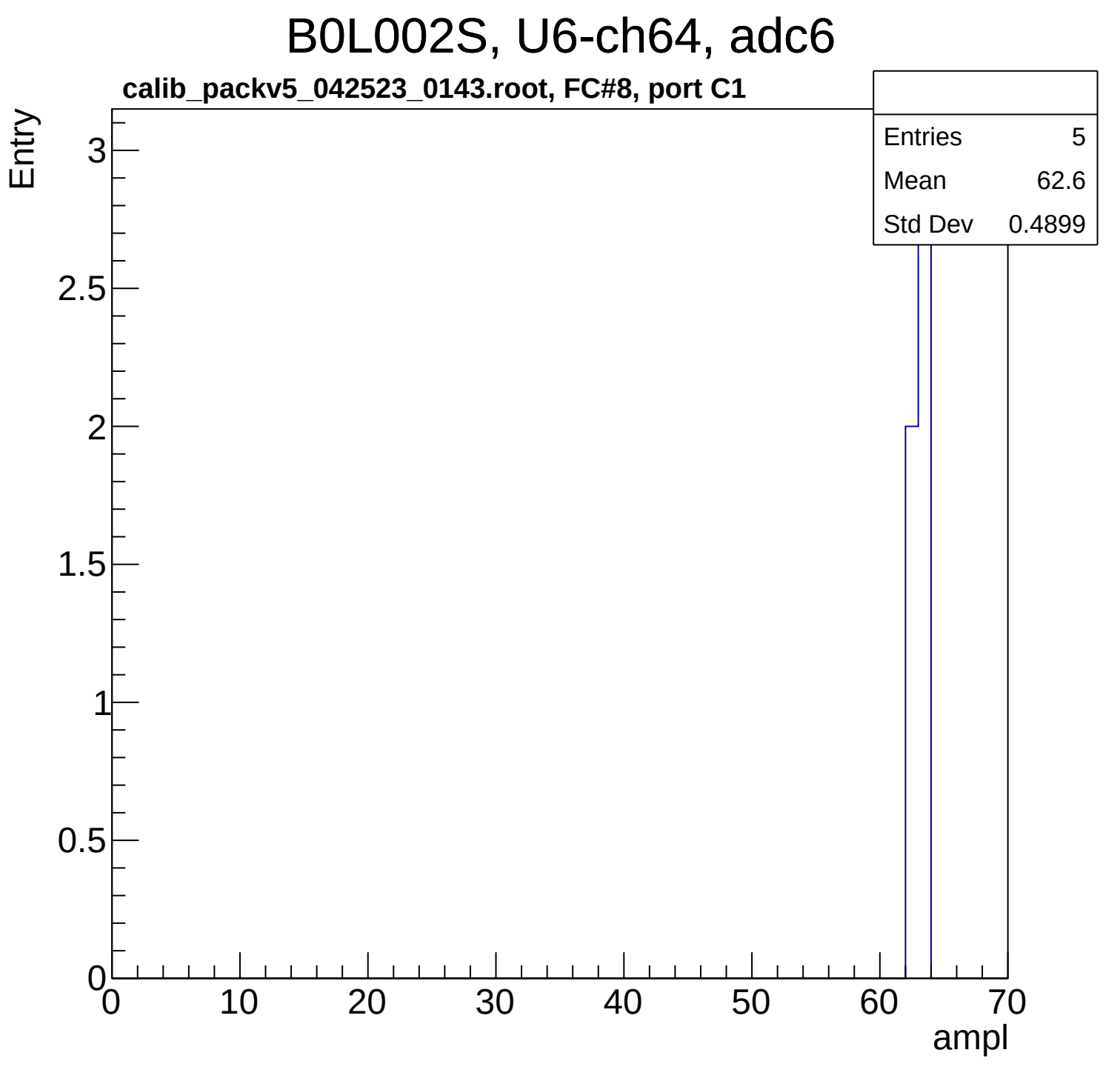
B0L002S, U6-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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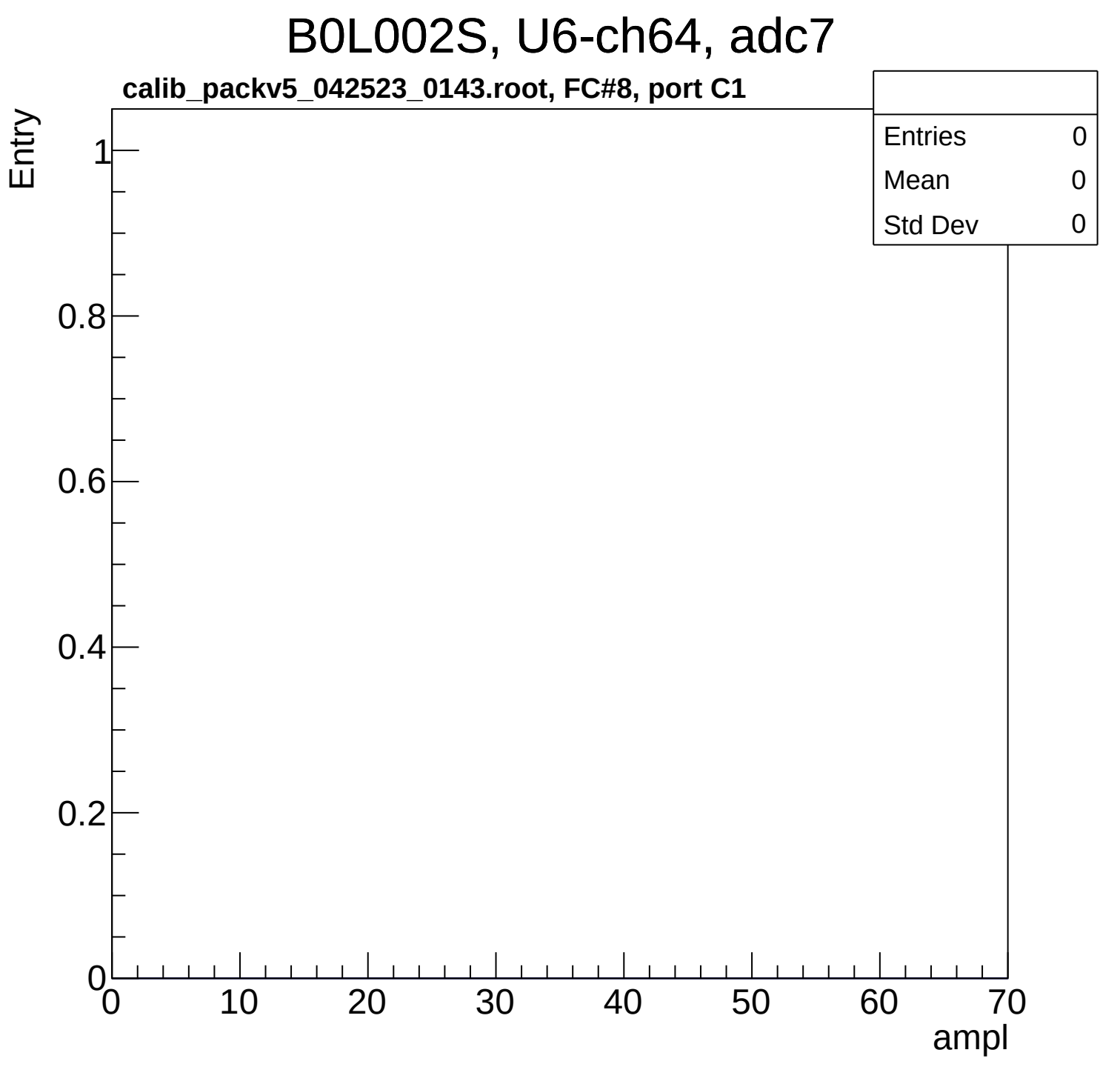
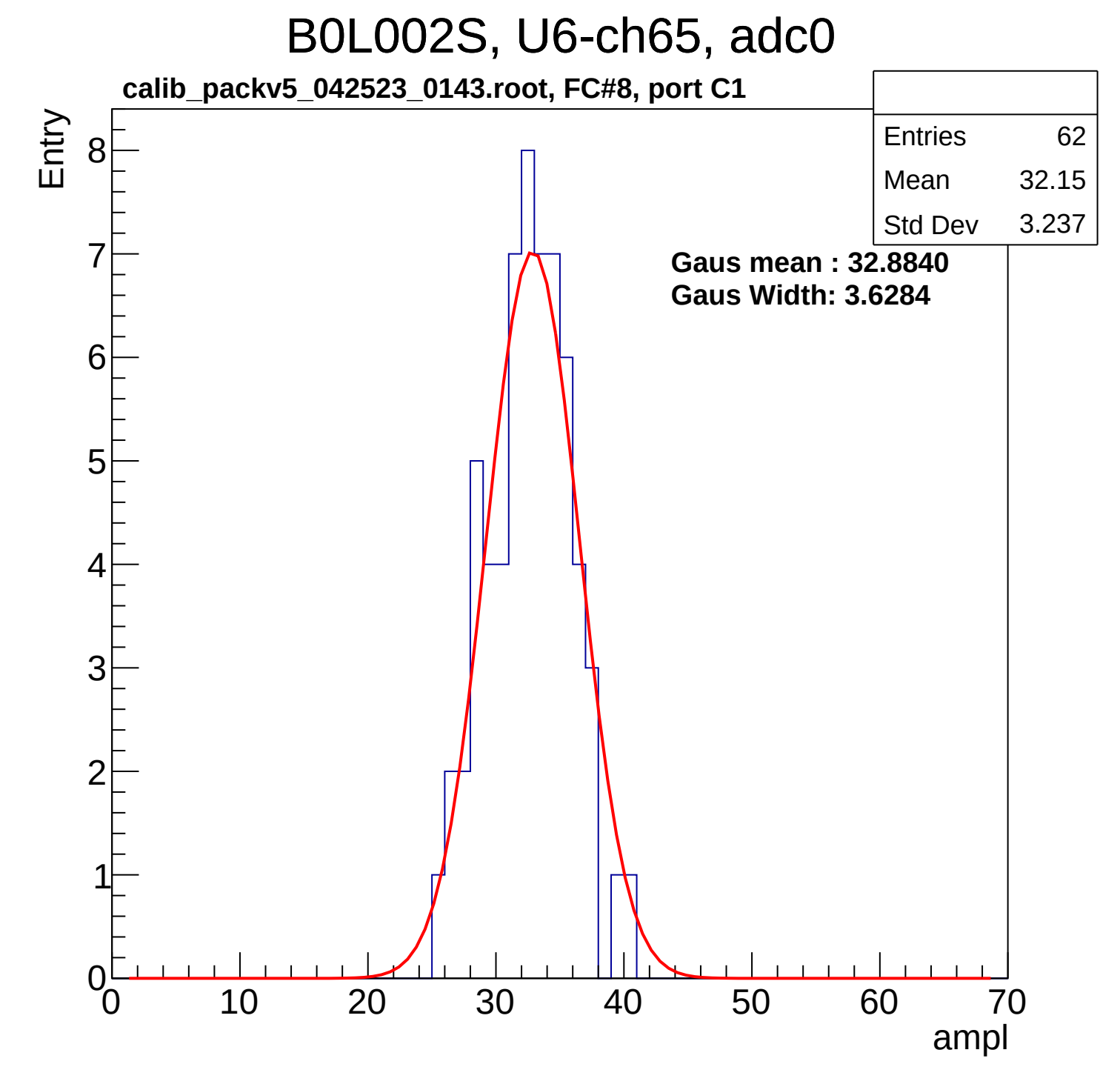
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 32.15
Std Dev 3.237
Gaus mean : 32.8840
Gaus Width: 3.6284
Entry
ampl
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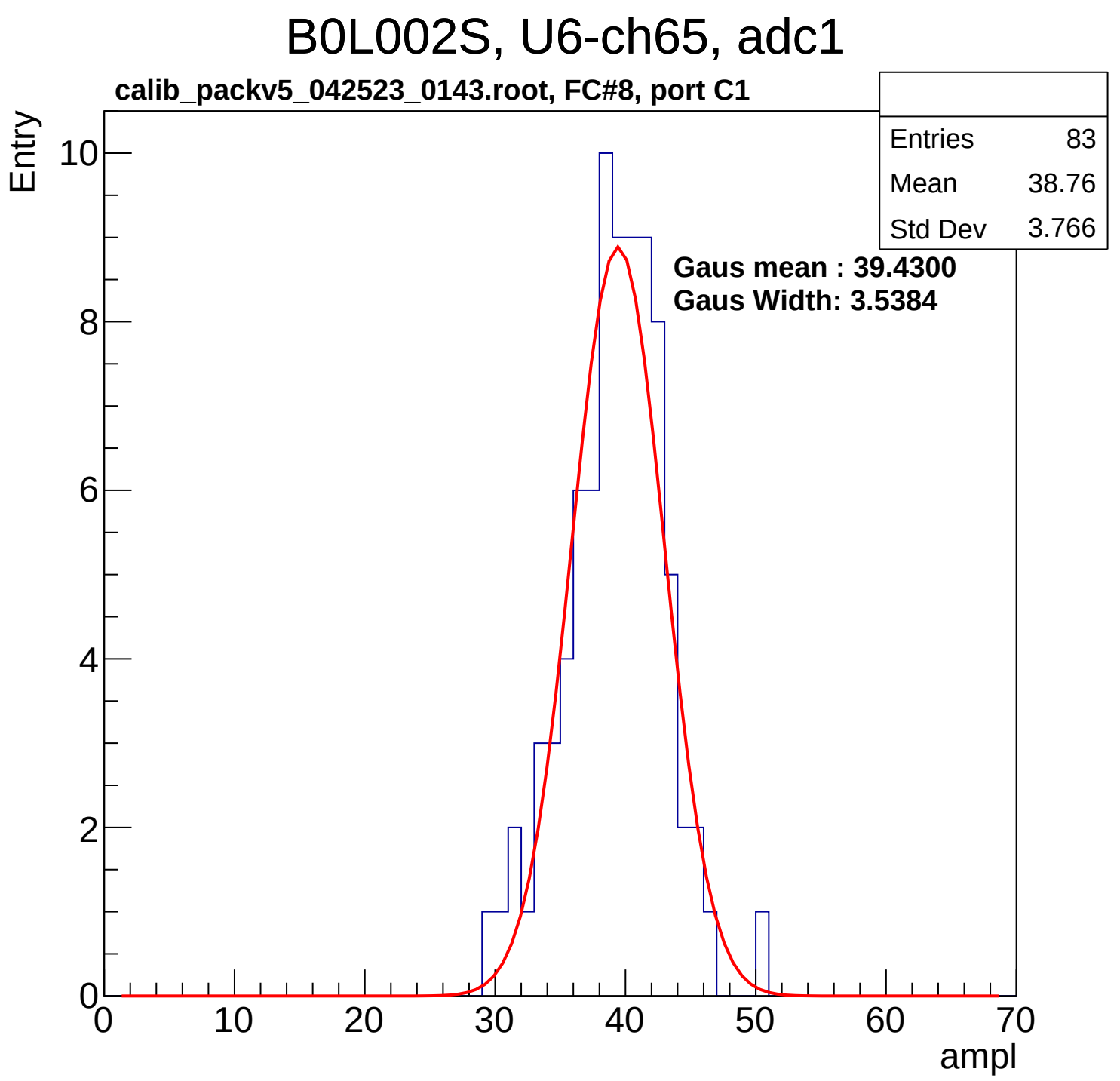
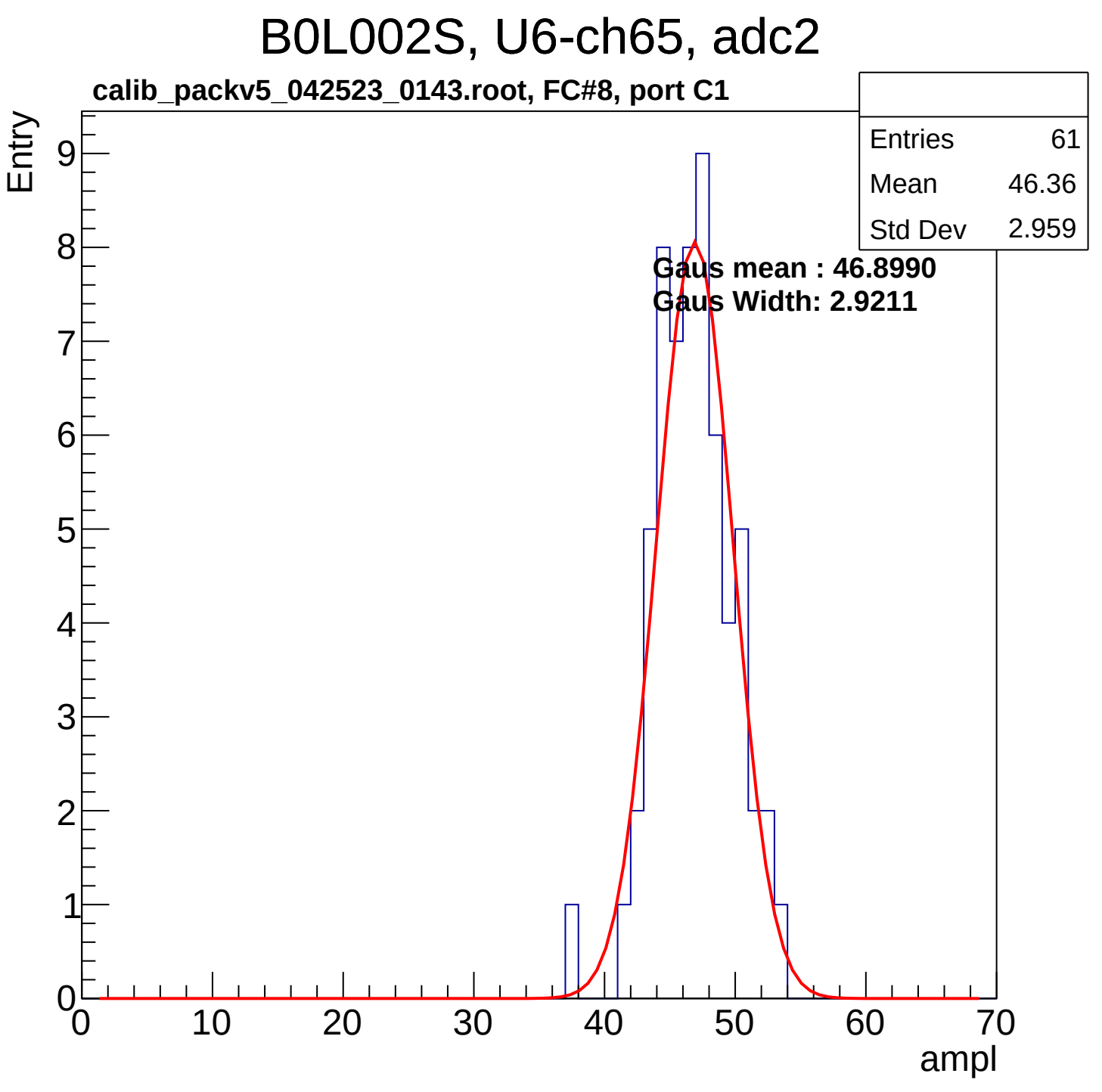
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 38.76
Std Dev 3.766
Gaus mean : 39.4300
Gaus Width: 3.5384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 46.36
Std Dev 2.959
Gaus mean : 46.8990
Gaus Width: 2.9211
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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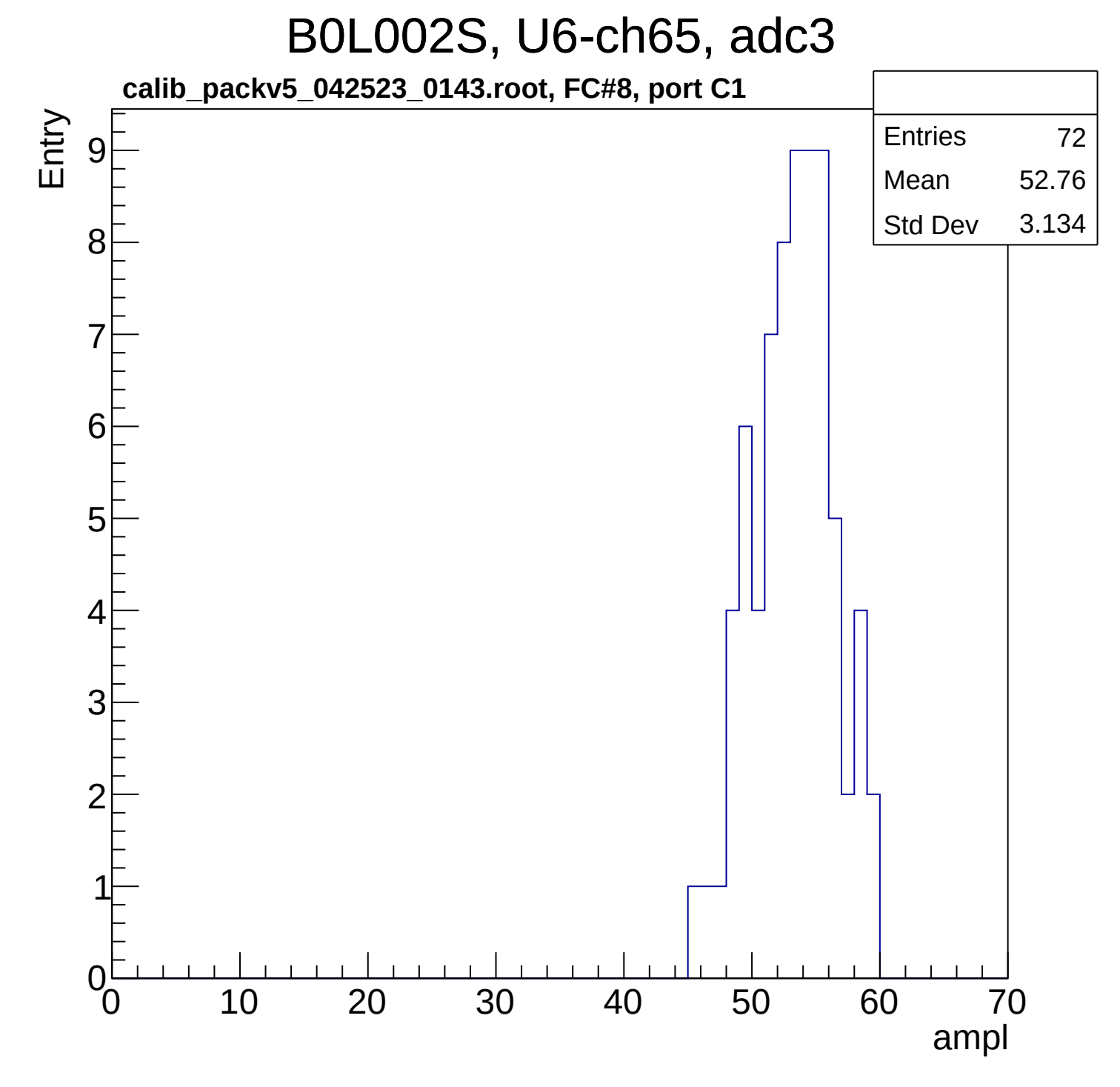
B0L002S, U6-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 52.76
Std Dev 3.134
Entry
ampl
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2
3
4
5
6
7
8
9
0
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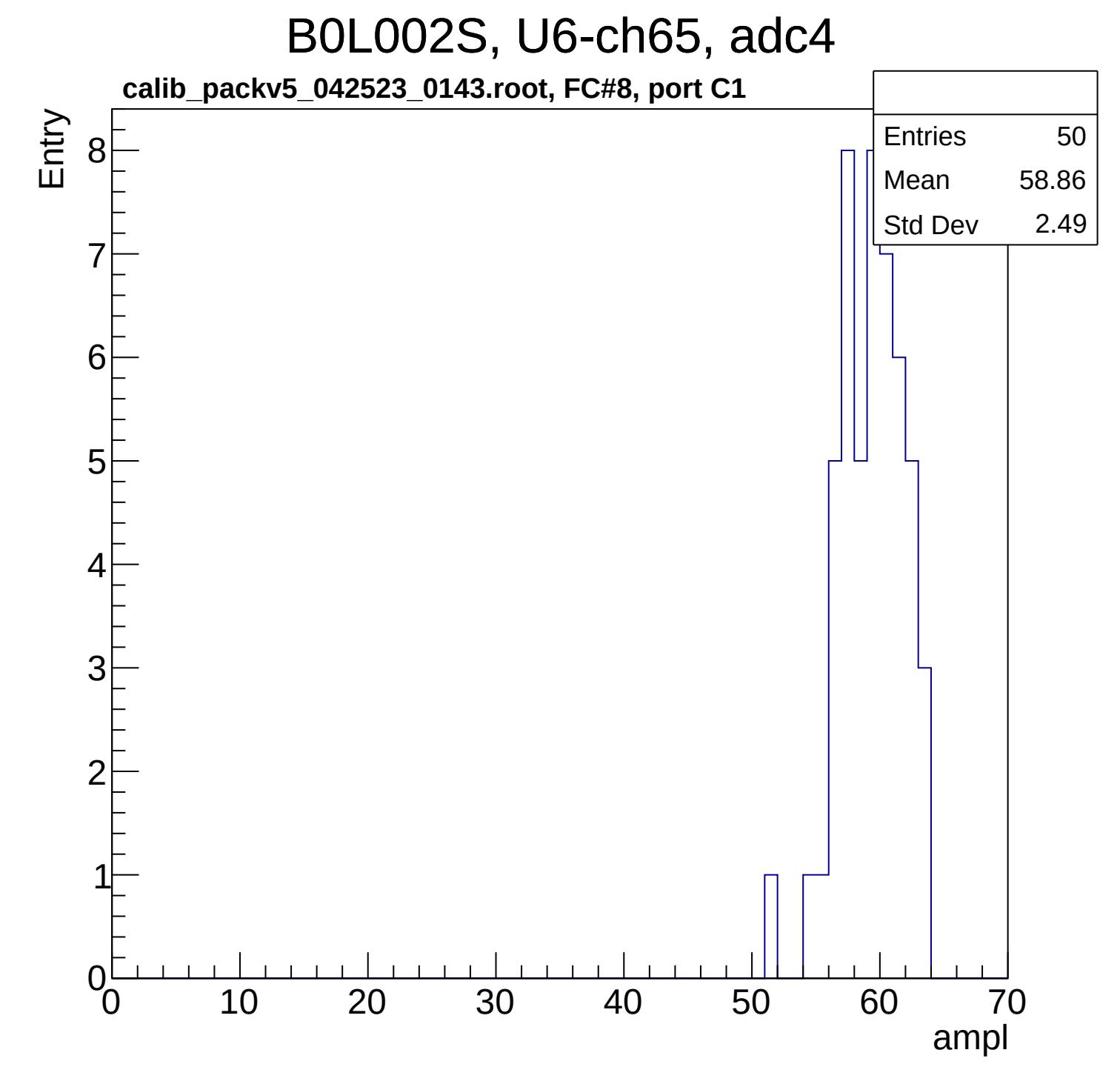
B0L002S, U6-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.86
Std Dev 2.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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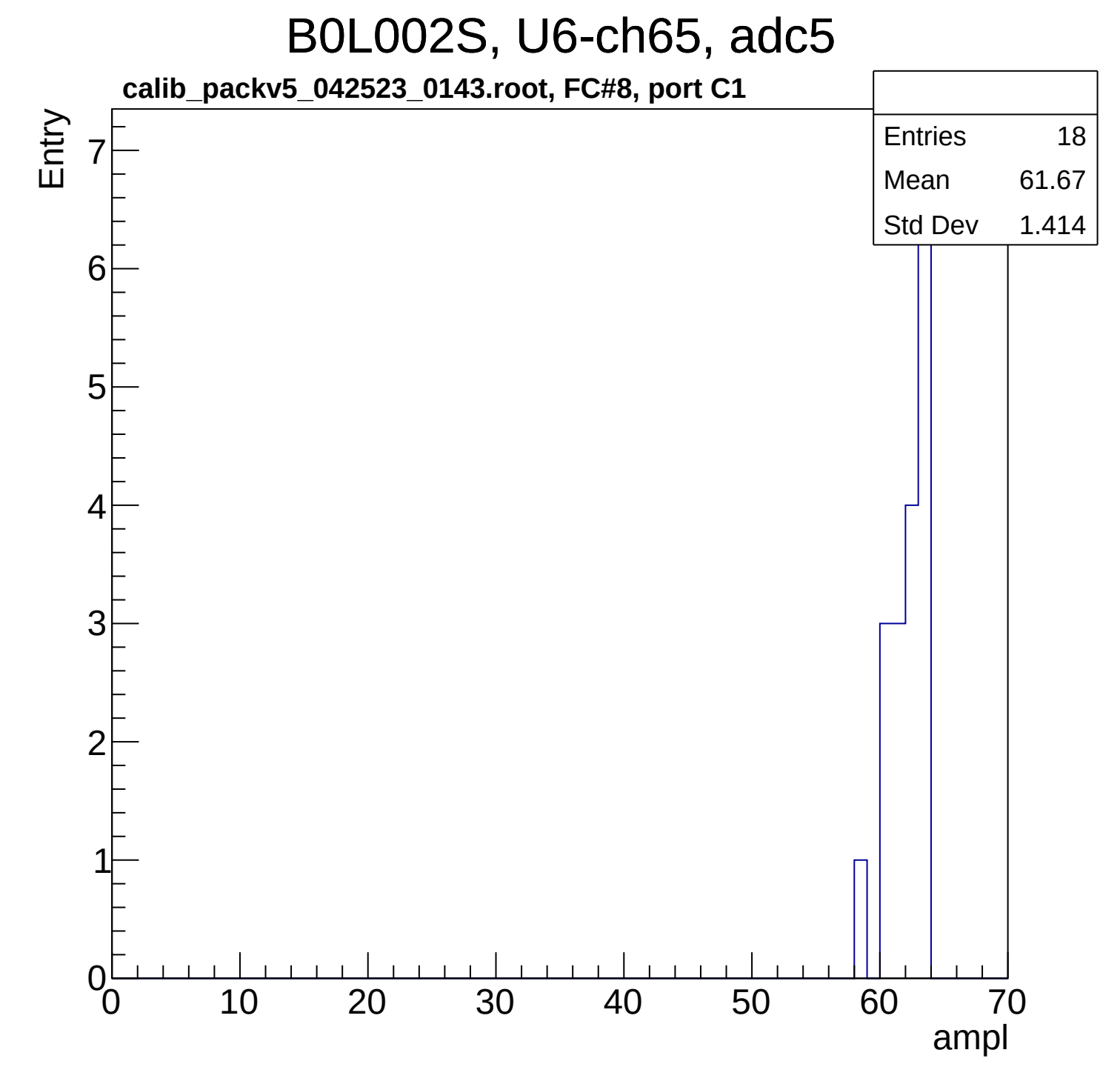
B0L002S, U6-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 61.67
Std Dev 1.414
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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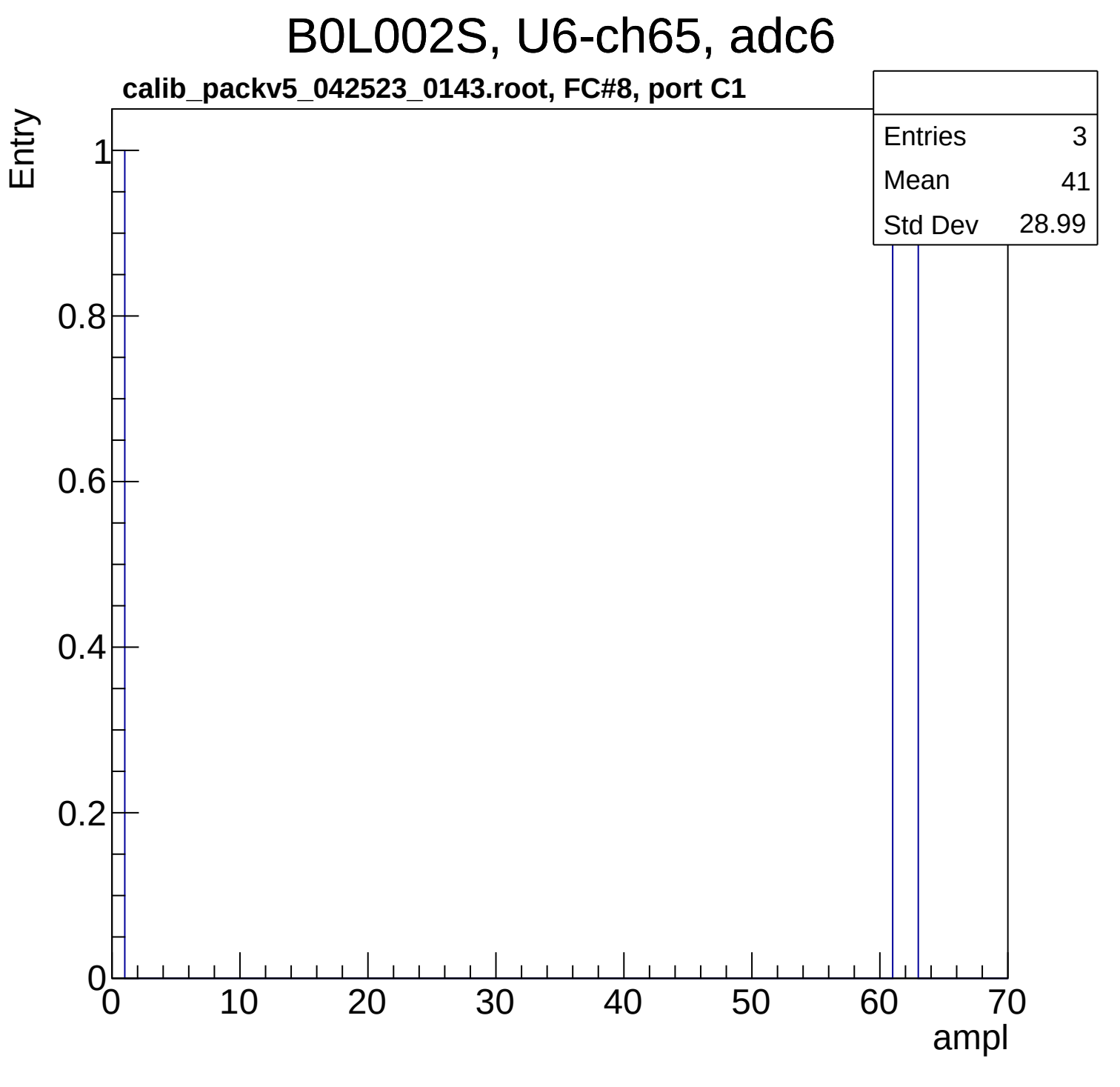
B0L002S, U6-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41
Std Dev 28.99
Entry
1
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0.6
0.4
0.2
0
0
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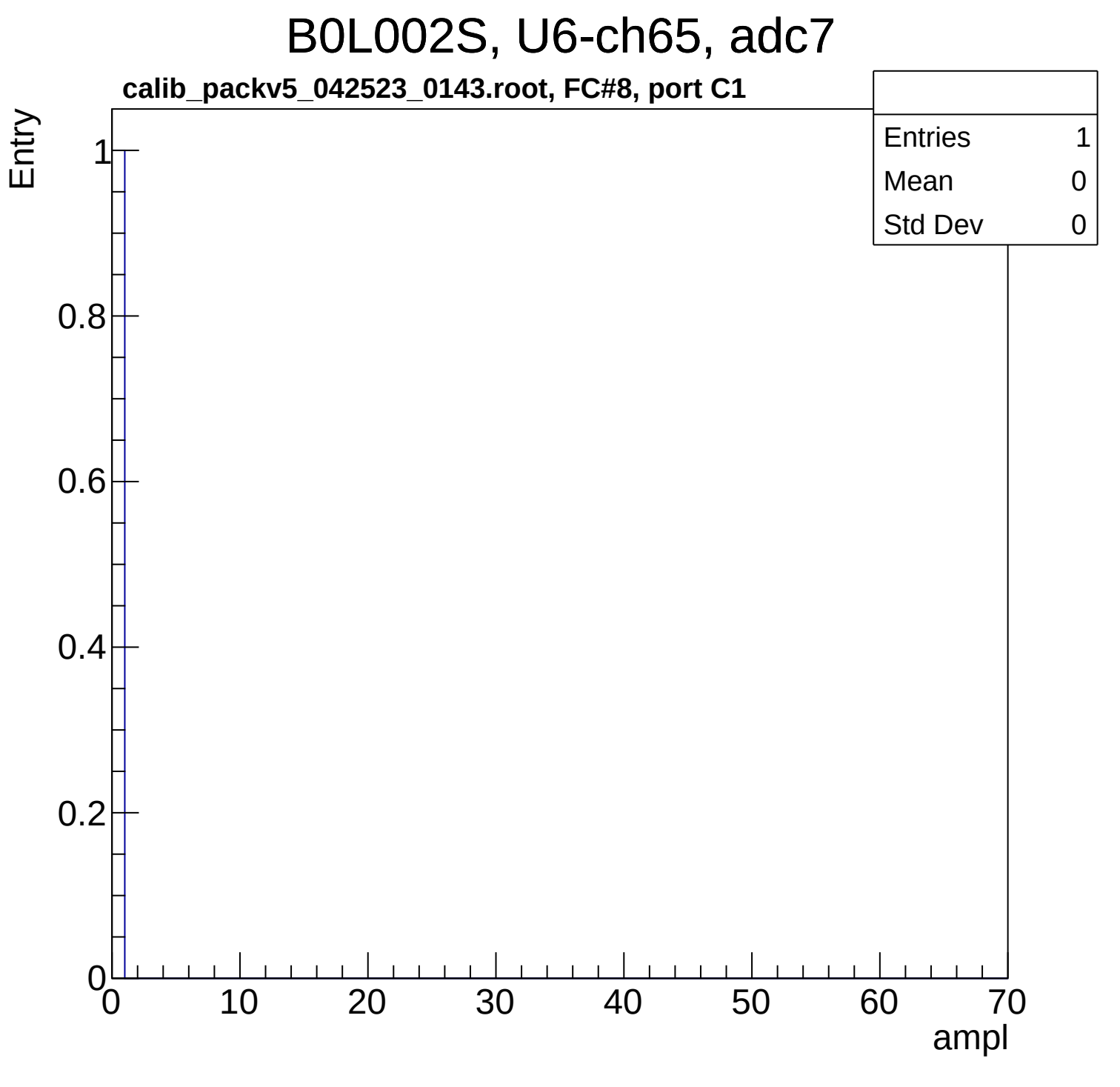
70
ampl


B0L002S, U6-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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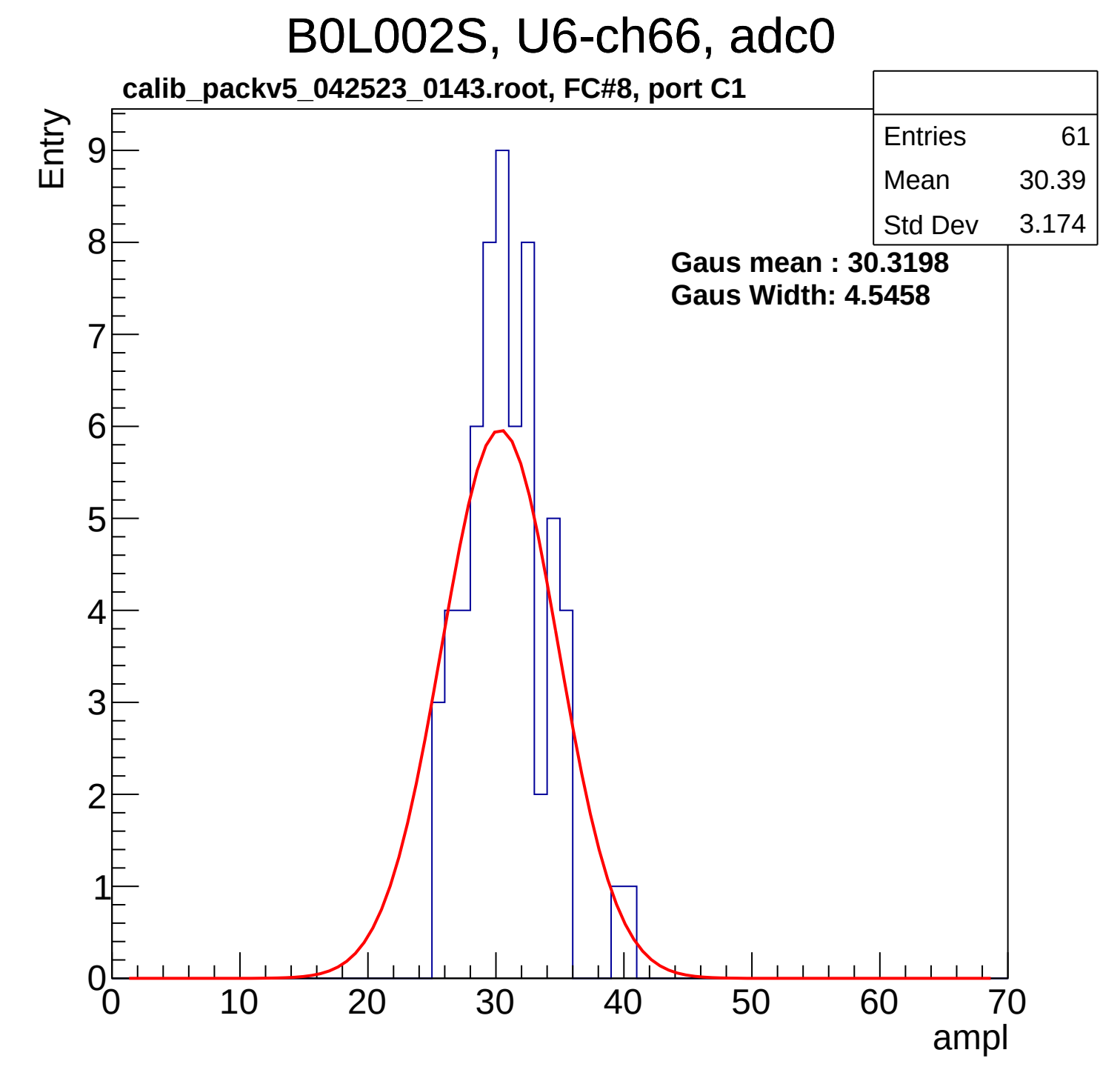
40
50
60
70
ampl


B0L002S, U6-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.39
Std Dev 3.174
Gaus mean : 30.3198
Gaus Width: 4.5458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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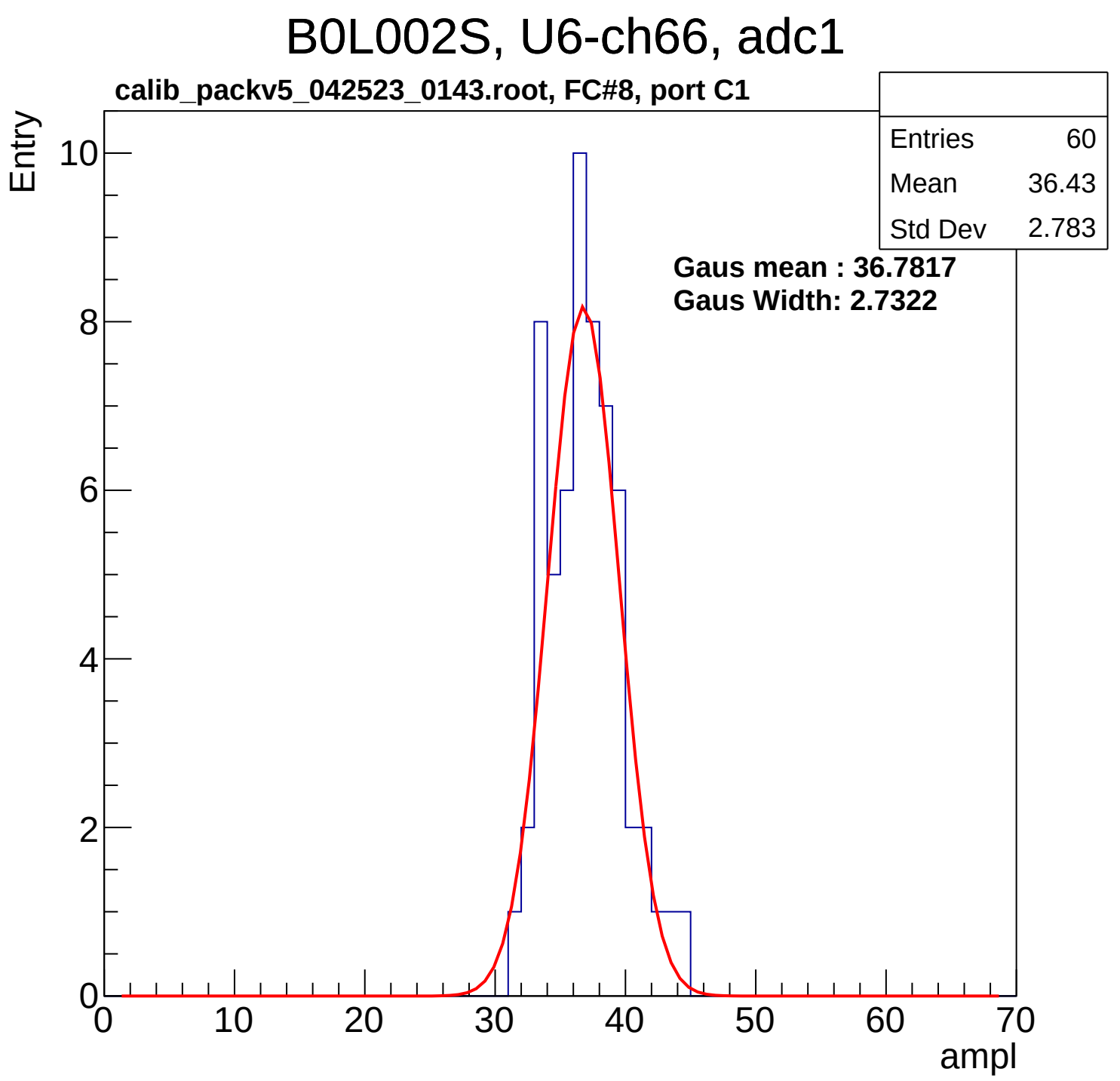
B0L002S, U6-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.43
Std Dev 2.783
Gaus mean : 36.7817
Gaus Width: 2.7322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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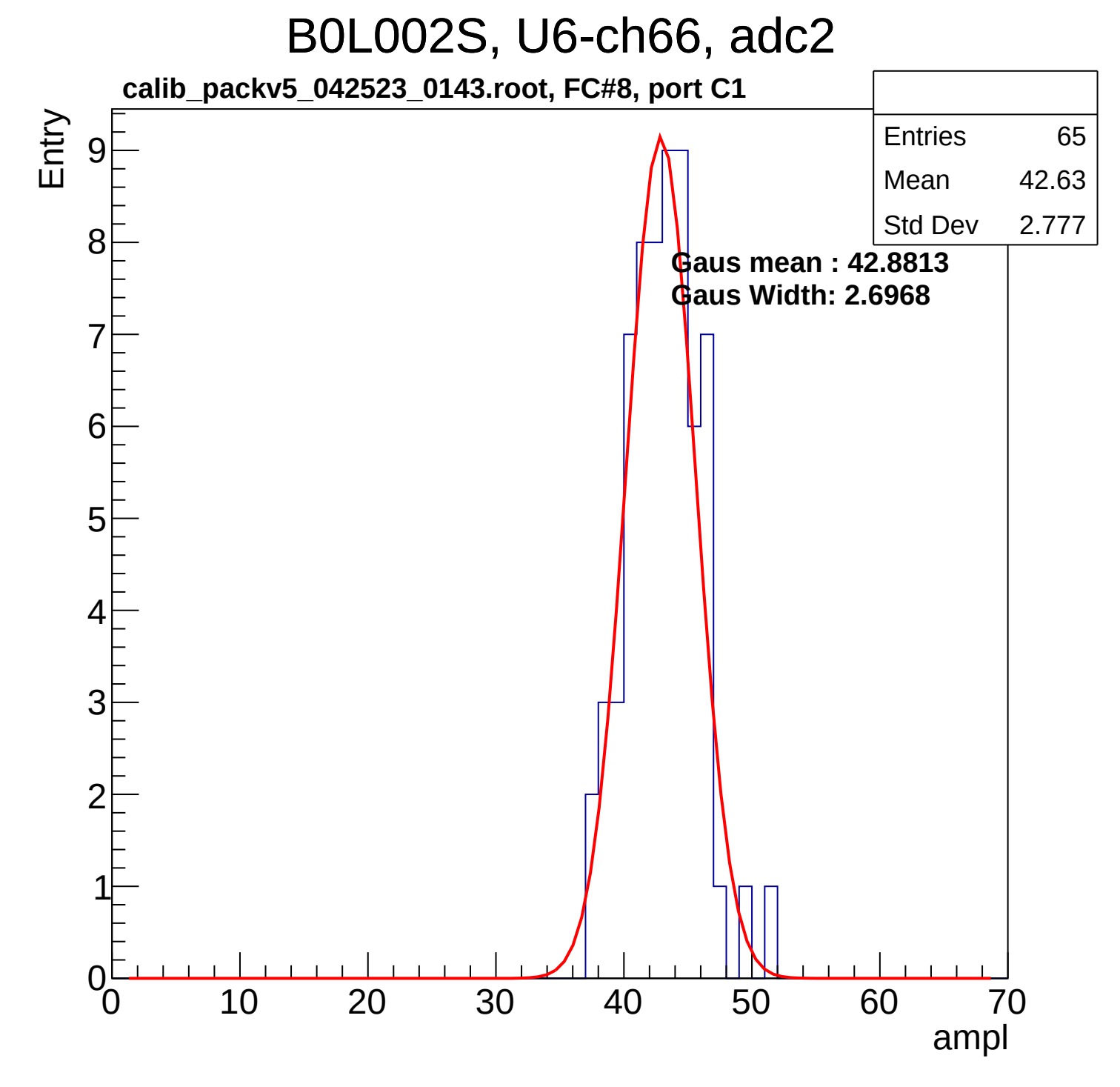
B0L002S, U6-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 42.63
Std Dev 2.777
Gaus mean : 42.8813
Gaus Width: 2.6968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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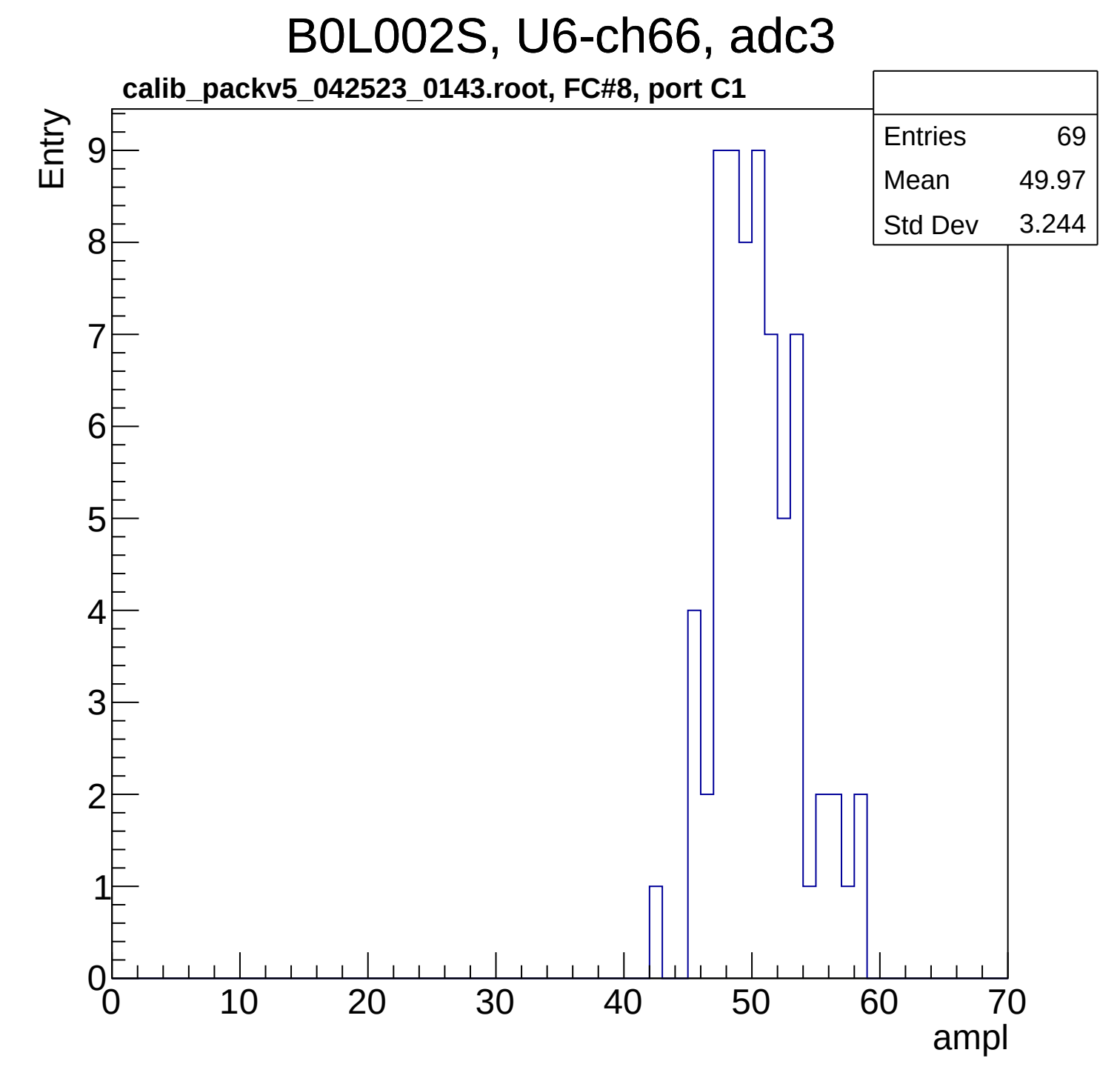
B0L002S, U6-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 49.97
Std Dev 3.244
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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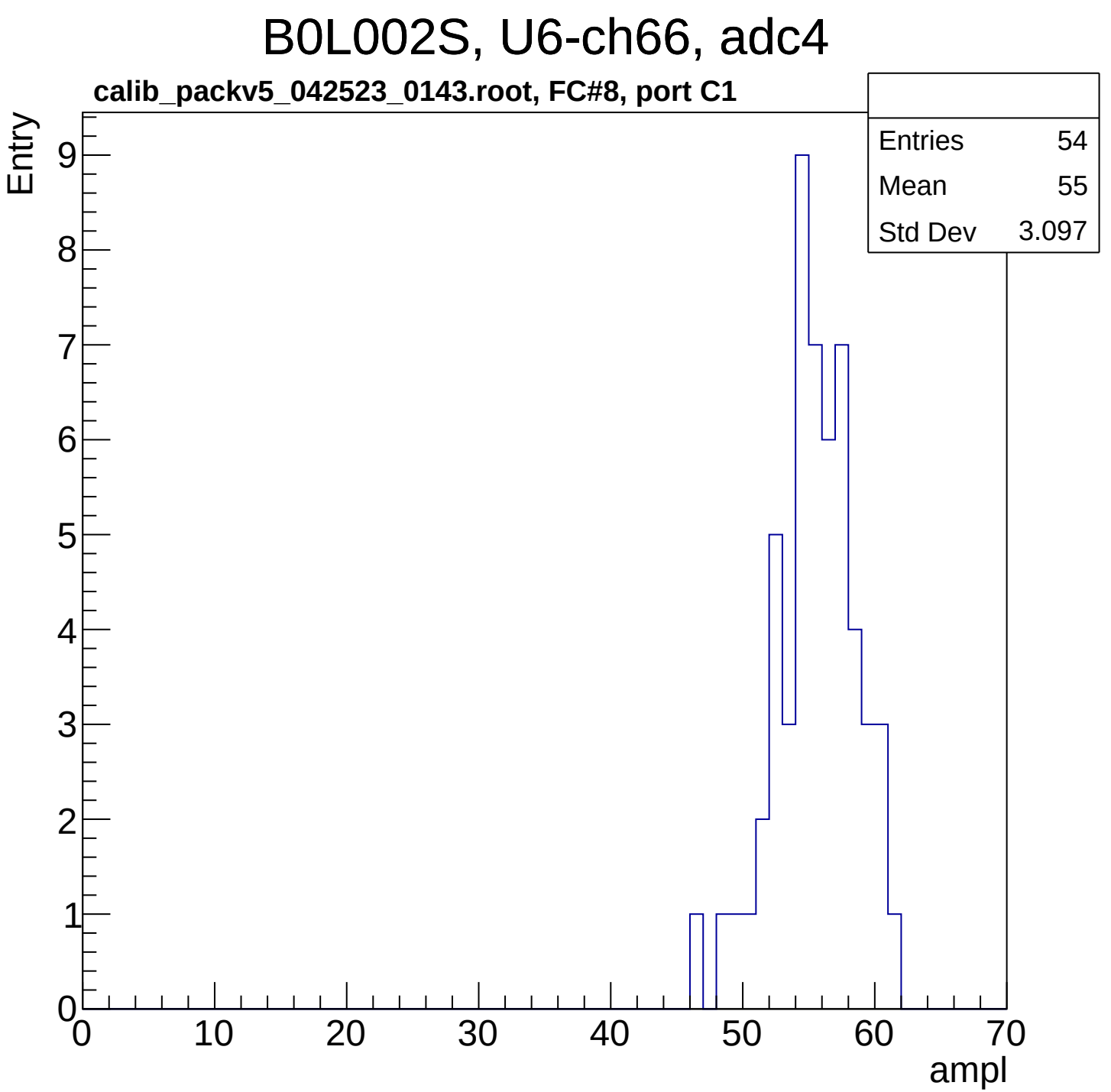
B0L002S, U6-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 55
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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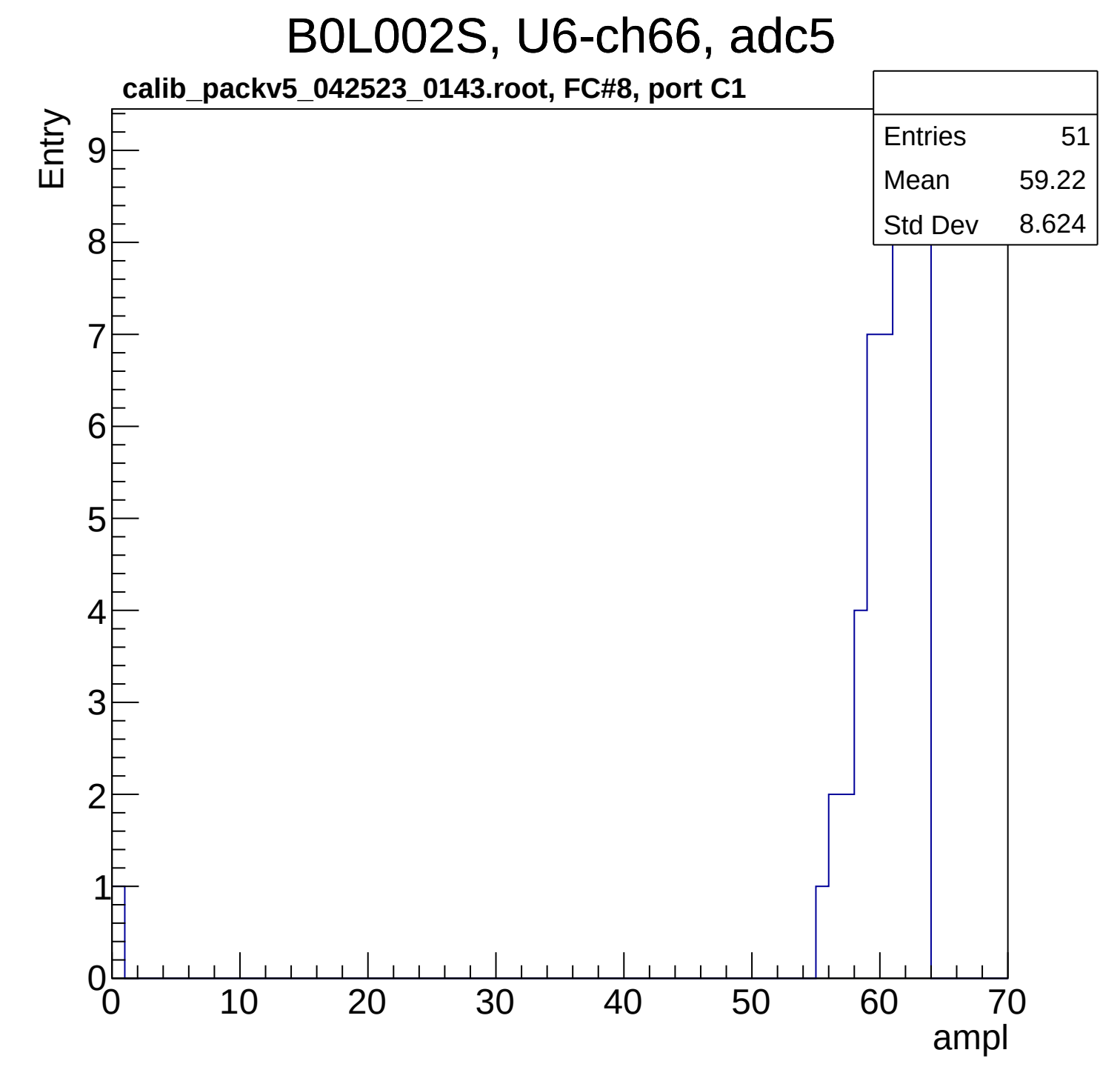
B0L002S, U6-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 59.22
Std Dev 8.624
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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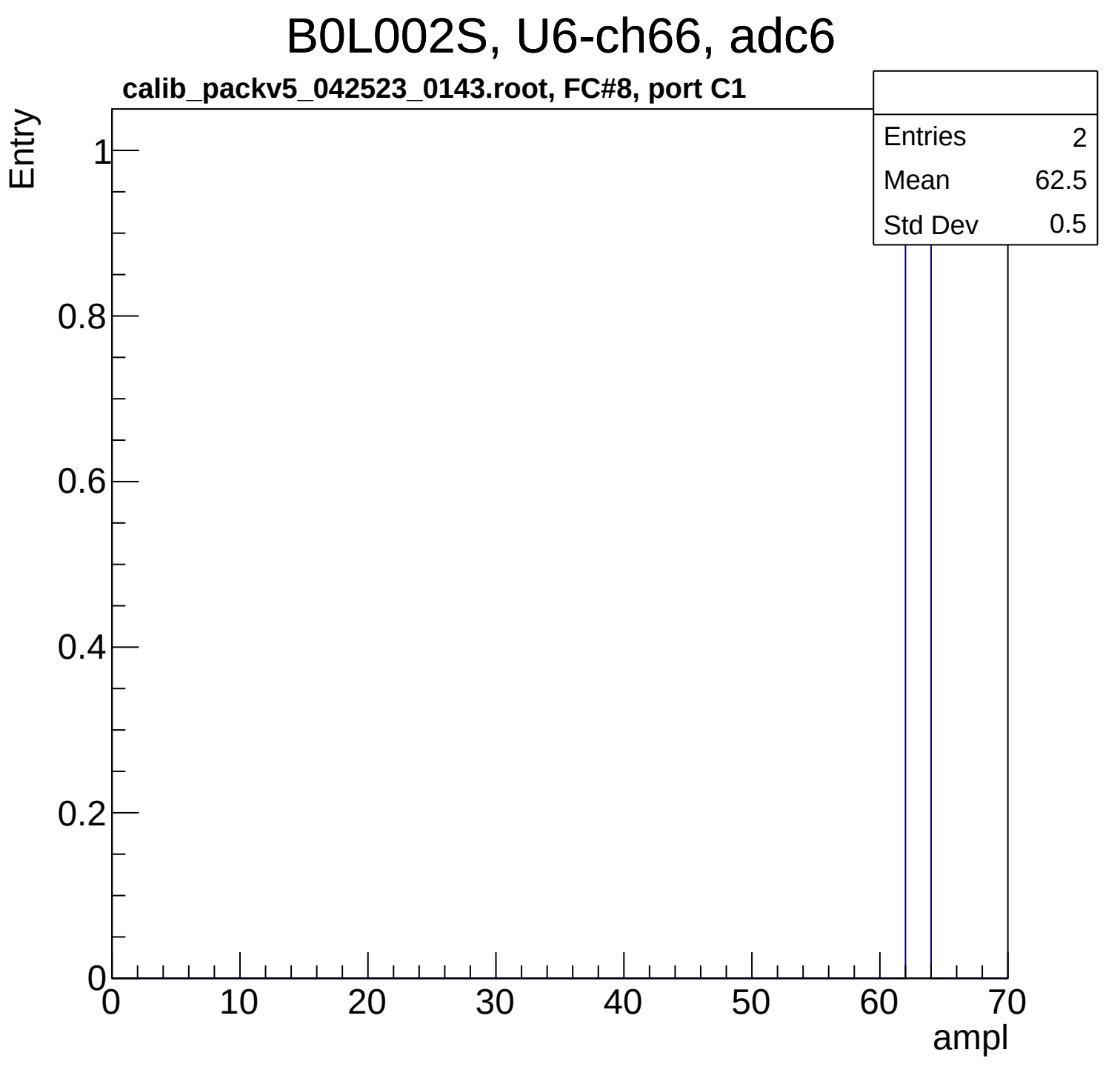
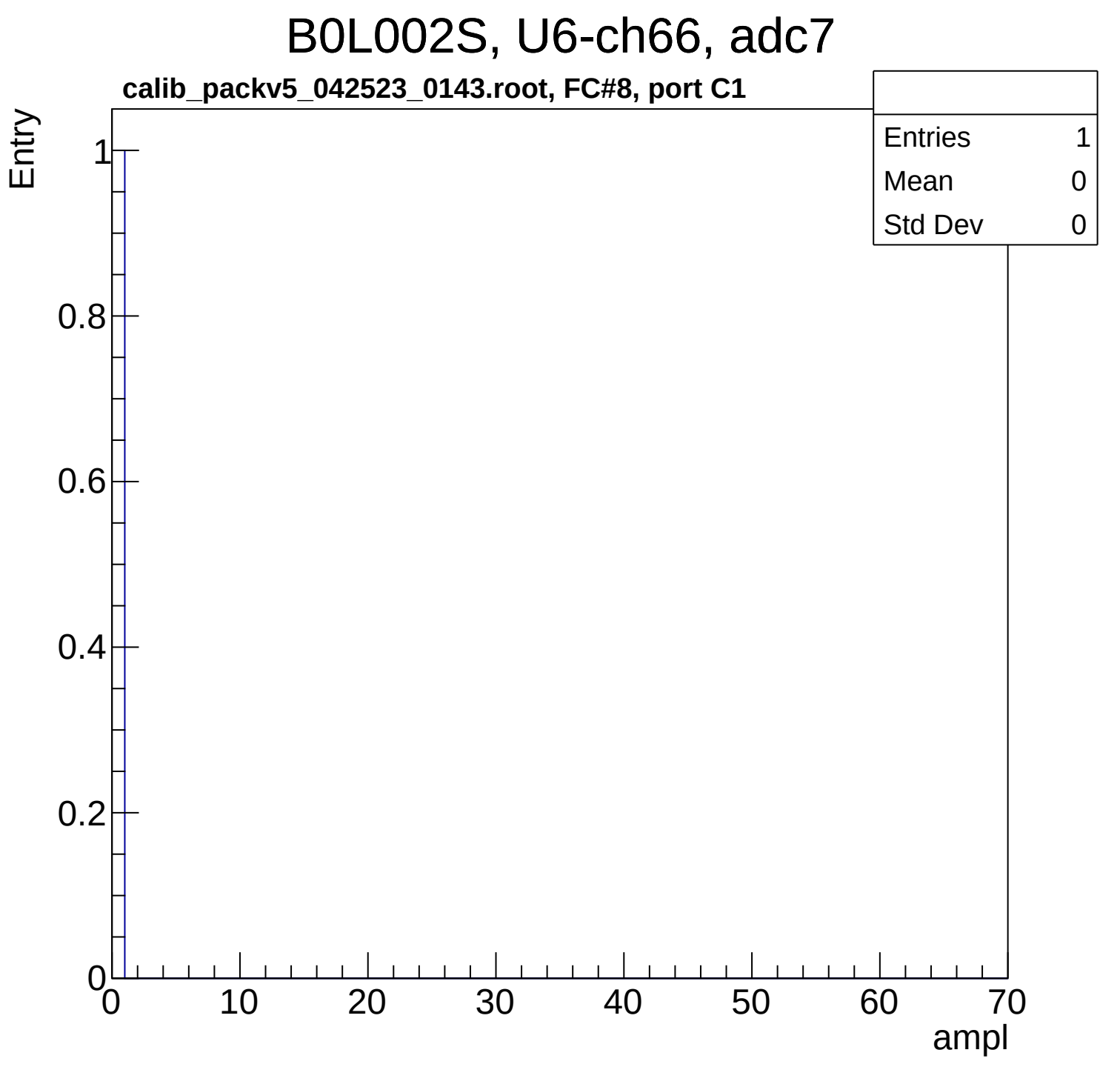

B0L002S, U6-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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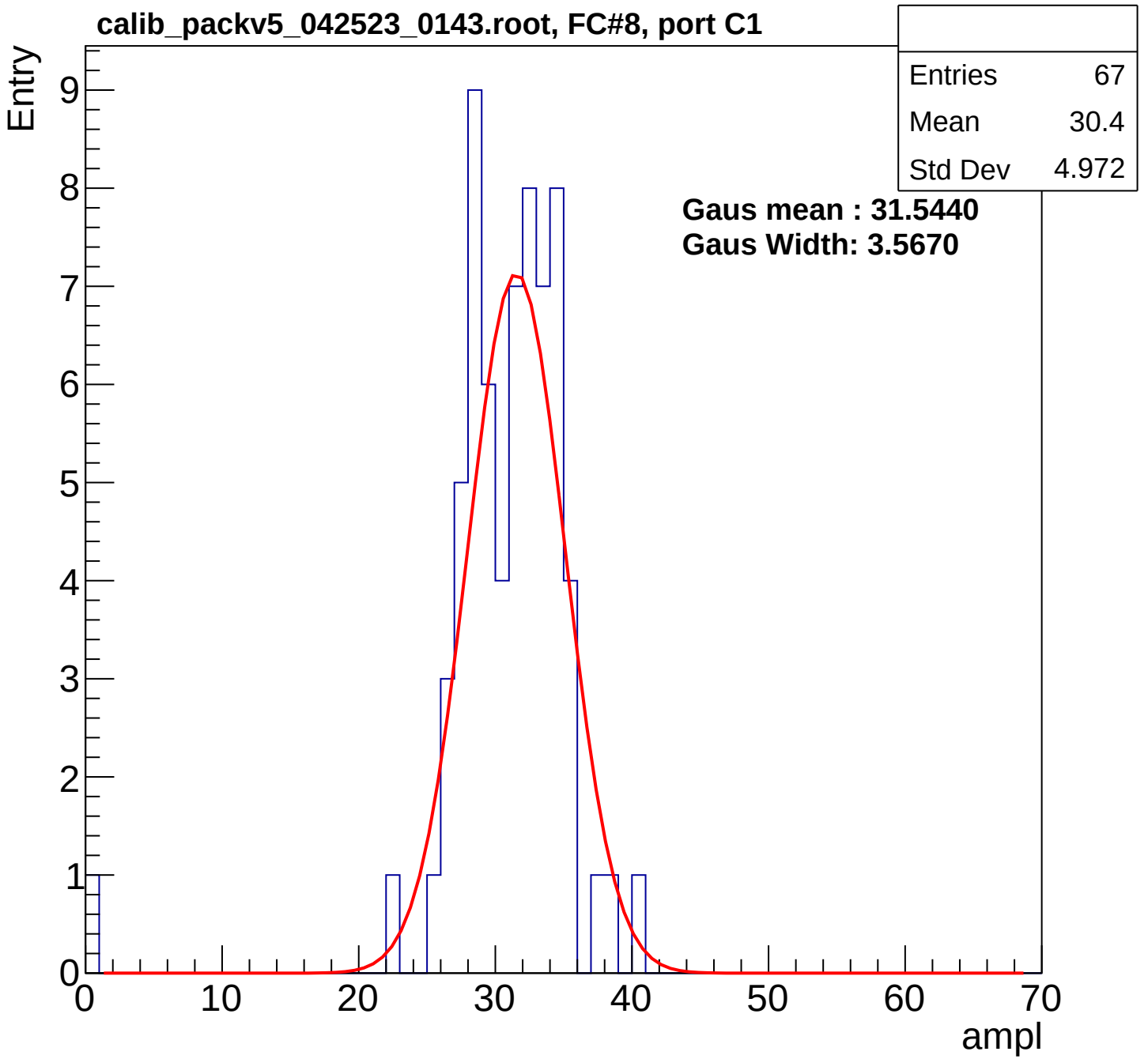
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.4
Std Dev 4.972
Gaus mean : 31.5440
Gaus Width: 3.5670
Entry
ampl
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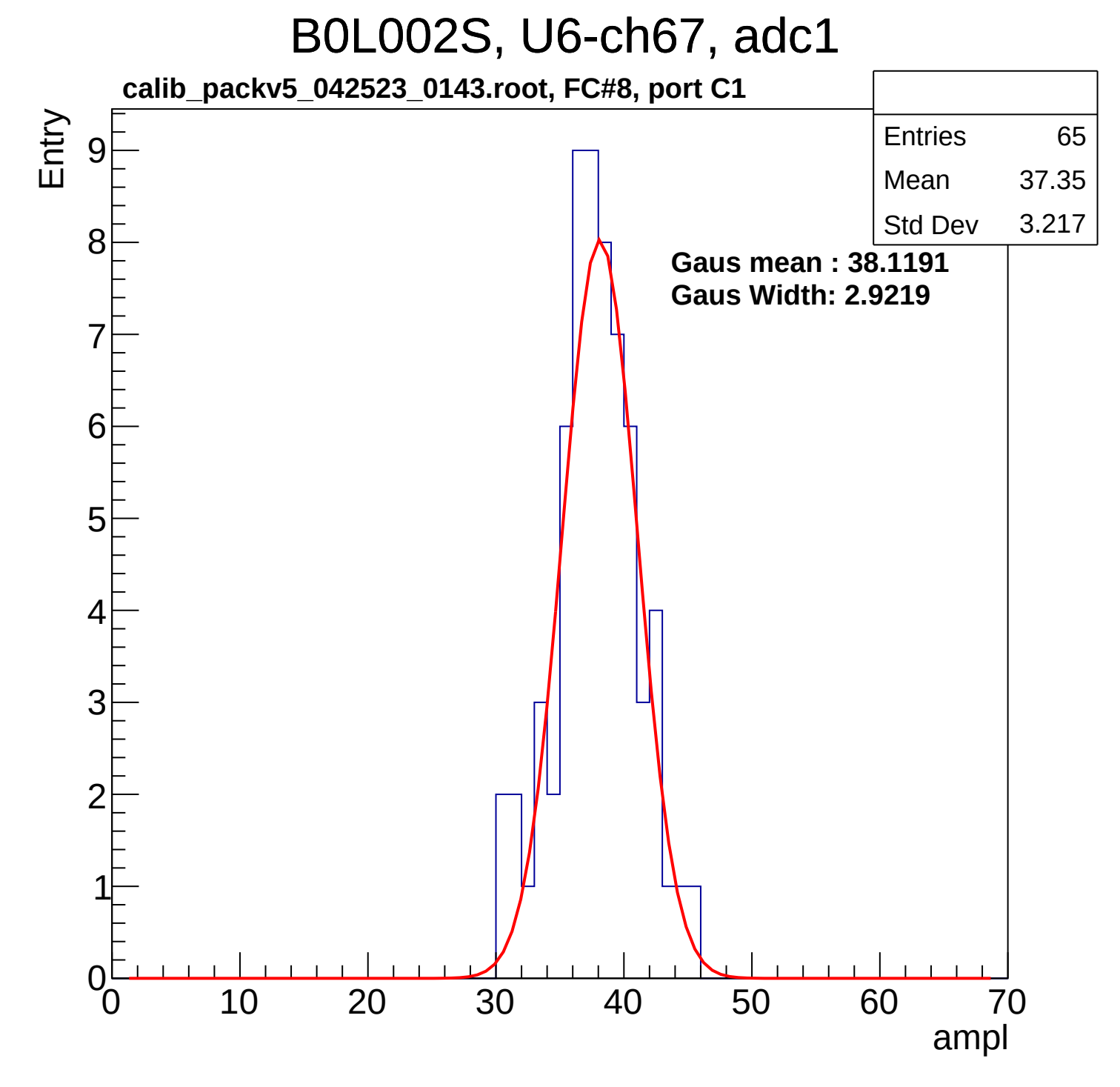
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.35
Std Dev 3.217
Gaus mean : 38.1191
Gaus Width: 2.9219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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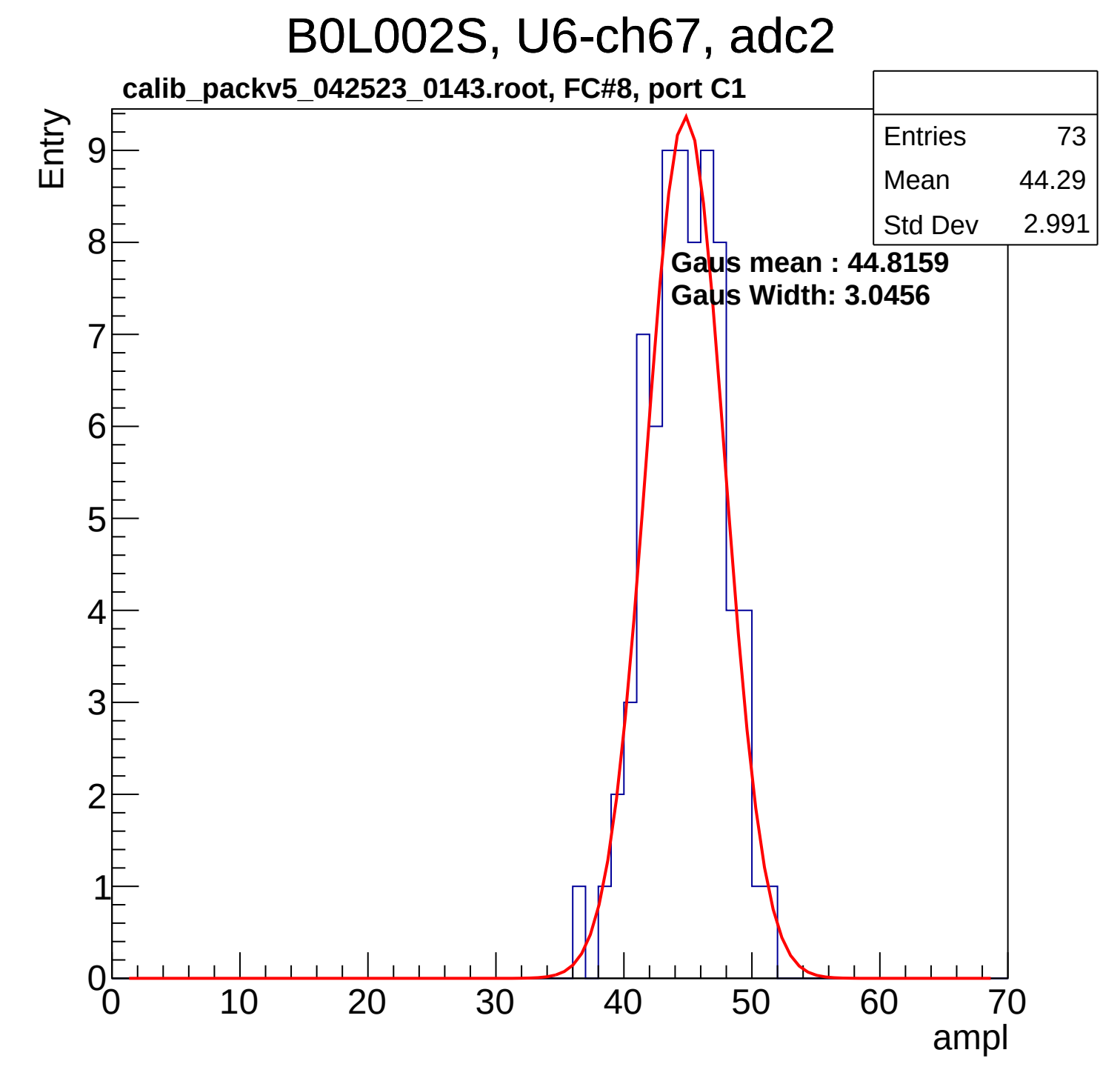
B0L002S, U6-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.29
Std Dev 2.991
Gaus mean : 44.8159
Gaus Width: 3.0456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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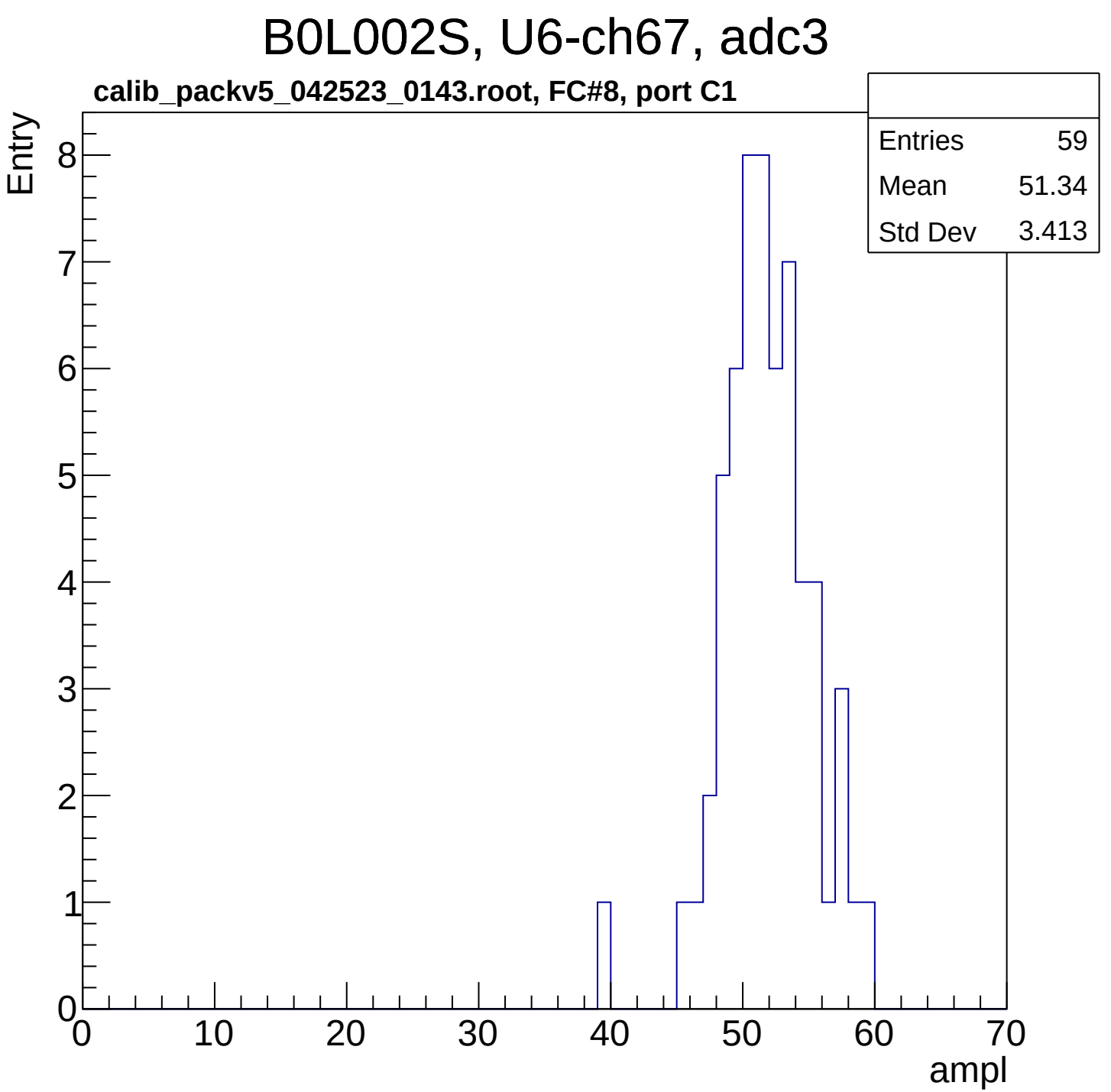
B0L002S, U6-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.34
Std Dev 3.413
Entry
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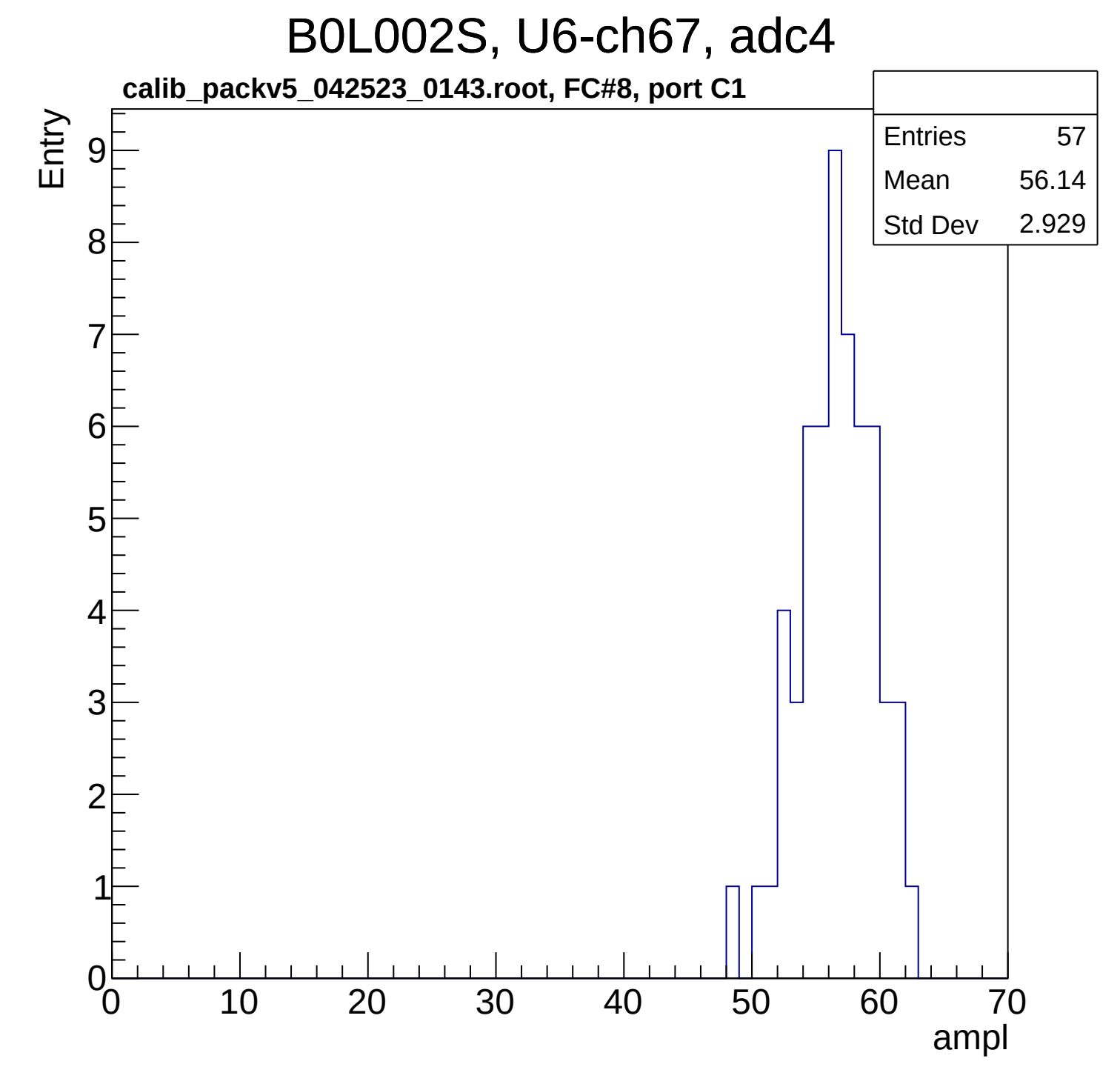
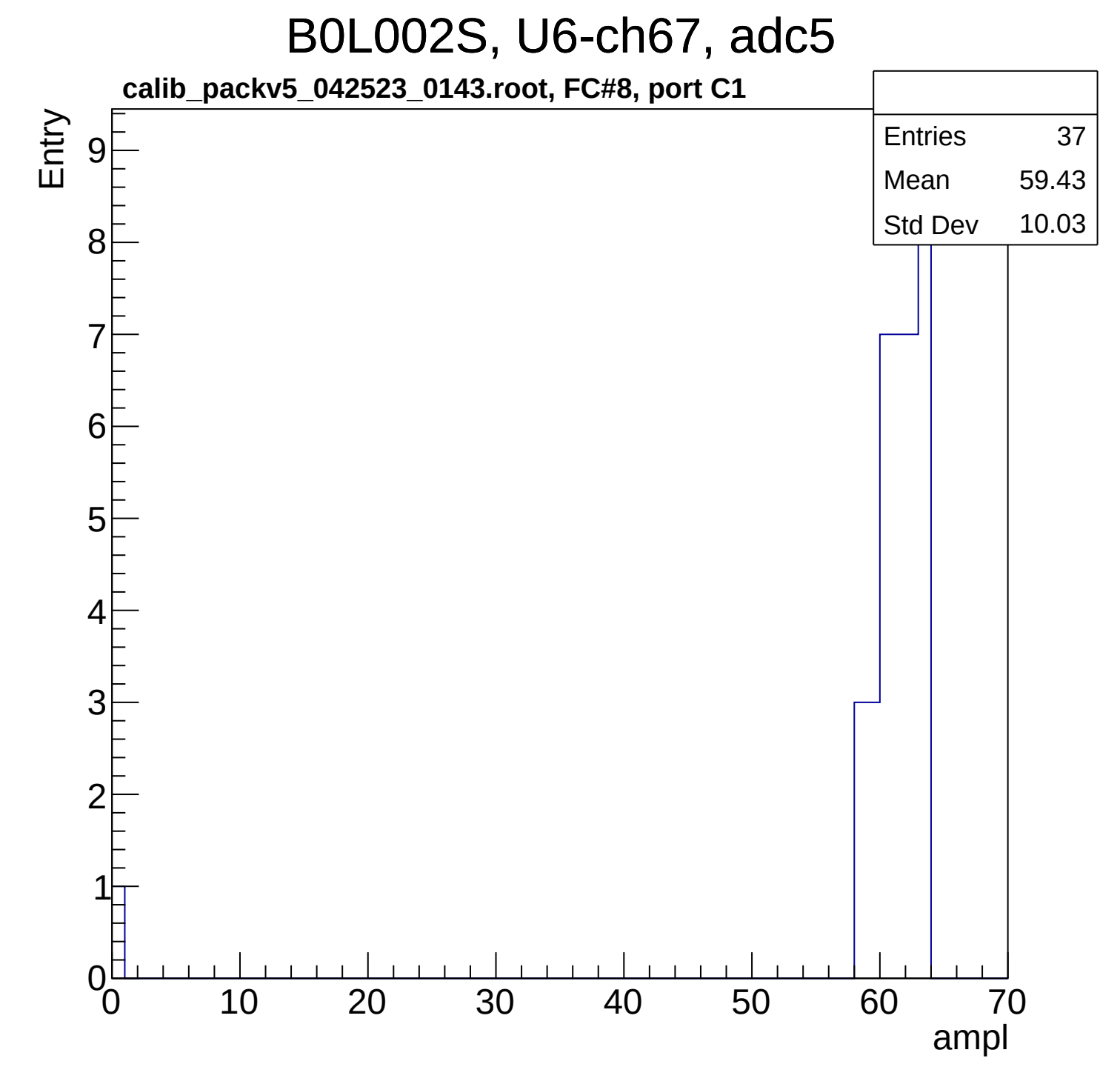
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.14
Std Dev 2.929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.43
Std Dev 10.03
Entry
ampl
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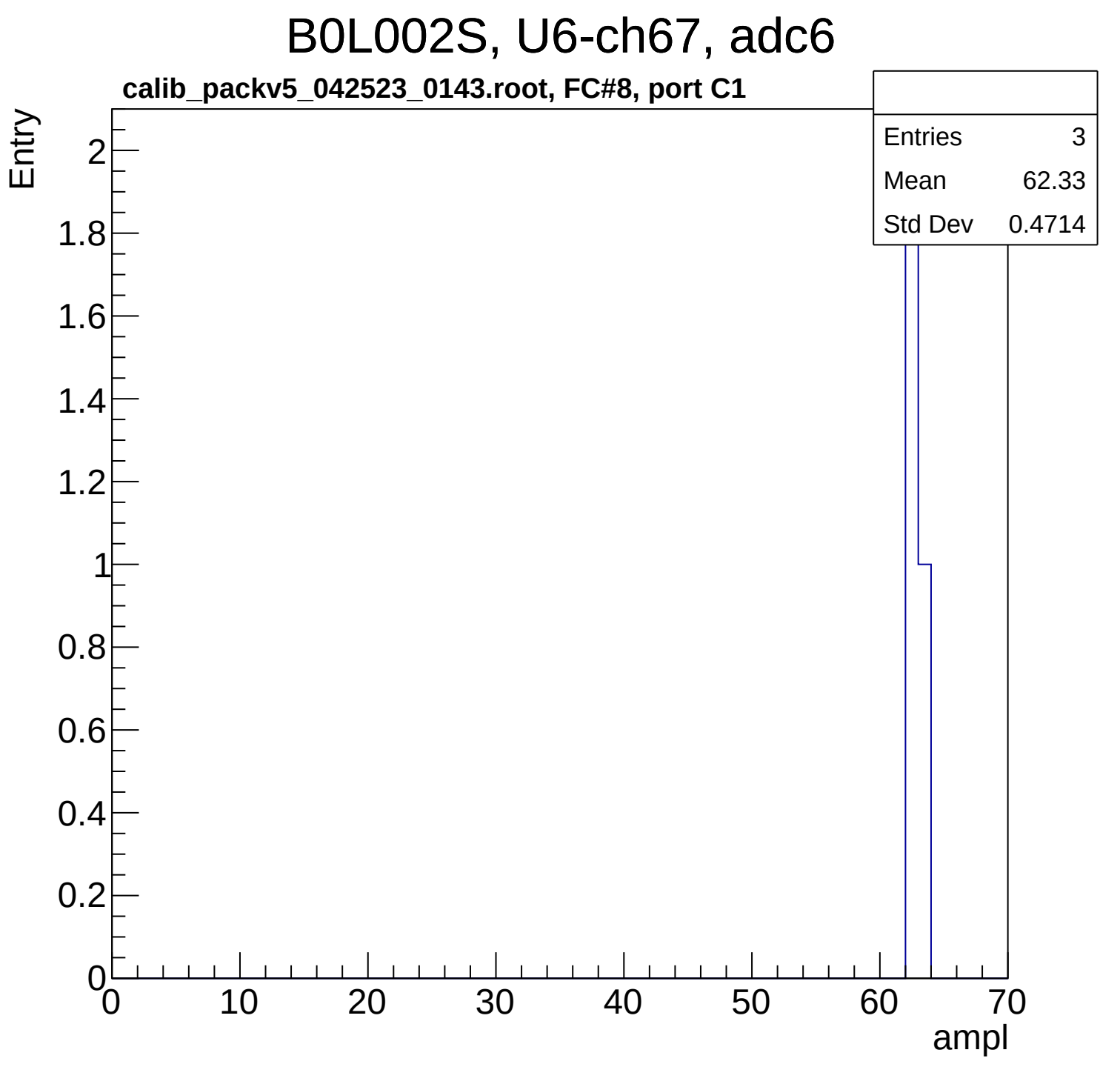
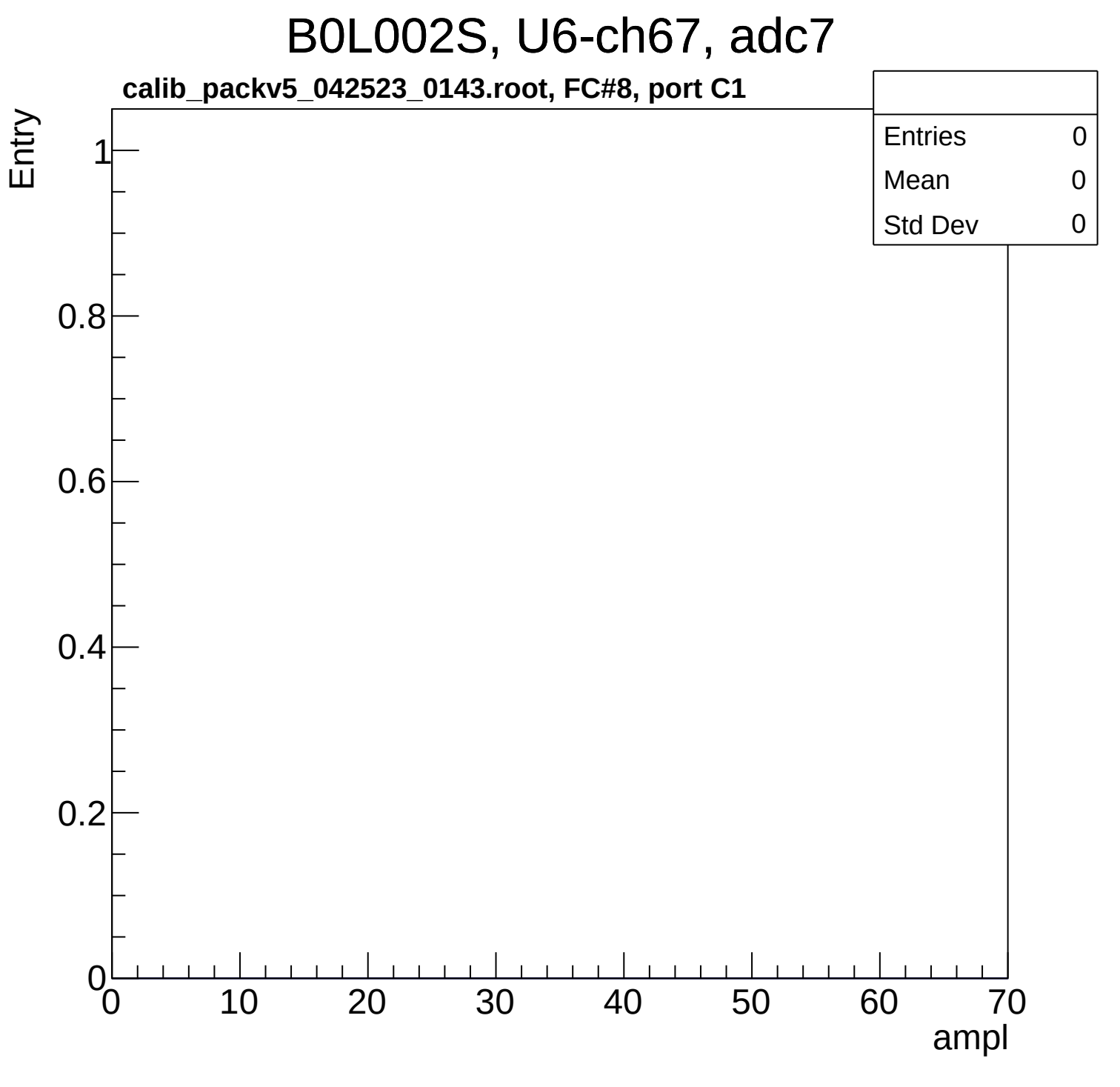
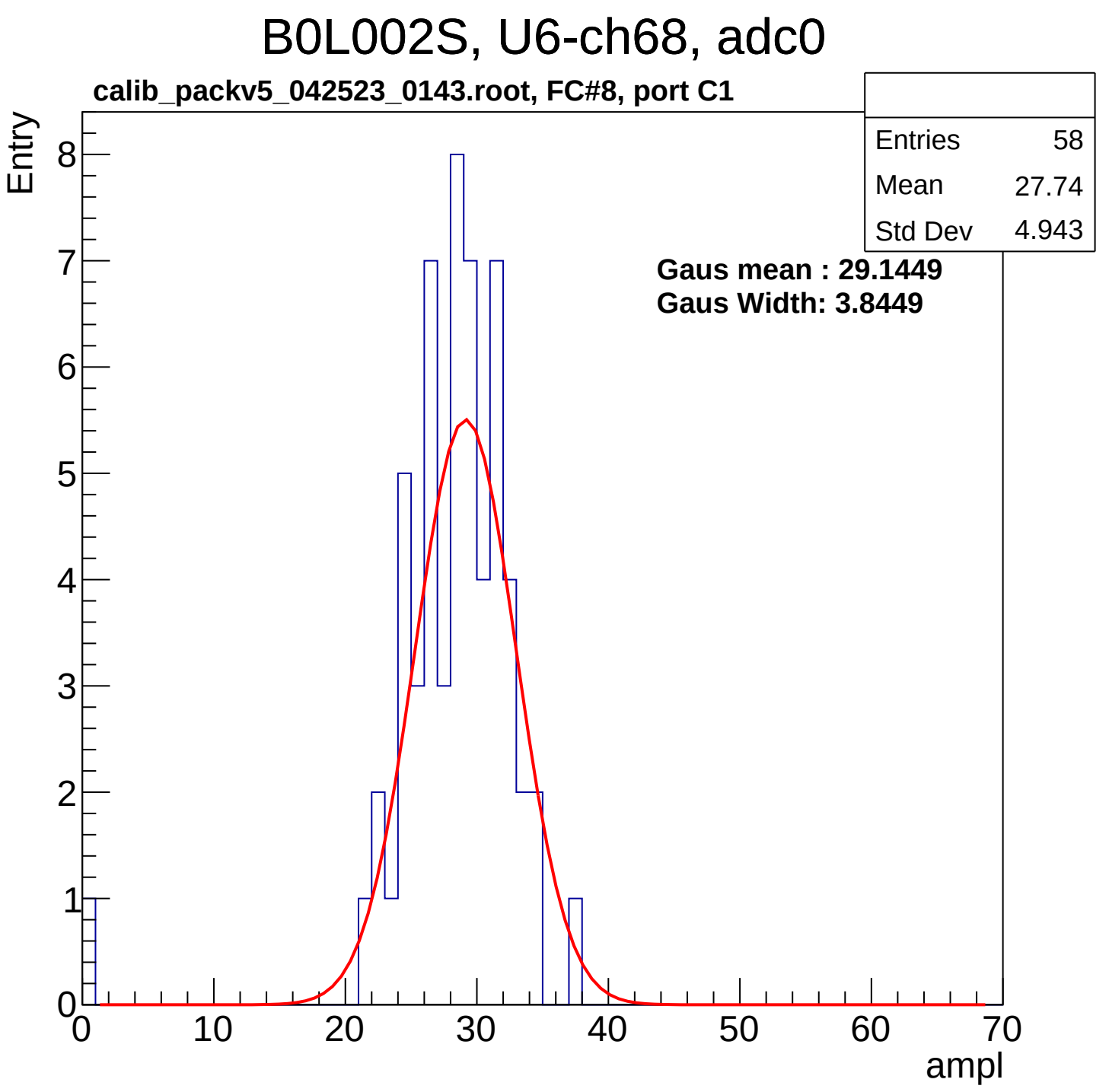
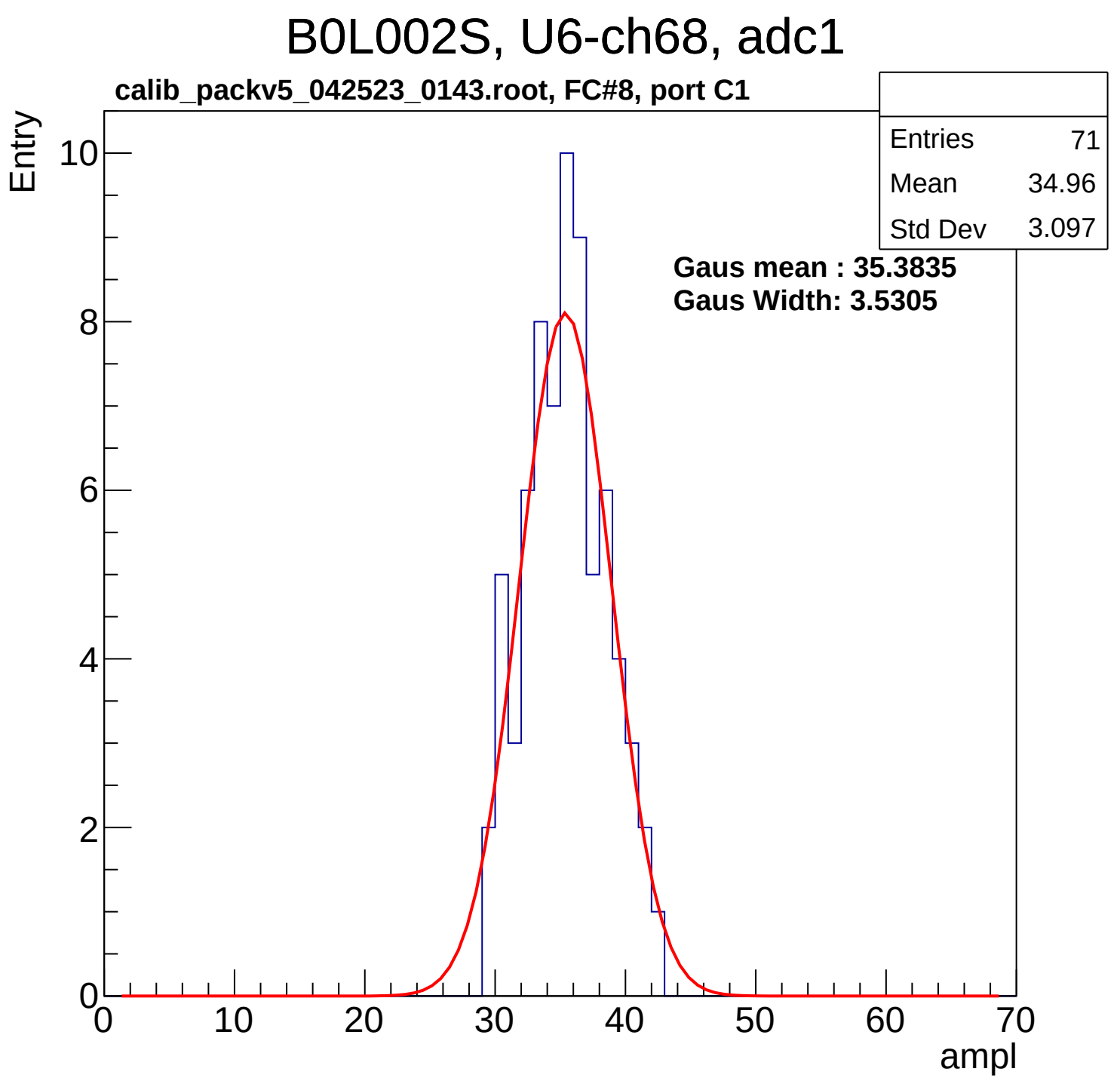
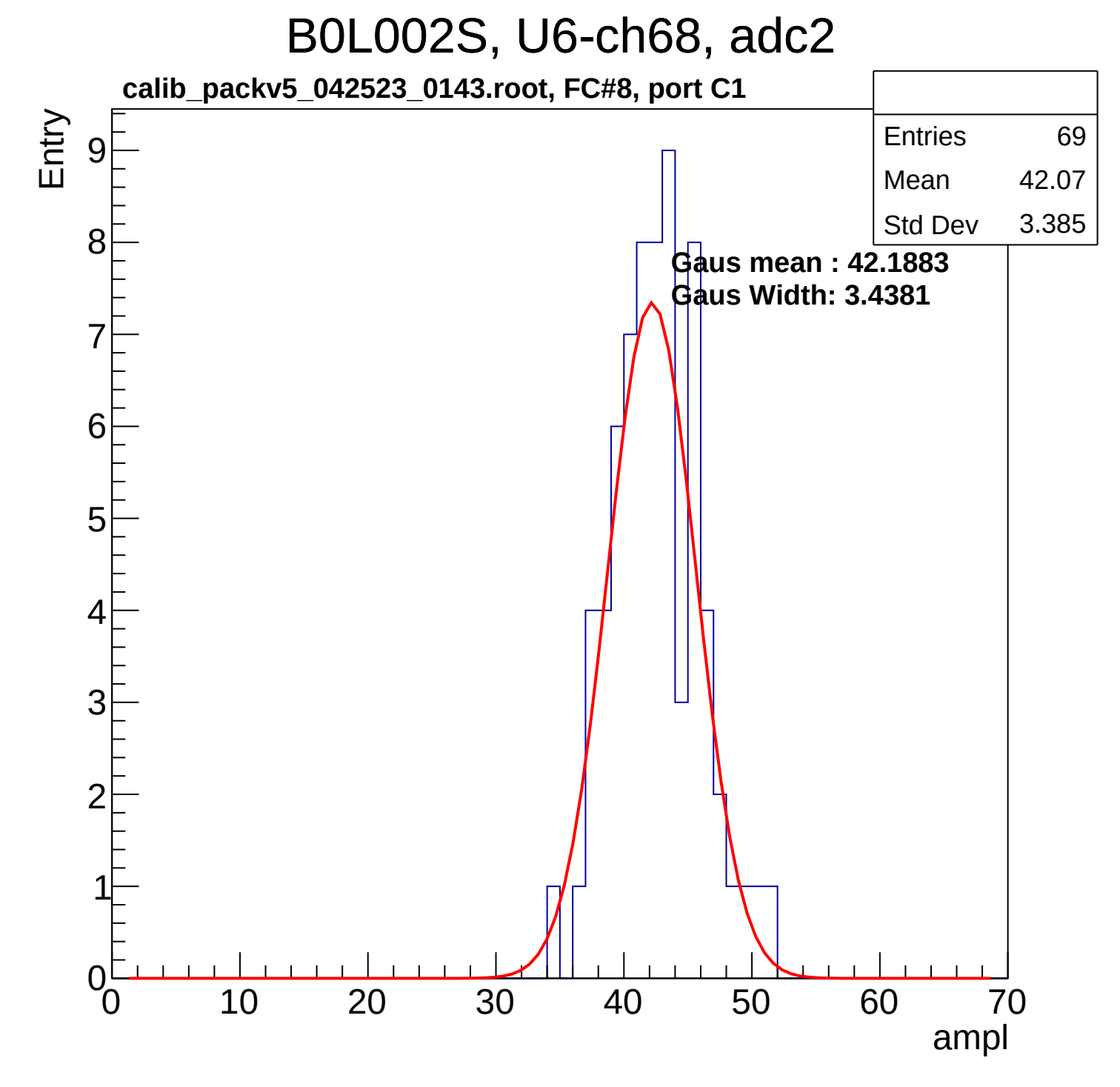
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 27.74
Std Dev 4.943
Gaus mean : 29.1449
Gaus Width: 3.8449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 34.96
Std Dev 3.097
Gaus mean : 35.3835
Gaus Width: 3.5305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.07
Std Dev 3.385
Gaus mean : 42.1883
Gaus Width: 3.4381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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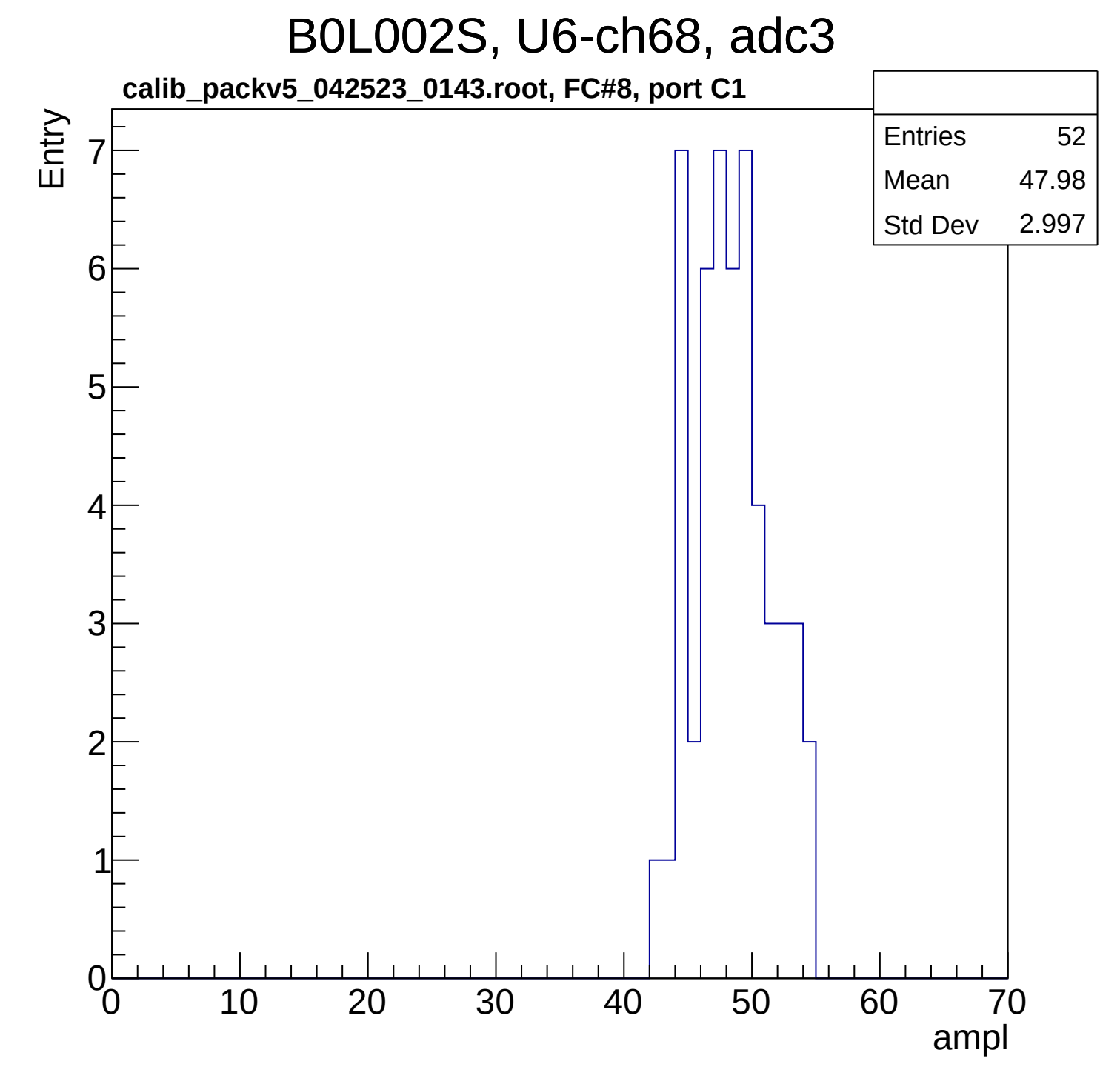
B0L002S, U6-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 47.98
Std Dev 2.997
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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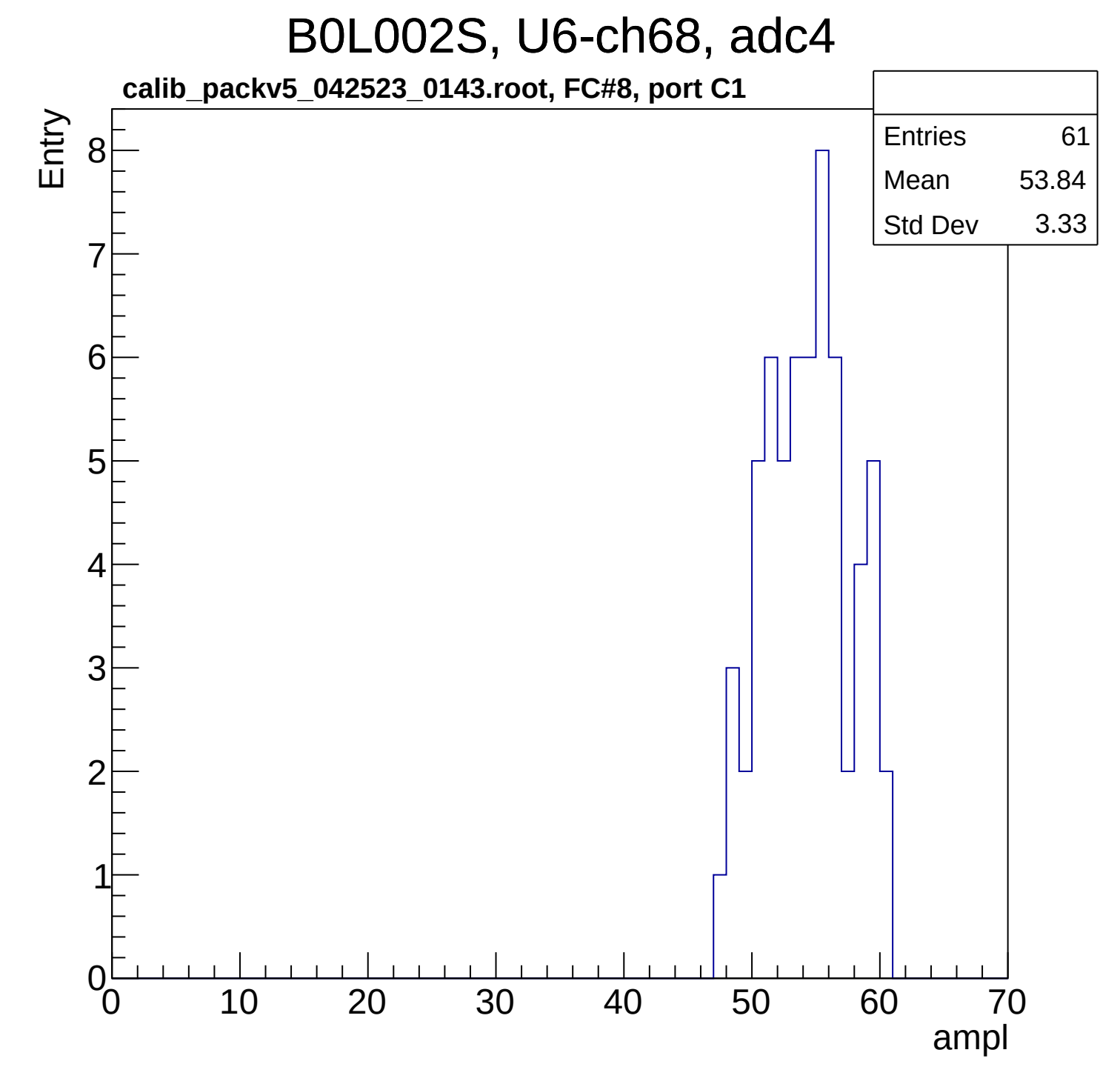
40
50
60
70


B0L002S, U6-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 53.84
Std Dev 3.33
Entry
ampl
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5
6
7
8
0
10
20
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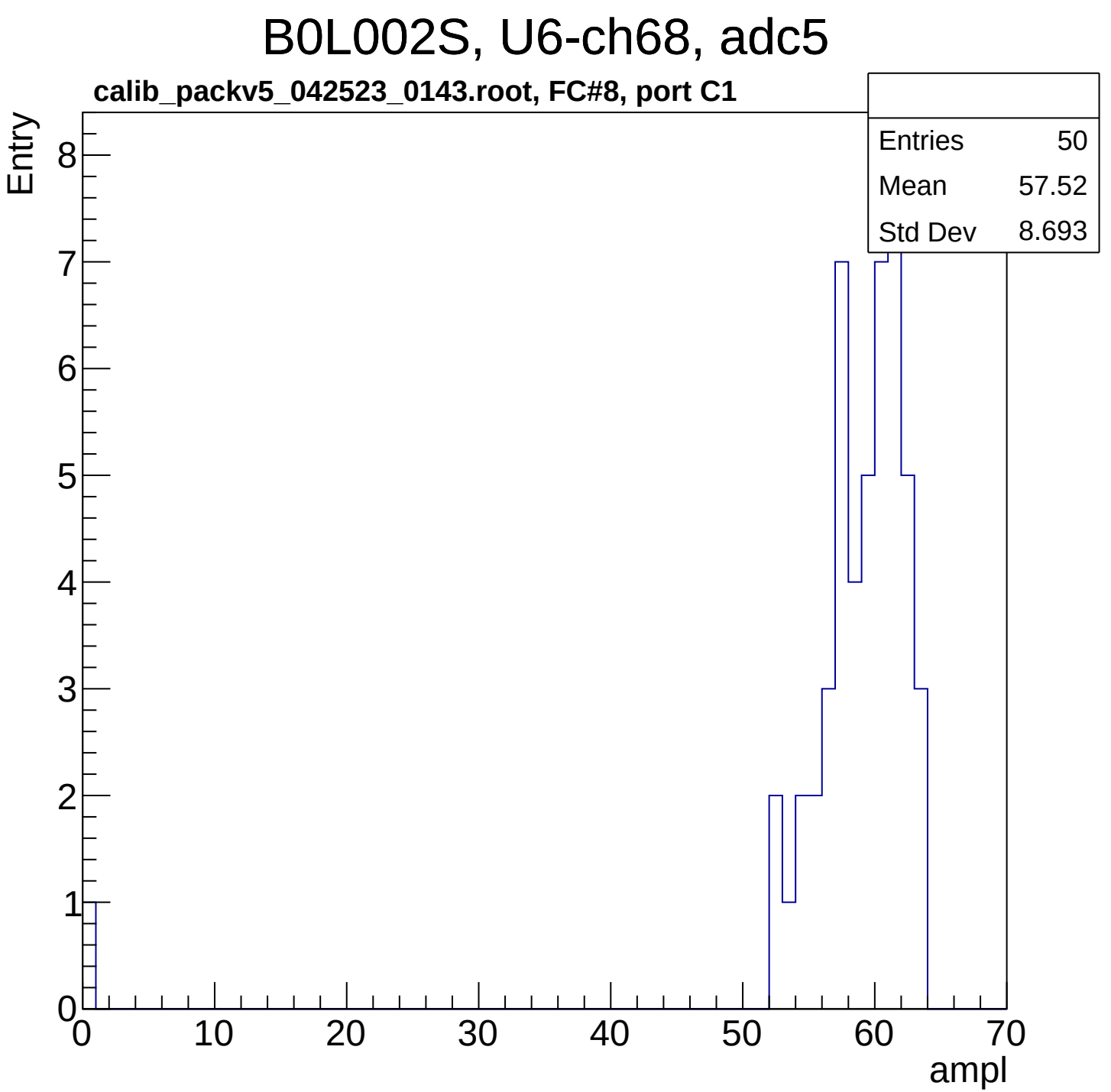
40
50
60
70


B0L002S, U6-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.52
Std Dev 8.693
Entry
ampl
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0
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60
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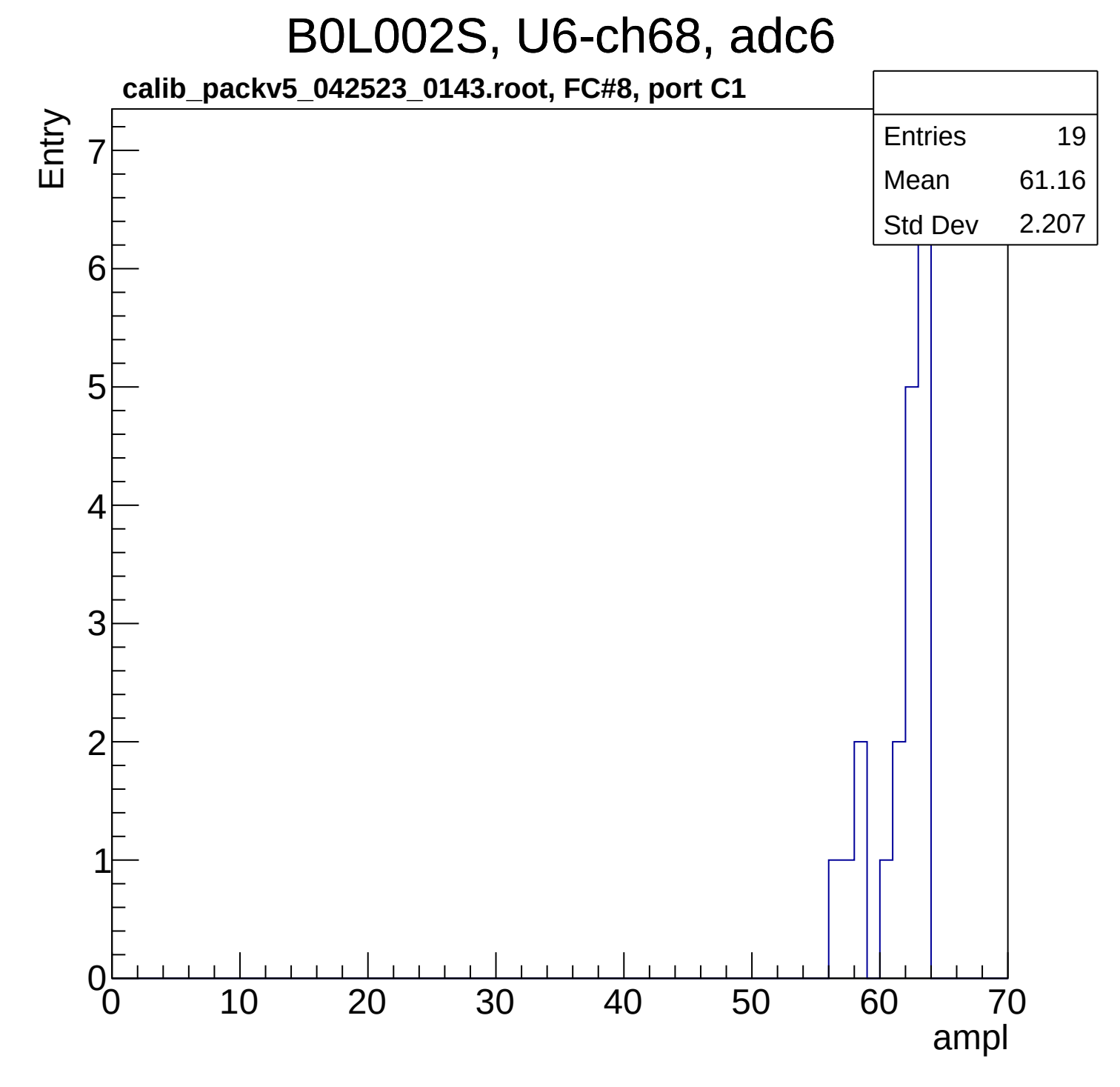
B0L002S, U6-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.16
Std Dev 2.207
Entry
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2
3
4
5
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7
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10
20
30
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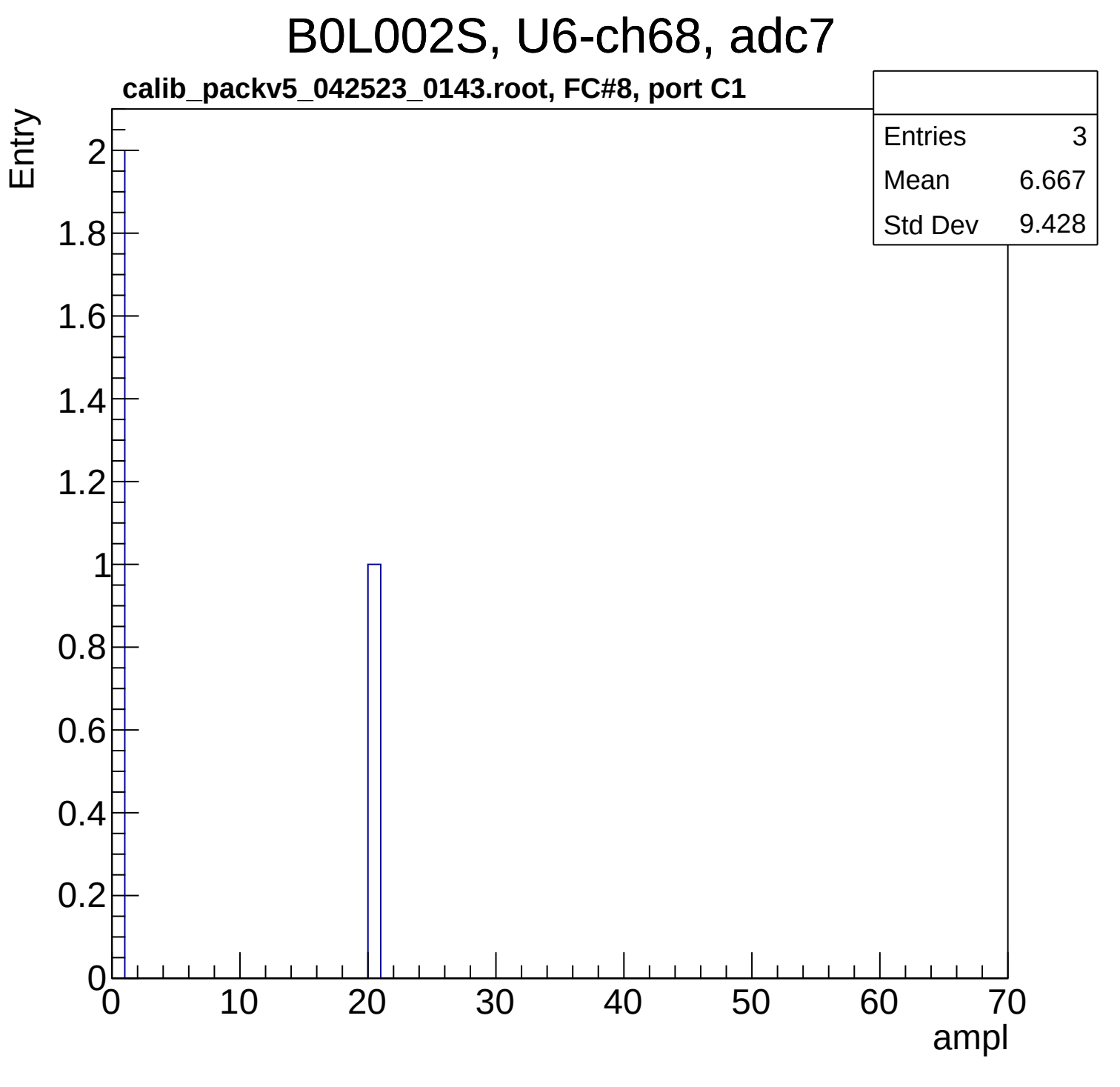
50
60
70
ampl


B0L002S, U6-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
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0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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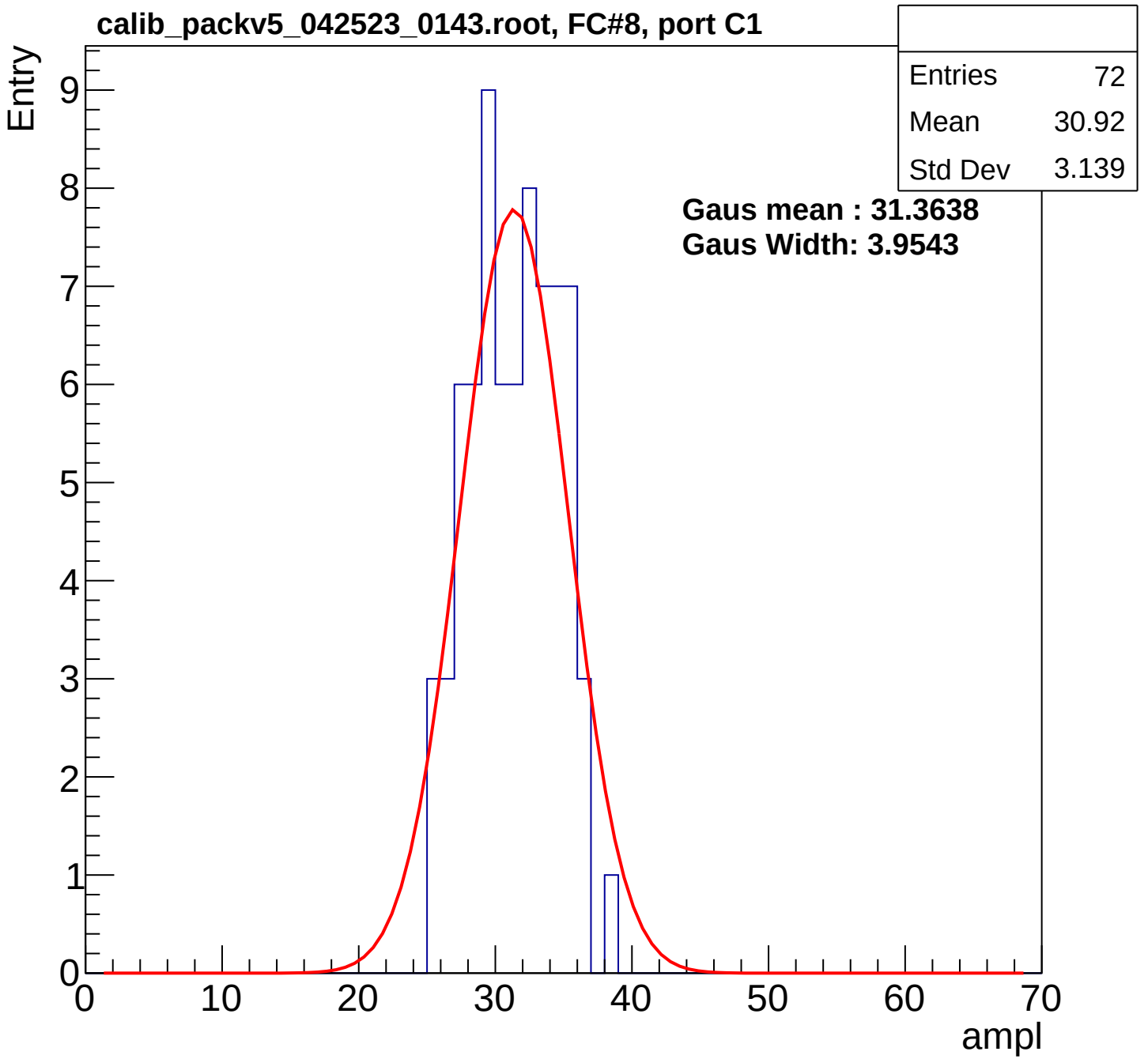
B0L002S, U6-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.92
Std Dev 3.139
Gaus mean : 31.3638
Gaus Width: 3.9543
Entry
ampl
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20
30
40
50
60
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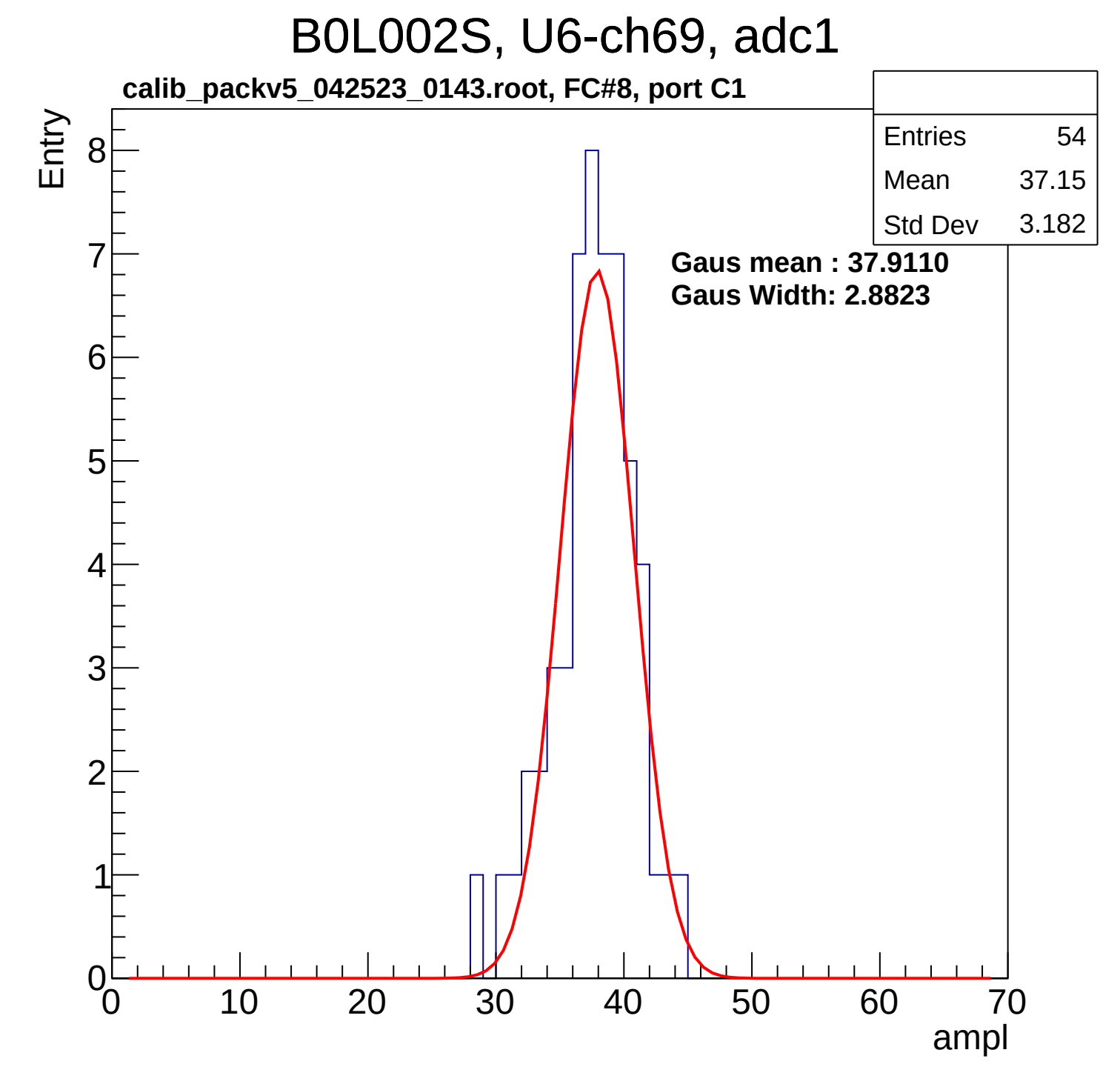
1
2
3
4
5
6
7
8
9


B0L002S, U6-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 37.15
Std Dev 3.182
Gaus mean : 37.9110
Gaus Width: 2.8823
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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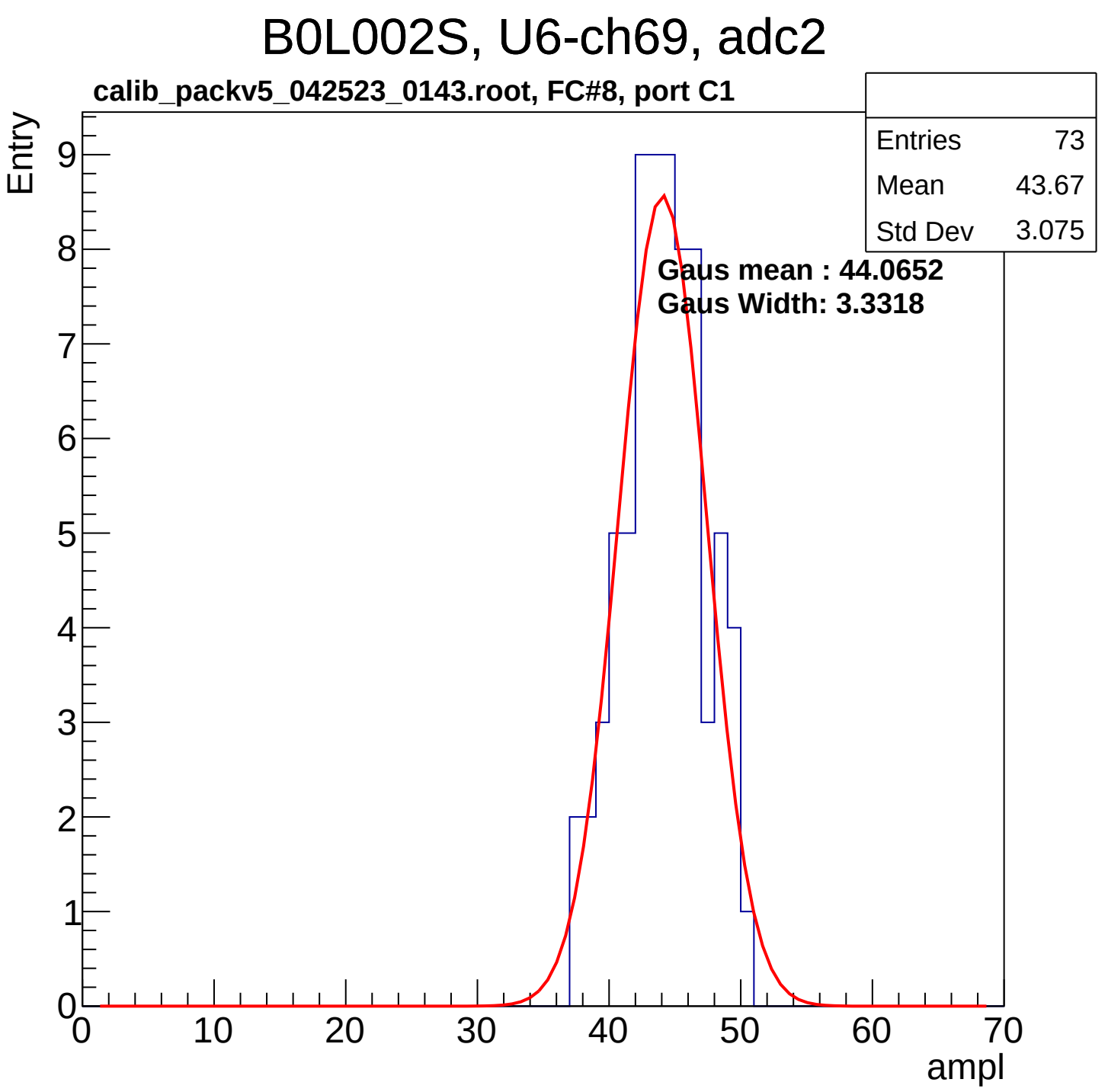
B0L002S, U6-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.67
Std Dev 3.075
Gaus mean : 44.0652
Gaus Width: 3.3318
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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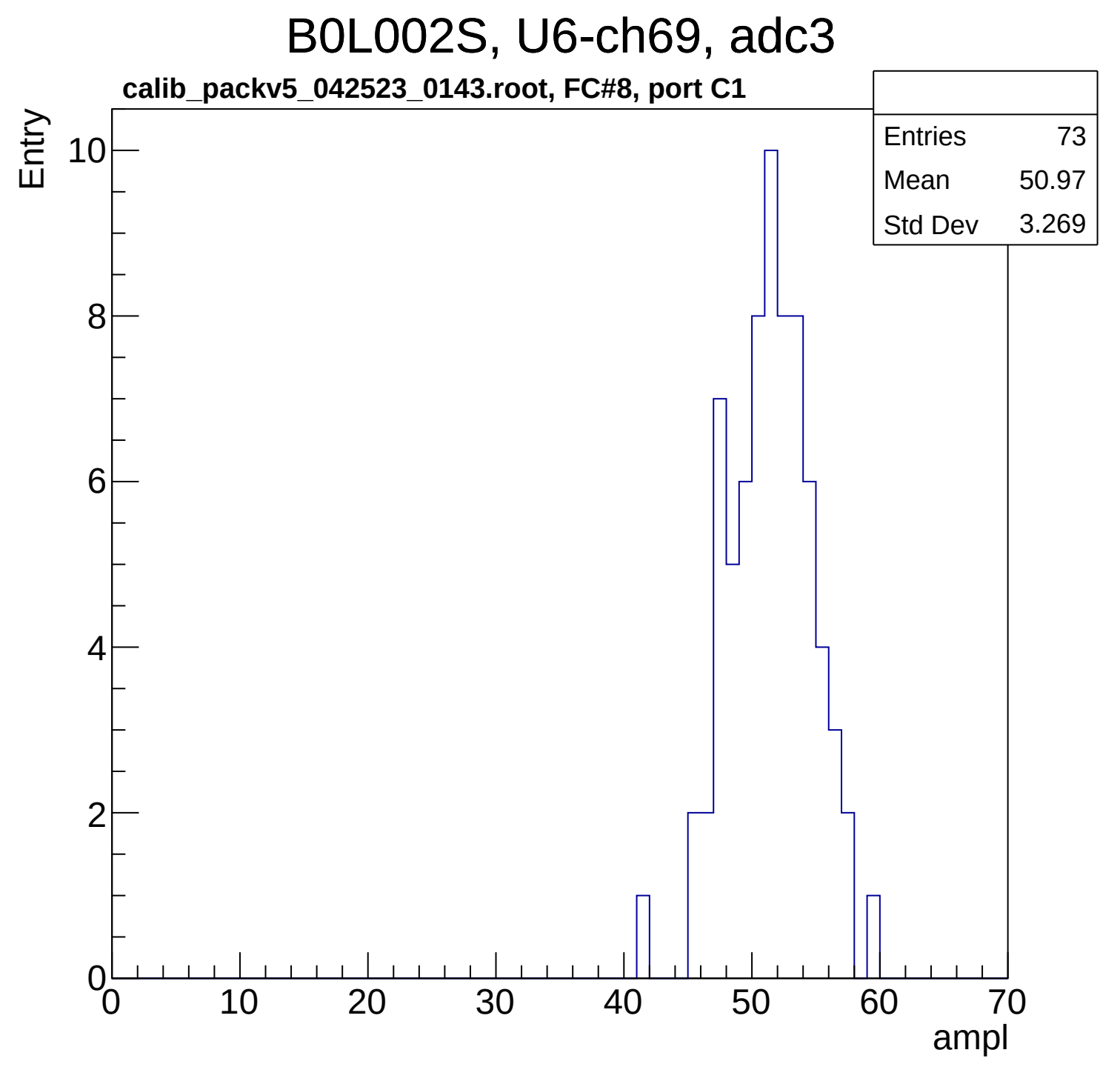
B0L002S, U6-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.97
Std Dev 3.269
Entry
ampl
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0
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20
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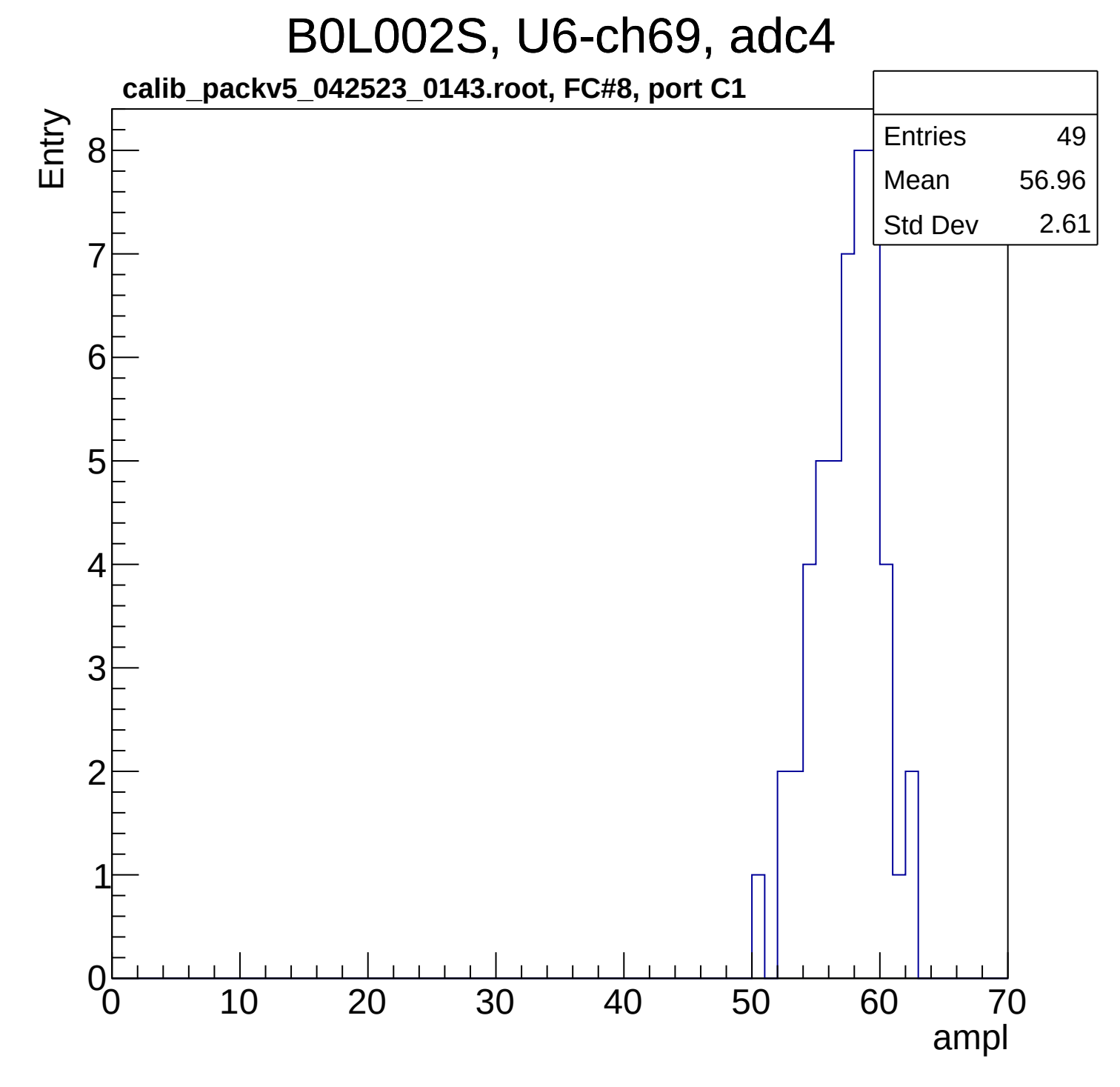
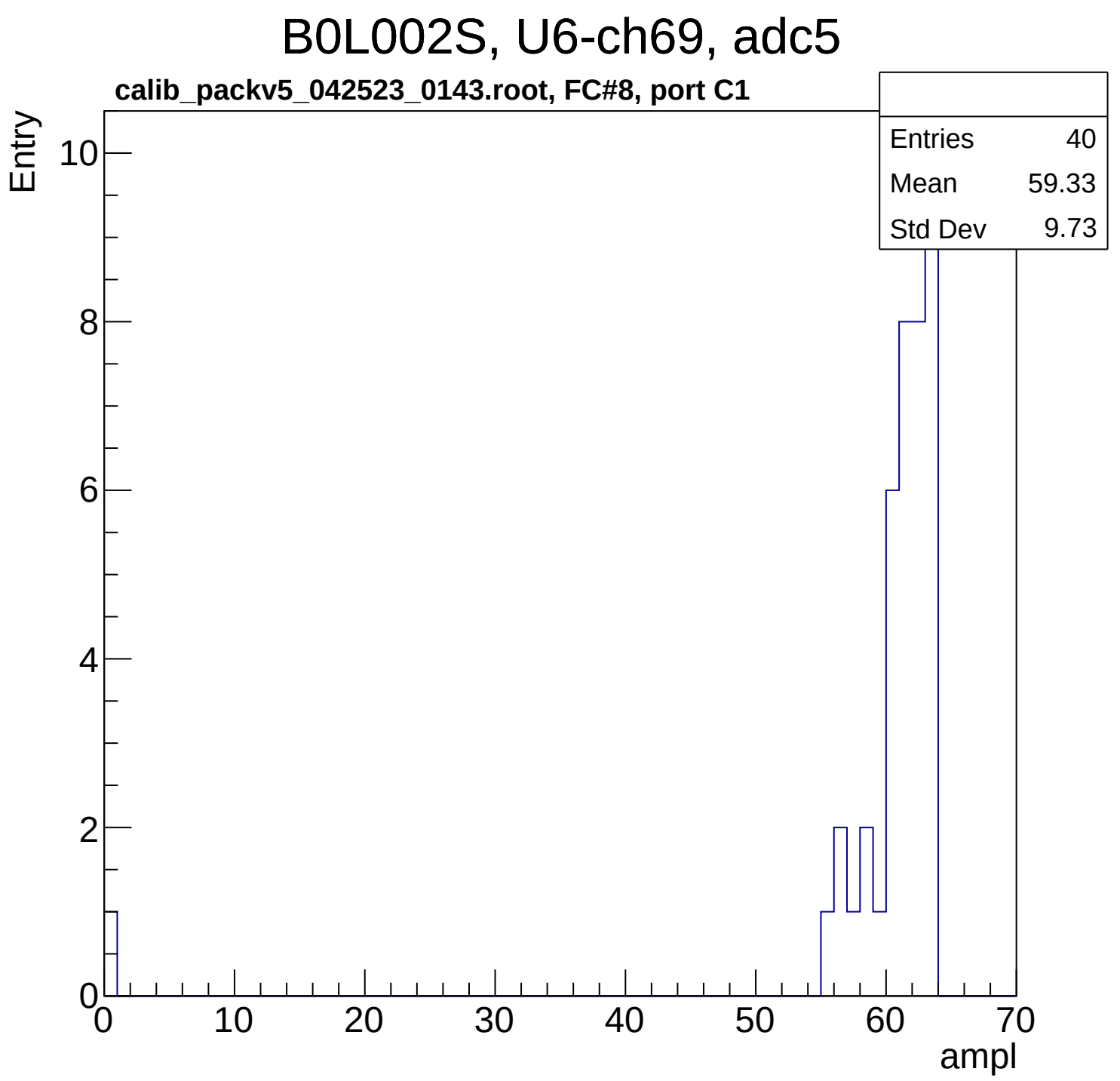
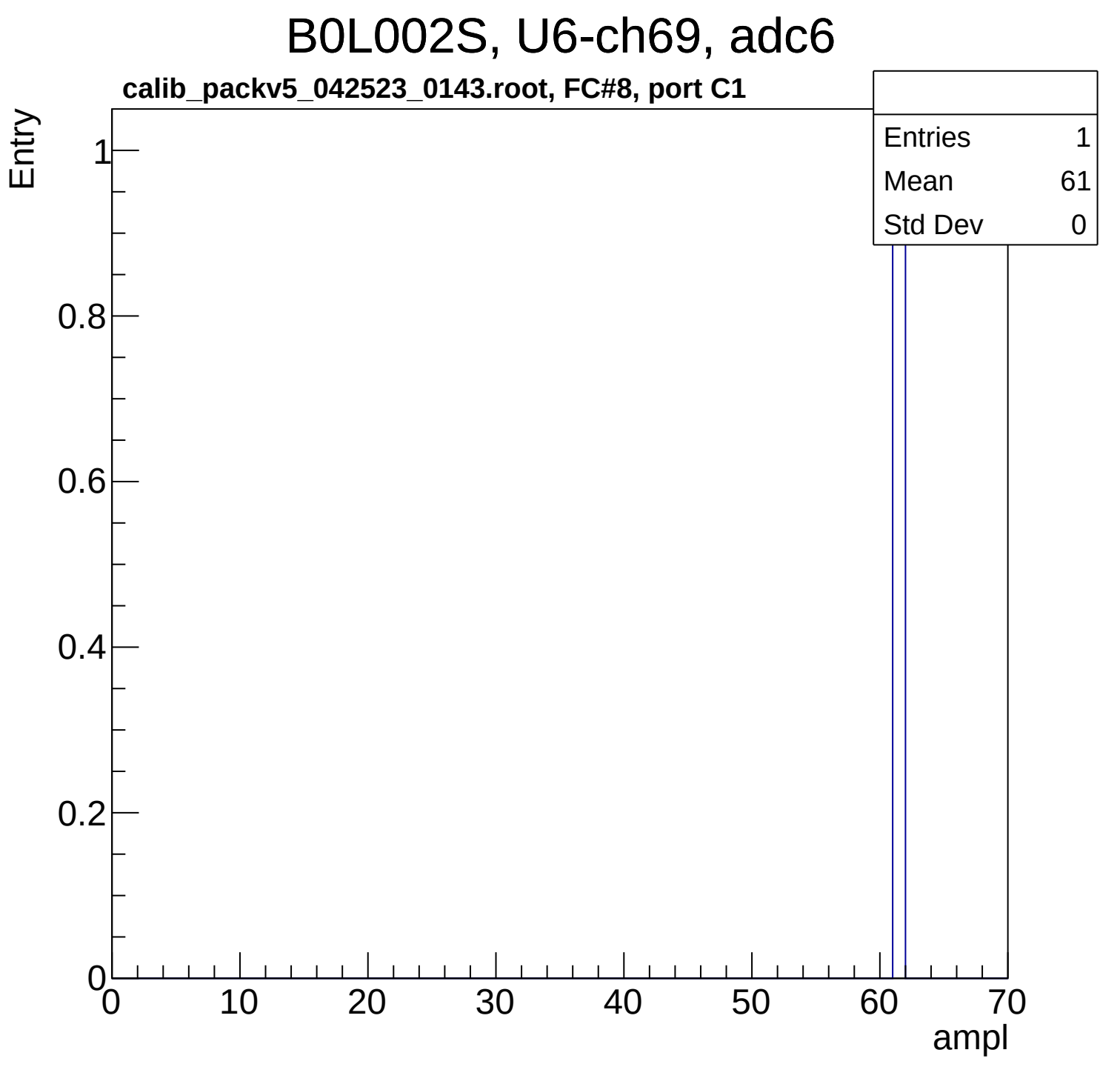
40
50
60
70


B0L002S, U6-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 56.96
Std Dev 2.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.33
Std Dev 9.73
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
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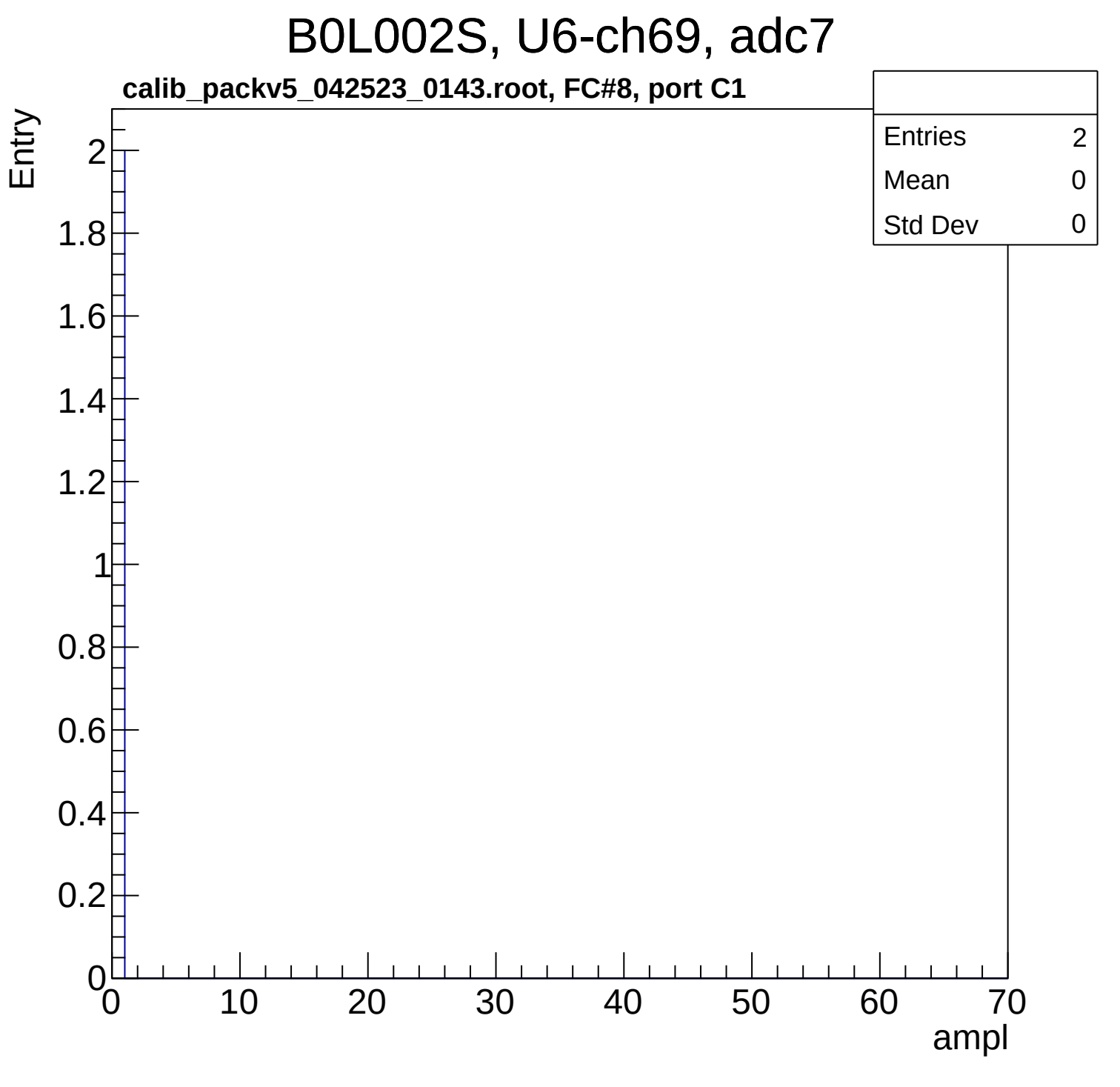
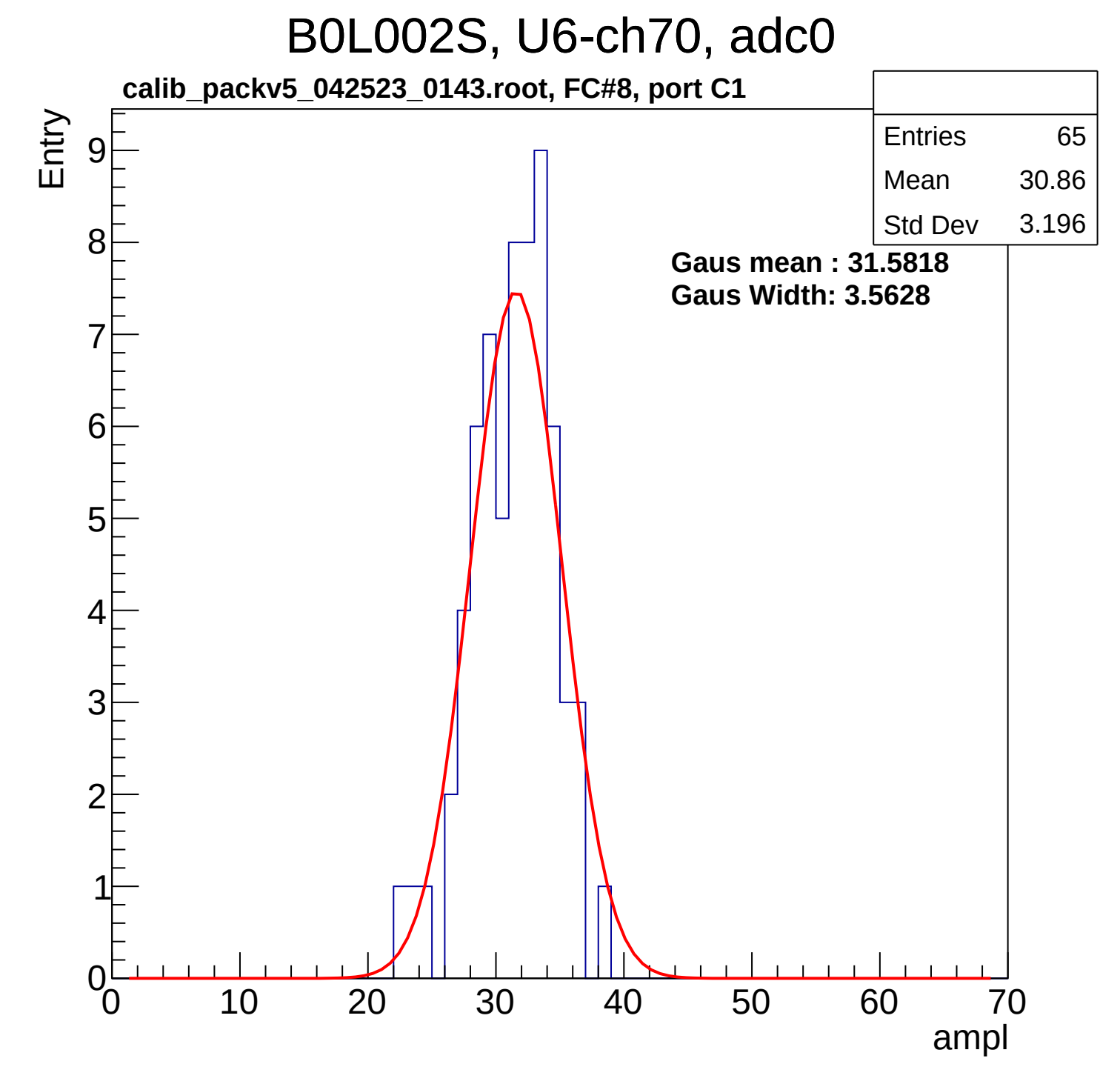
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.86
Std Dev 3.196
Gaus mean : 31.5818
Gaus Width: 3.5628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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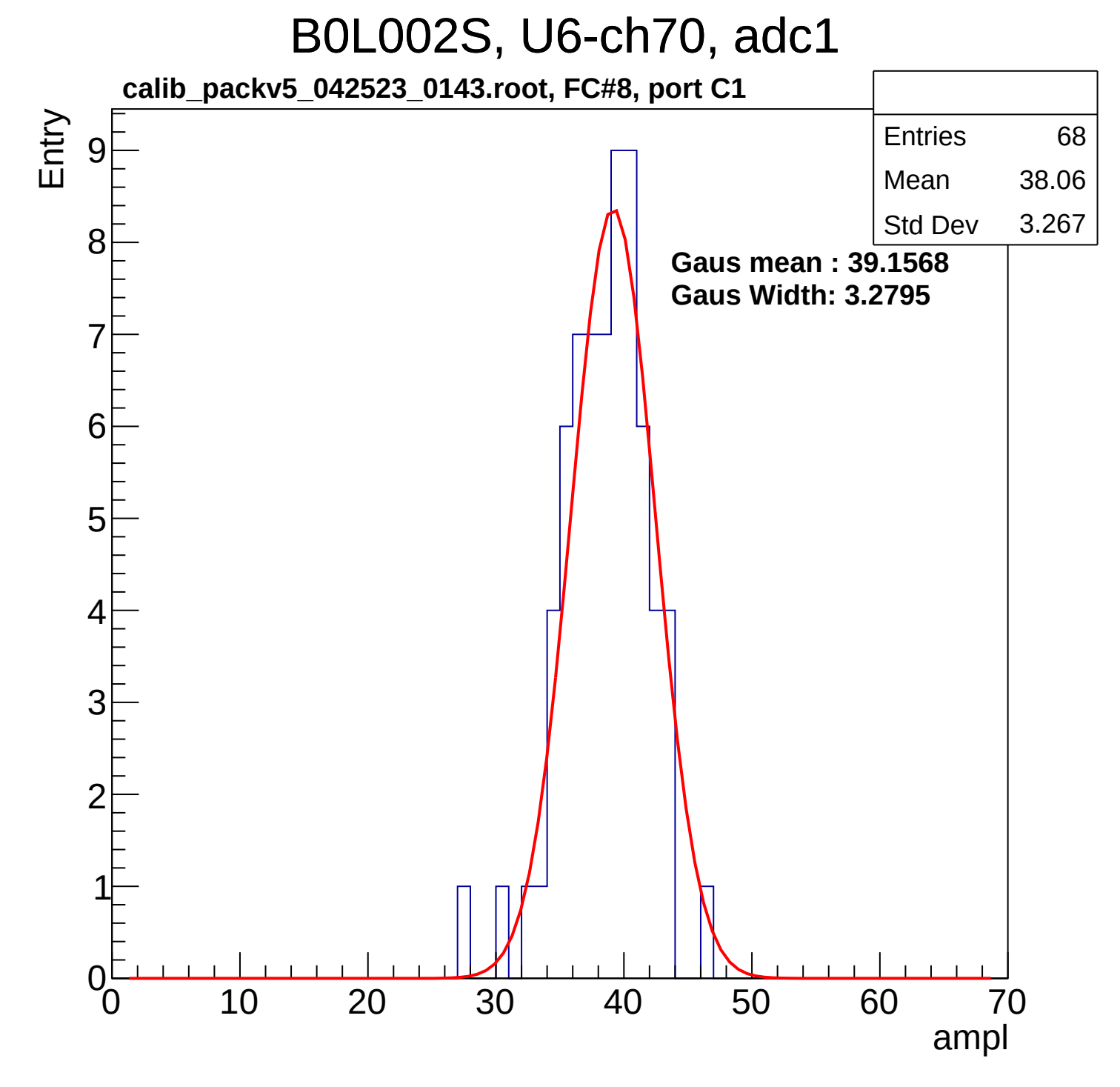
B0L002S, U6-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.06
Std Dev 3.267
Gaus mean : 39.1568
Gaus Width: 3.2795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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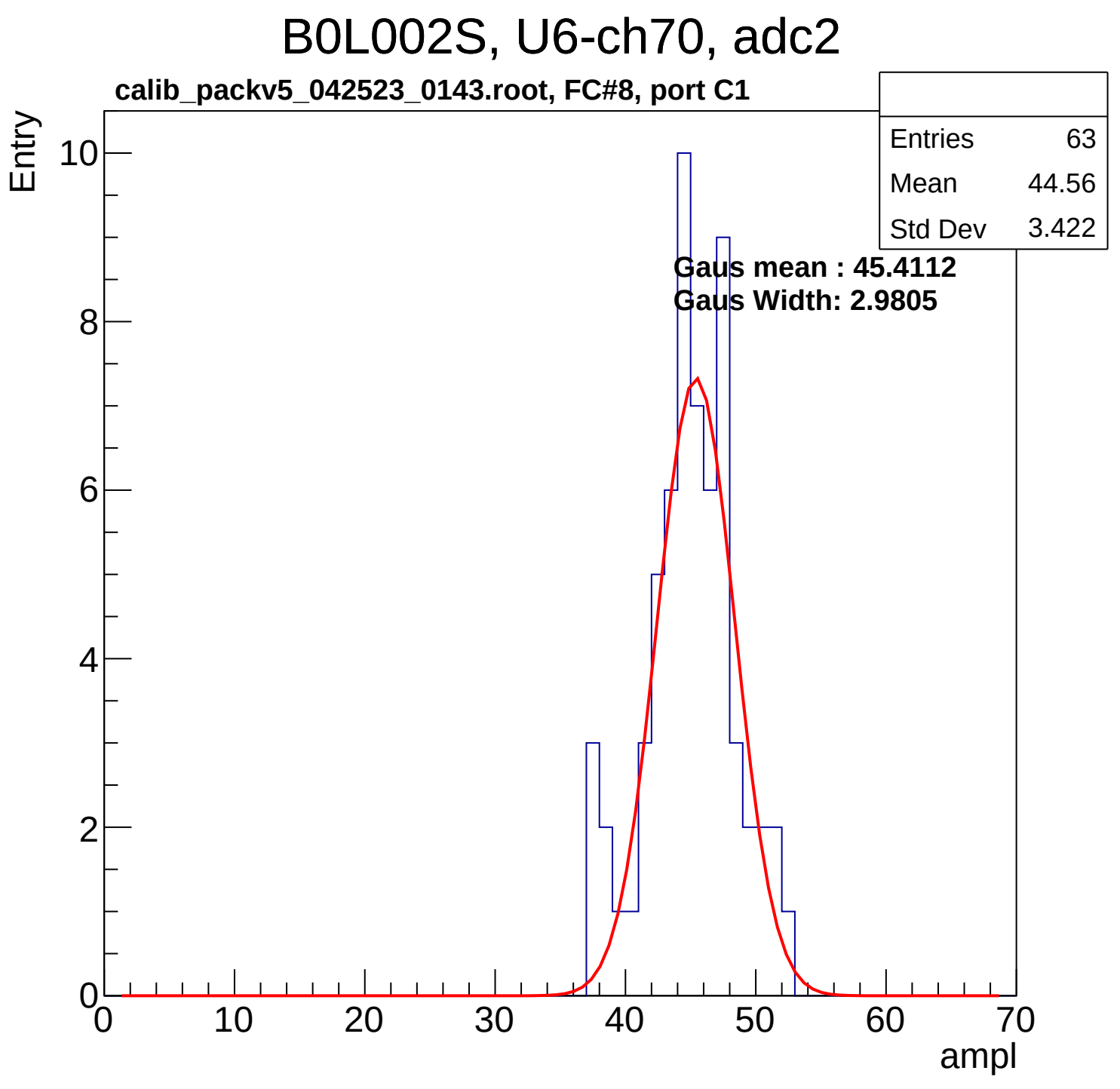
B0L002S, U6-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.56
Std Dev 3.422
Gaus mean : 45.4112
Gaus Width: 2.9805
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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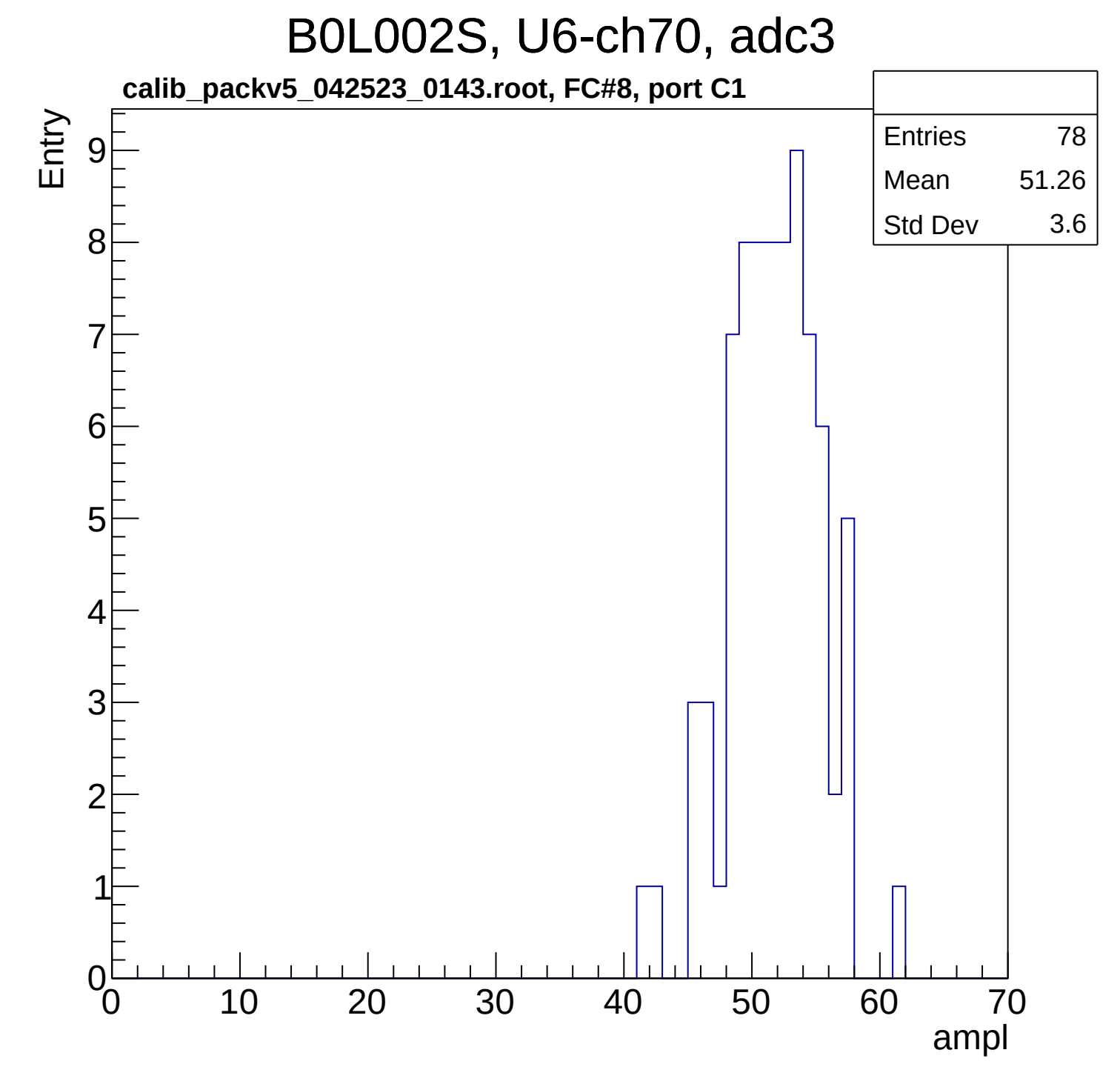
B0L002S, U6-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 51.26
Std Dev 3.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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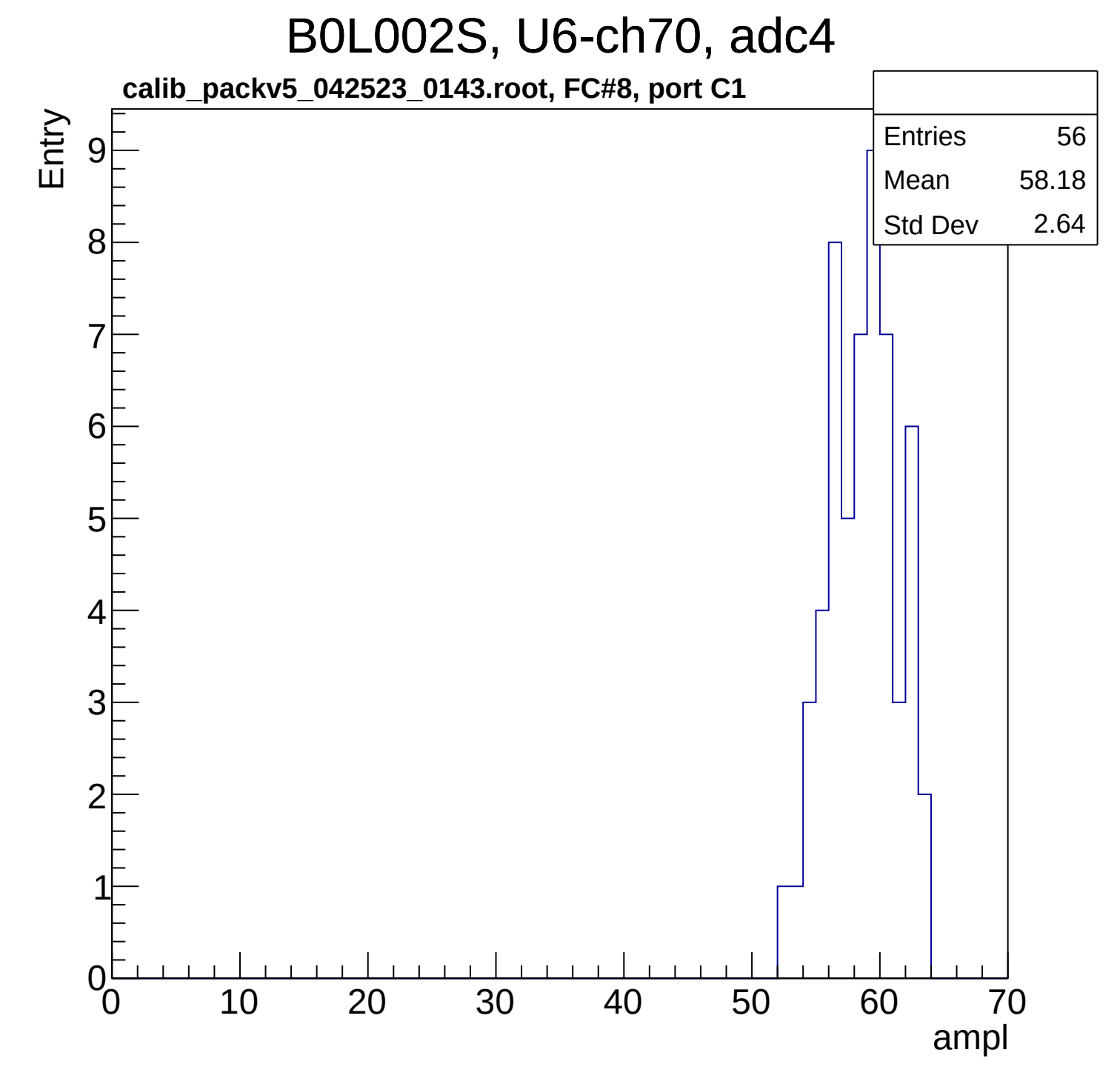
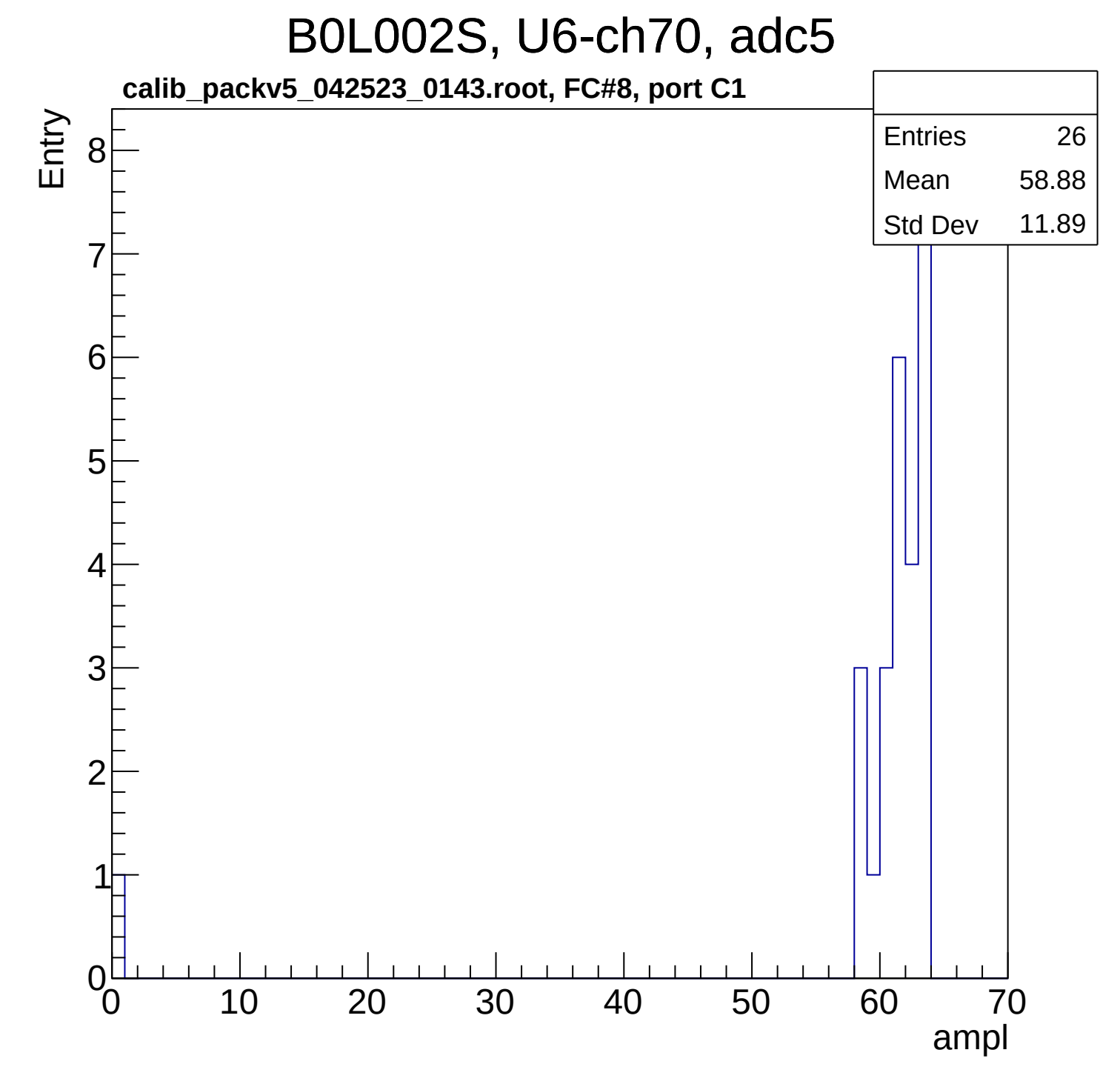

B0L002S, U6-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 58.18
Std Dev 2.64
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.88
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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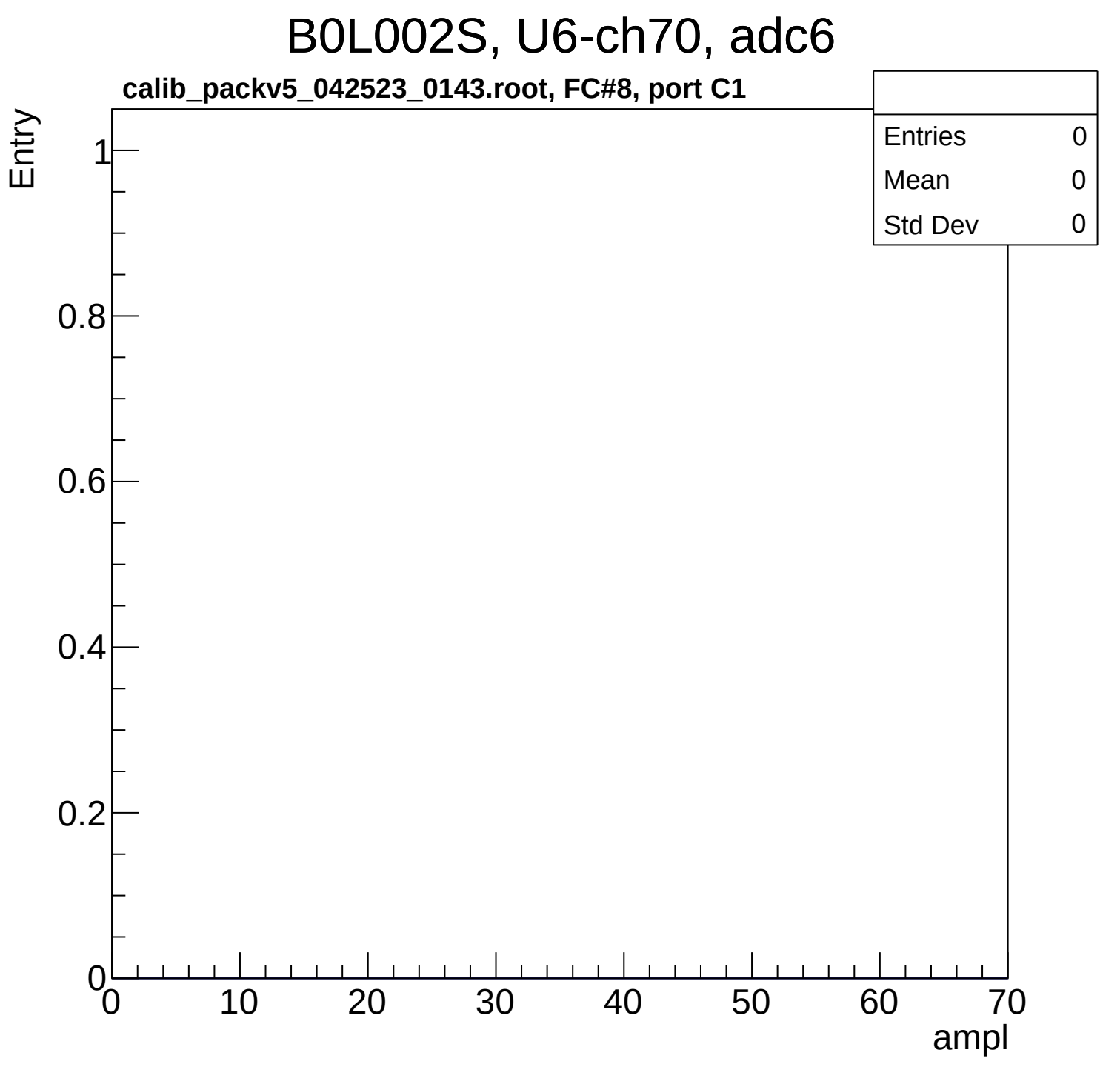
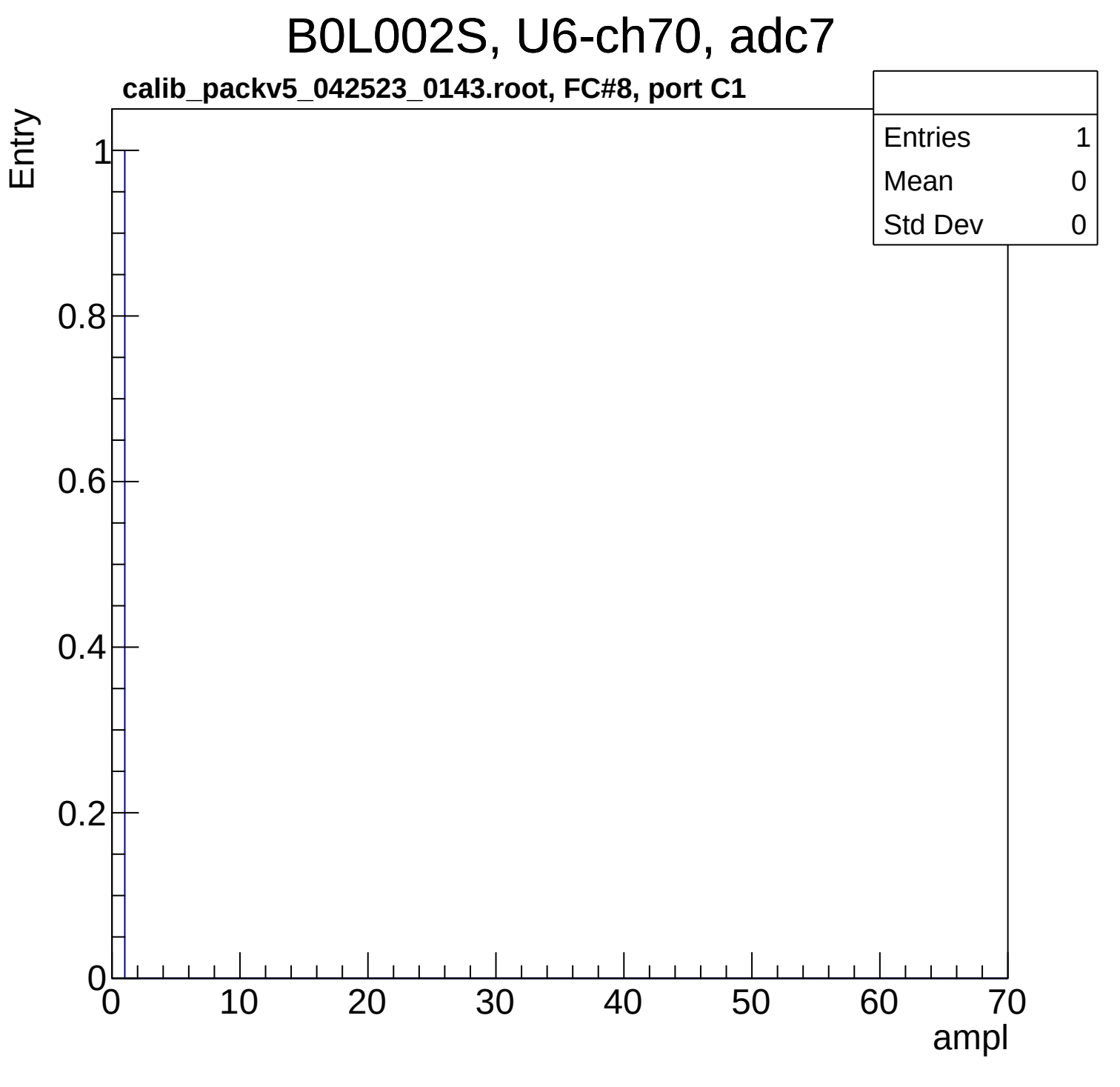
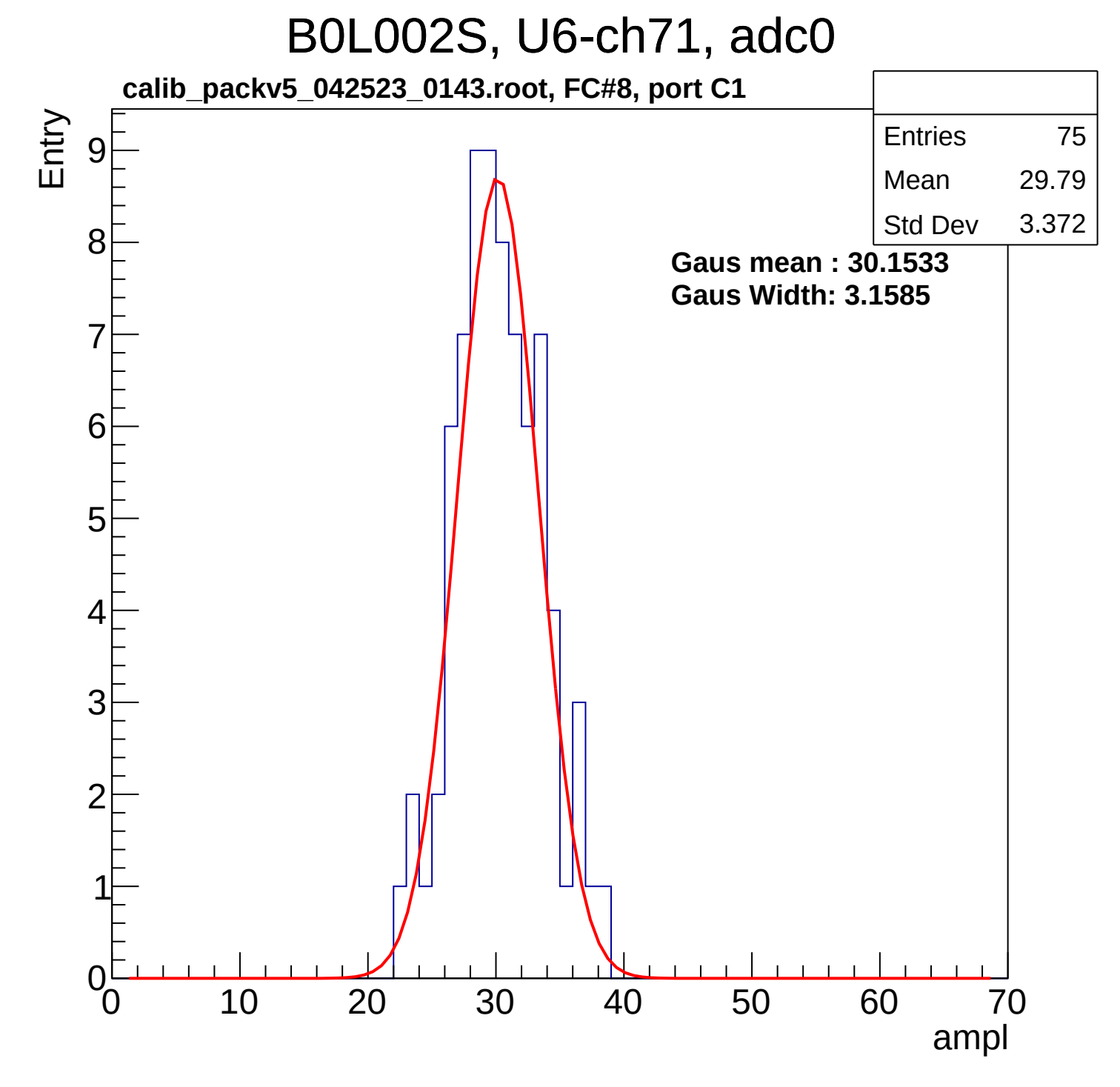
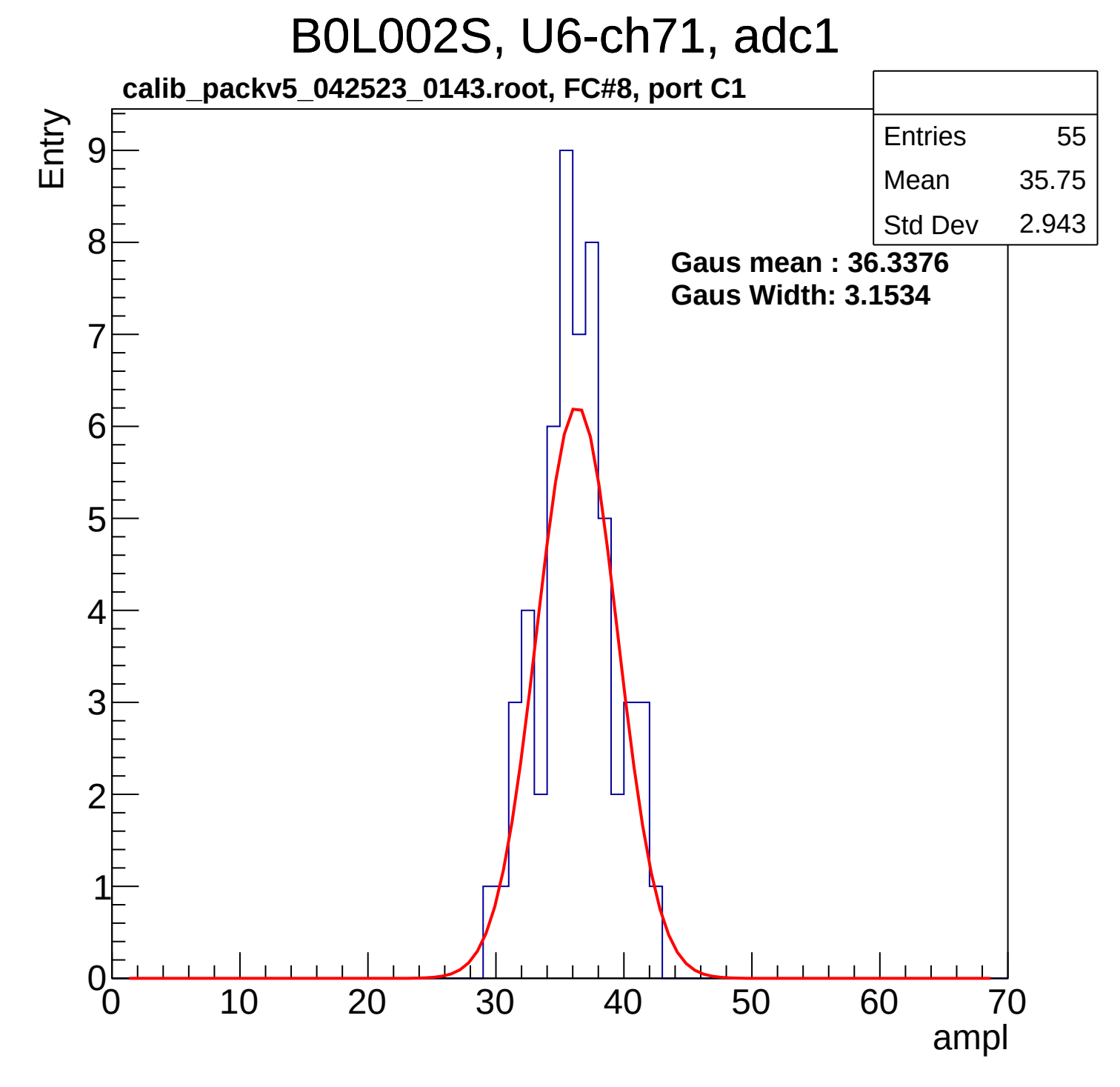

B0L002S, U6-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.79
Std Dev 3.372
Gaus mean : 30.1533
Gaus Width: 3.1585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 35.75
Std Dev 2.943
Gaus mean : 36.3376
Gaus Width: 3.1534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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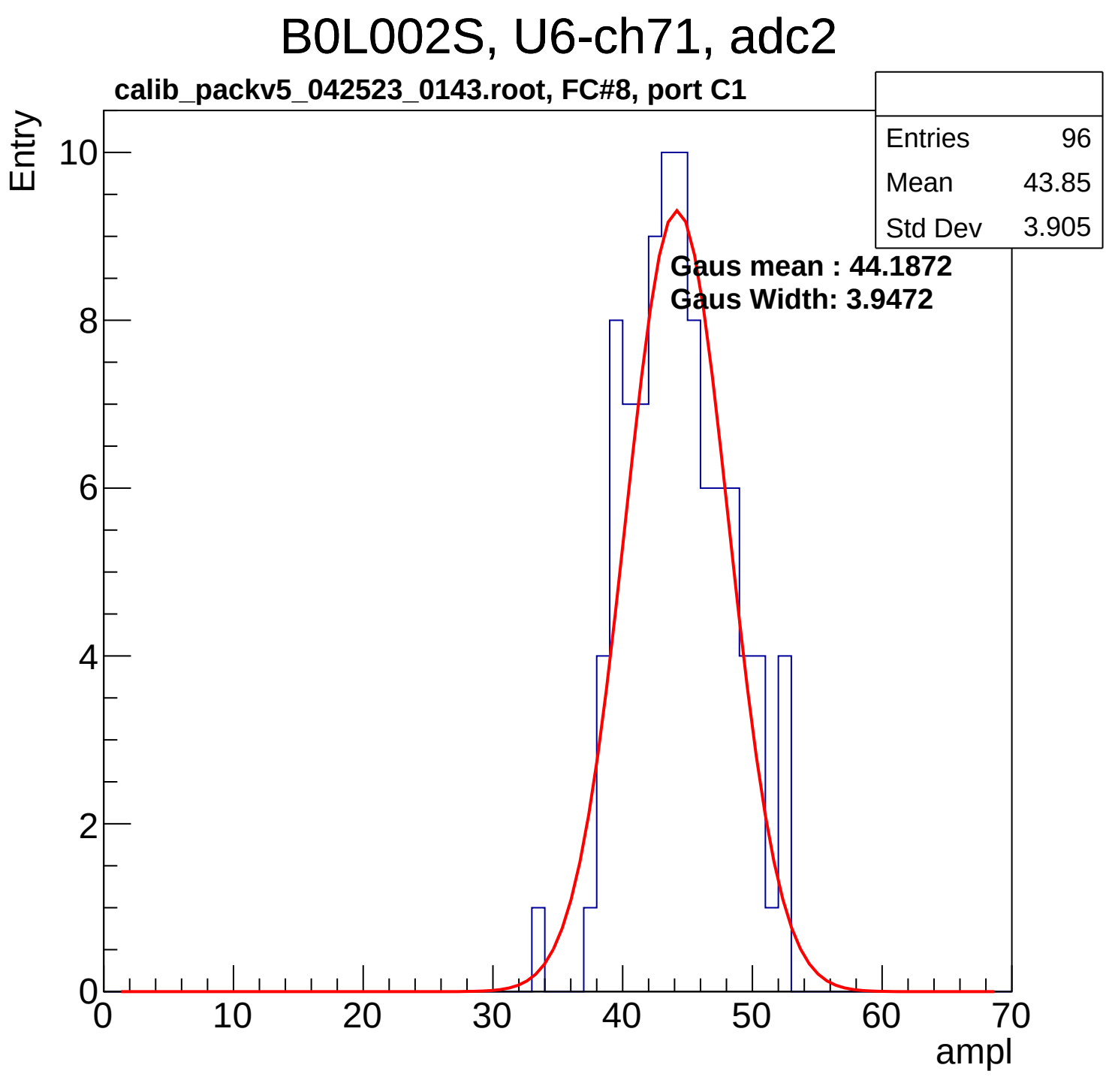
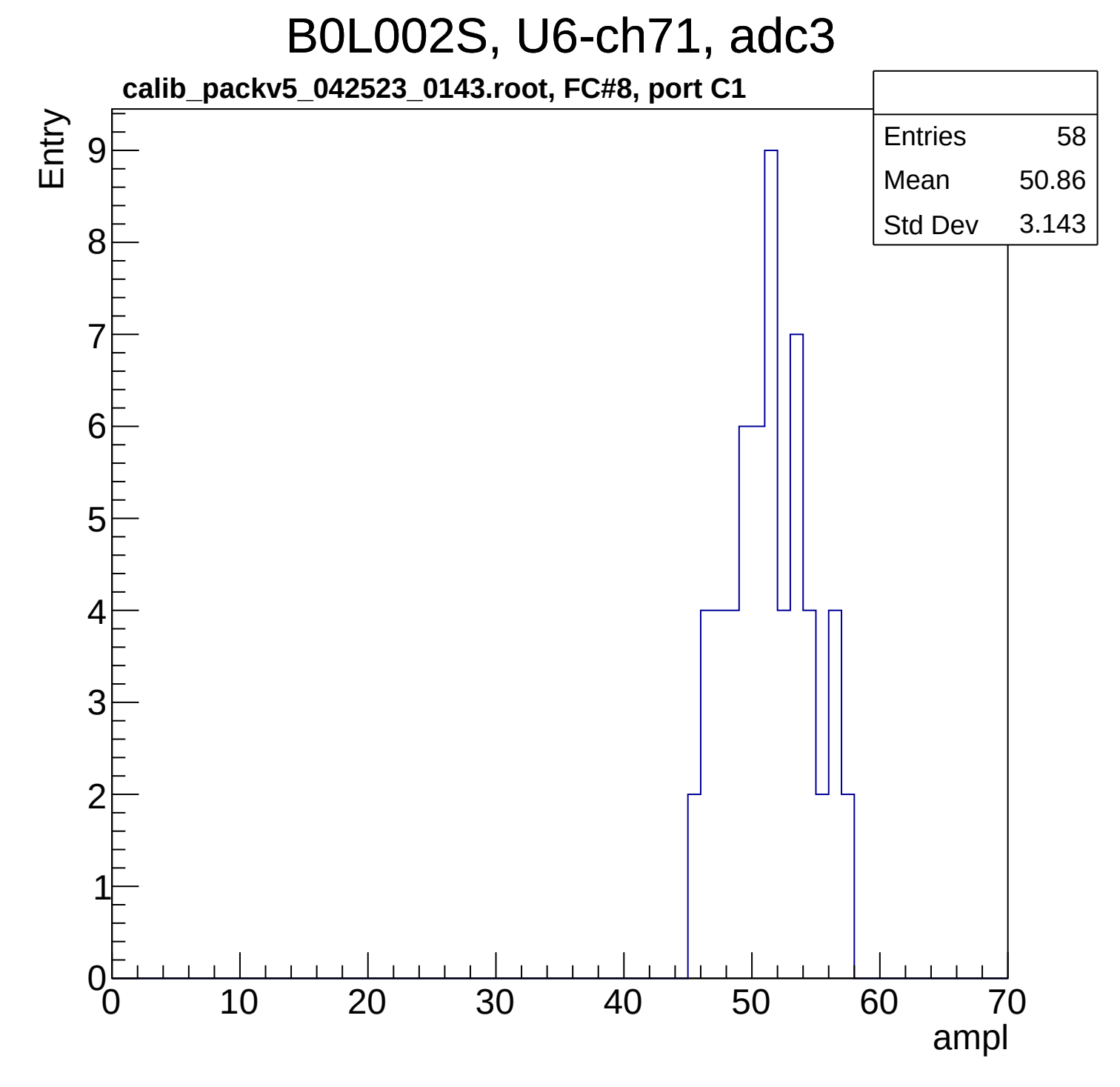
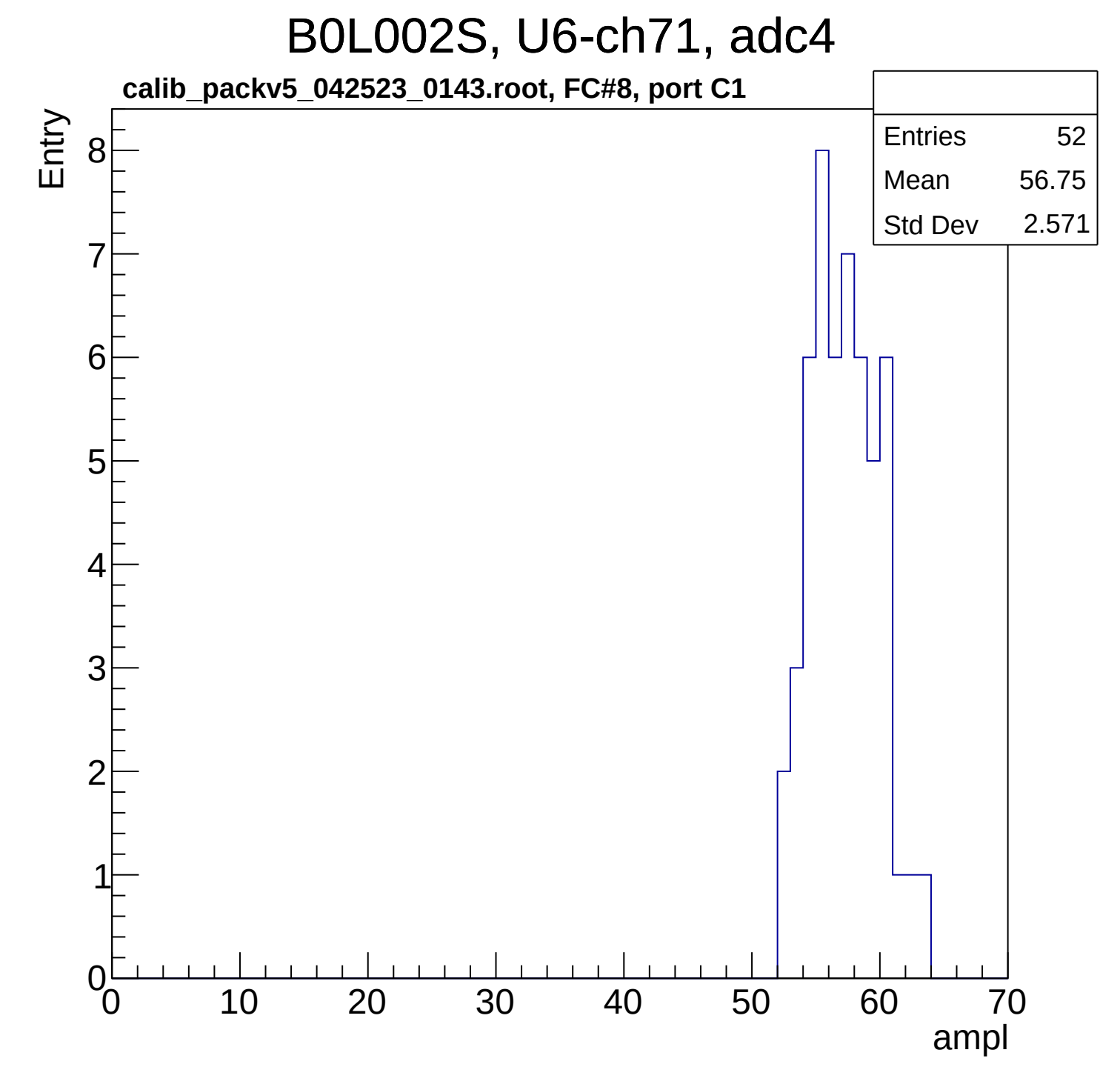
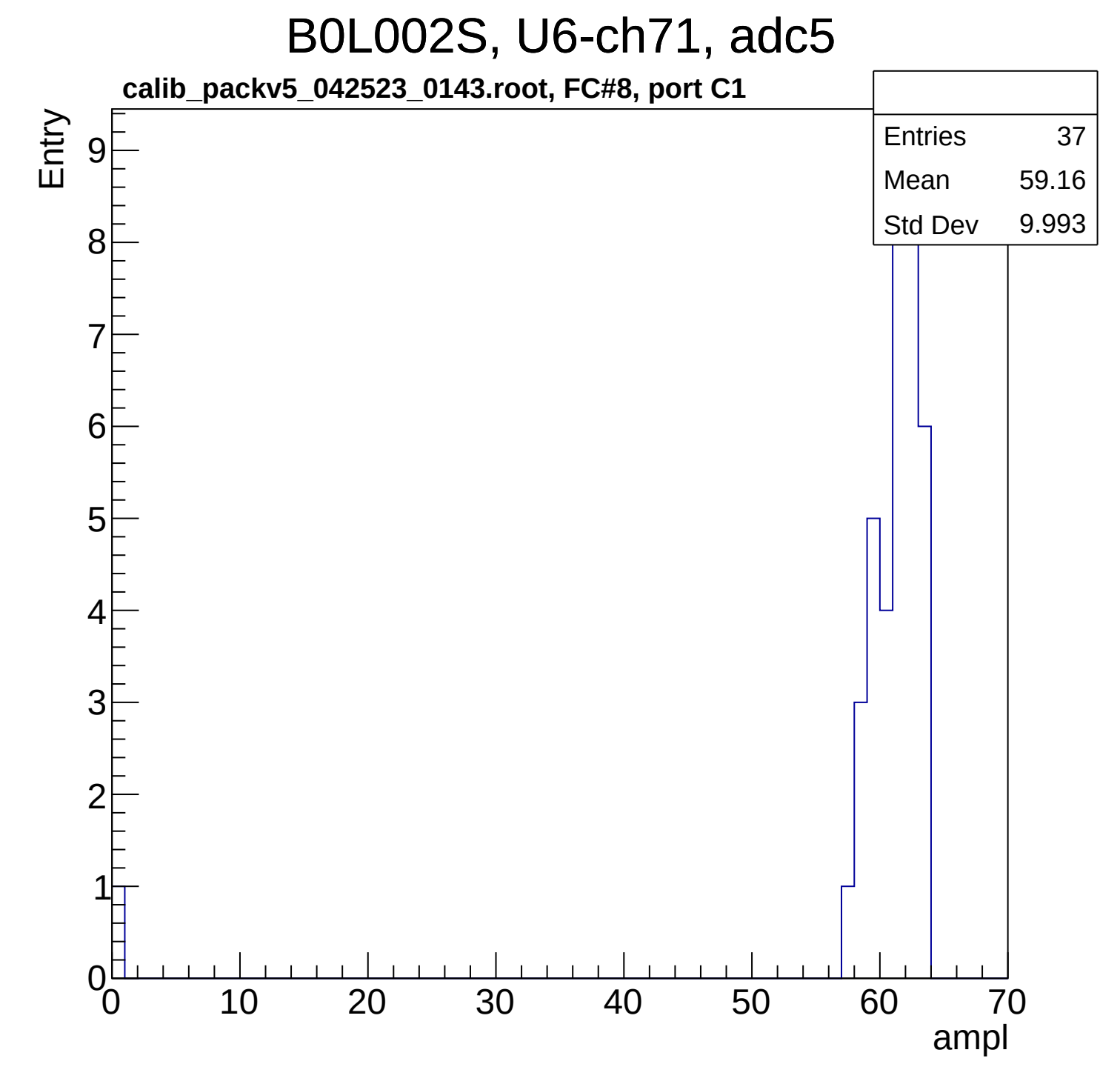
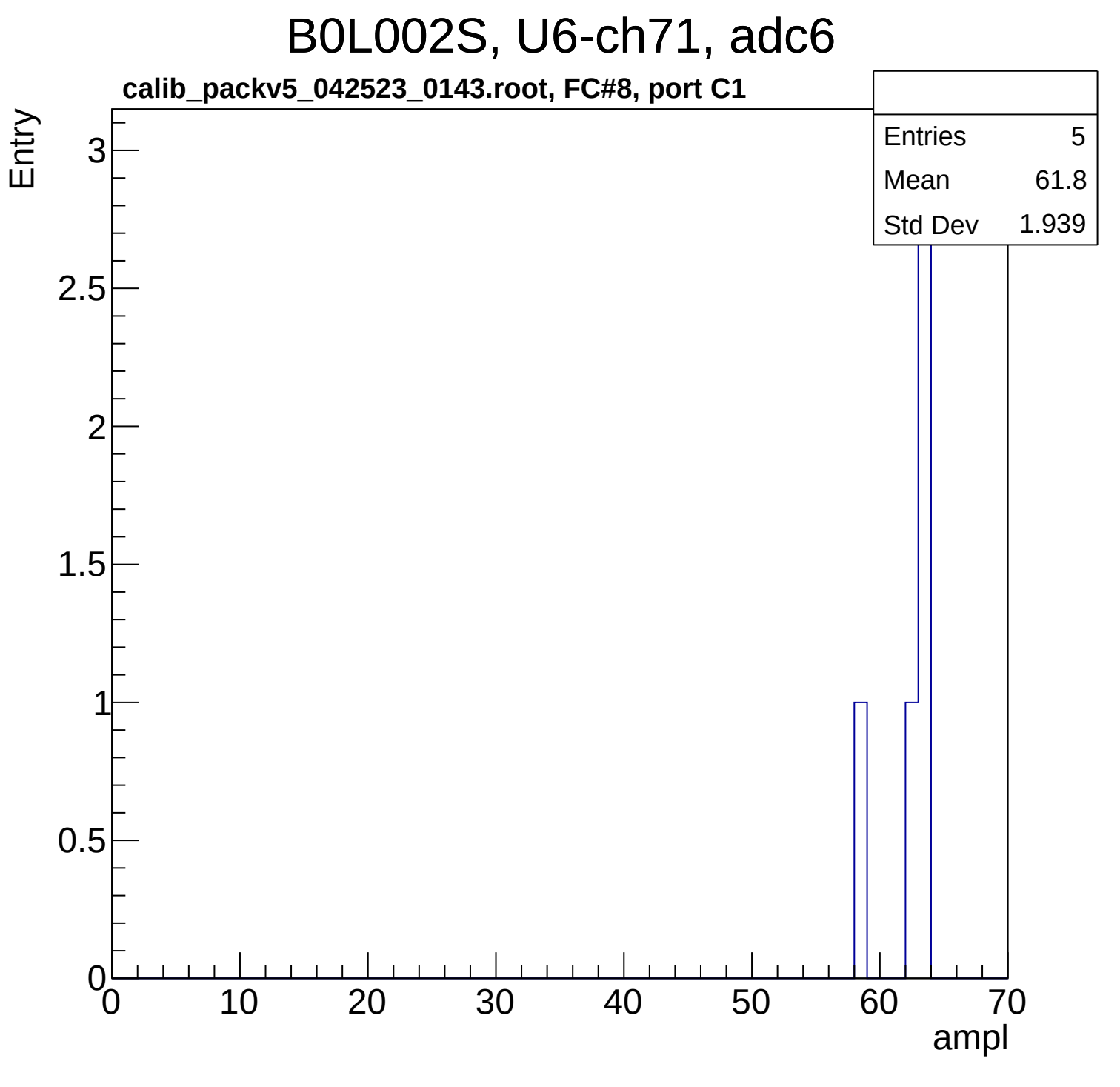
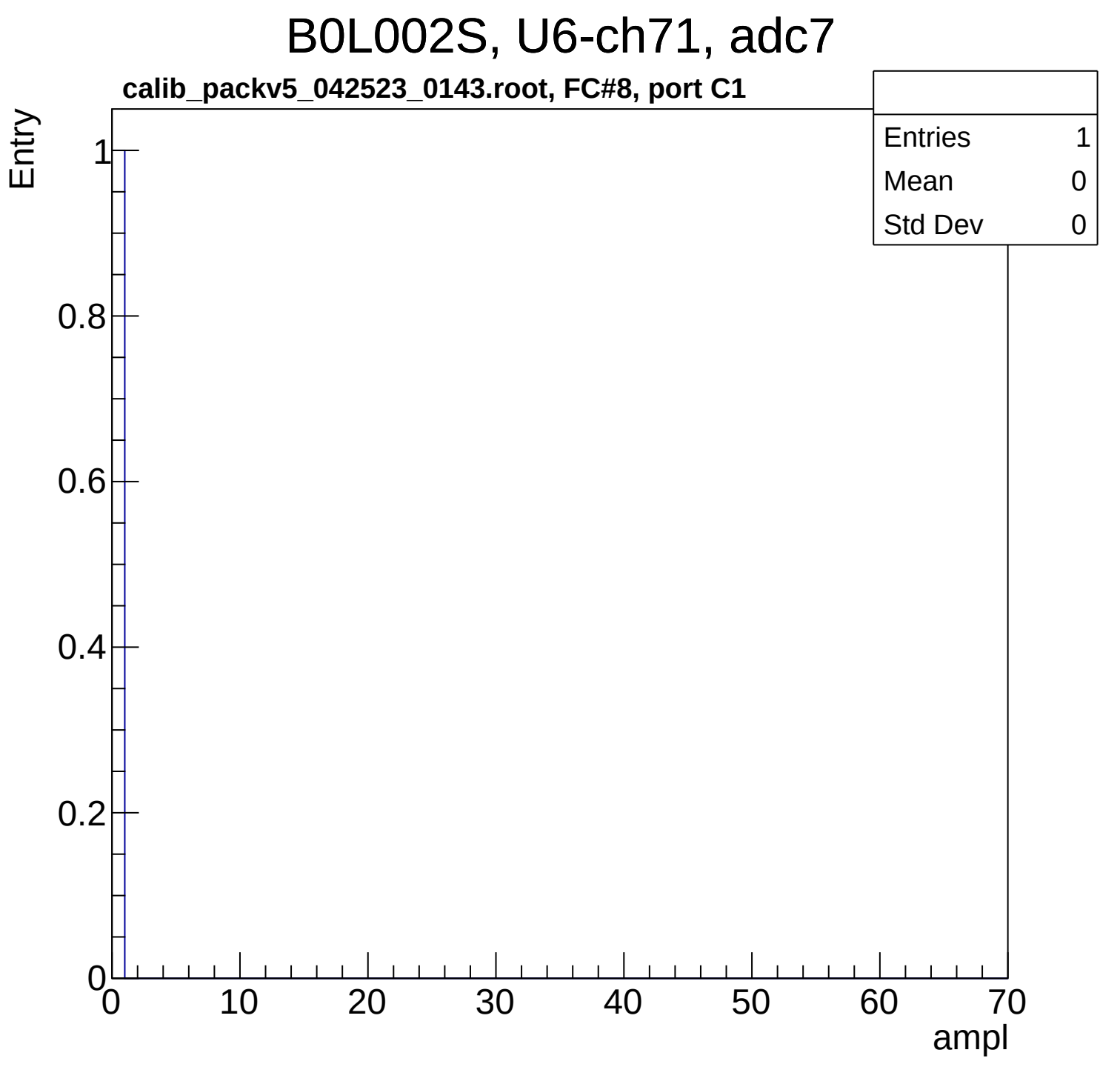

B0L002S, U6-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 96
Mean 43.85
Std Dev 3.905
Gaus mean : 44.1872
Gaus Width: 3.9472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

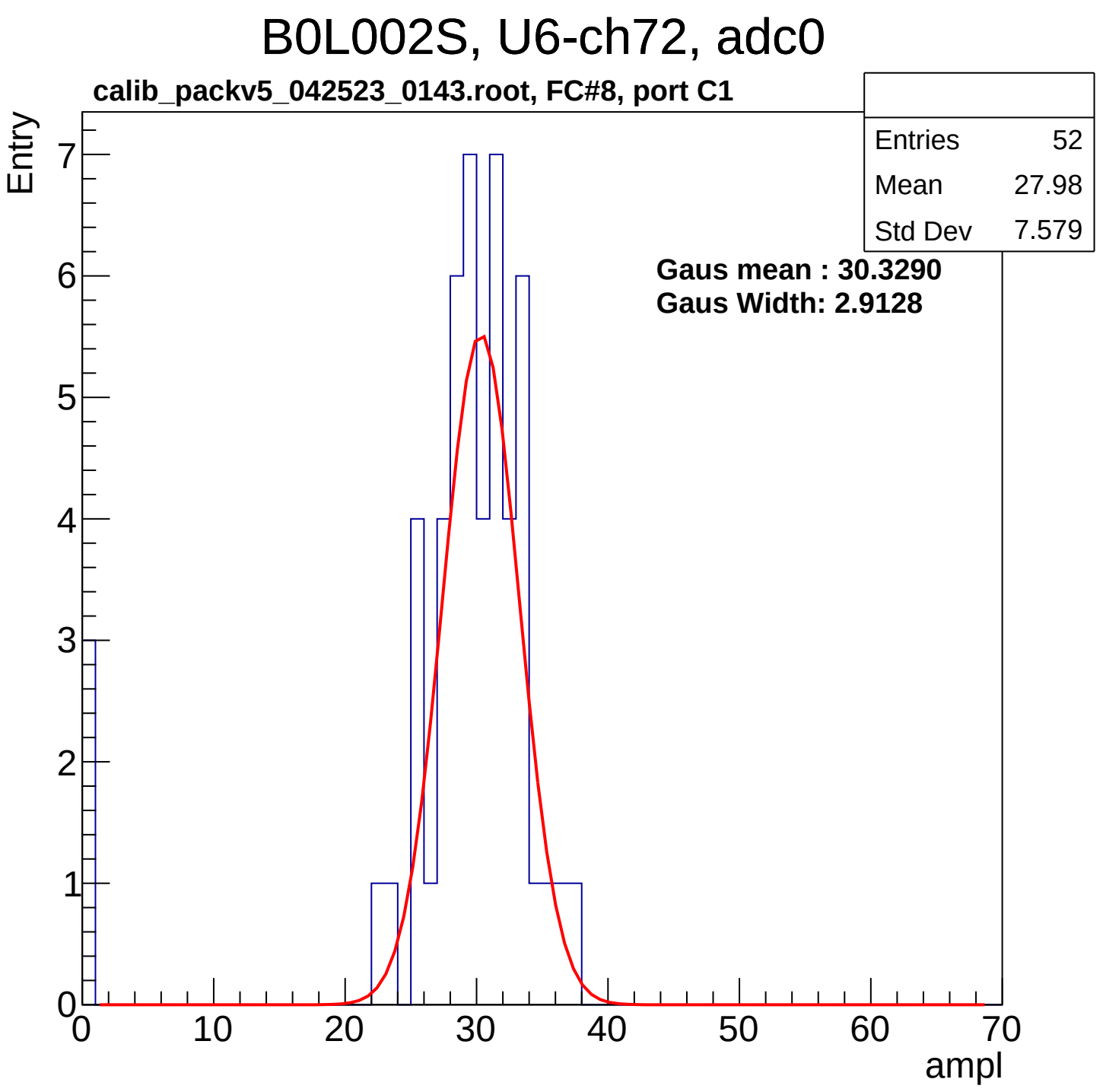

B0L002S, U6-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 50.86
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

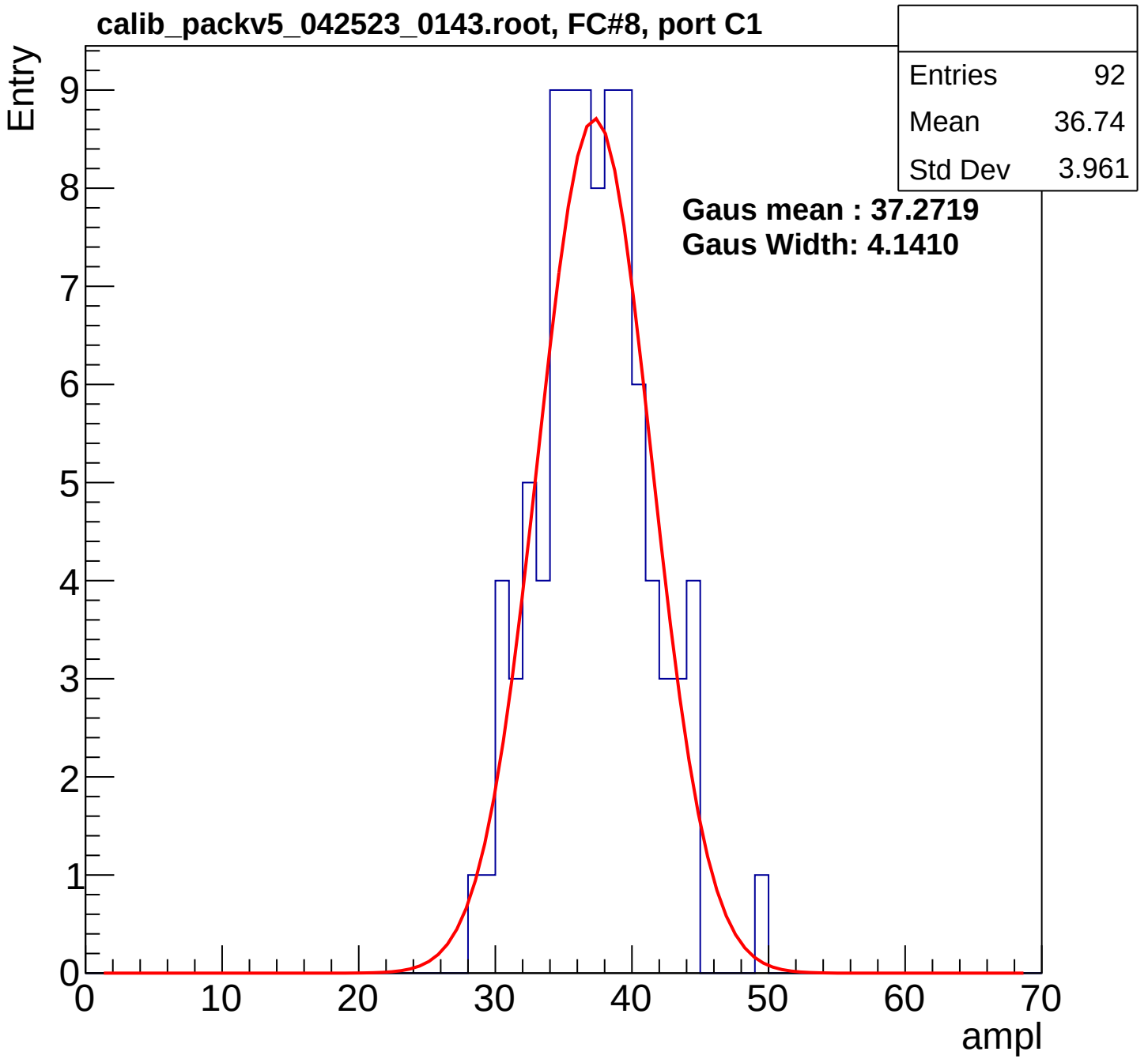

B0L002S, U6-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.75
Std Dev 2.571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

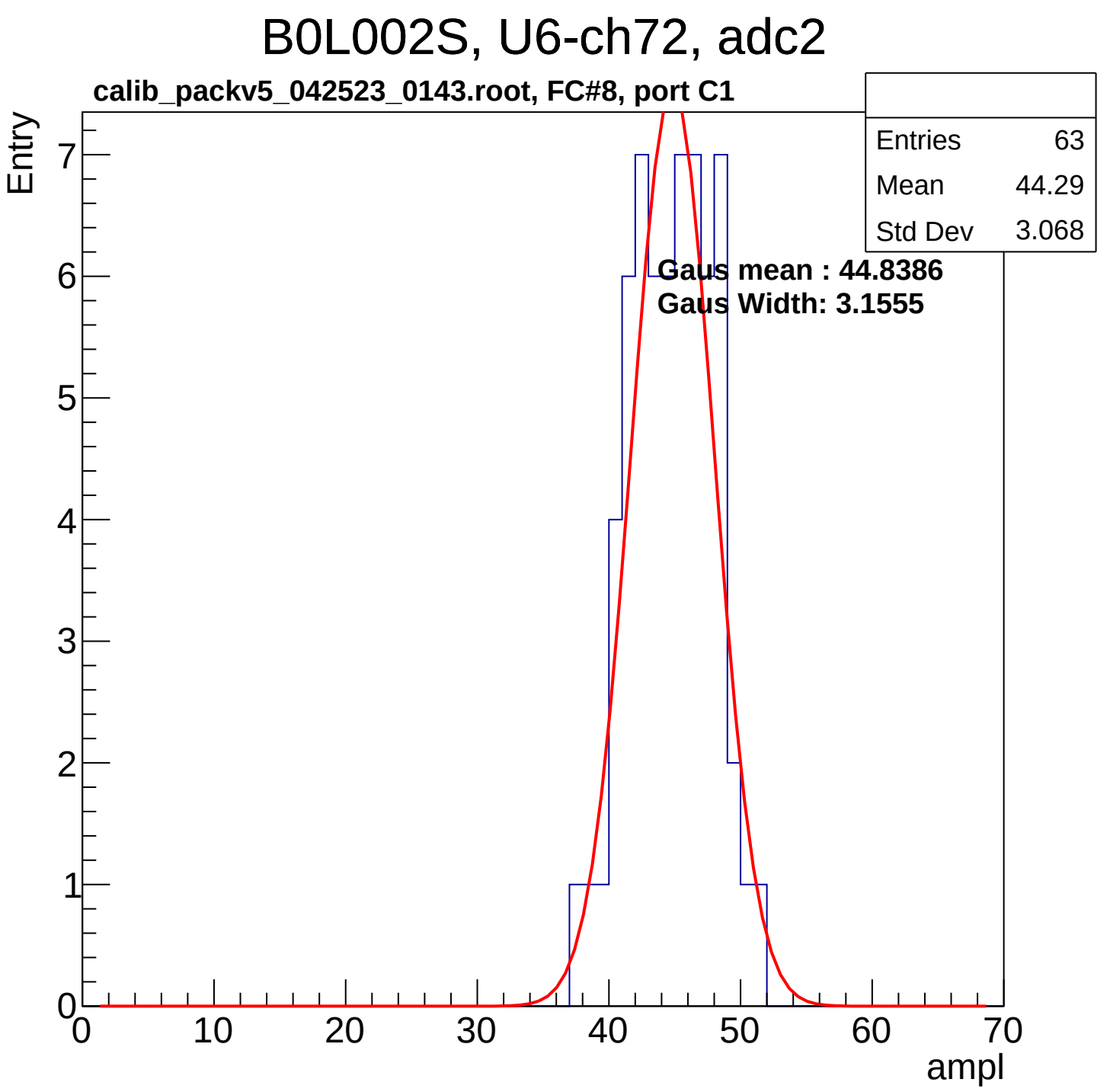

B0L002S, U6-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.16
Std Dev 9.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.8
Std Dev 1.939
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 27.98
Std Dev 7.579
Gaus mean : 30.3290
Gaus Width: 2.9128
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 92
Mean 36.74
Std Dev 3.961
Gaus mean : 37.2719
Gaus Width: 4.1410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.29
Std Dev 3.068
Gaus mean : 44.8386
Gaus Width: 3.1555
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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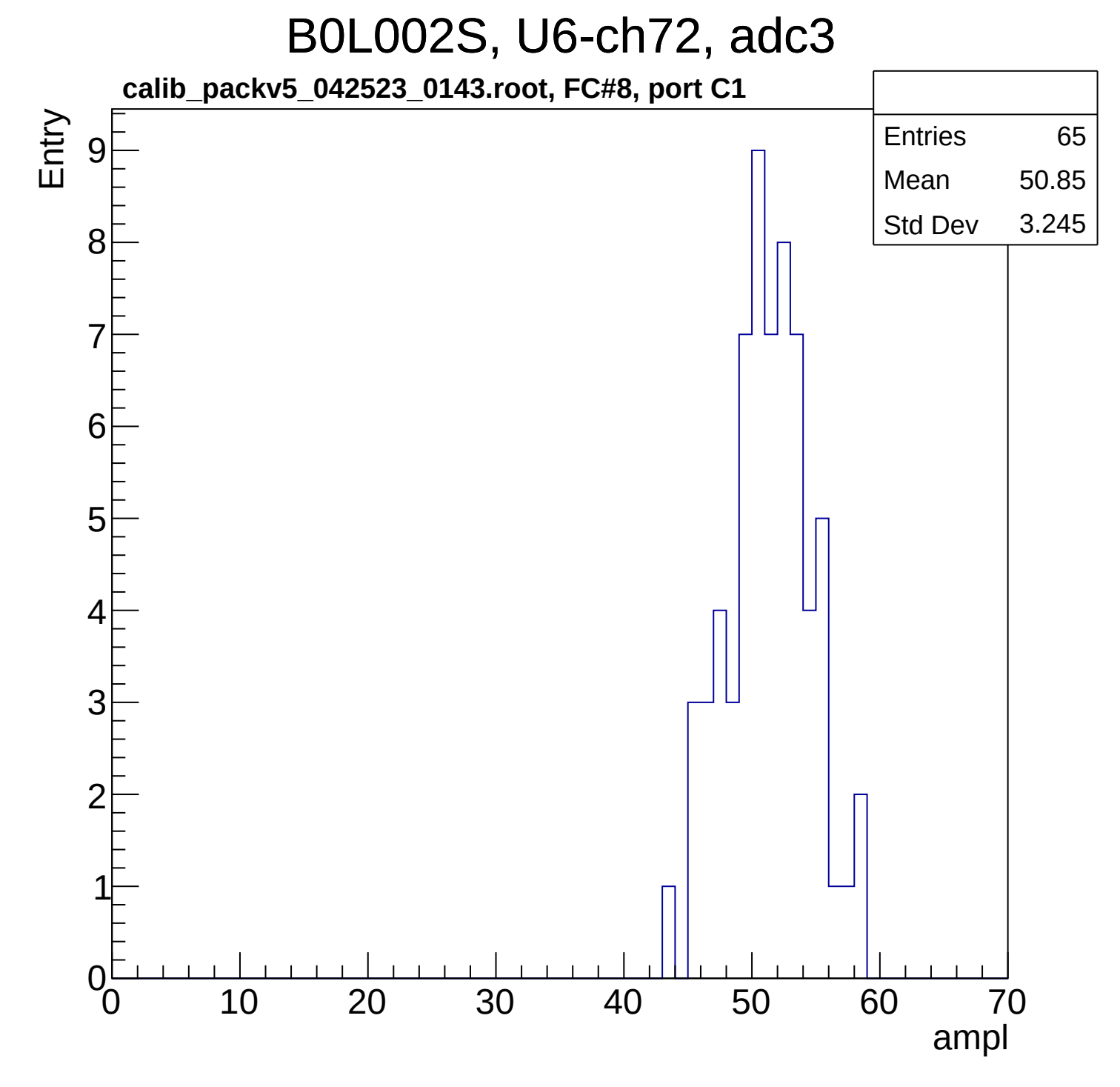
B0L002S, U6-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.85
Std Dev 3.245
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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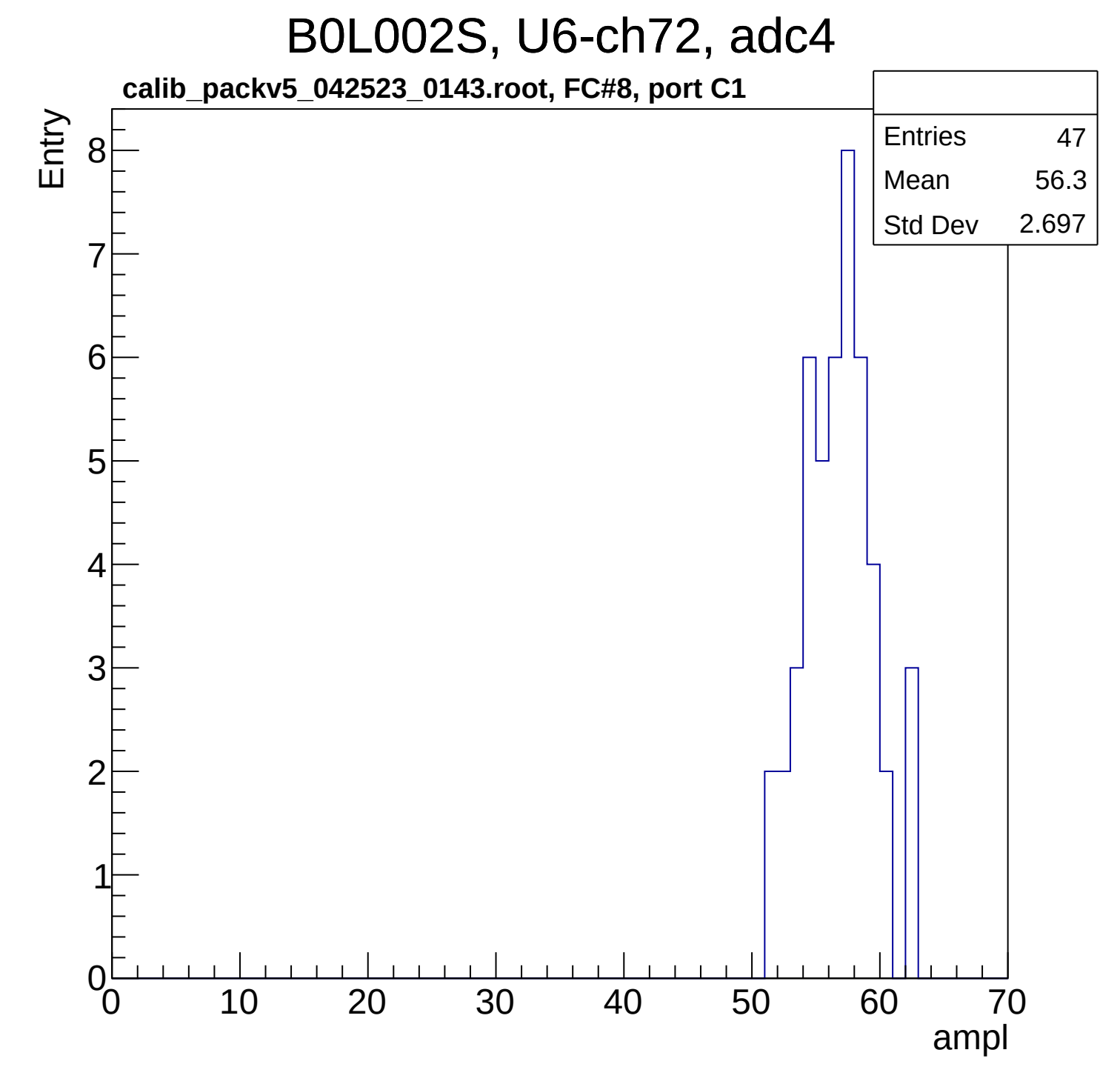
B0L002S, U6-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 56.3
Std Dev 2.697
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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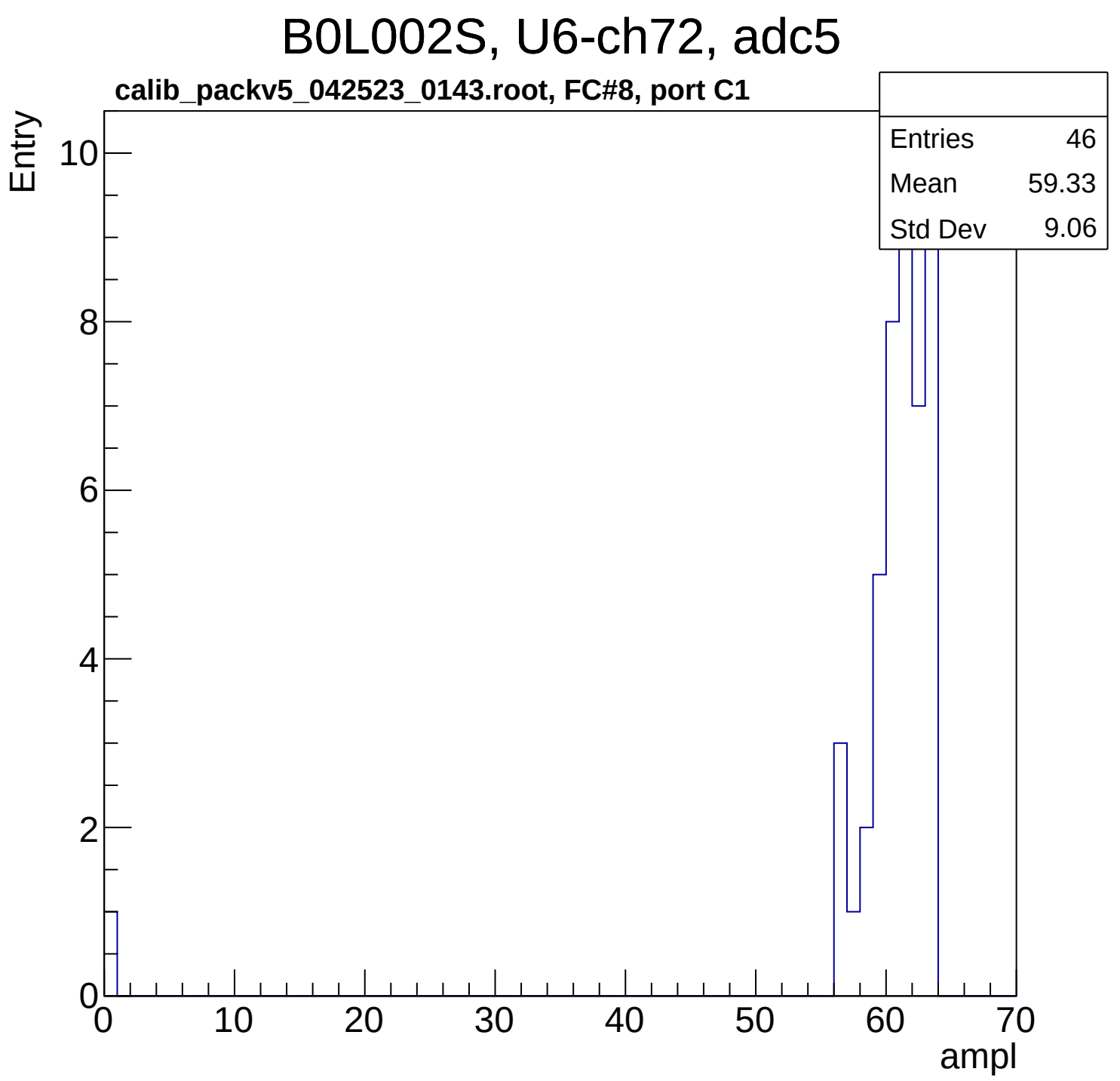
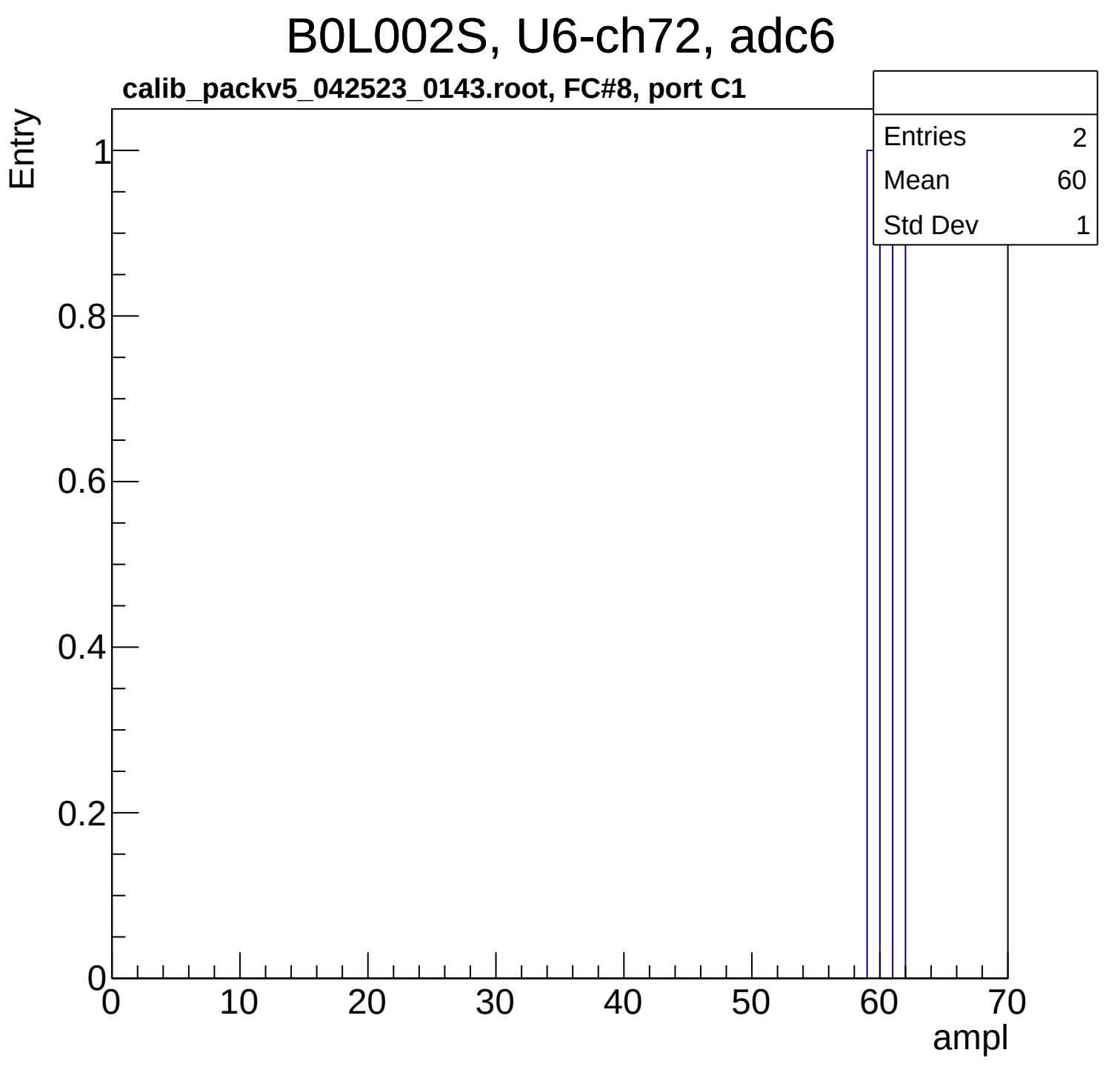
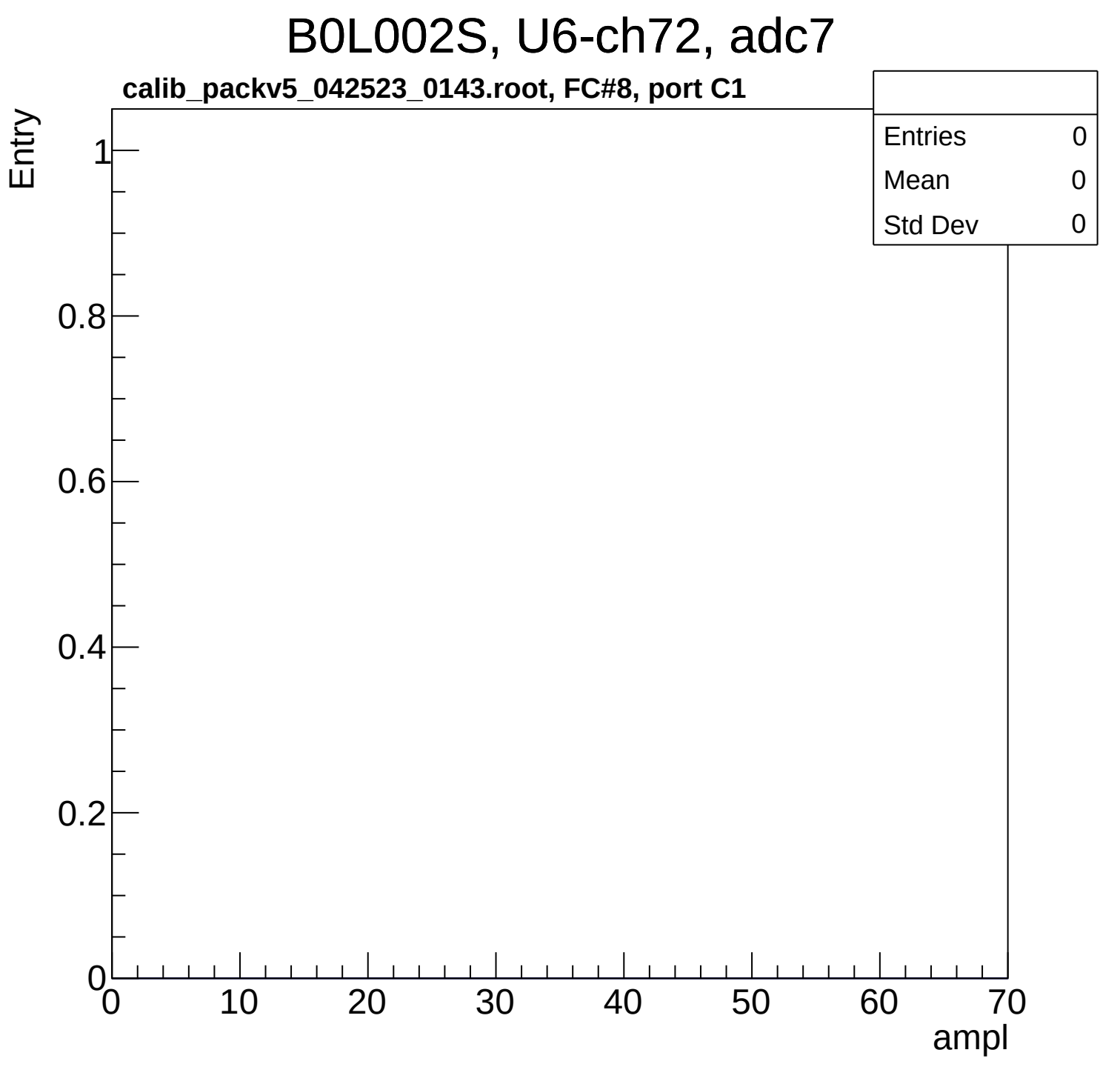

B0L002S, U6-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.33
Std Dev 9.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 60
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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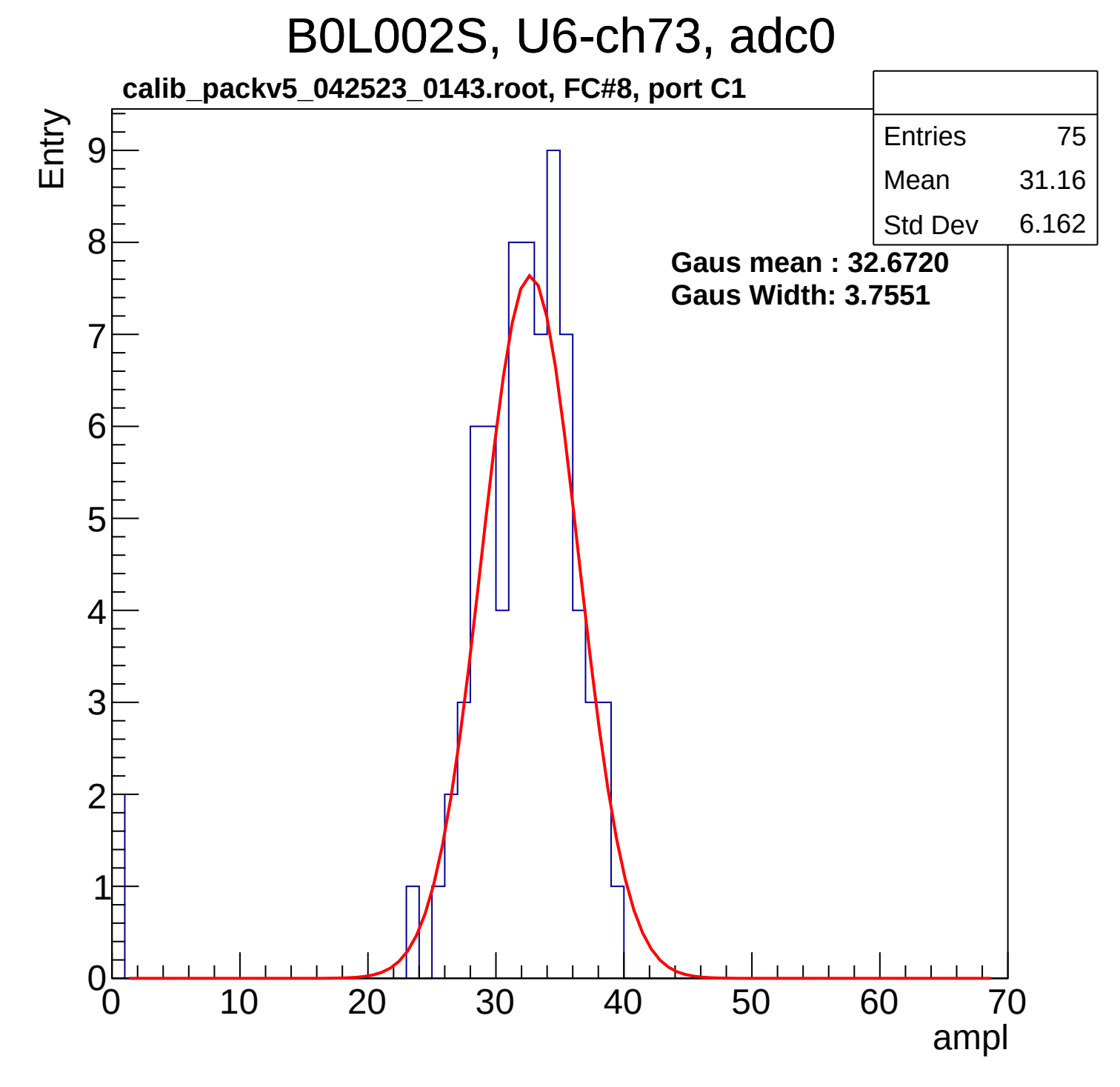
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.16
Std Dev 6.162
Gaus mean : 32.6720
Gaus Width: 3.7551
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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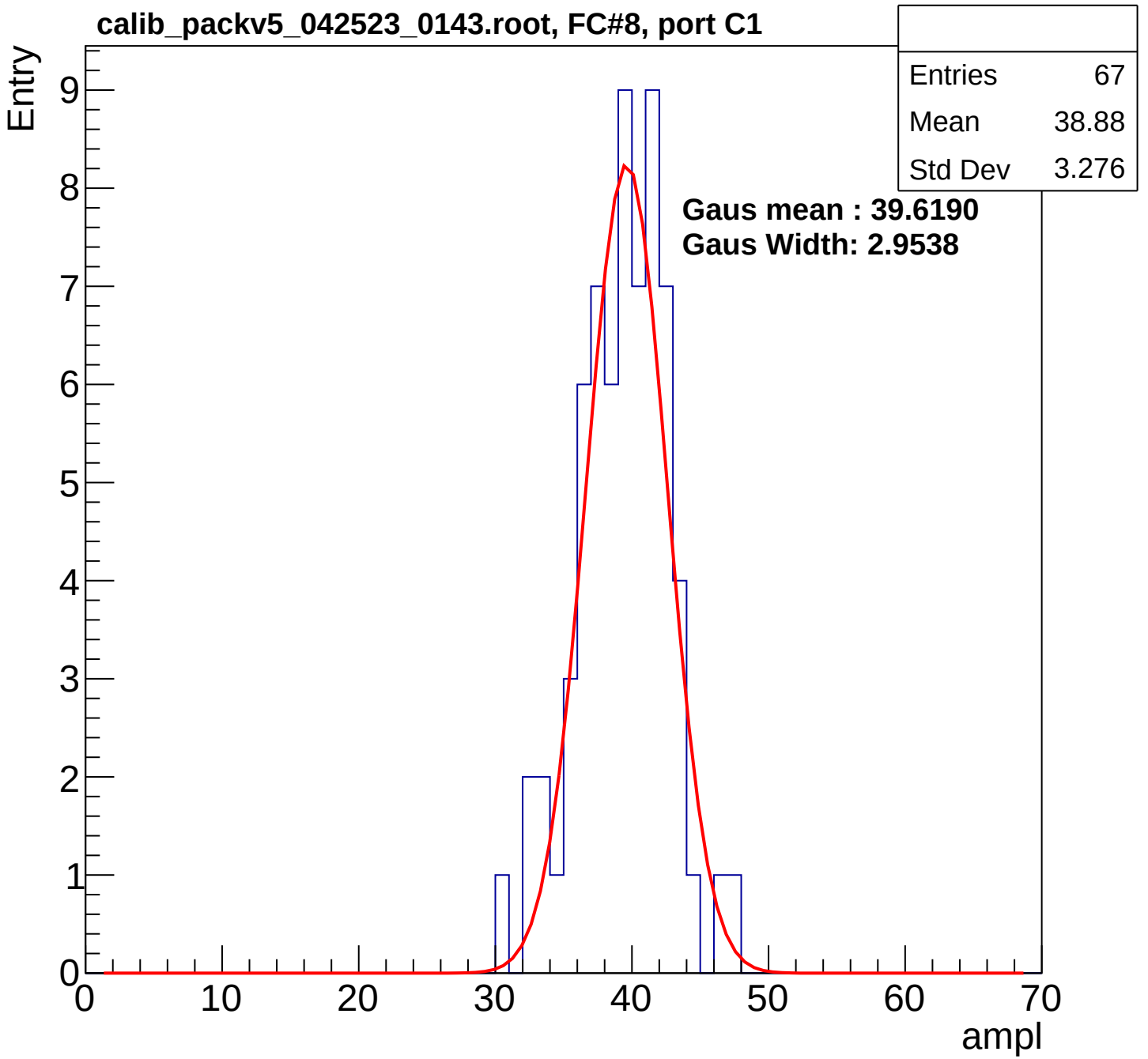
B0L002S, U6-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.88
Std Dev 3.276
Gaus mean : 39.6190
Gaus Width: 2.9538
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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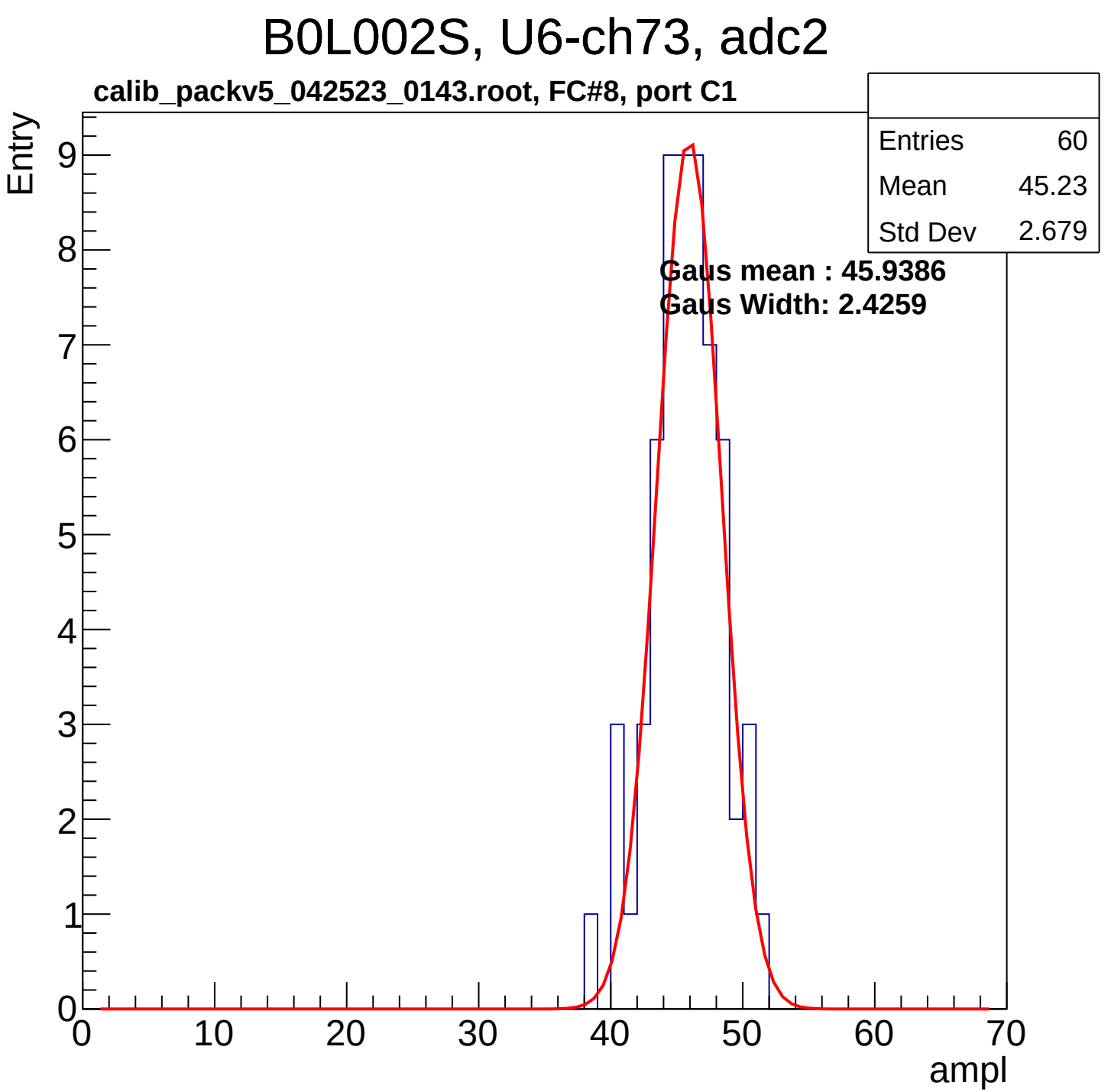
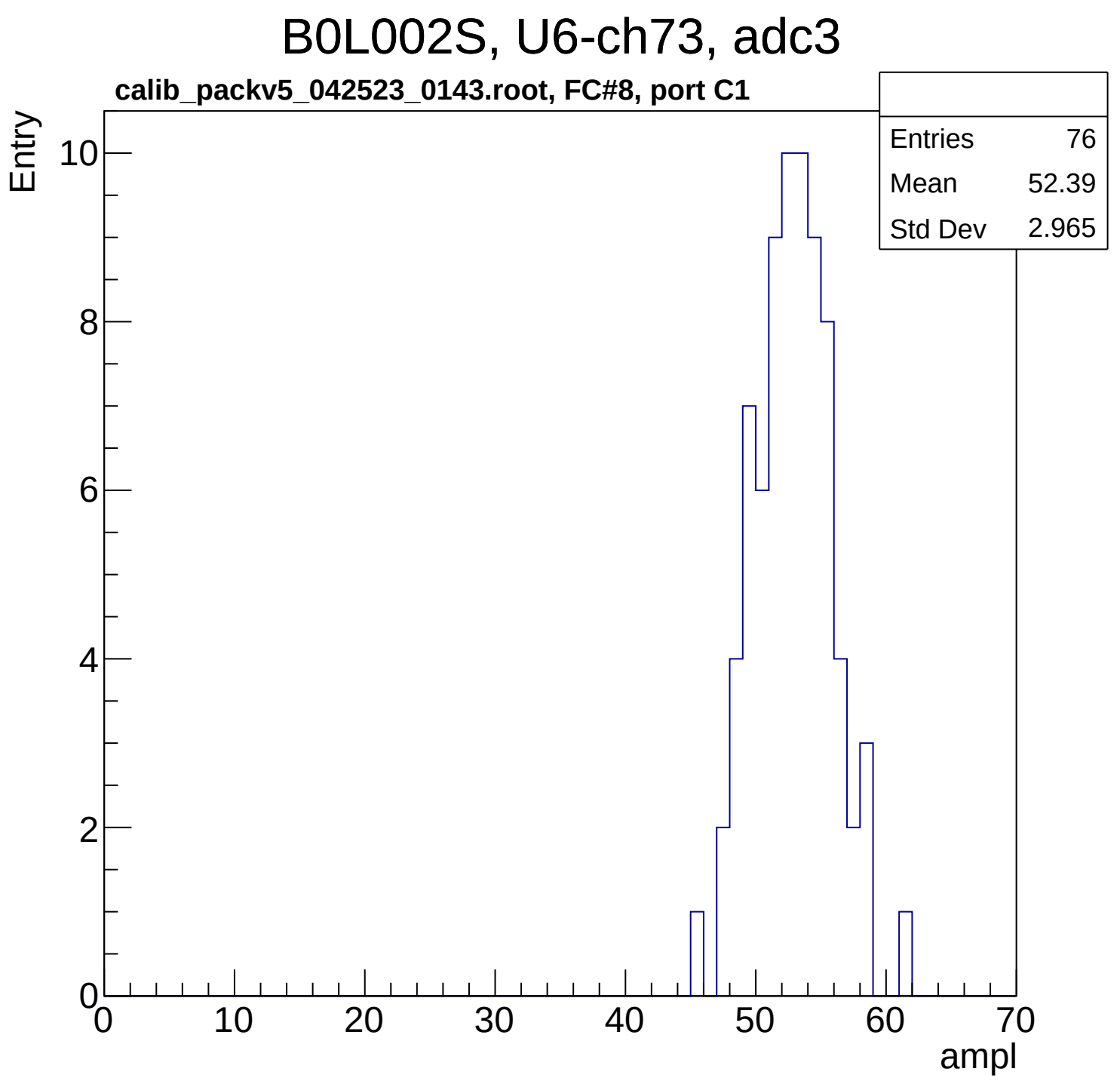
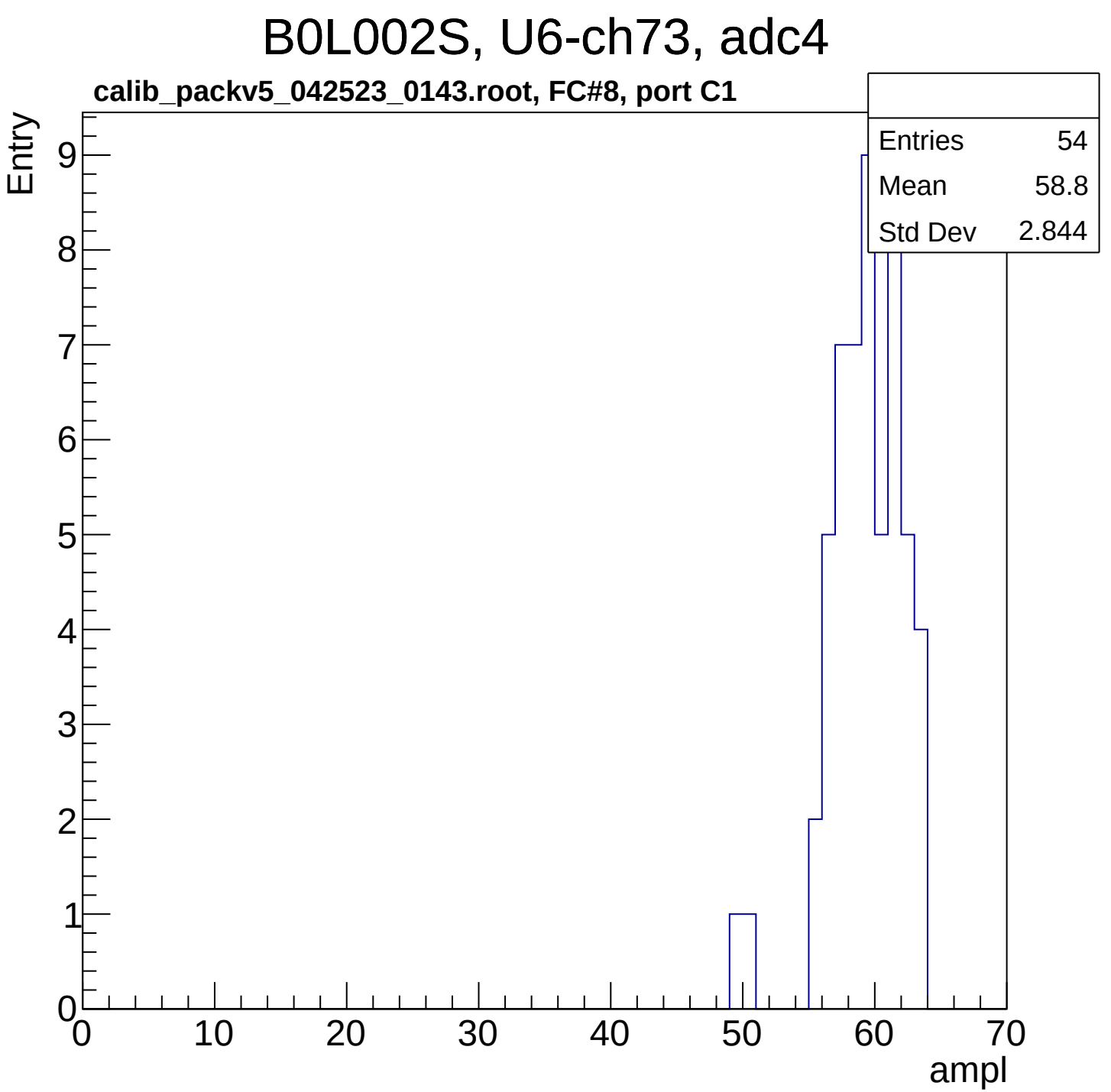
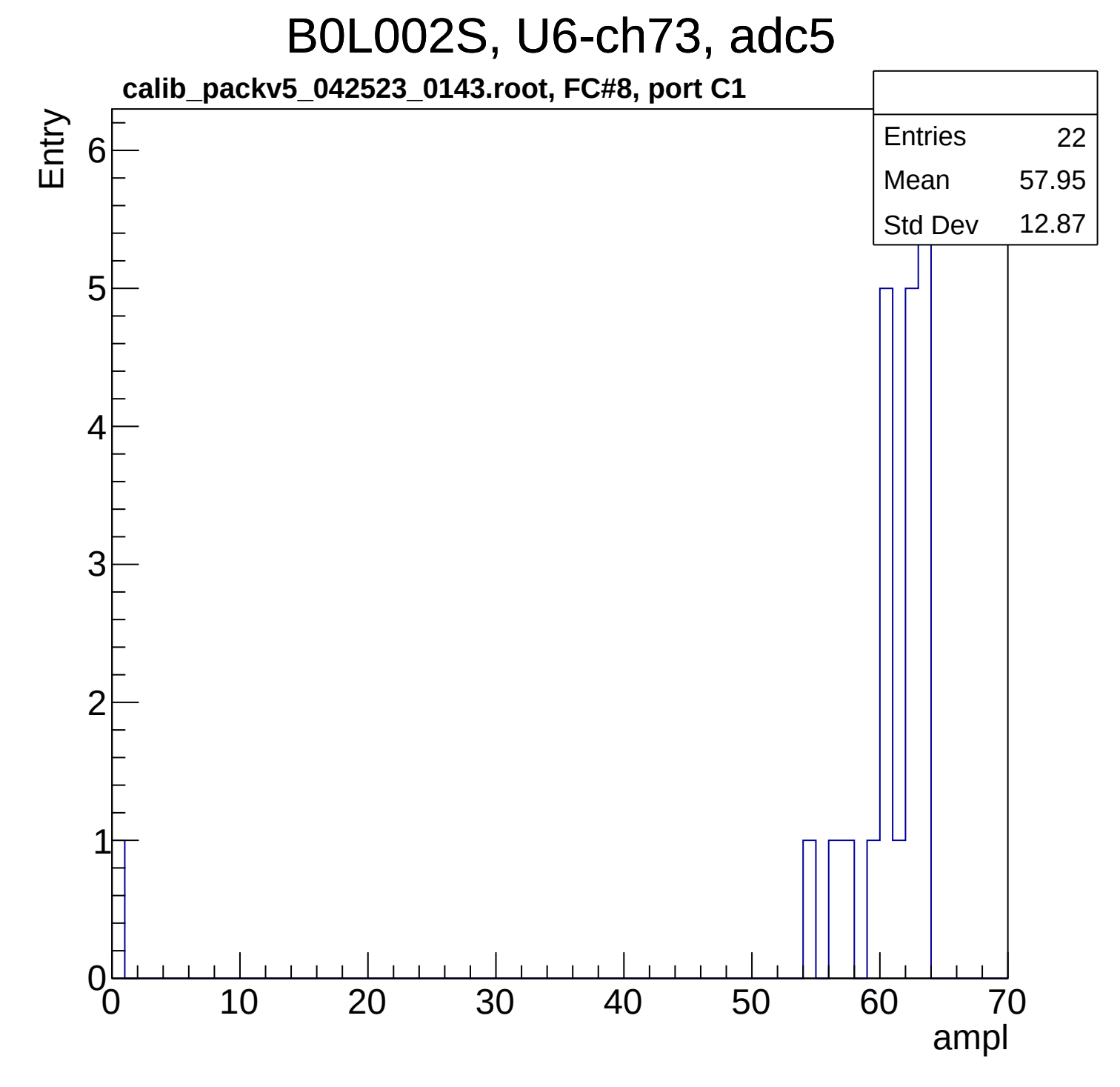
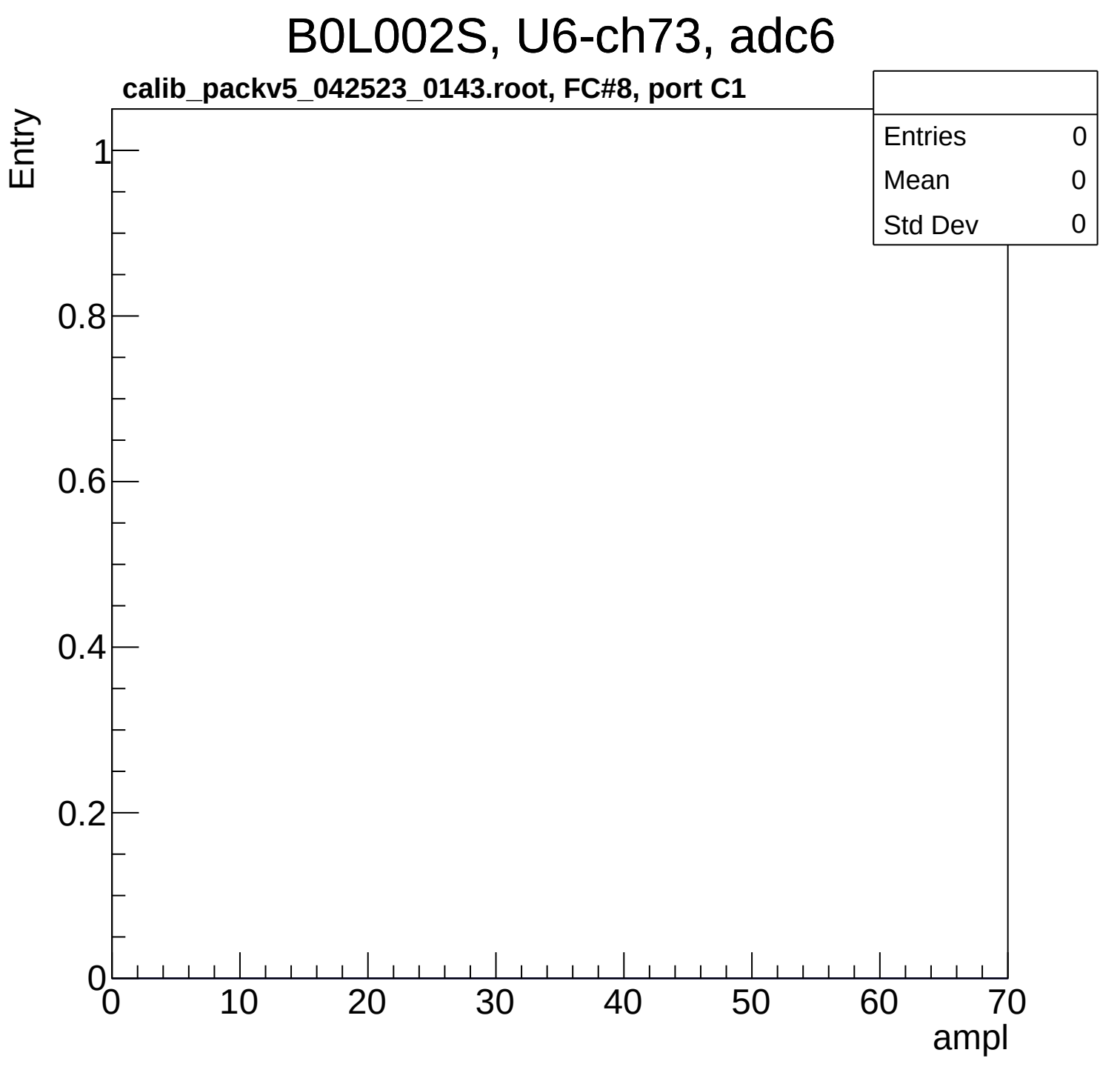
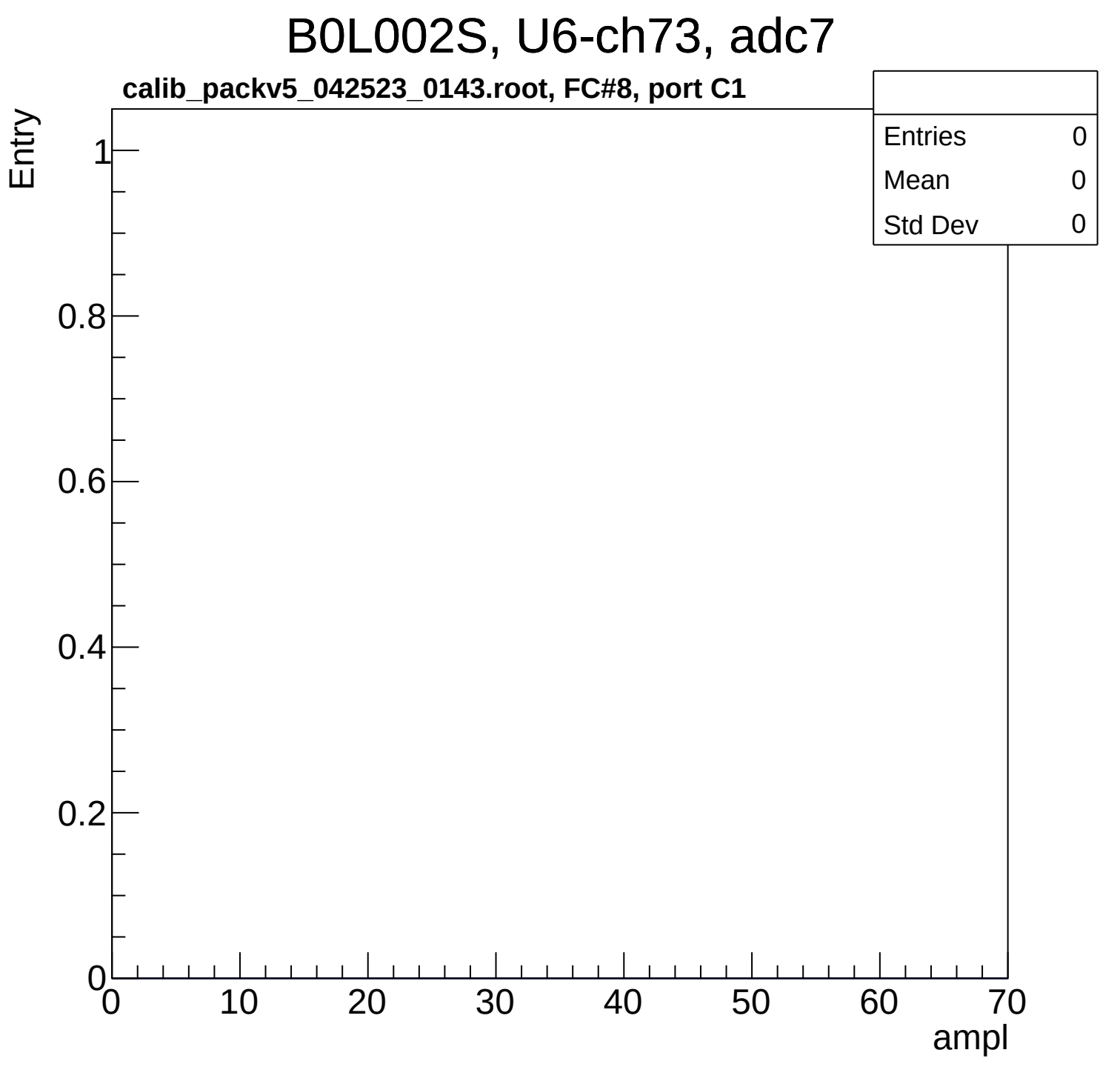

B0L002S, U6-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.23
Std Dev 2.679
Gaus mean : 45.9386
Gaus Width: 2.4259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

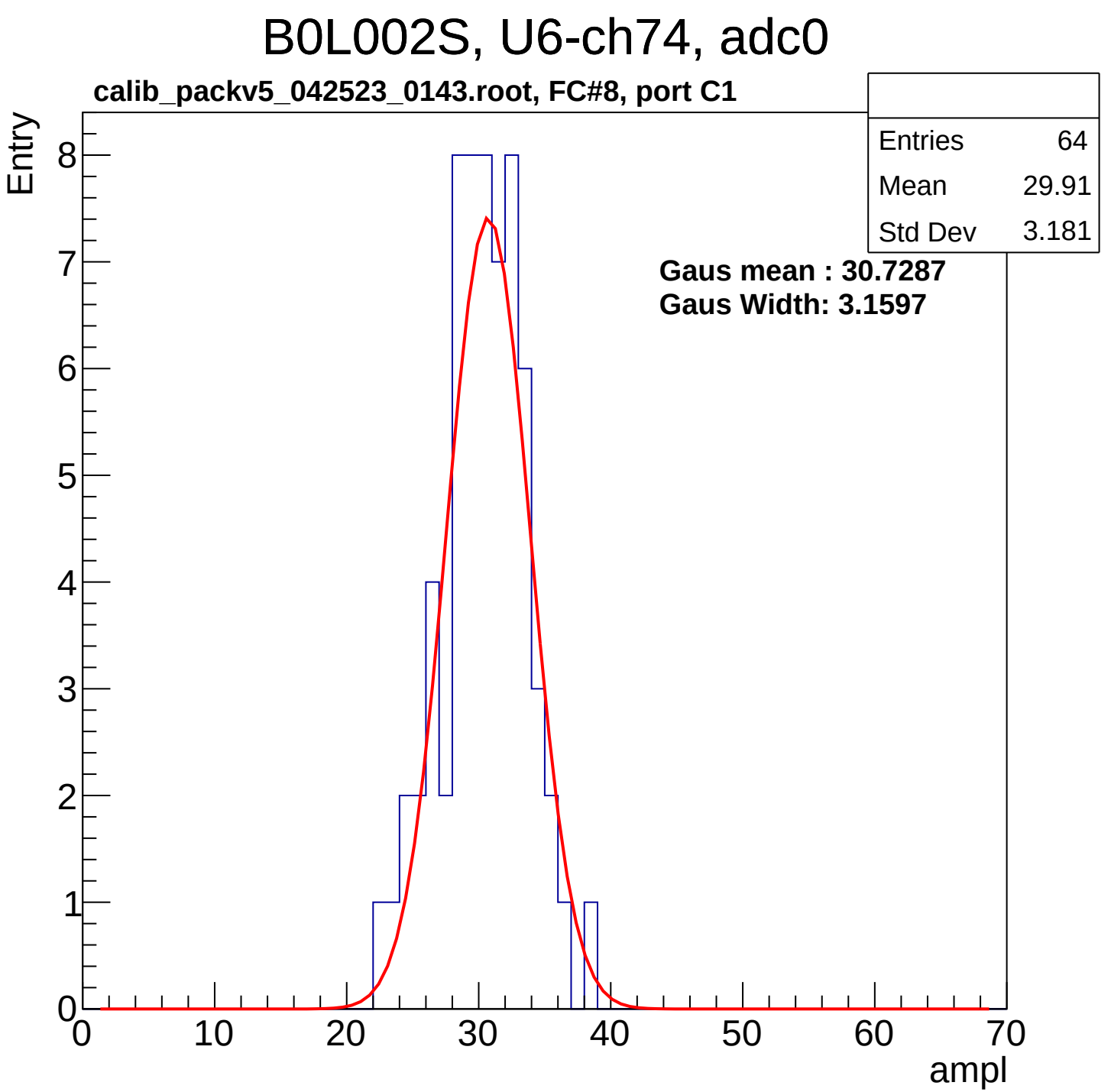

B0L002S, U6-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.39
Std Dev 2.965
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

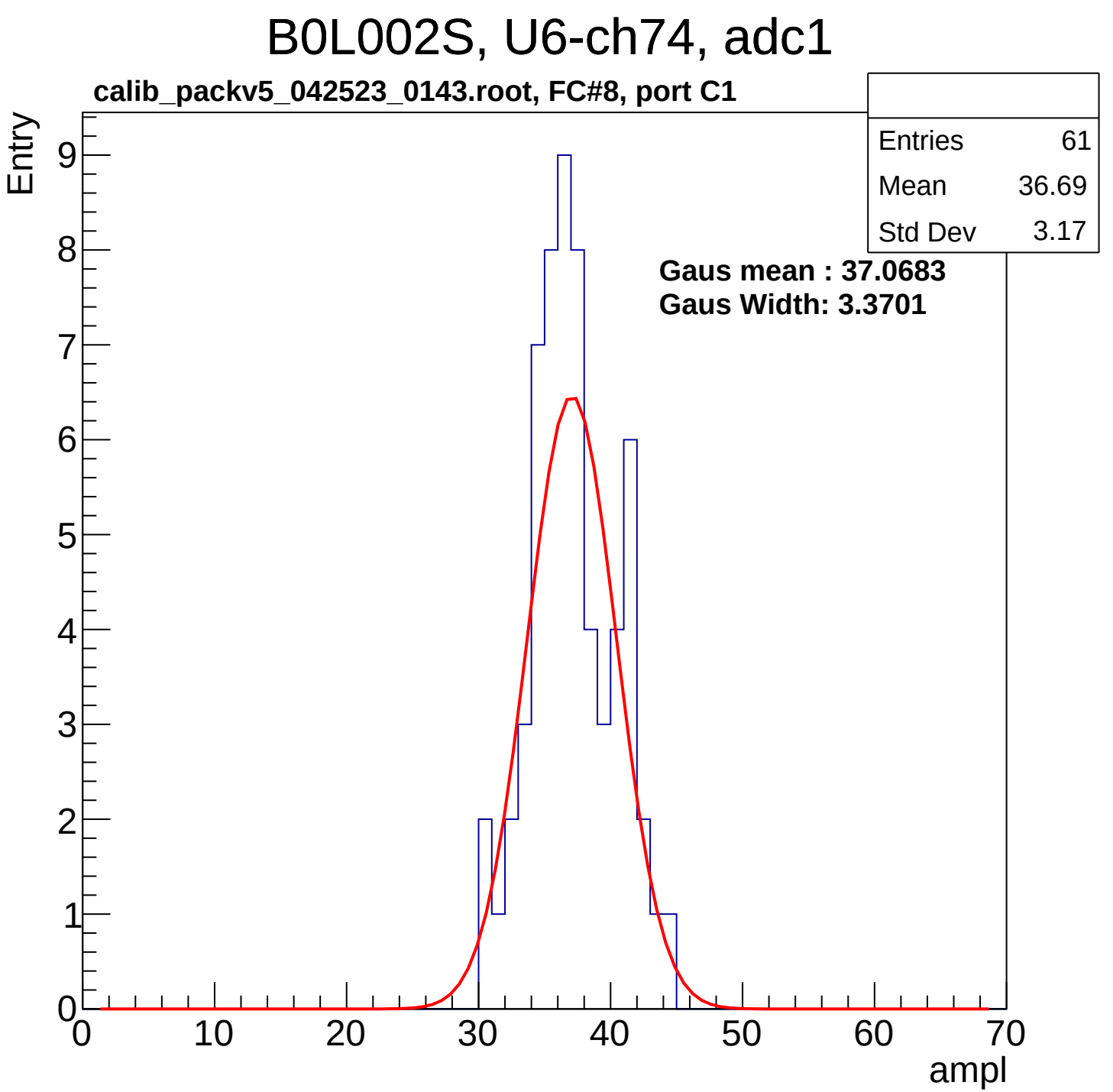

B0L002S, U6-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 58.8
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

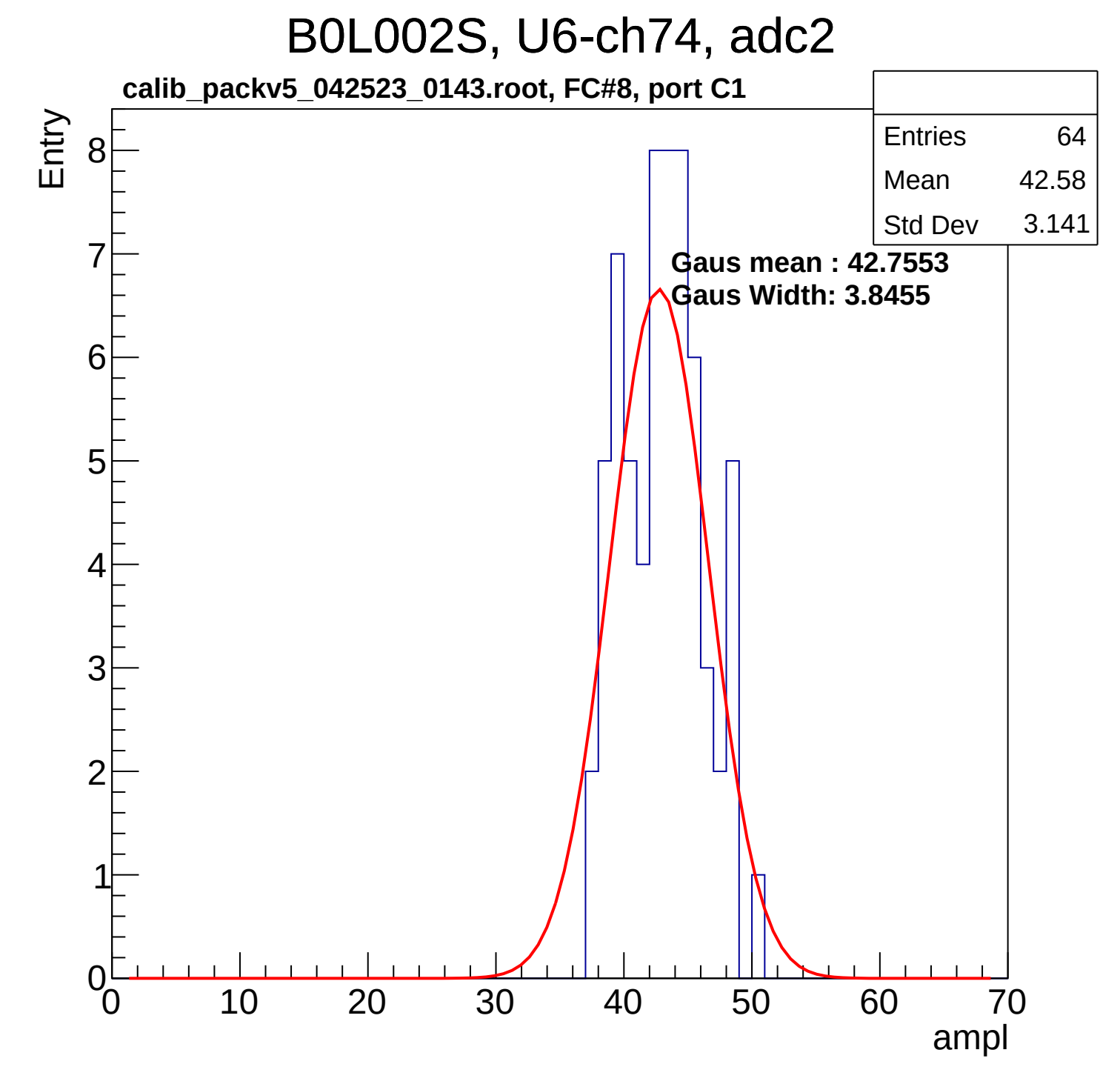

B0L002S, U6-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 57.95
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

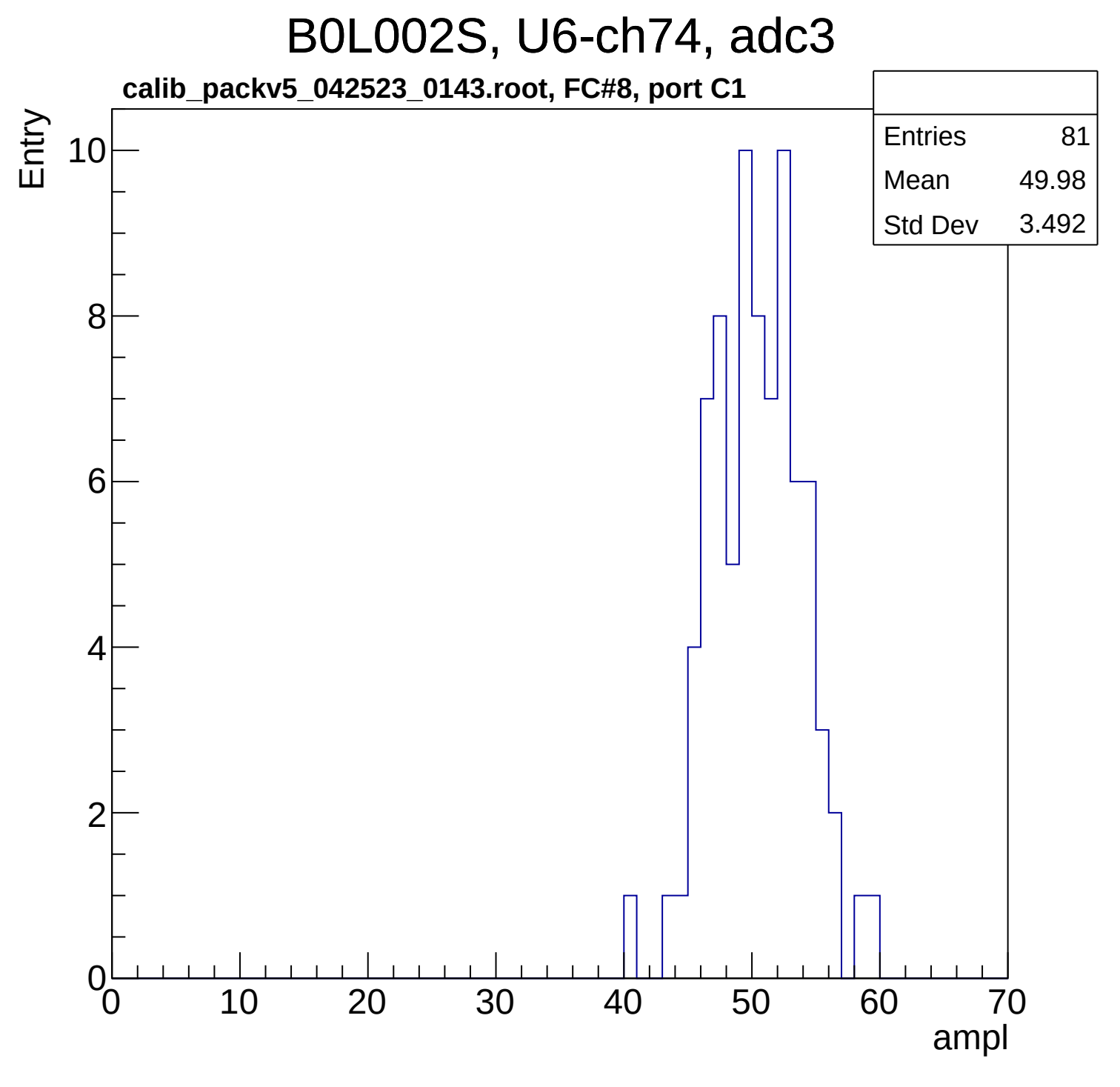

B0L002S, U6-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

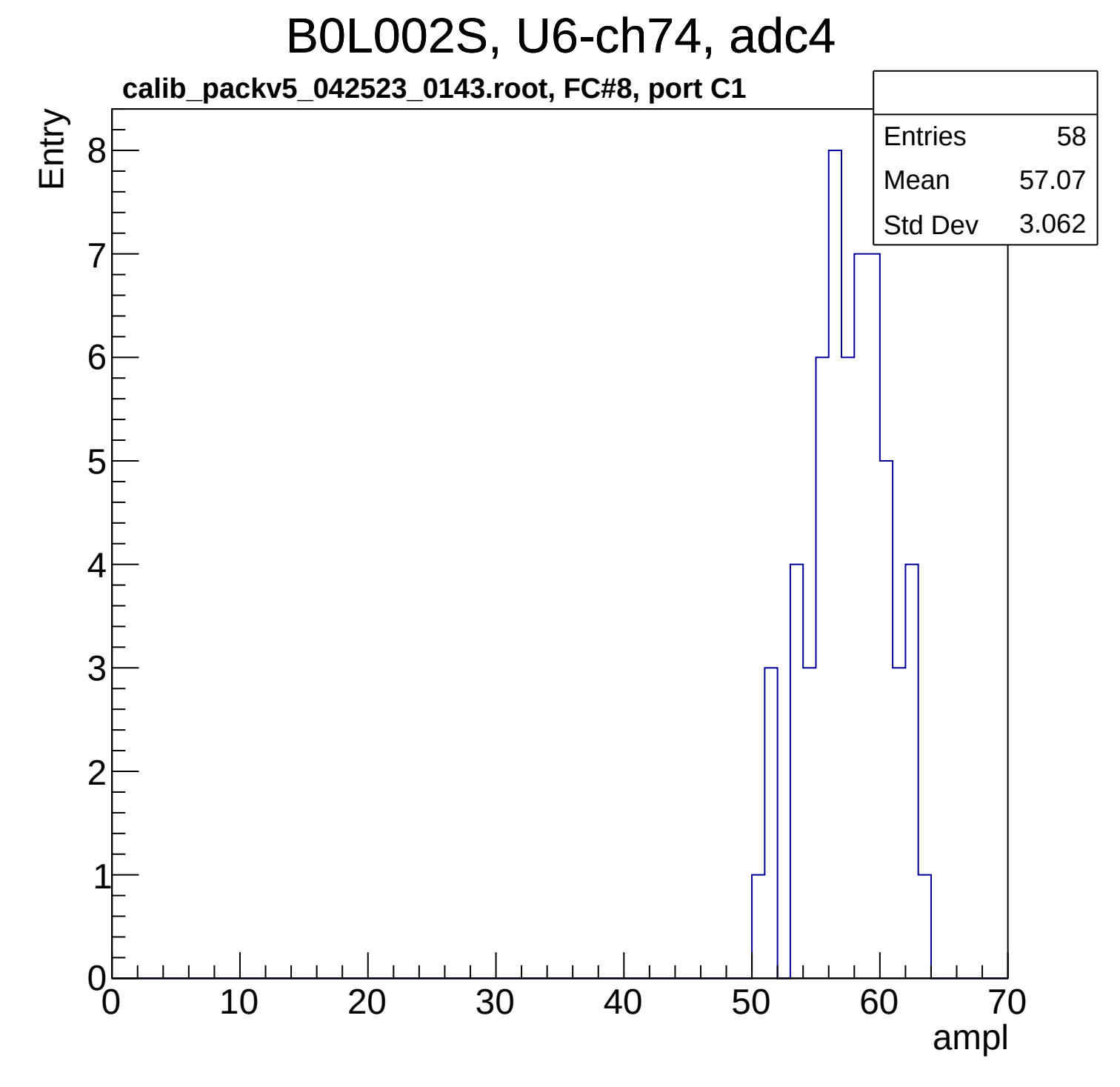

B0L002S, U6-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

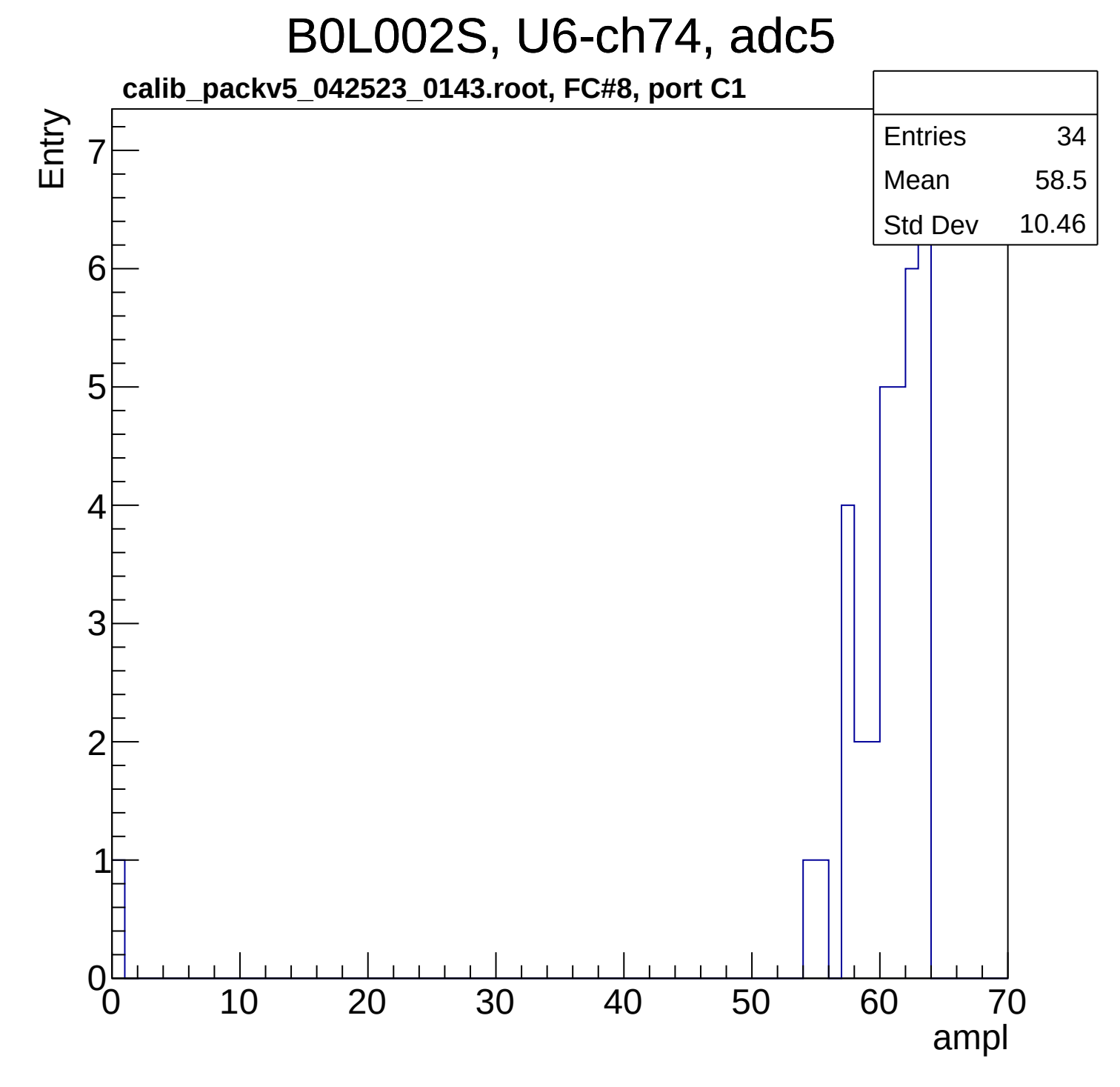

B0L002S, U6-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.91
Std Dev 3.181
Gaus mean : 30.7287
Gaus Width: 3.1597
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

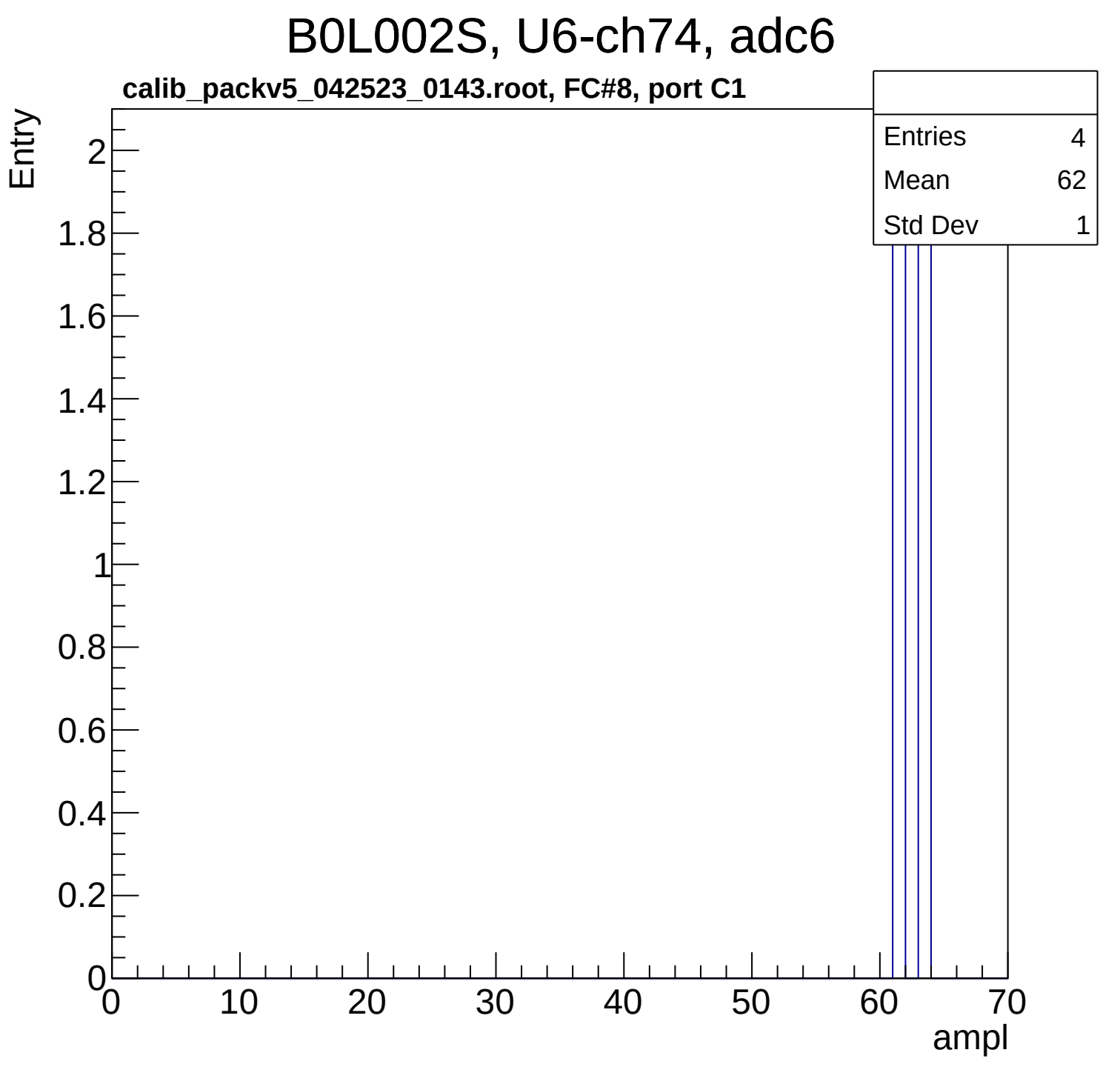

B0L002S, U6-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.69
Std Dev 3.17
Gaus mean : 37.0683
Gaus Width: 3.3701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

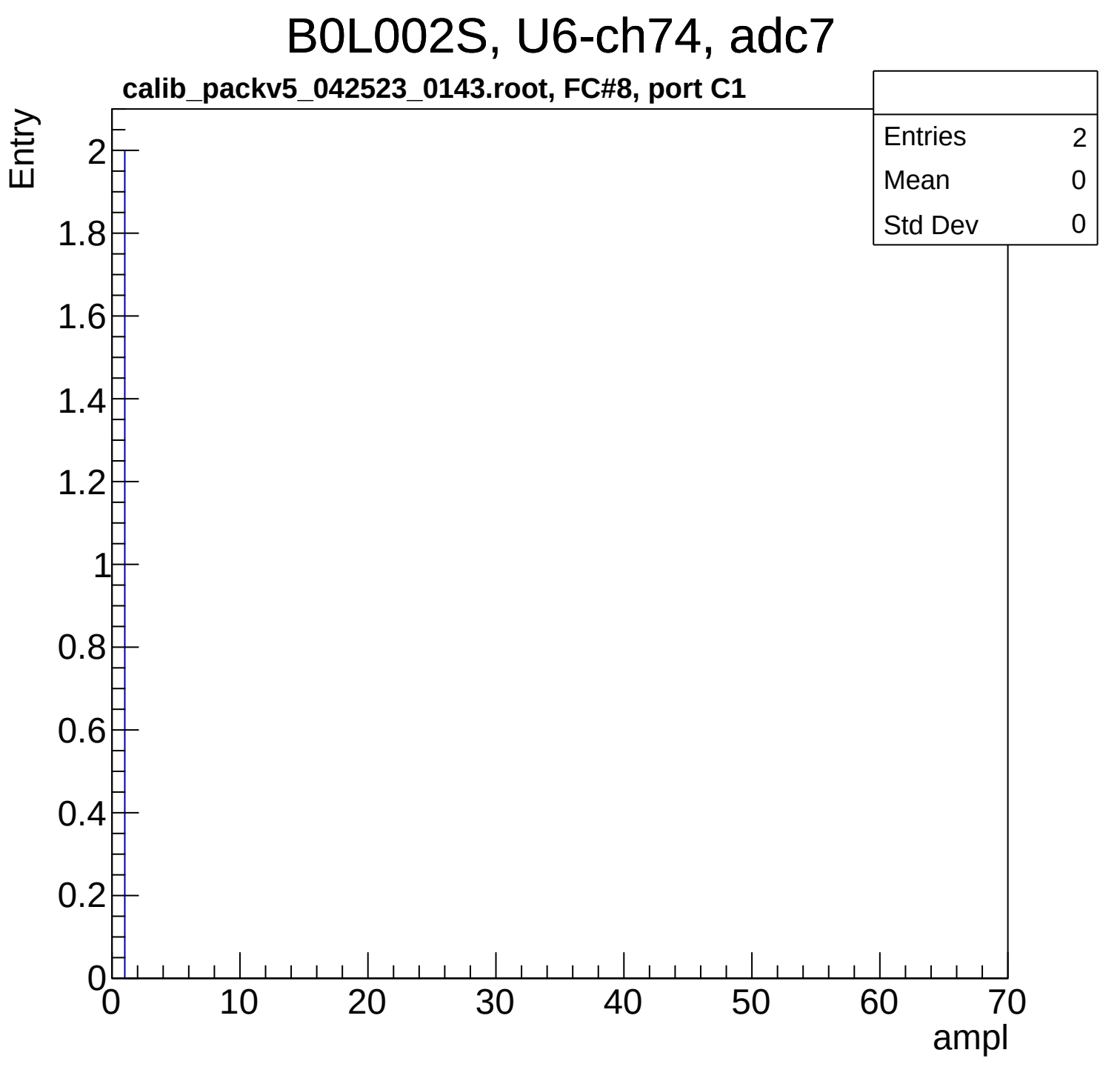

B0L002S, U6-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 42.58
Std Dev 3.141
Gaus mean : 42.7553
Gaus Width: 3.8455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

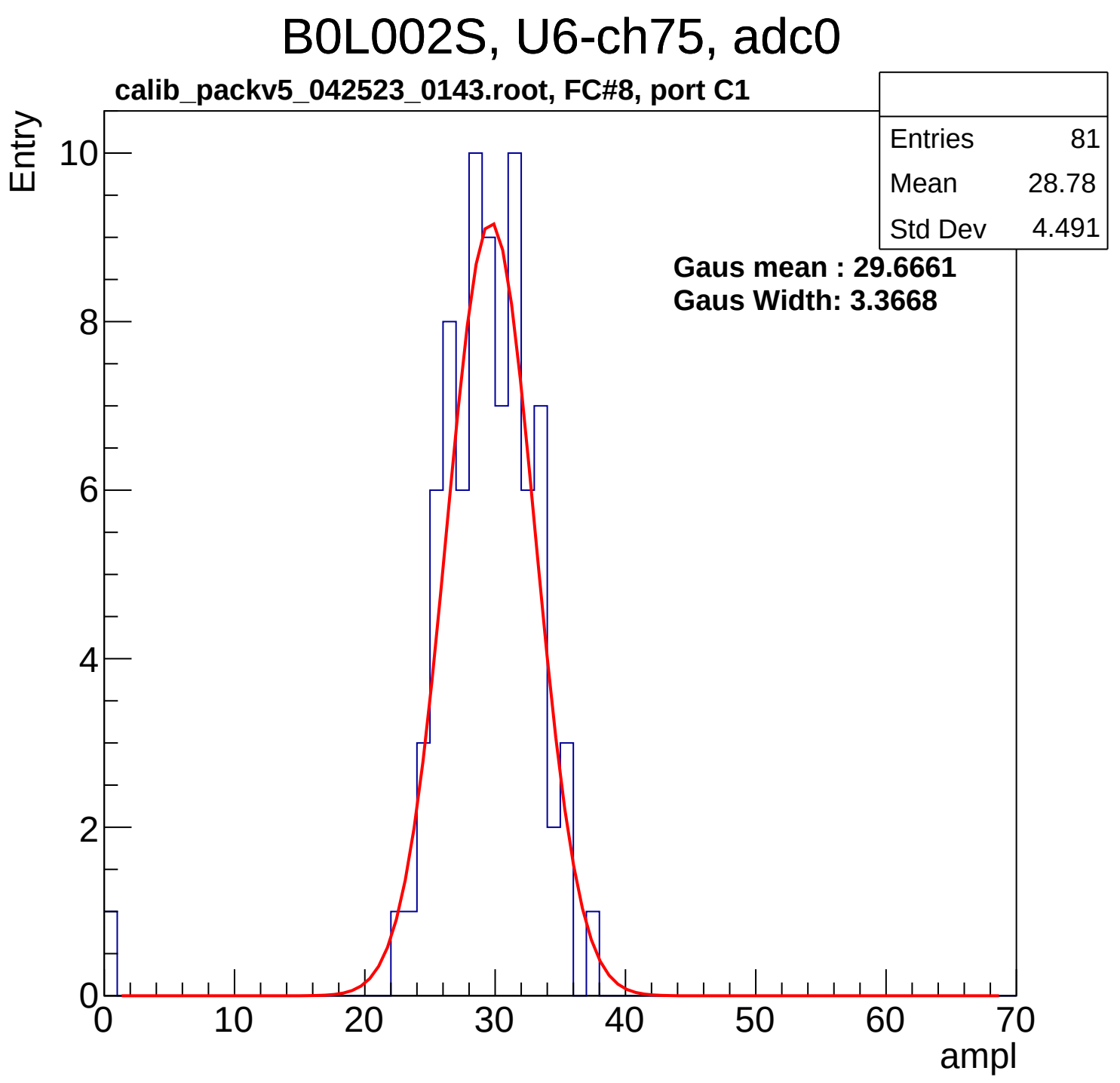

B0L002S, U6-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 49.98
Std Dev 3.492
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

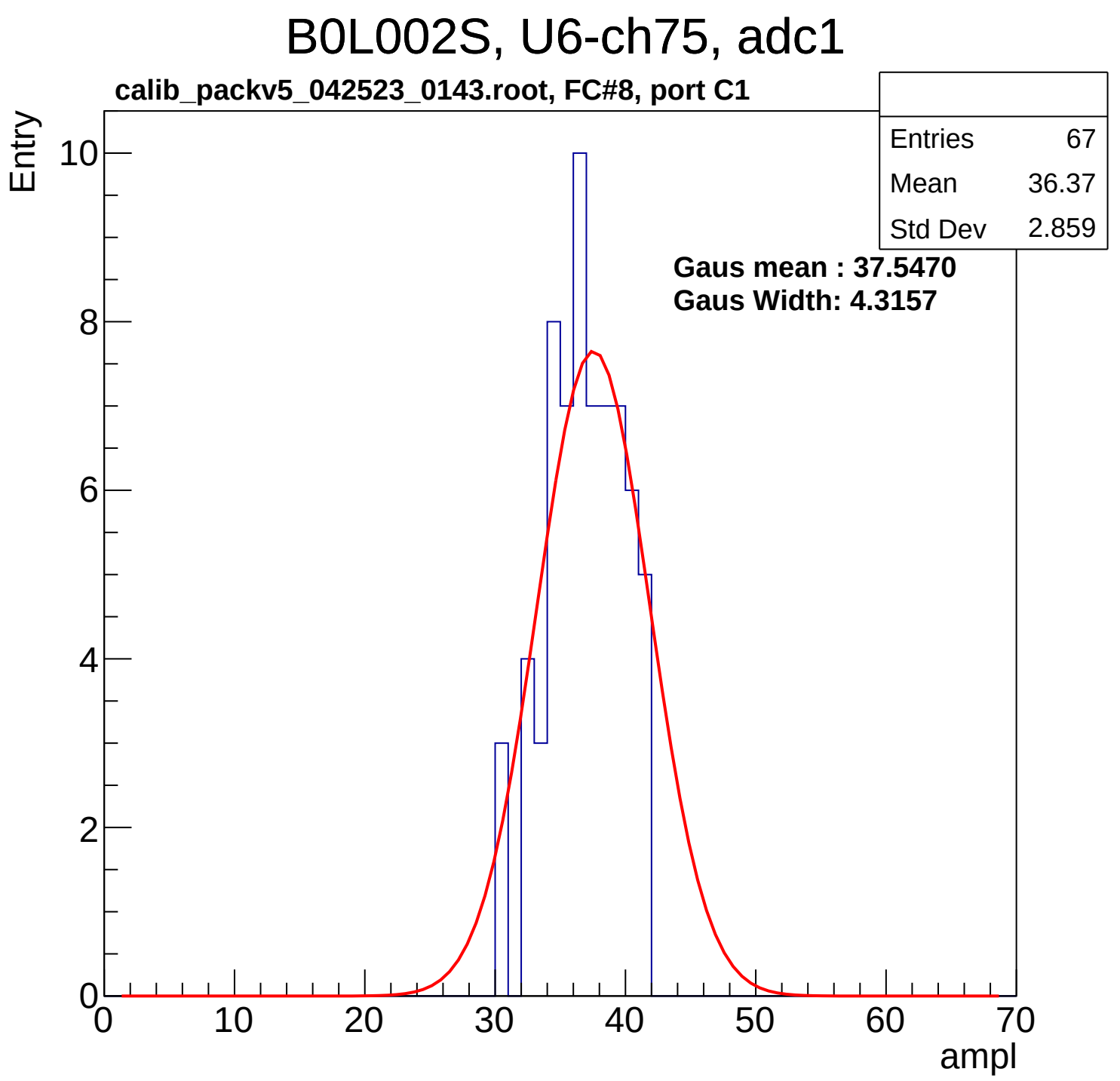

B0L002S, U6-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.07
Std Dev 3.062
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

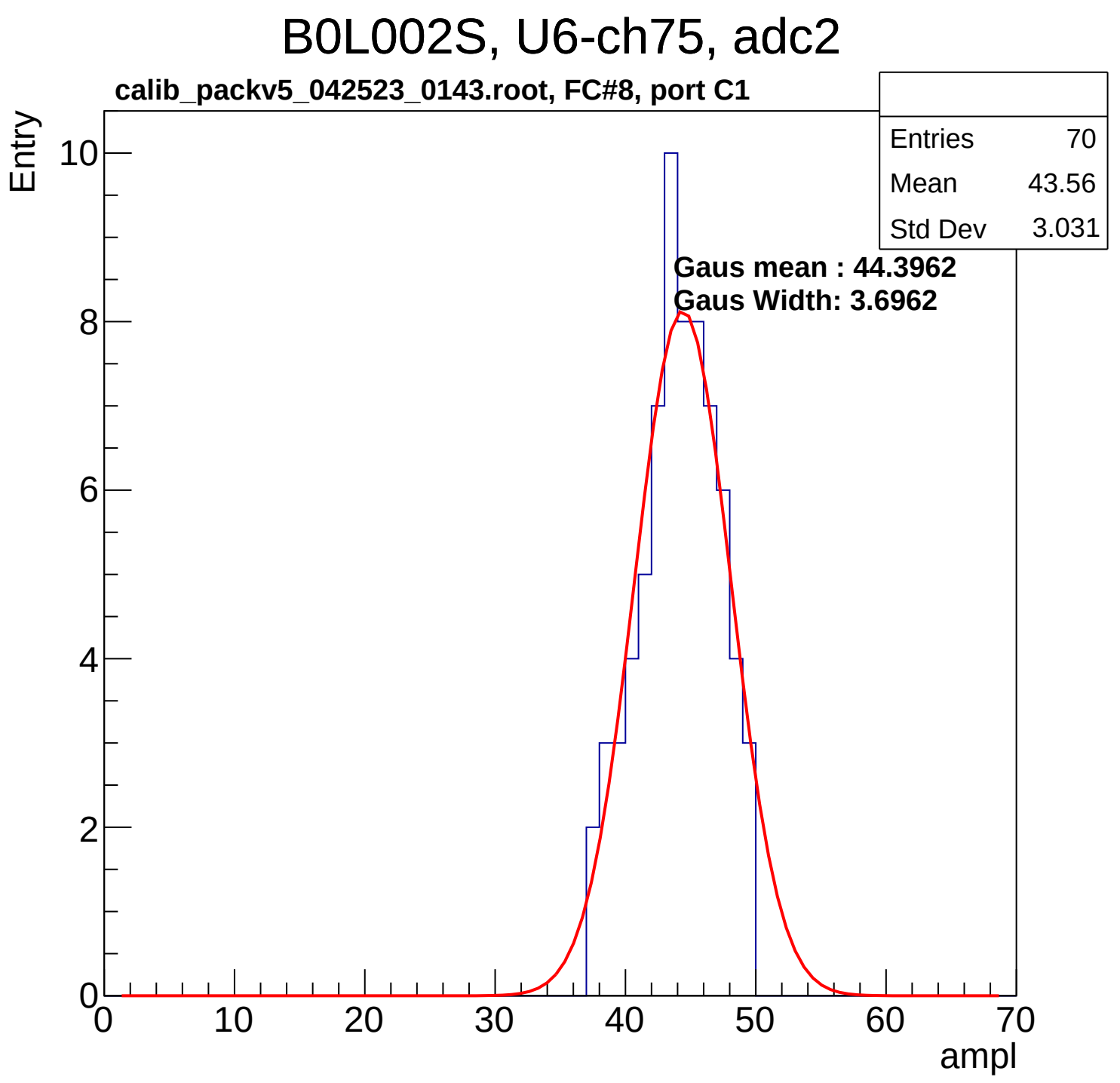

B0L002S, U6-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.5
Std Dev 10.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

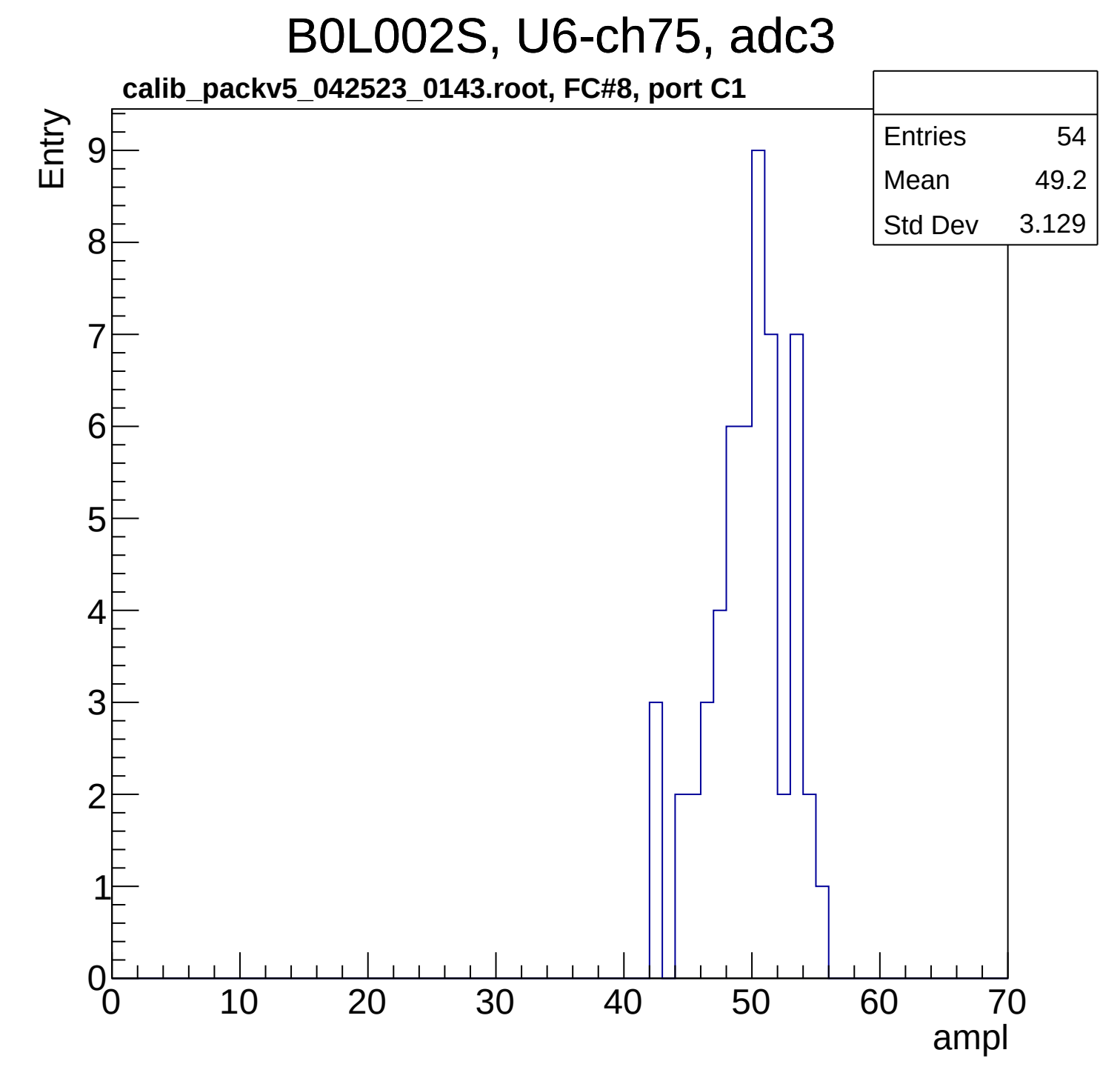

B0L002S, U6-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

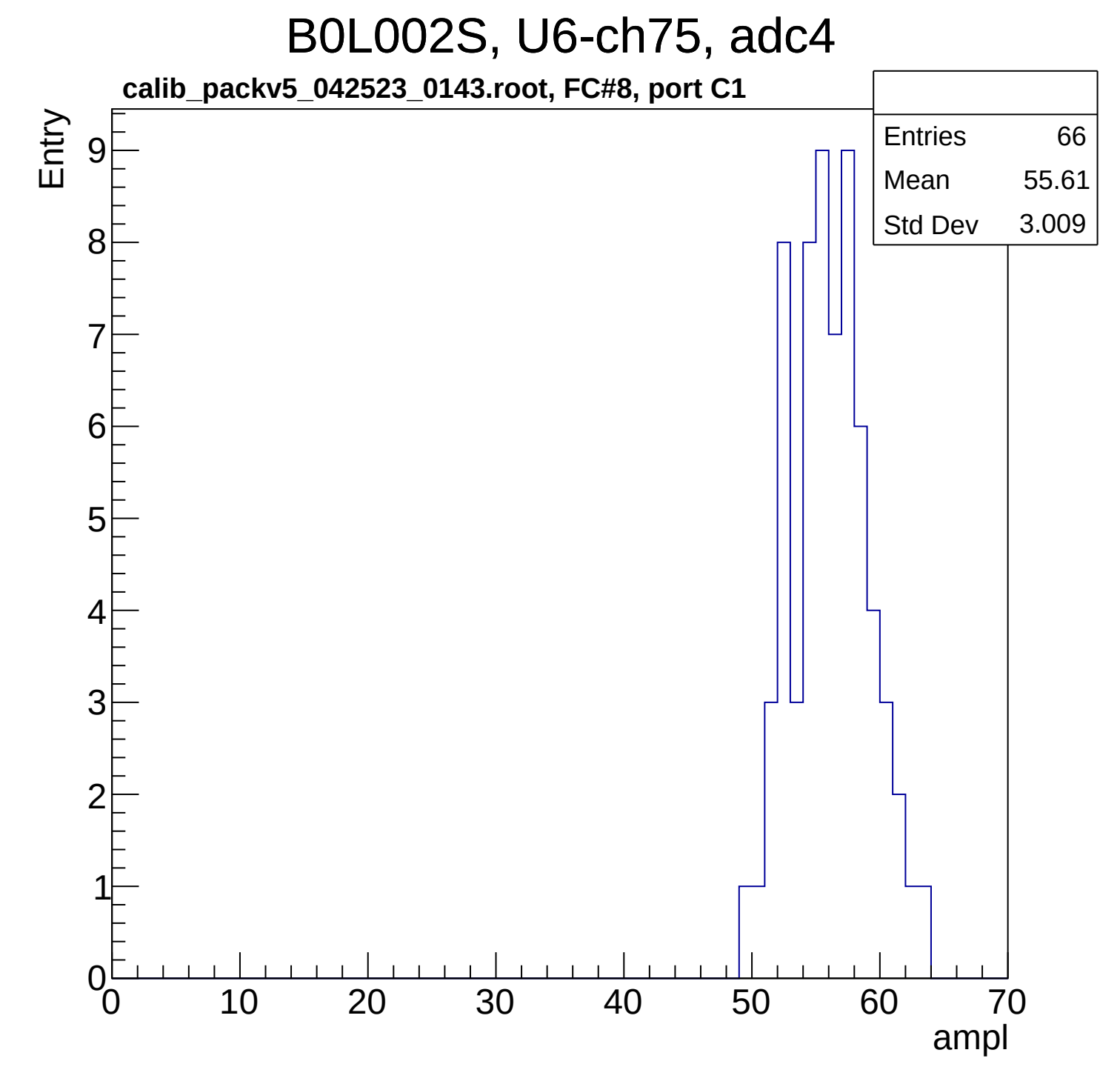

B0L002S, U6-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 28.78
Std Dev 4.491
Gaus mean : 29.6661
Gaus Width: 3.3668
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.37
Std Dev 2.859
Gaus mean : 37.5470
Gaus Width: 4.3157
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.56
Std Dev 3.031
Gaus mean : 44.3962
Gaus Width: 3.6962
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 49.2
Std Dev 3.129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 55.61
Std Dev 3.009
Entry
ampl
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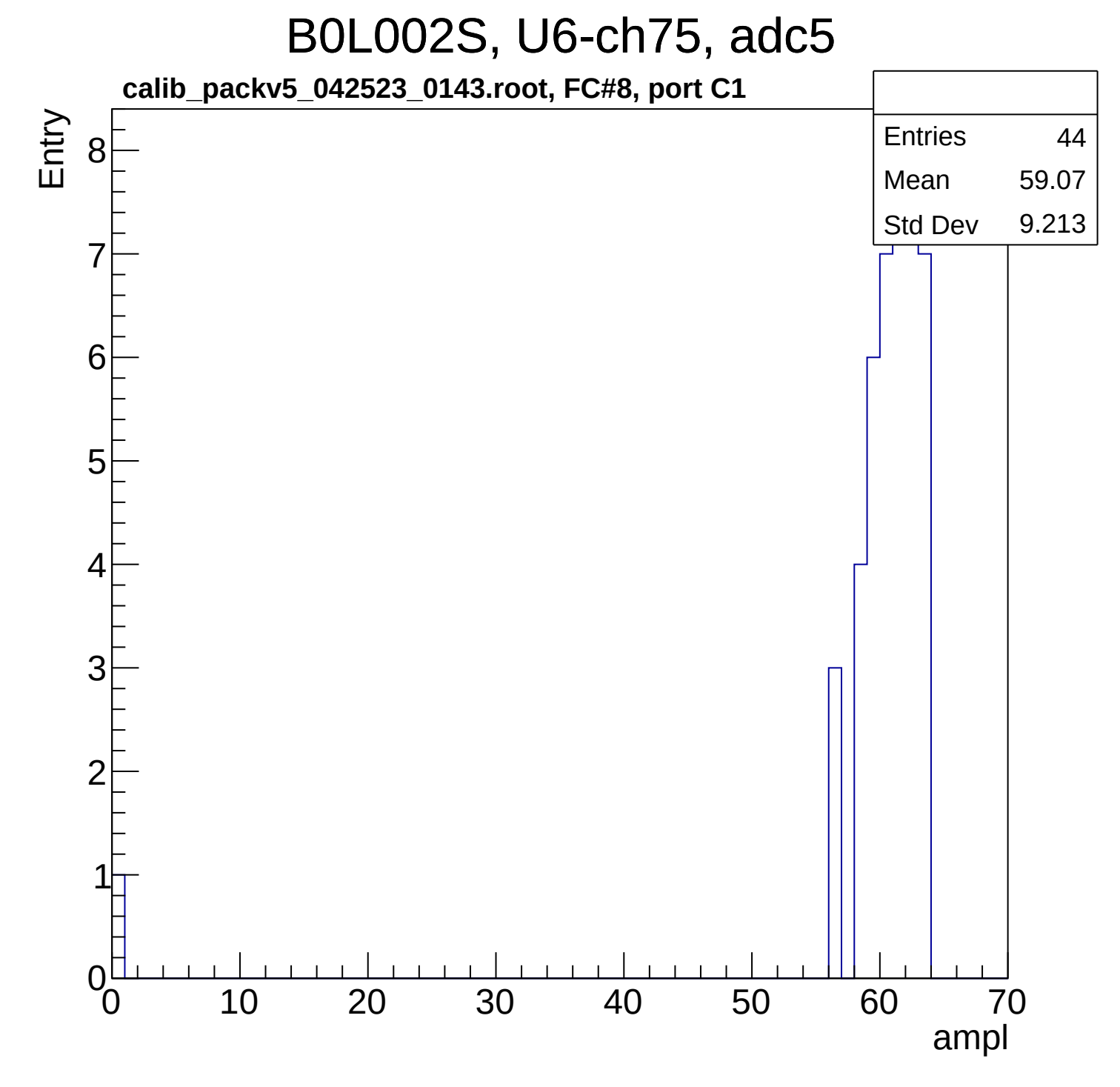
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.07
Std Dev 9.213
Entry
ampl
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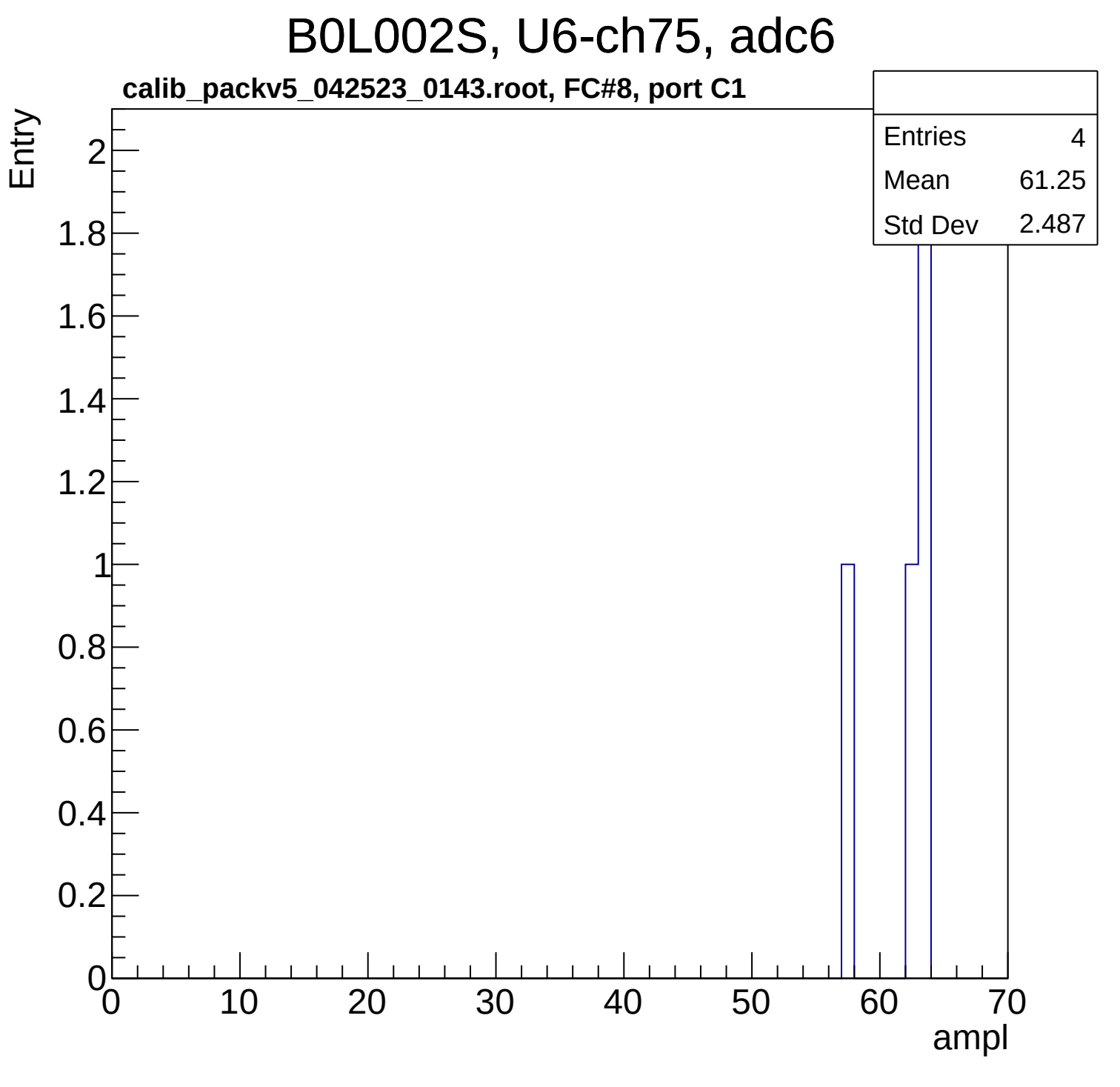
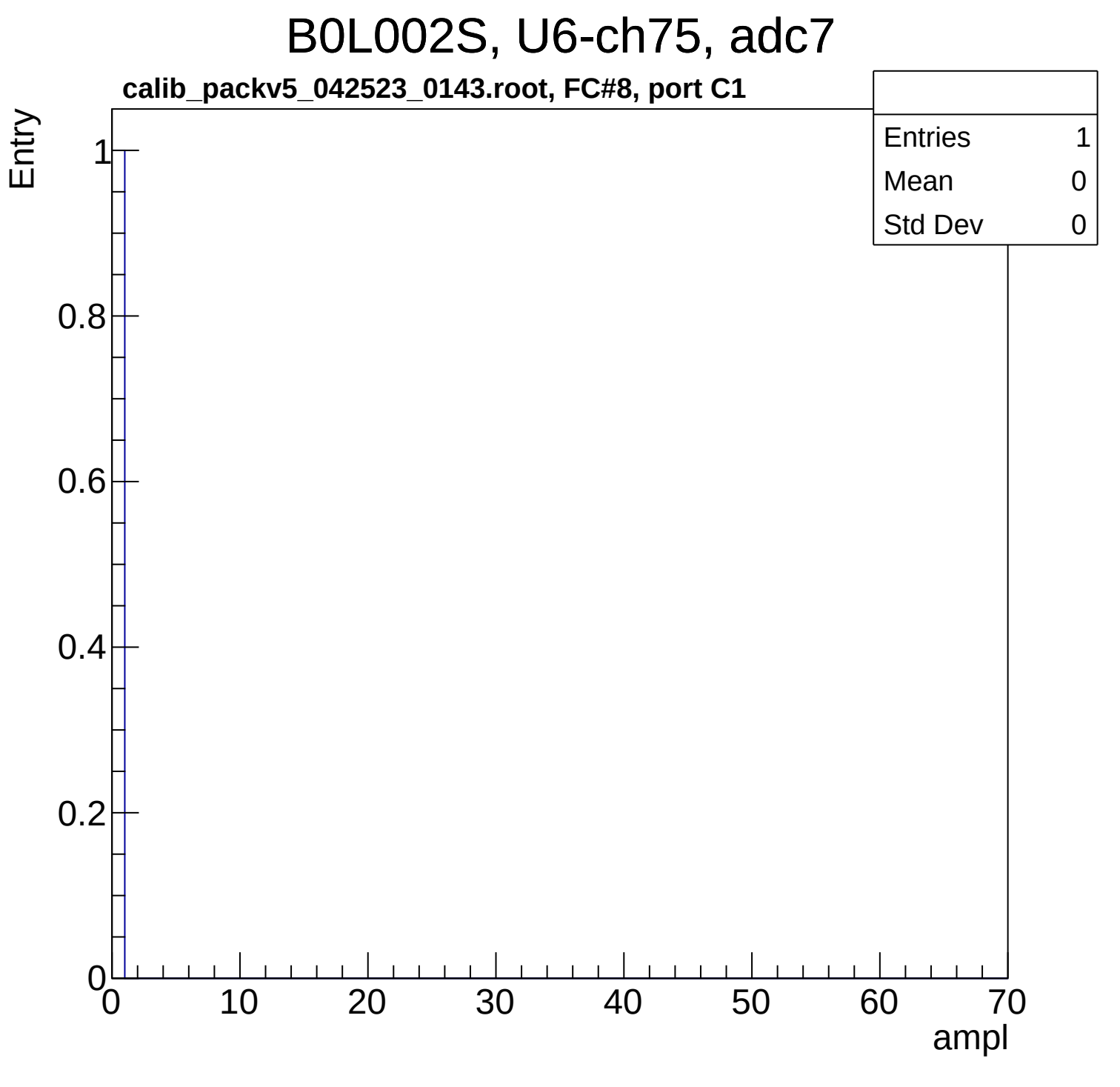
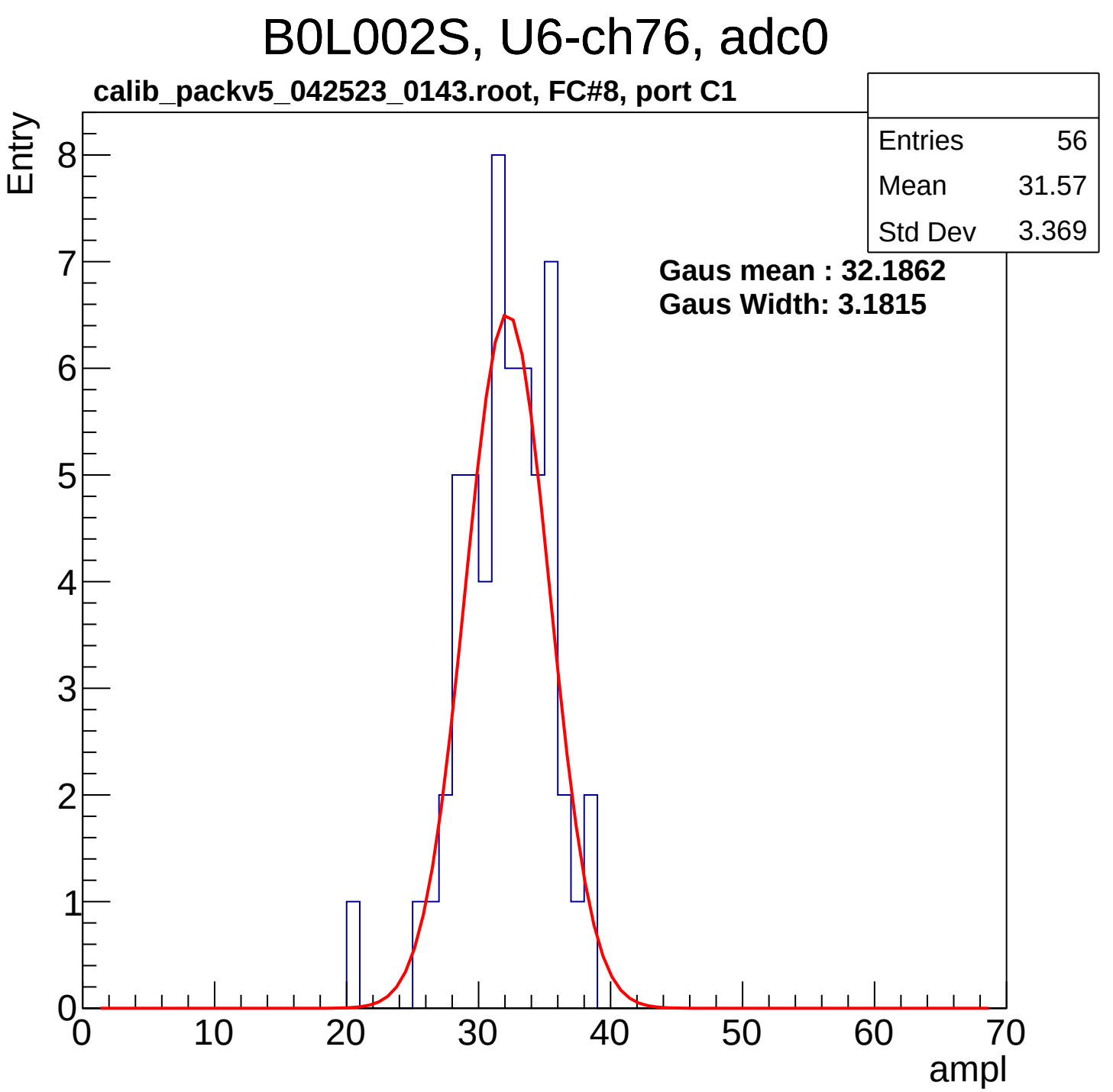
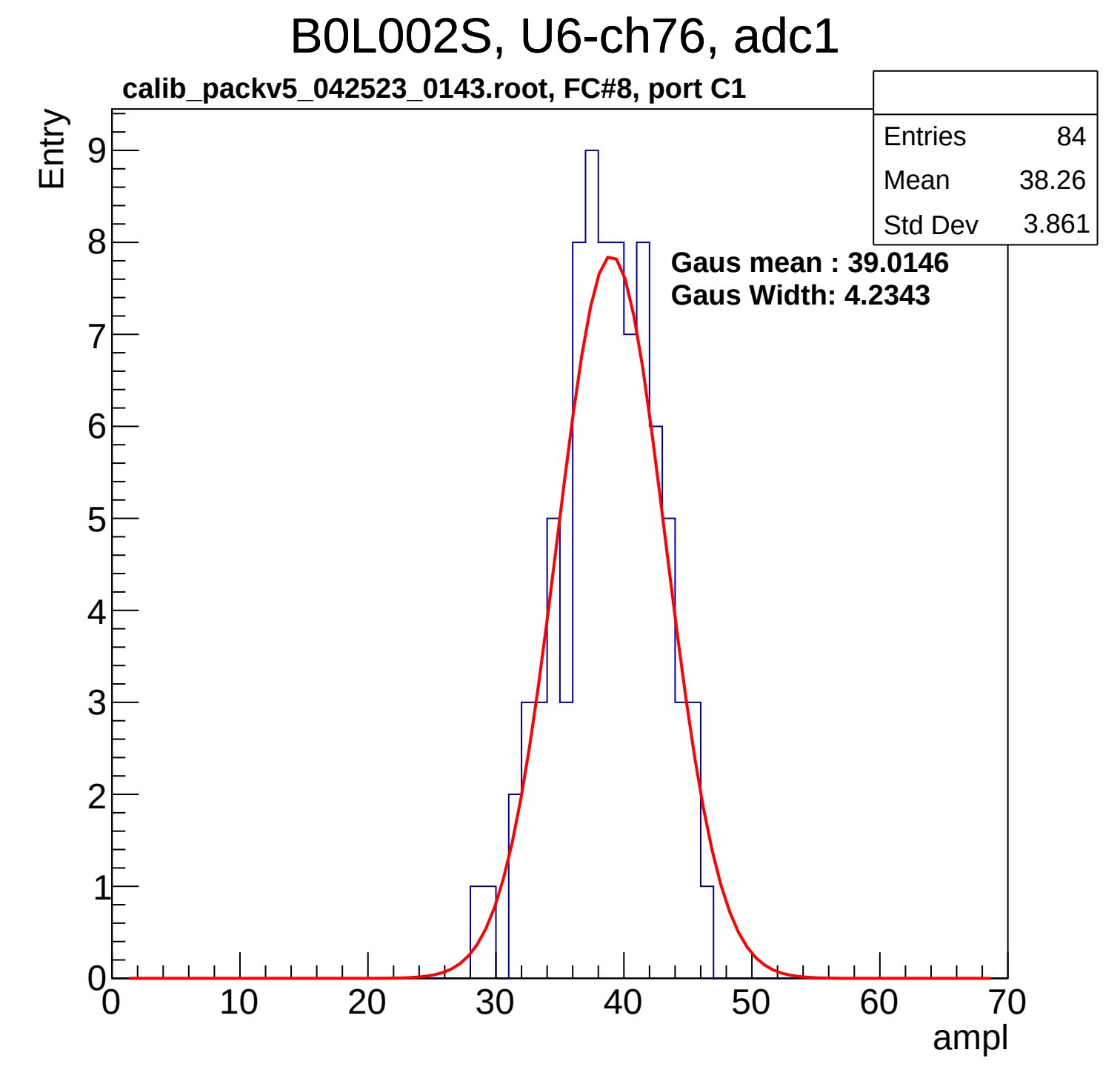
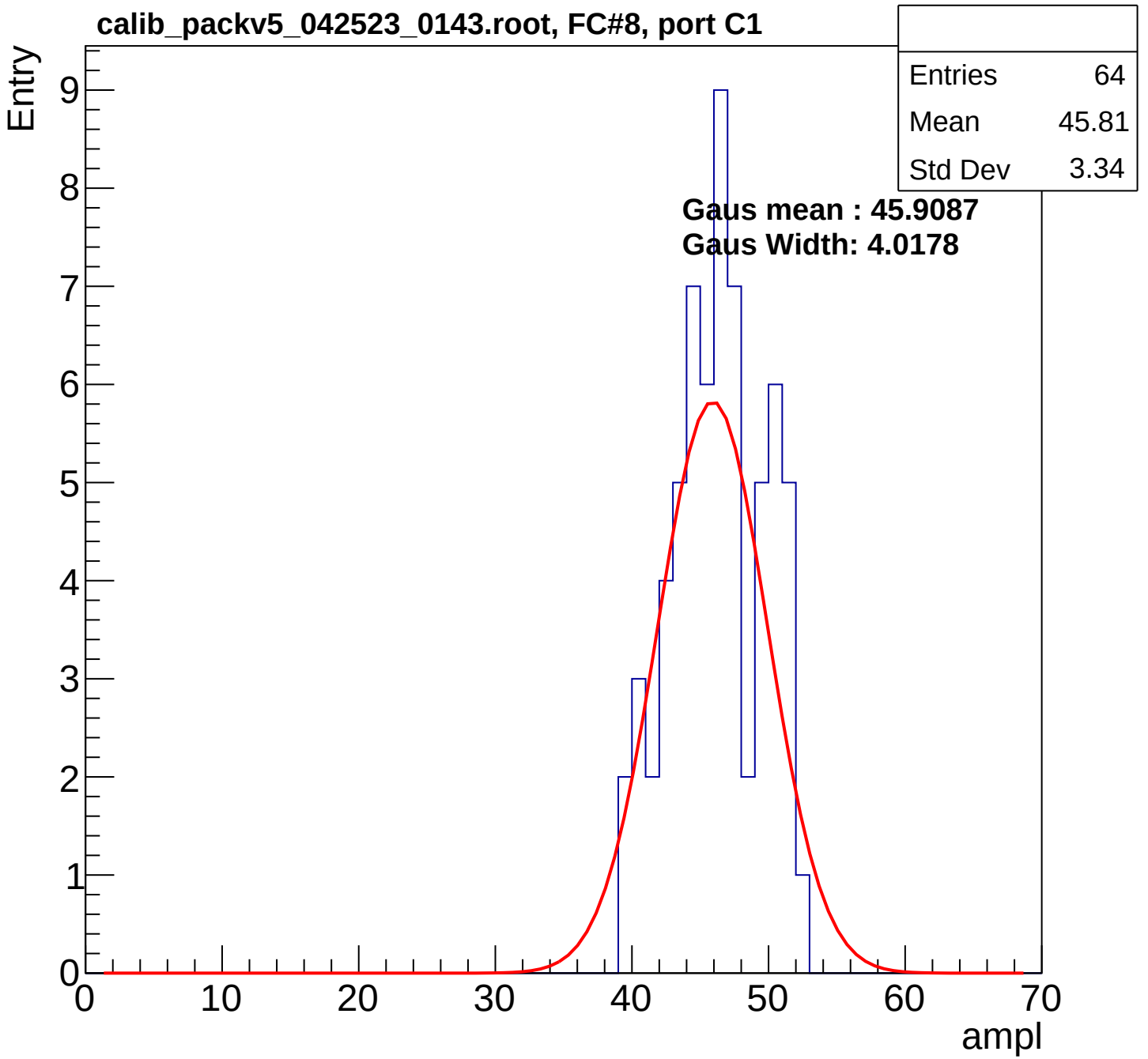
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

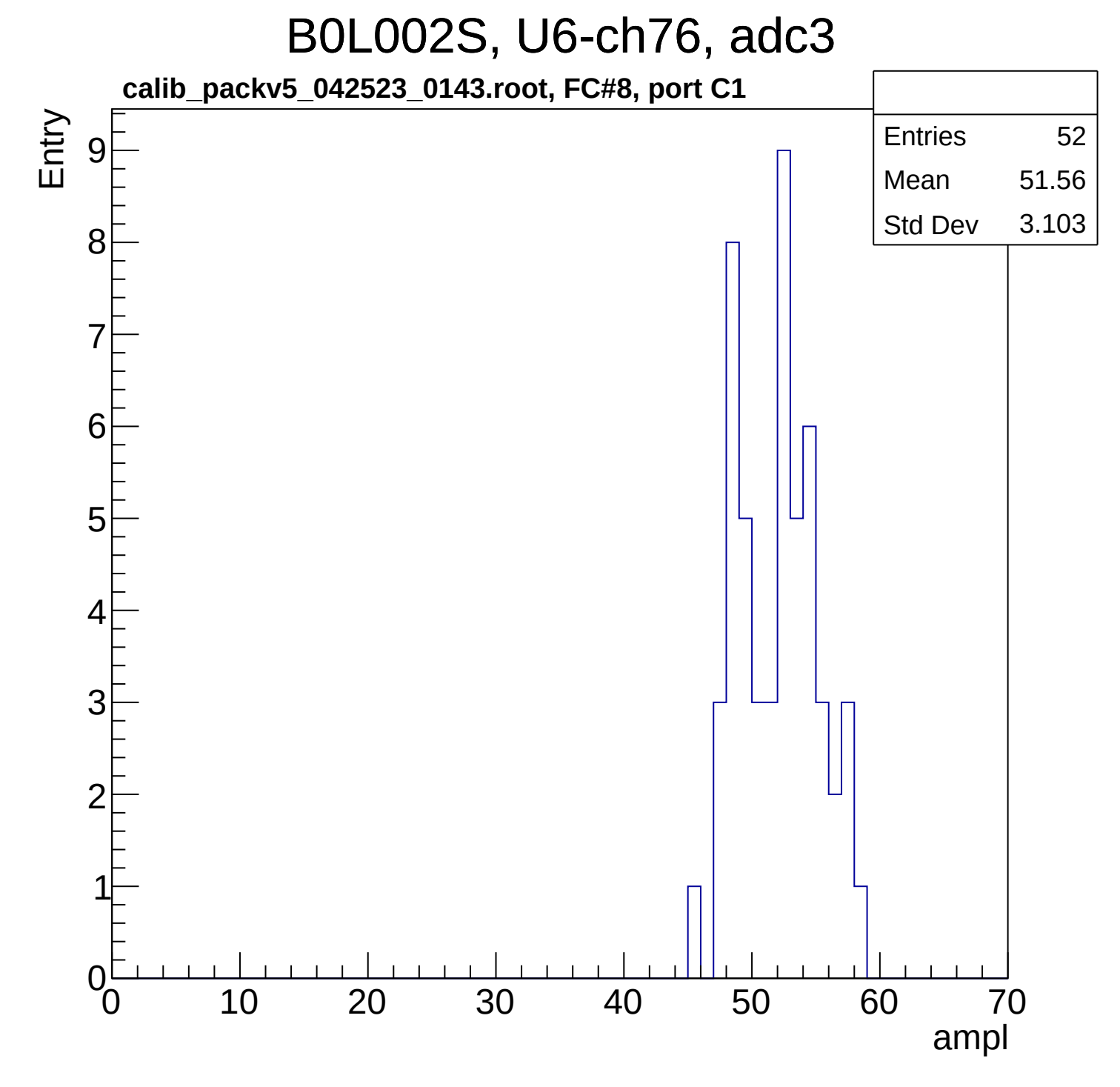

B0L002S, U6-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.25
Std Dev 2.487
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.57
Std Dev 3.369
Gaus mean : 32.1862
Gaus Width: 3.1815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 38.26
Std Dev 3.861
Gaus mean : 39.0146
Gaus Width: 4.2343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 45.81
Std Dev 3.34
Gaus mean : 45.9087
Gaus Width: 4.0178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 51.56
Std Dev 3.103
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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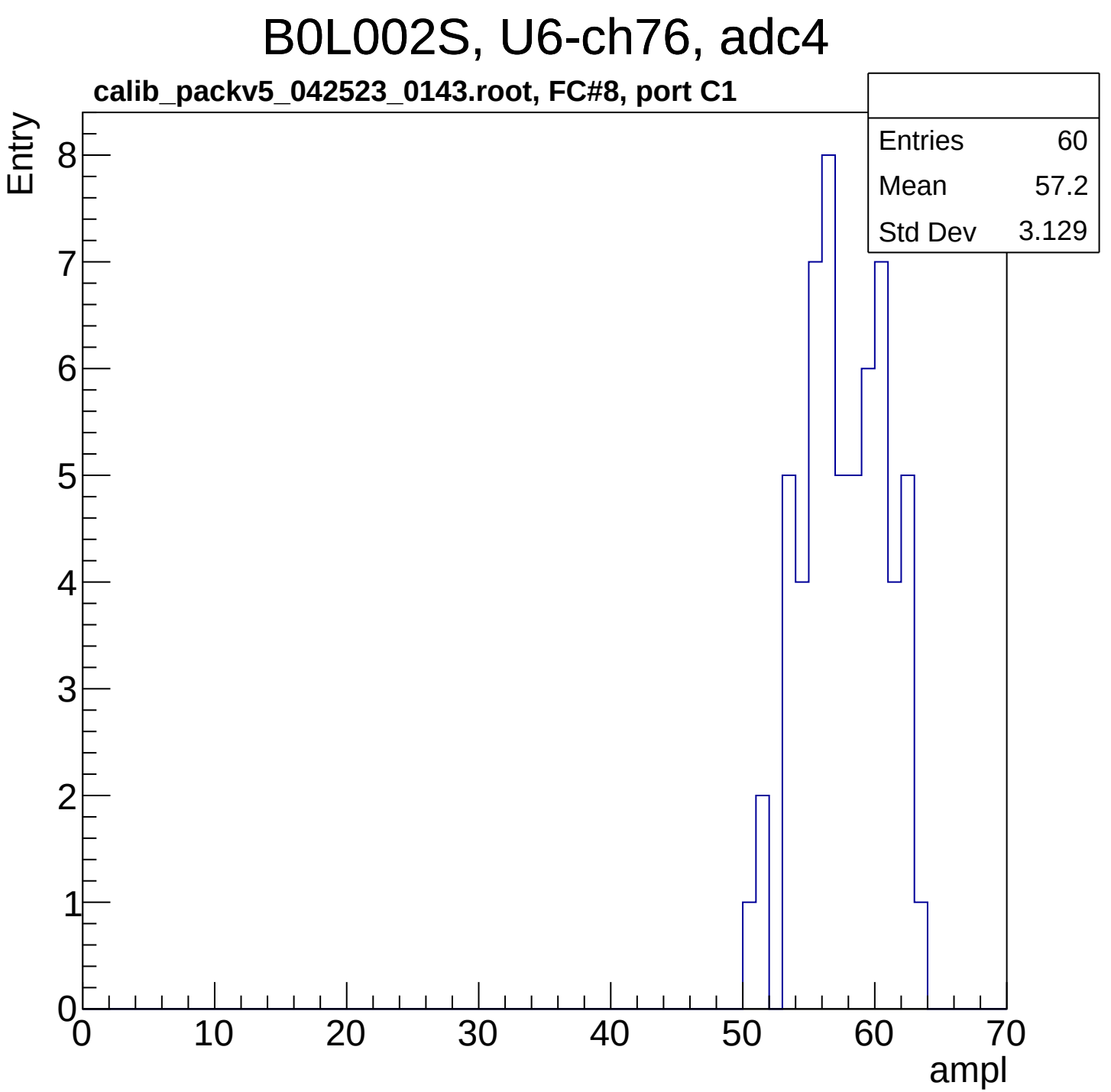
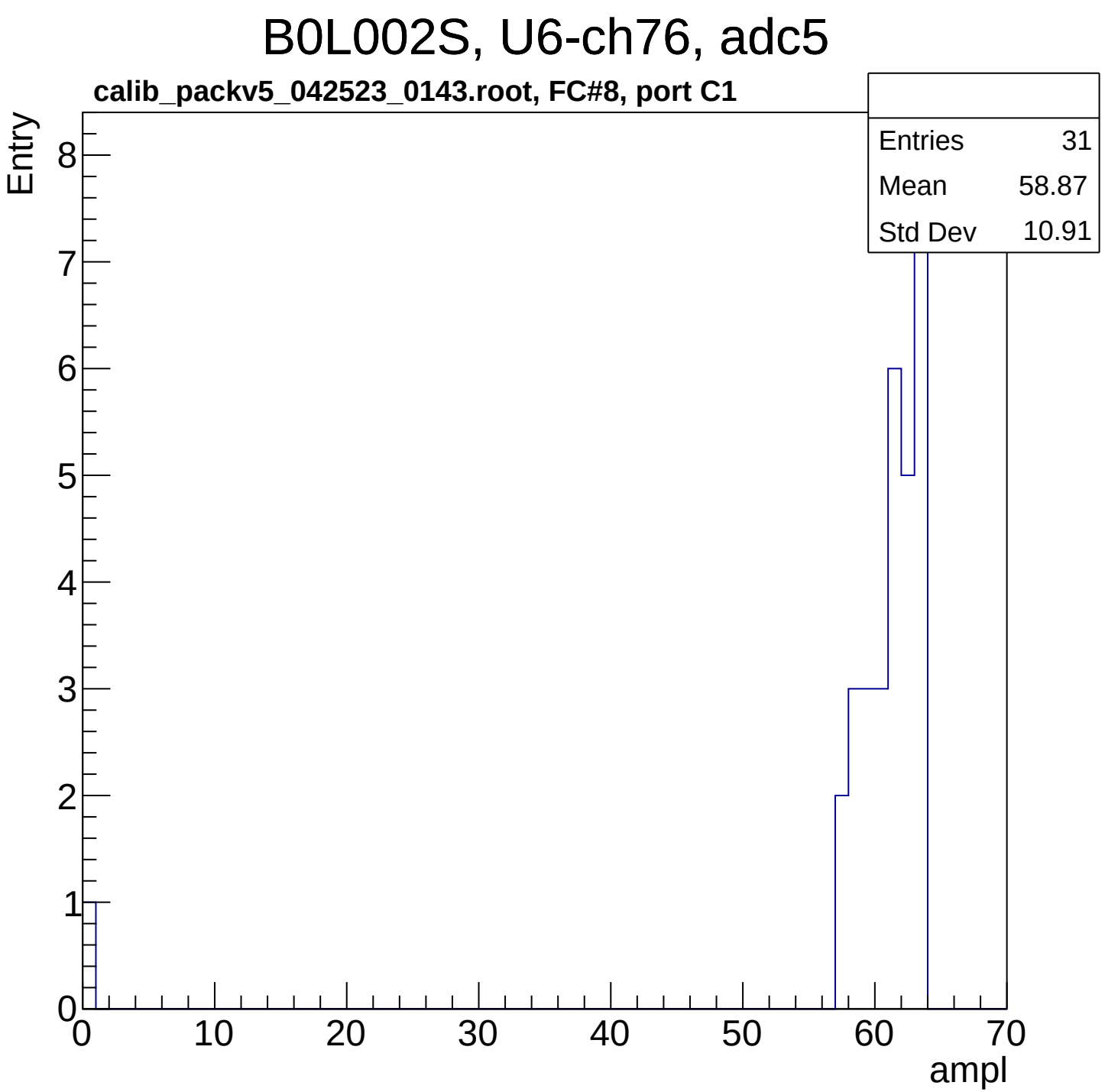
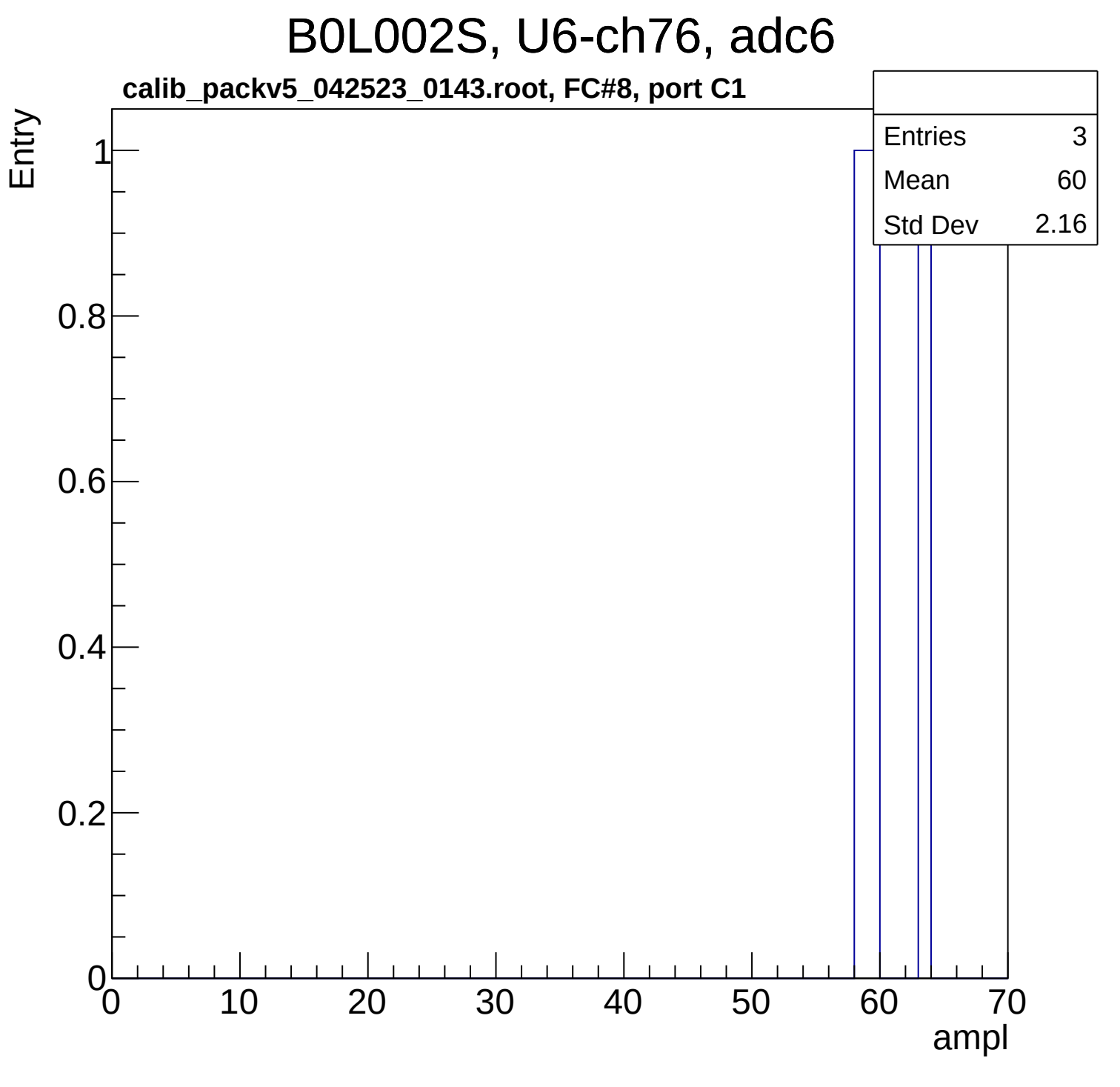
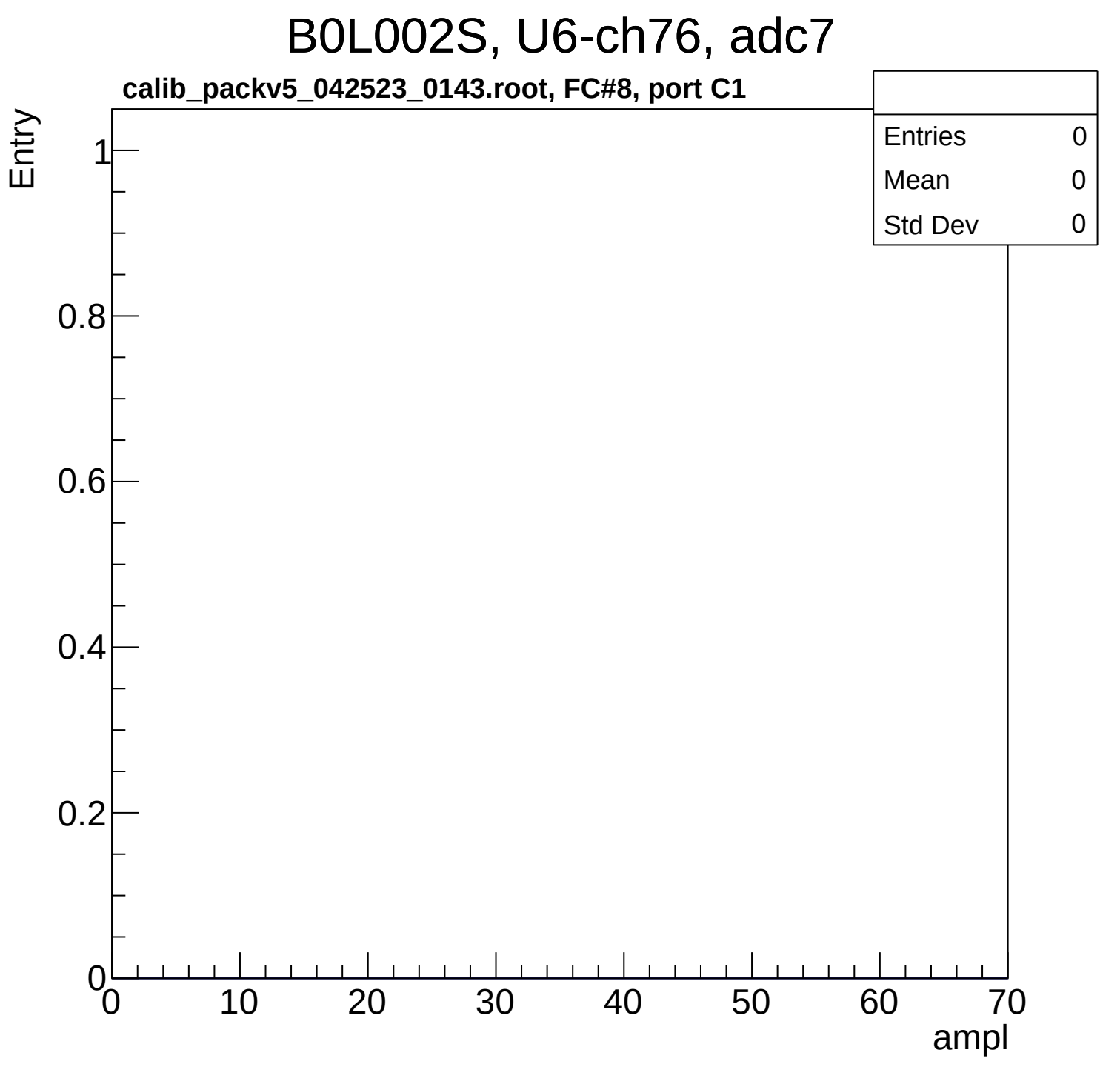
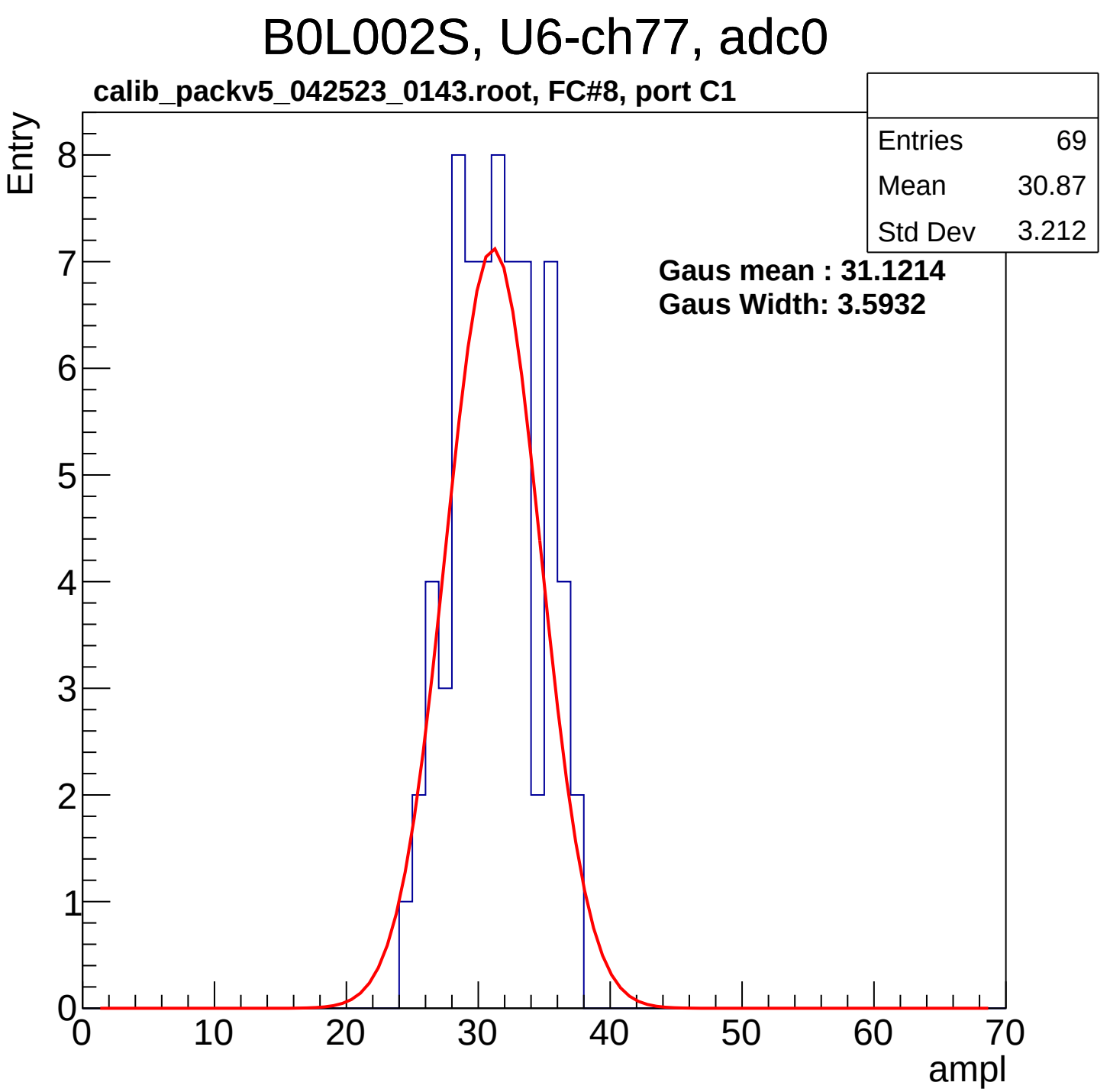
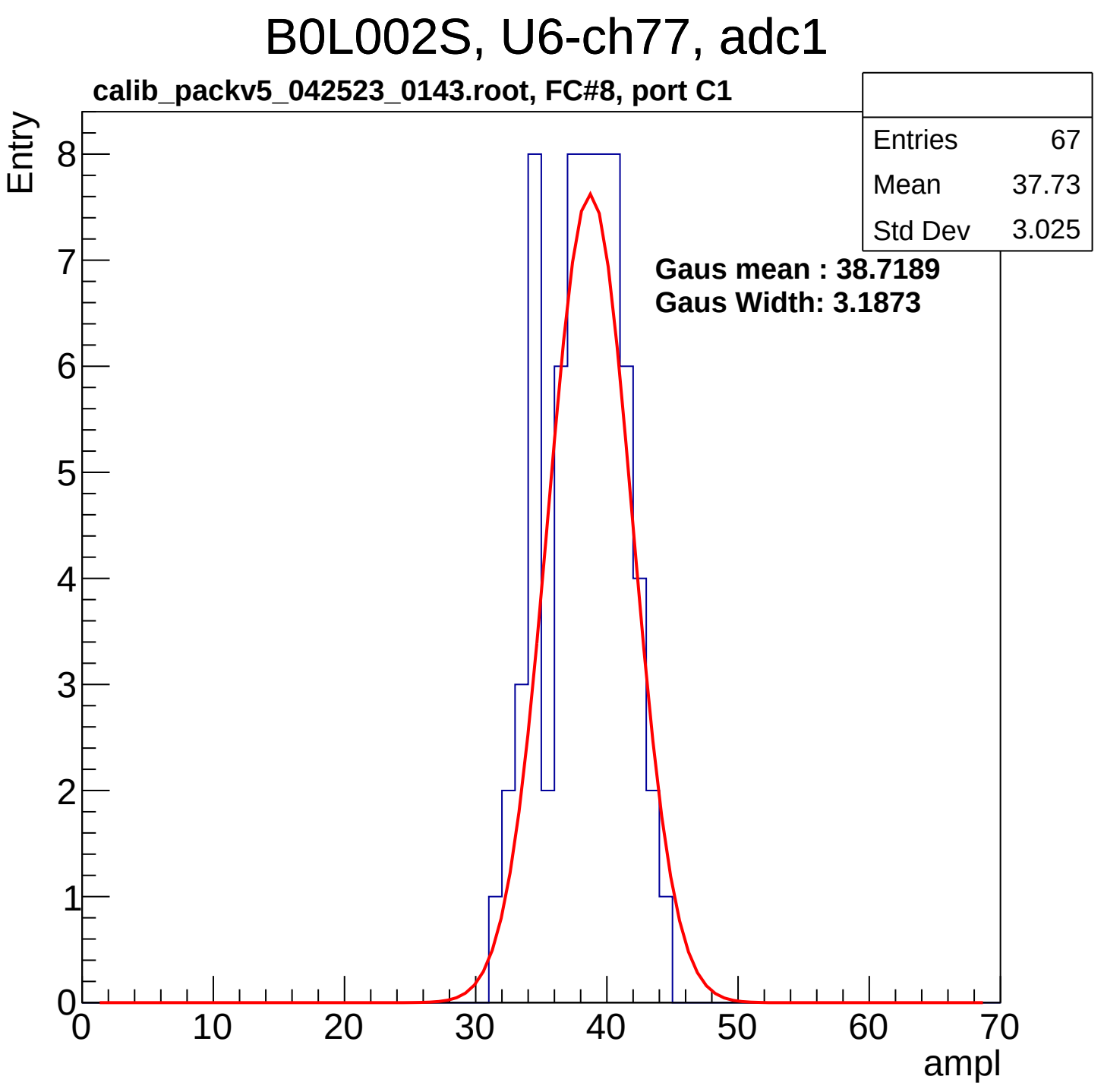

B0L002S, U6-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.2
Std Dev 3.129
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

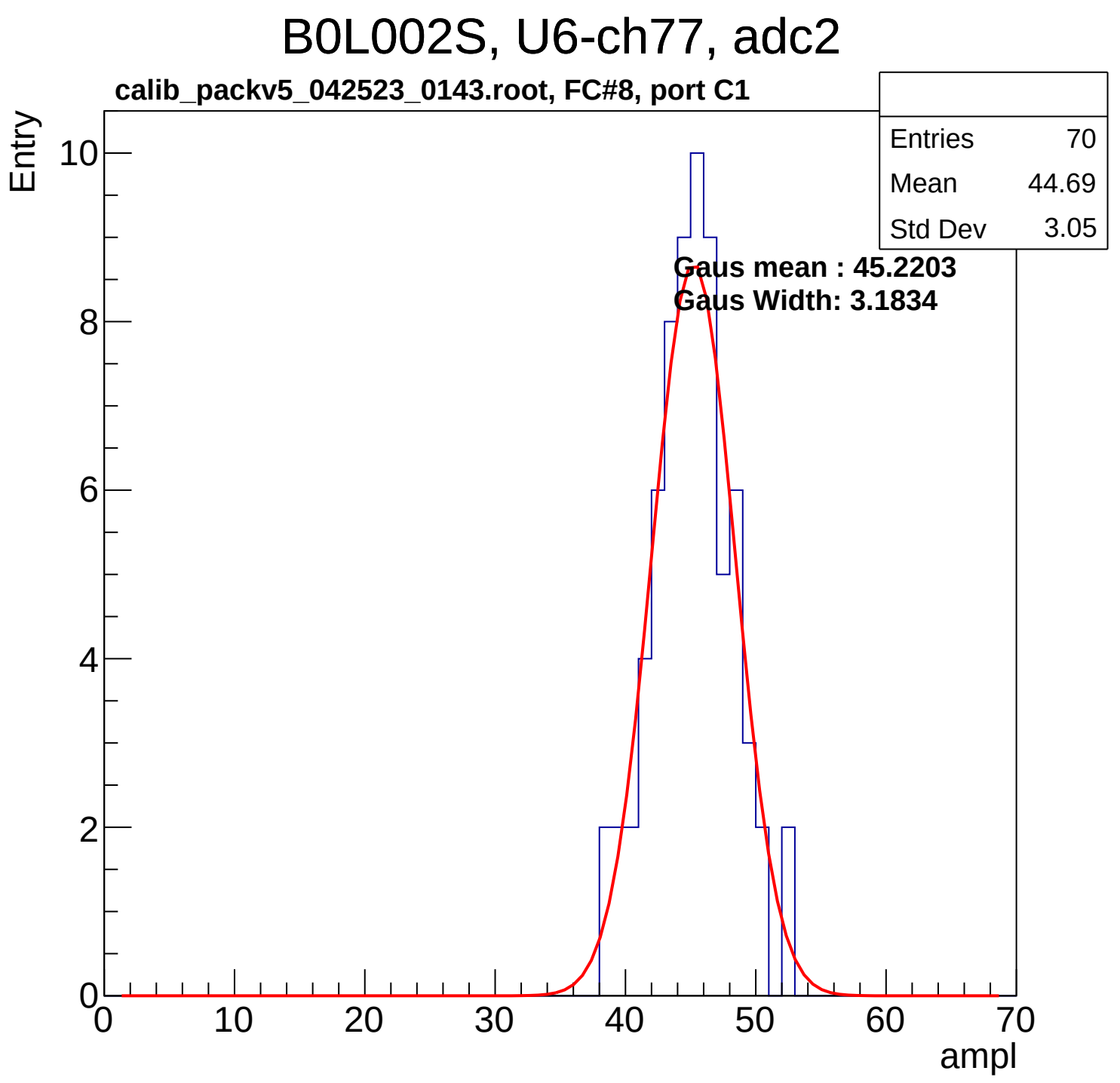

B0L002S, U6-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 58.87
Std Dev 10.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 60
Std Dev 2.16
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.87
Std Dev 3.212
Gaus mean : 31.1214
Gaus Width: 3.5932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.73
Std Dev 3.025
Gaus mean : 38.7189
Gaus Width: 3.1873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.69
Std Dev 3.05
Gaus mean : 45.2203
Gaus Width: 3.1834
Entry
ampl
0
2
4
6
8
10
0
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60
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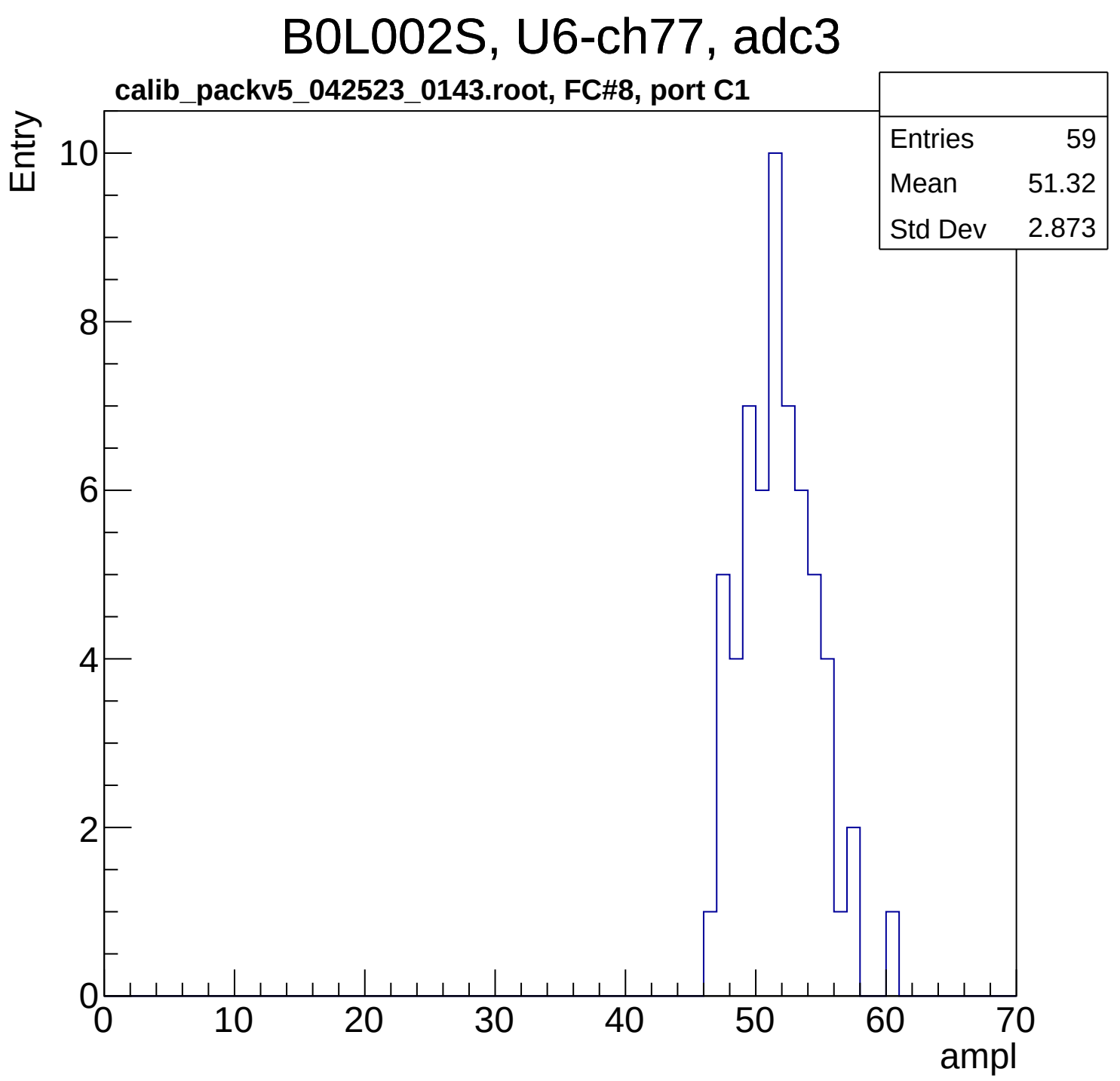
B0L002S, U6-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.32
Std Dev 2.873
Entry
ampl
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2
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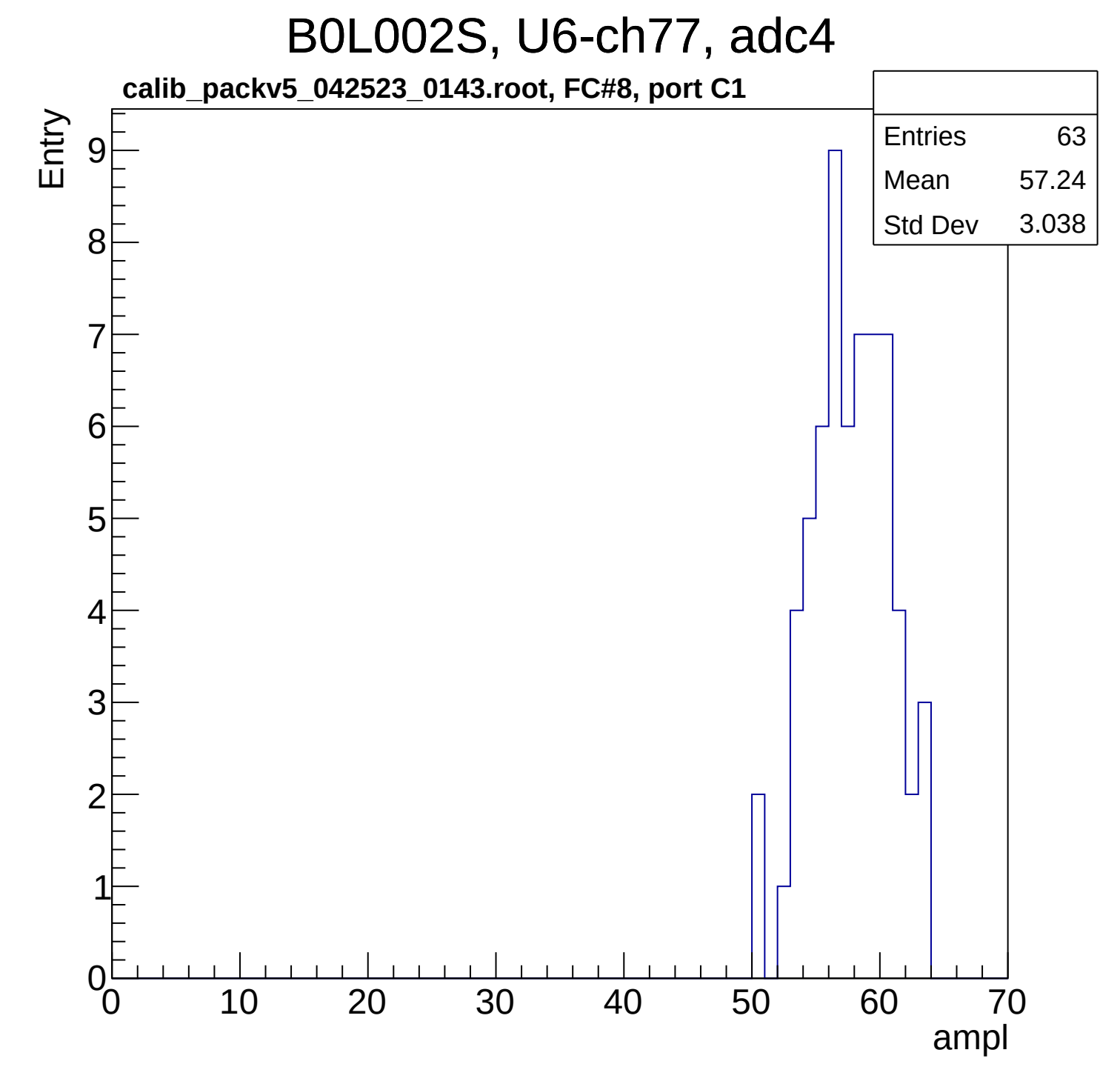
B0L002S, U6-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.24
Std Dev 3.038
Entry
ampl
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2
3
4
5
6
7
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0
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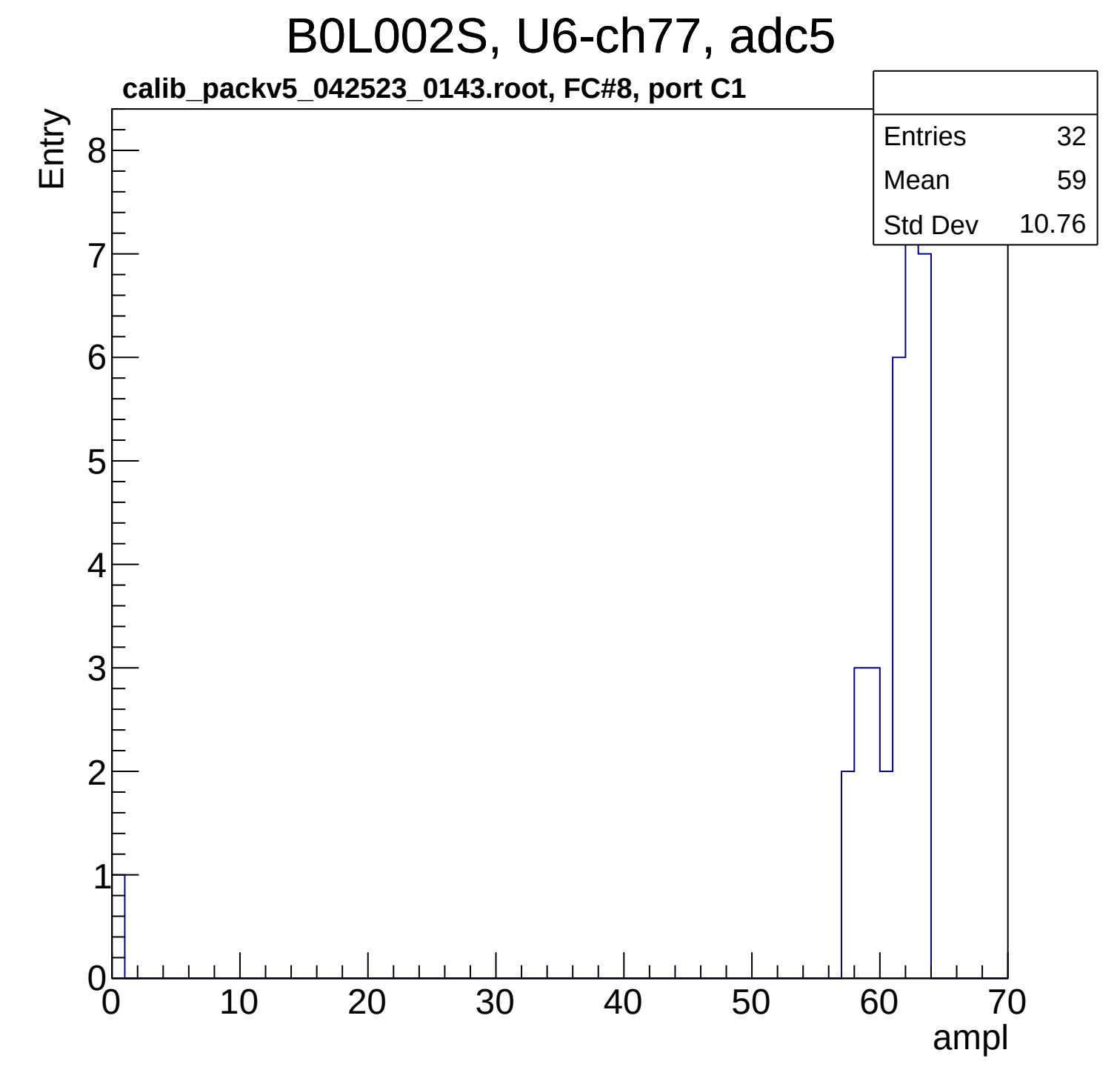
B0L002S, U6-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
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0
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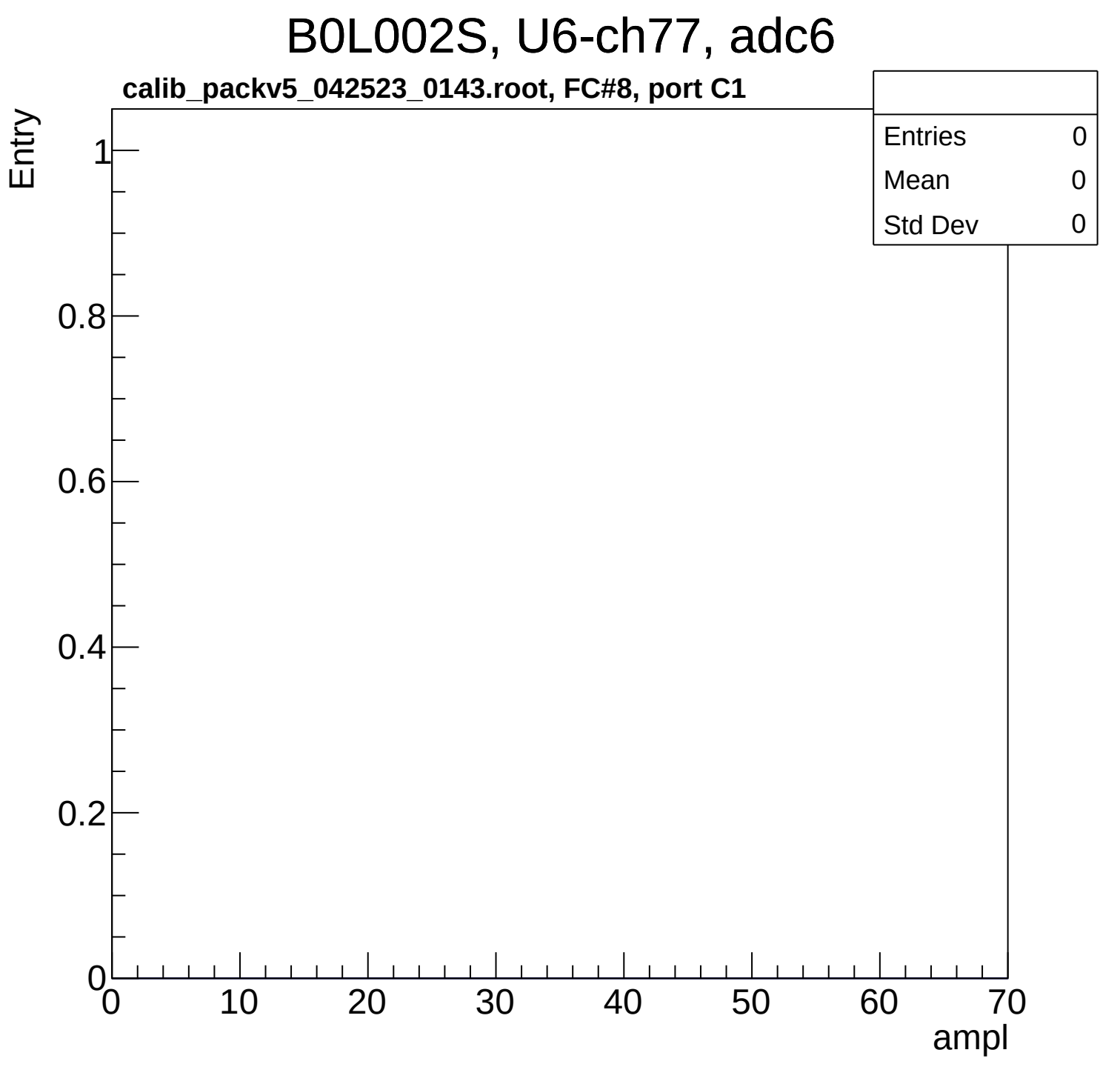
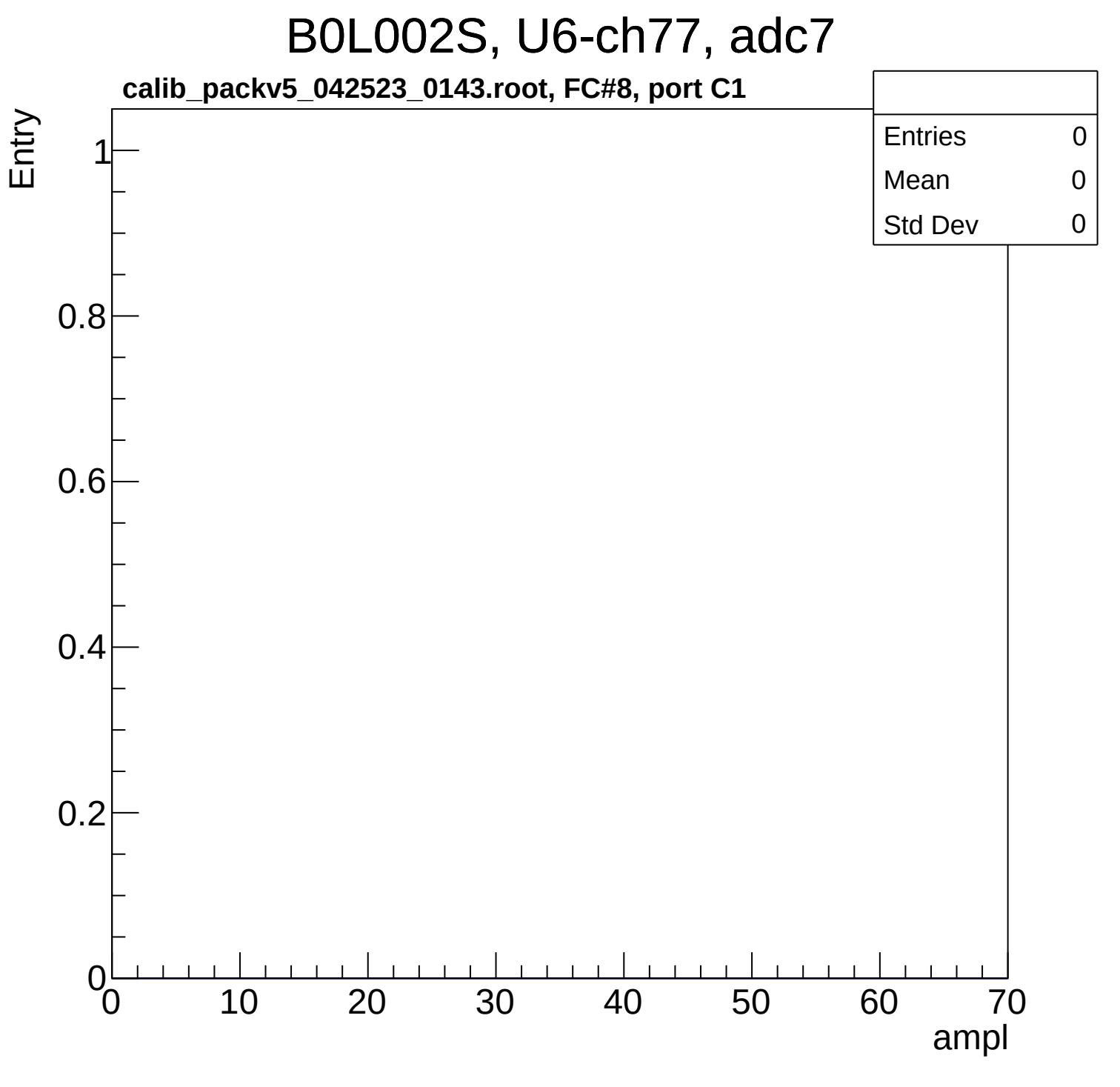
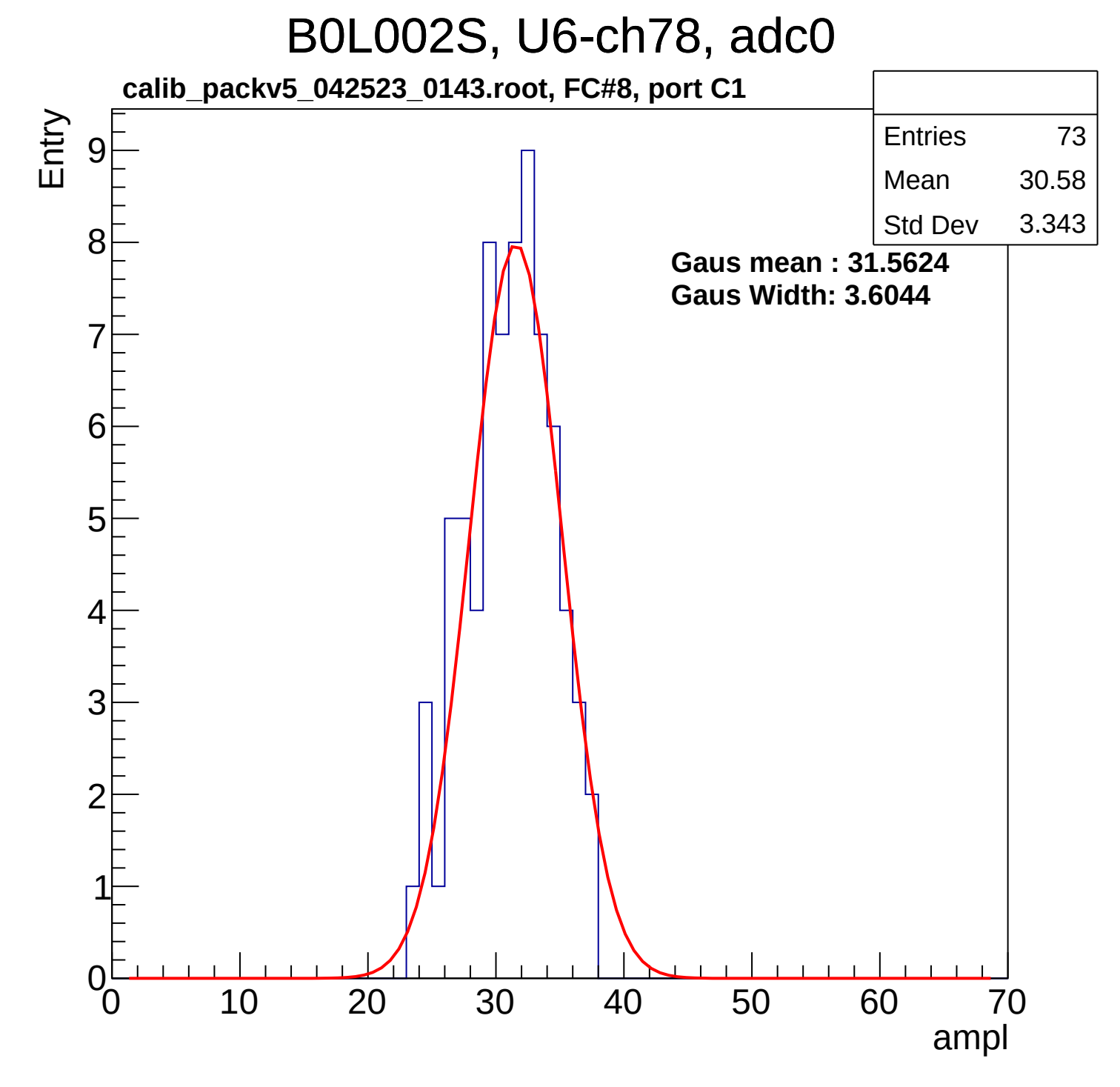

B0L002S, U6-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.58
Std Dev 3.343
Gaus mean : 31.5624
Gaus Width: 3.6044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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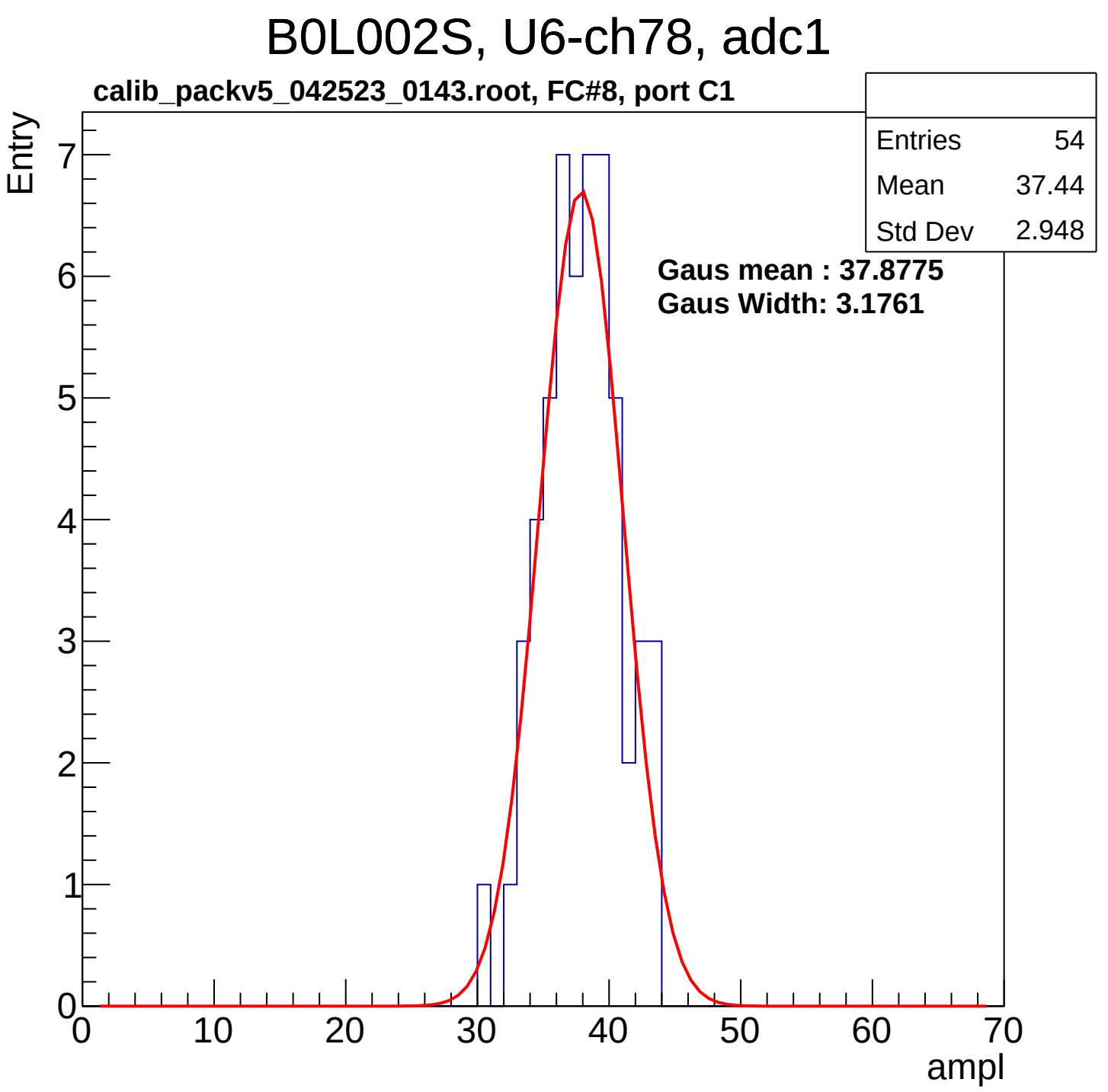
B0L002S, U6-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 37.44
Std Dev 2.948
Gaus mean : 37.8775
Gaus Width: 3.1761
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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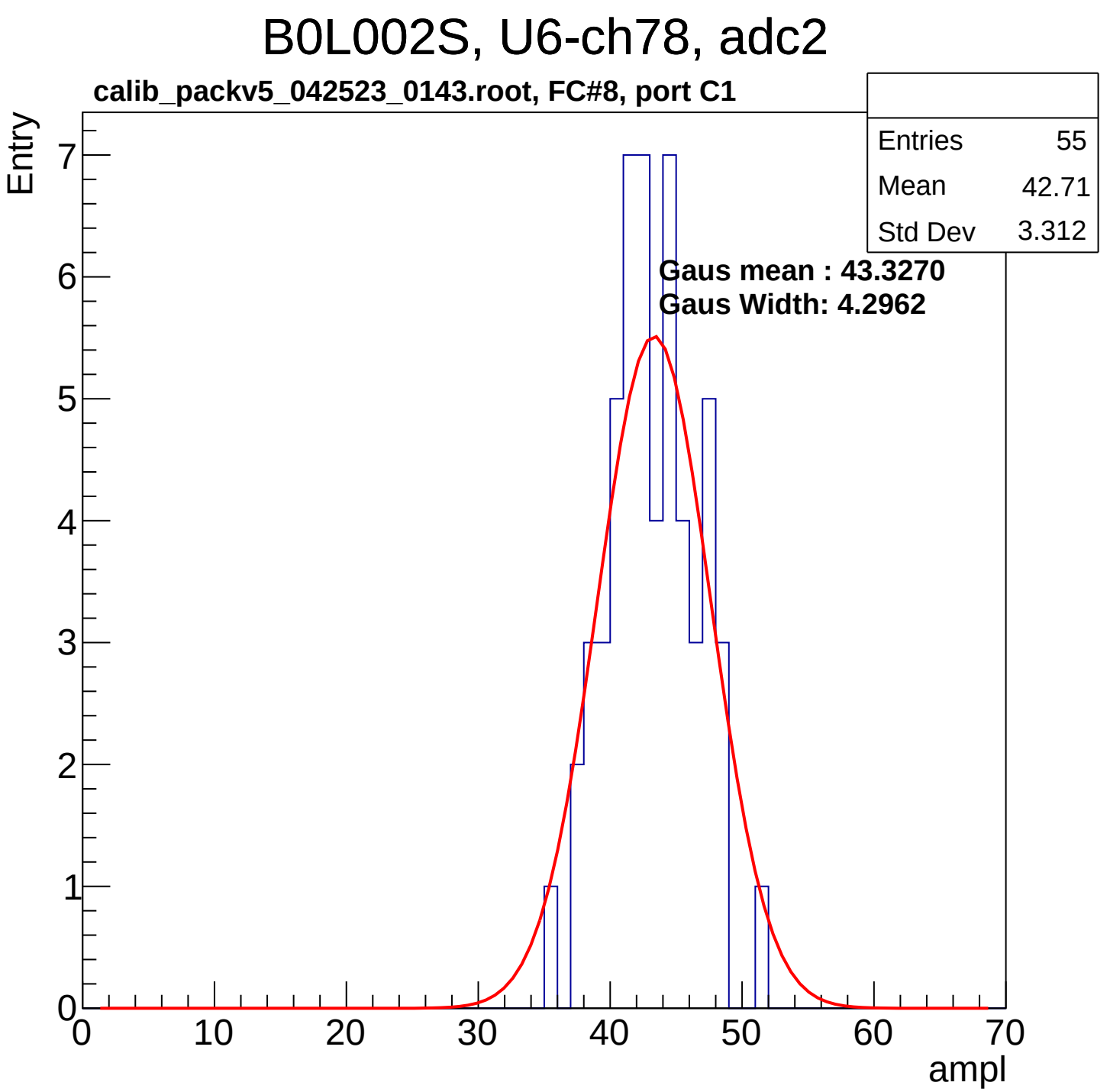
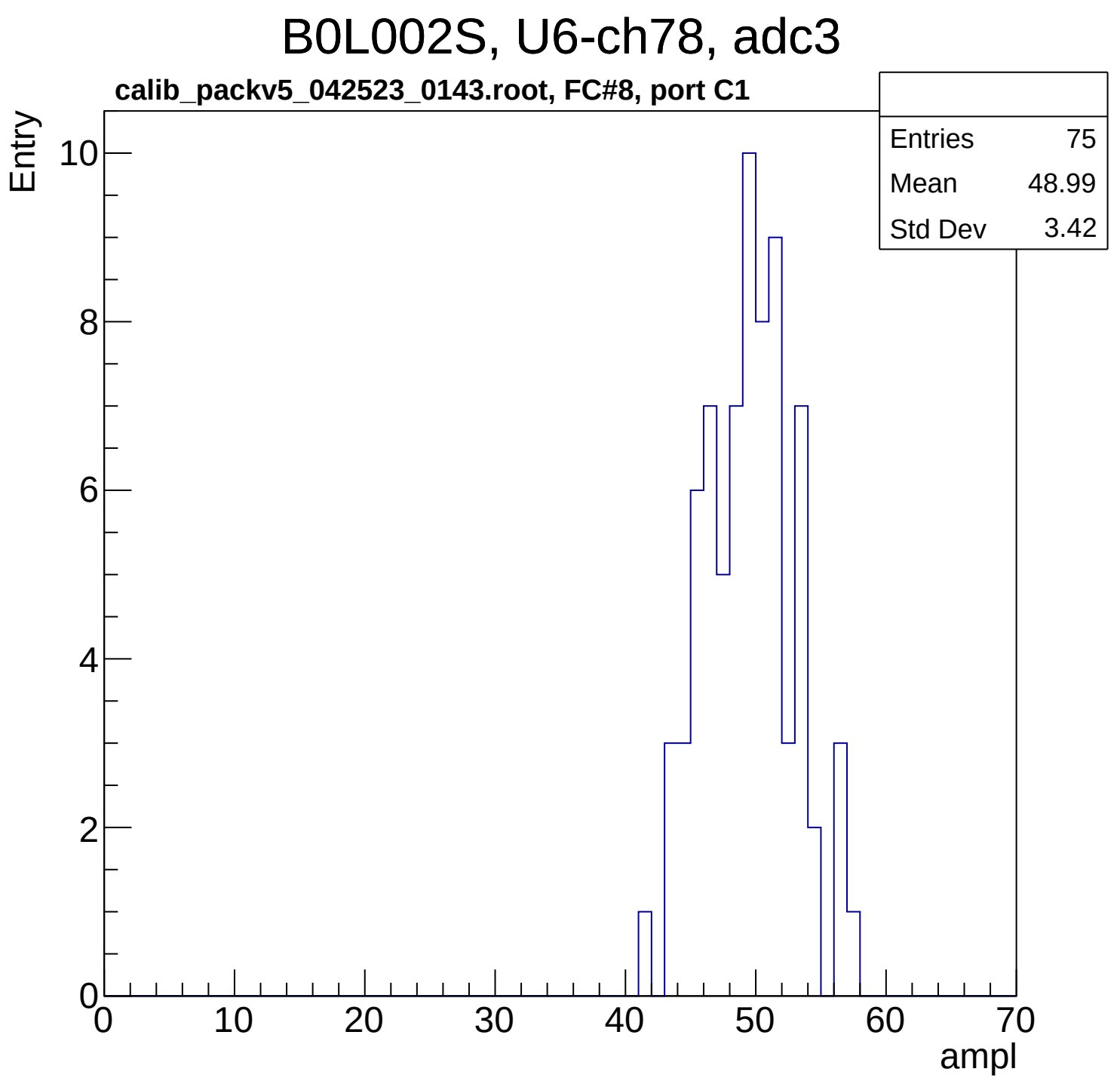
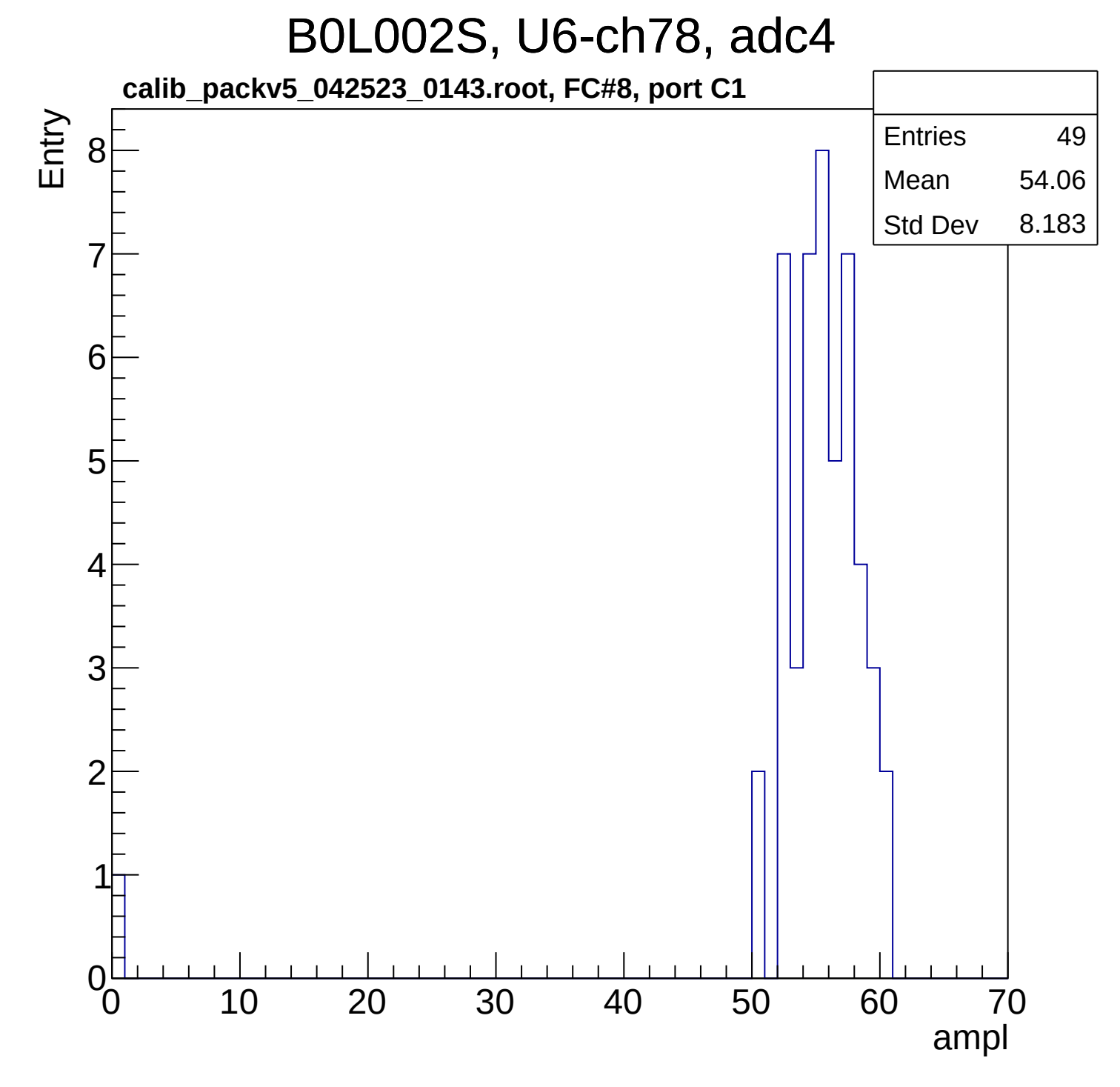
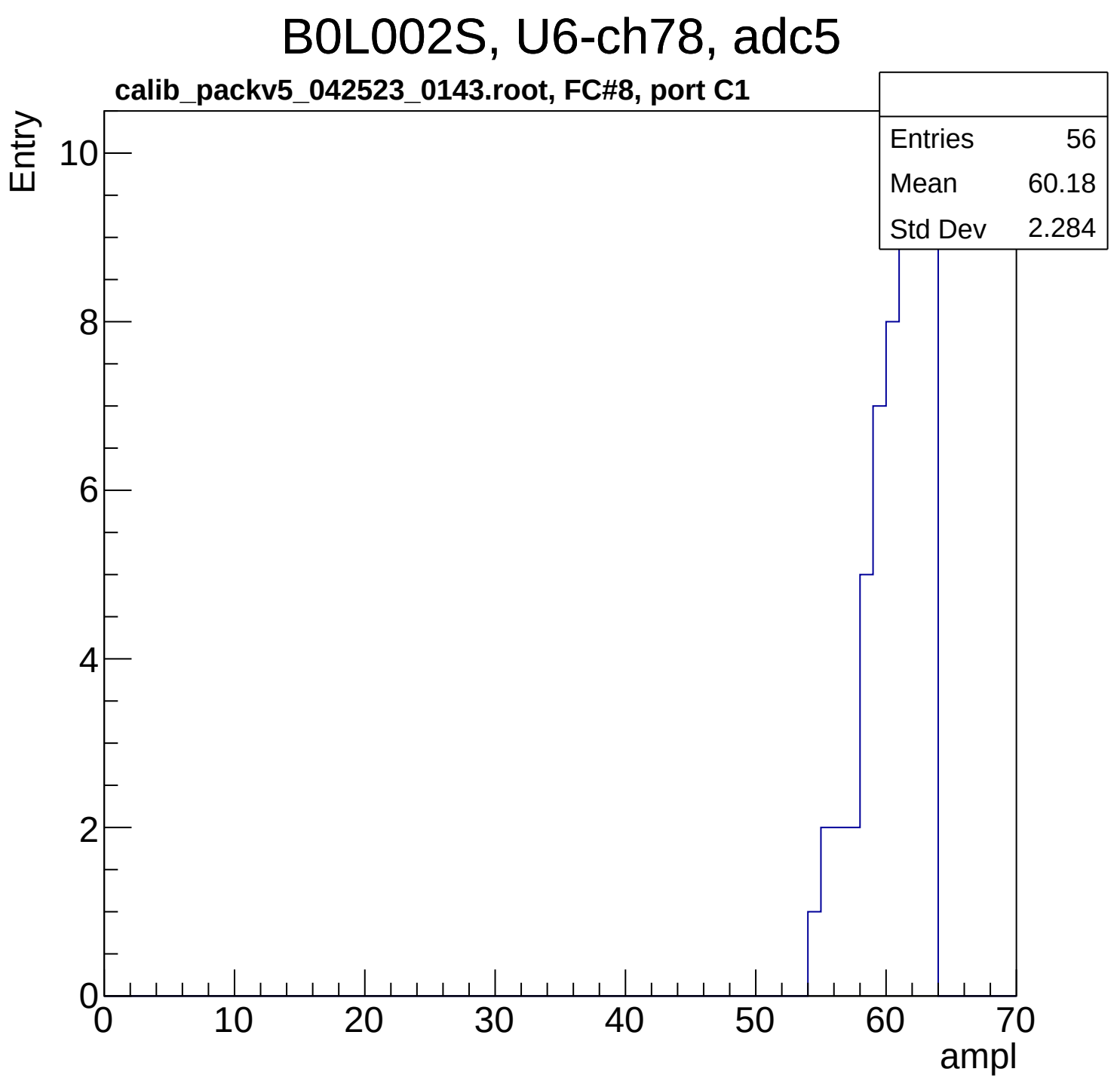
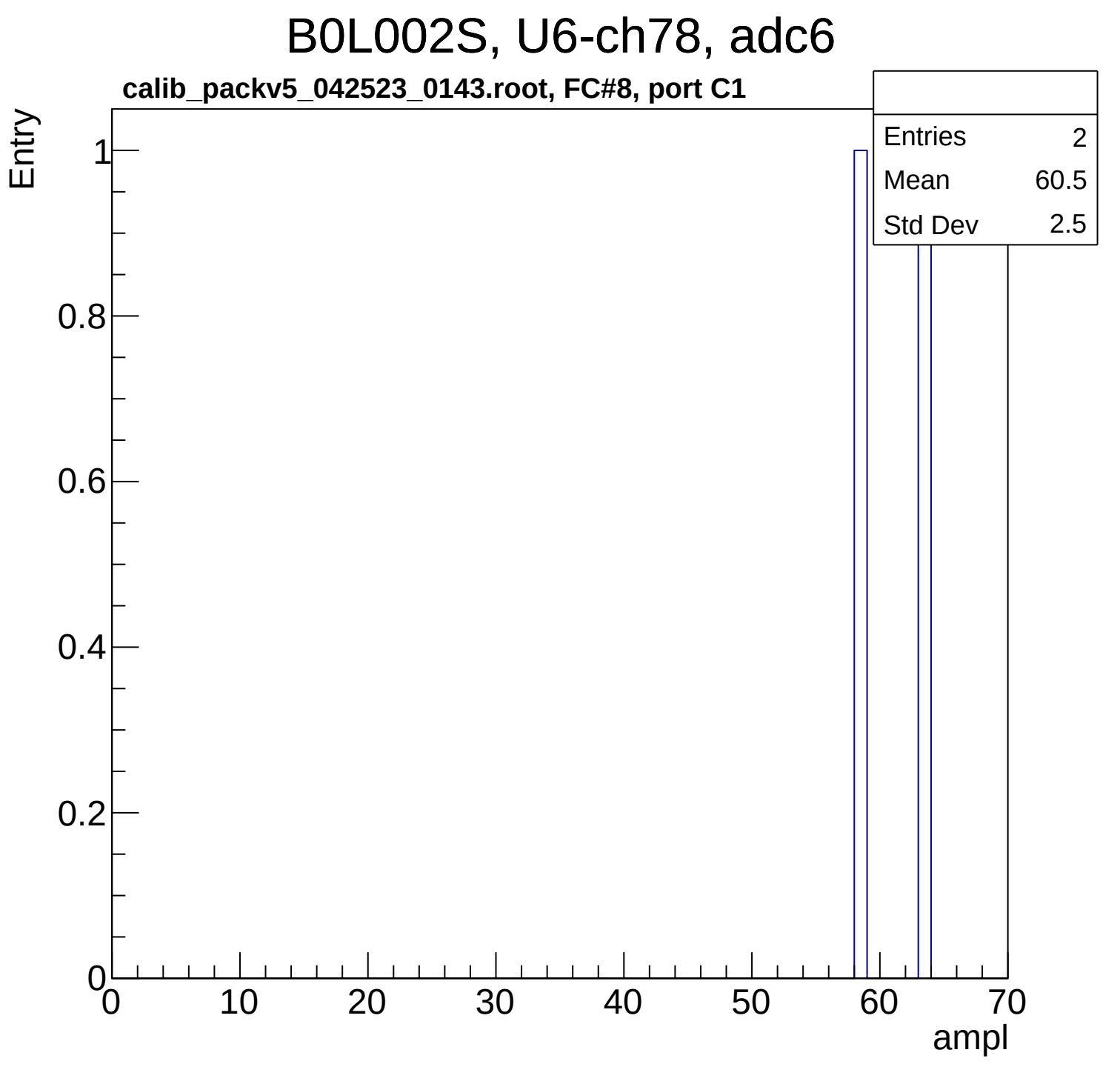
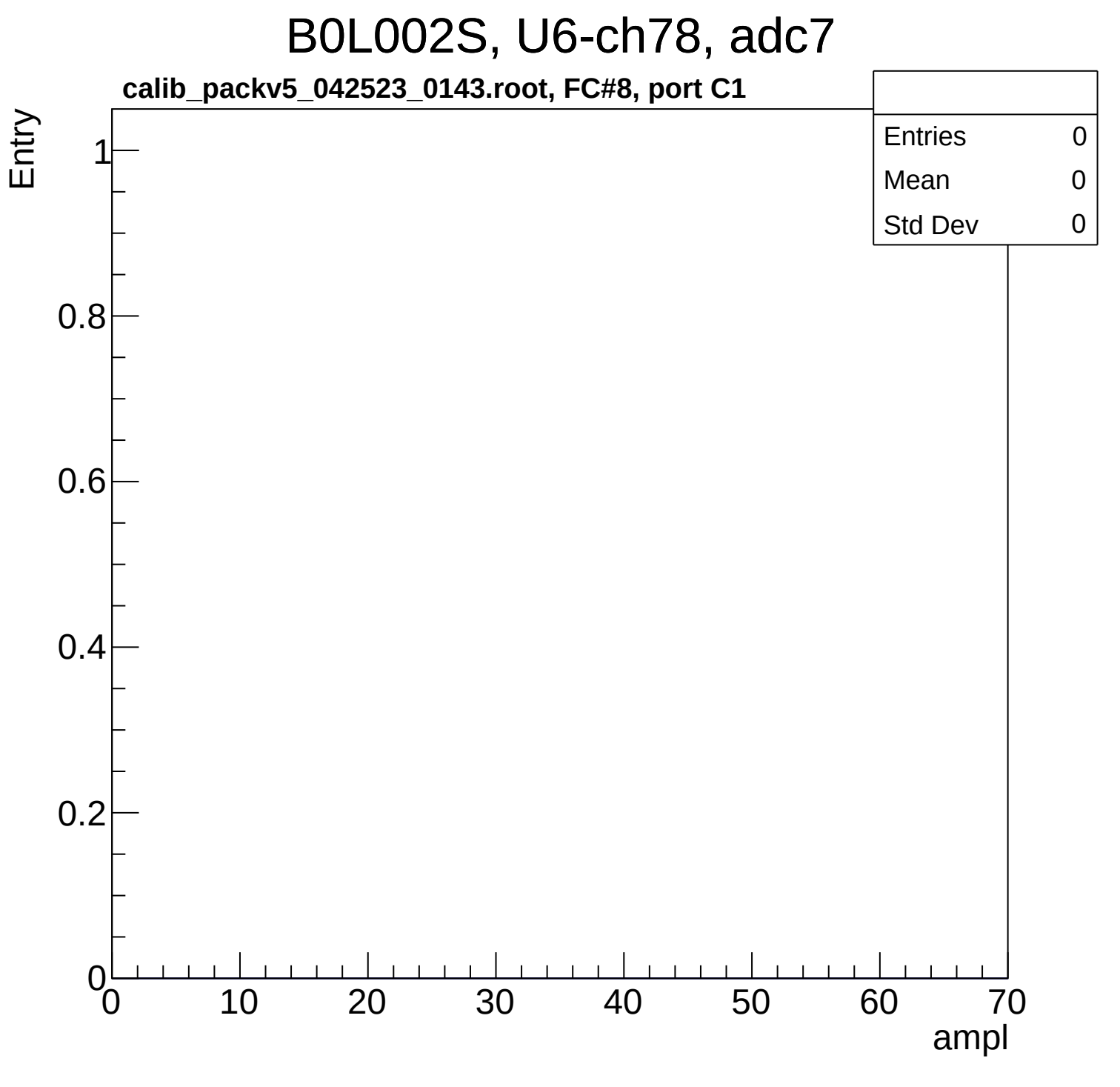

B0L002S, U6-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 42.71
Std Dev 3.312
Gaus mean : 43.3270
Gaus Width: 4.2962
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

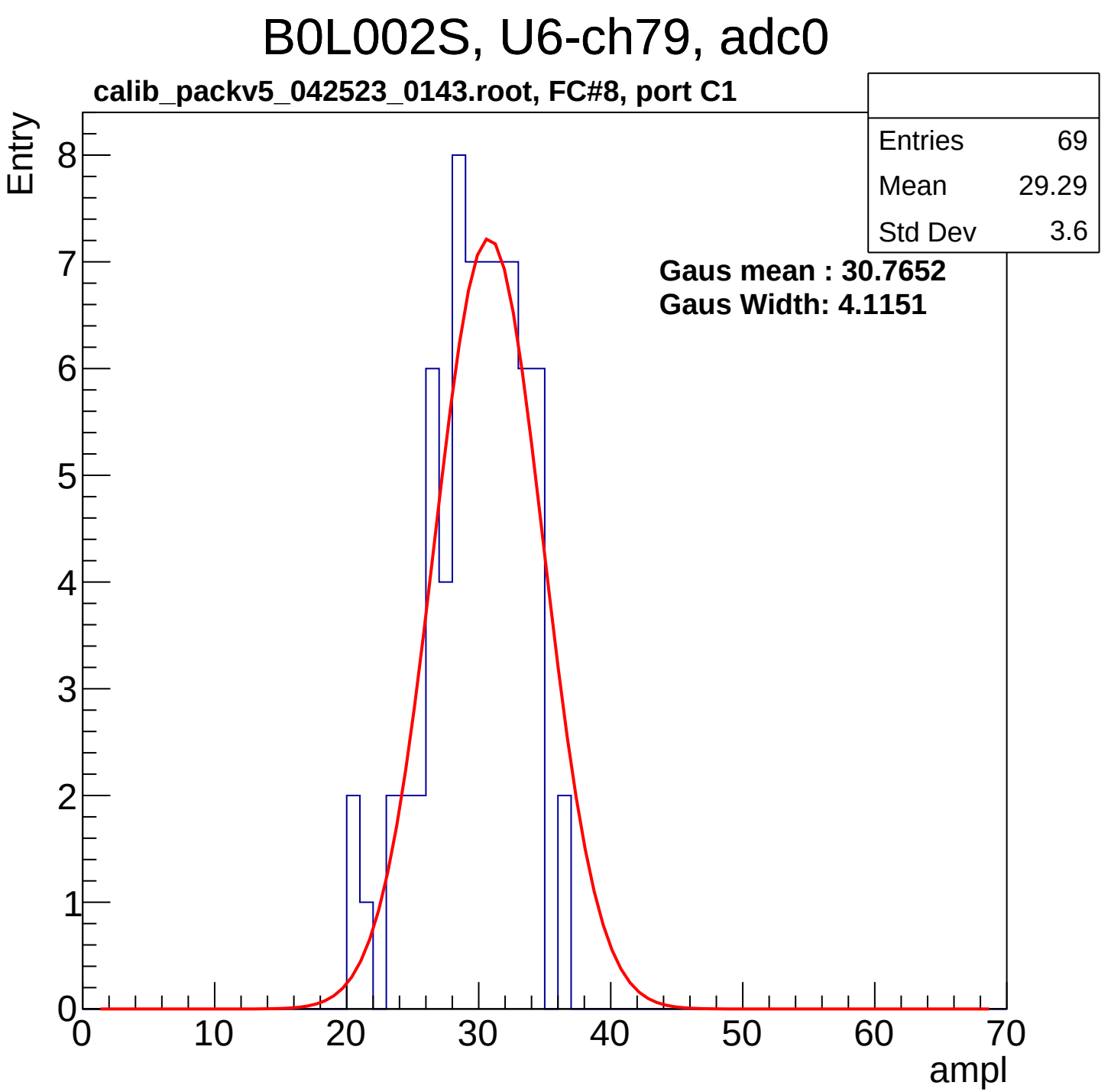

B0L002S, U6-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 48.99
Std Dev 3.42
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 54.06
Std Dev 8.183
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 60.18
Std Dev 2.284
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.29
Std Dev 3.6
Gaus mean : 30.7652
Gaus Width: 4.1151
Entry
ampl
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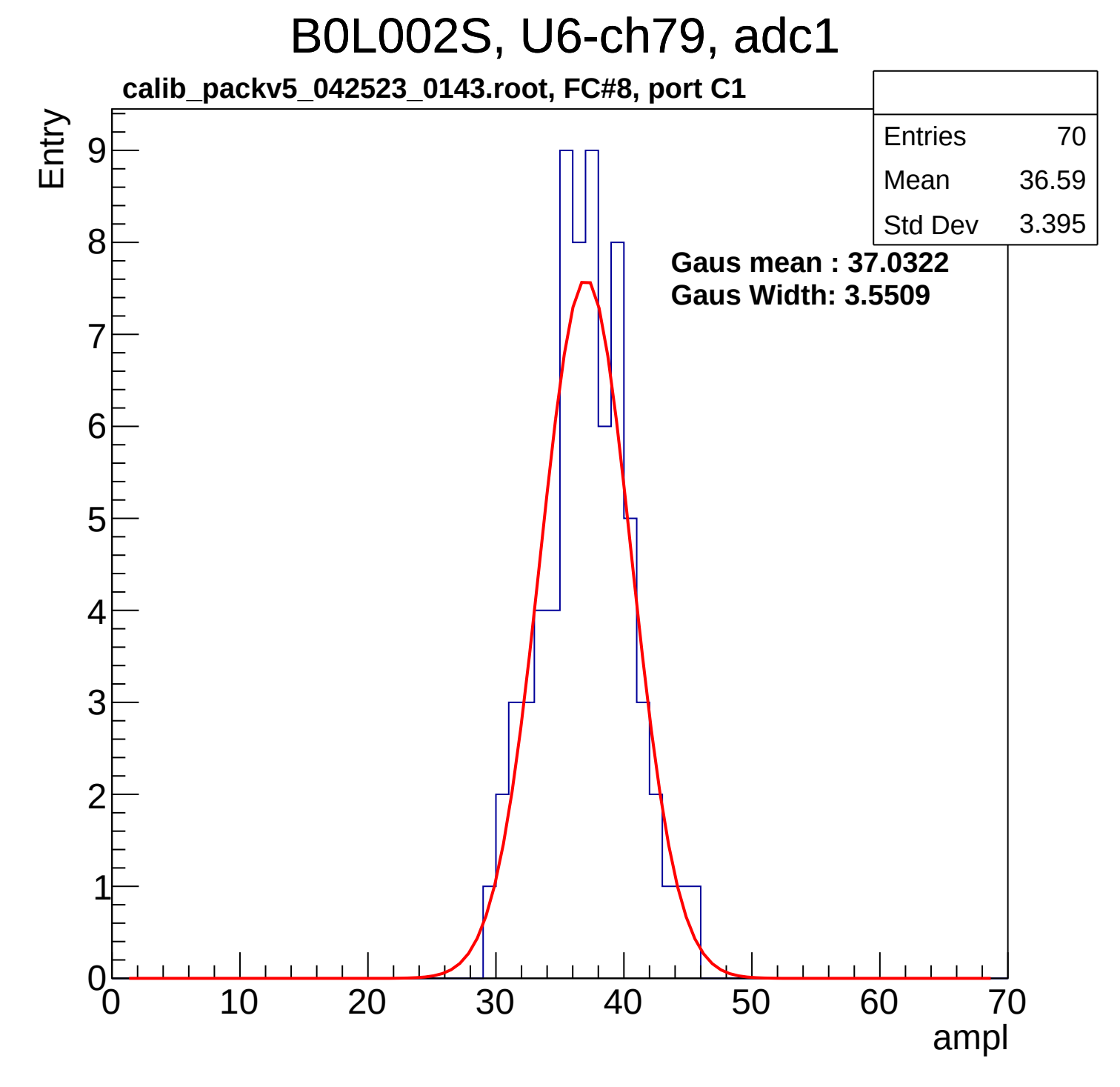
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.59
Std Dev 3.395
Gaus mean : 37.0322
Gaus Width: 3.5509
Entry
ampl
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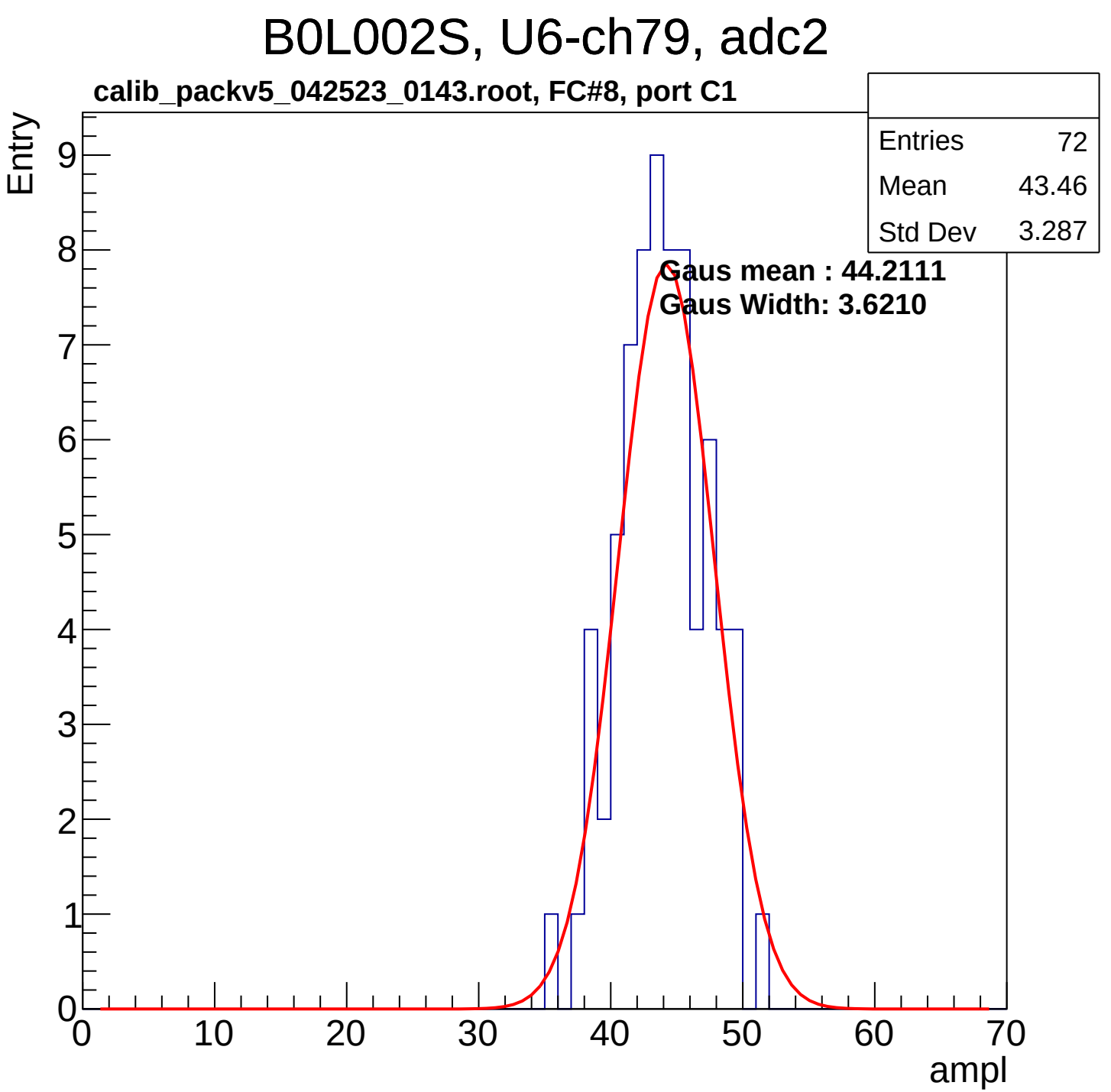
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.46
Std Dev 3.287
Gaus mean : 44.2111
Gaus Width: 3.6210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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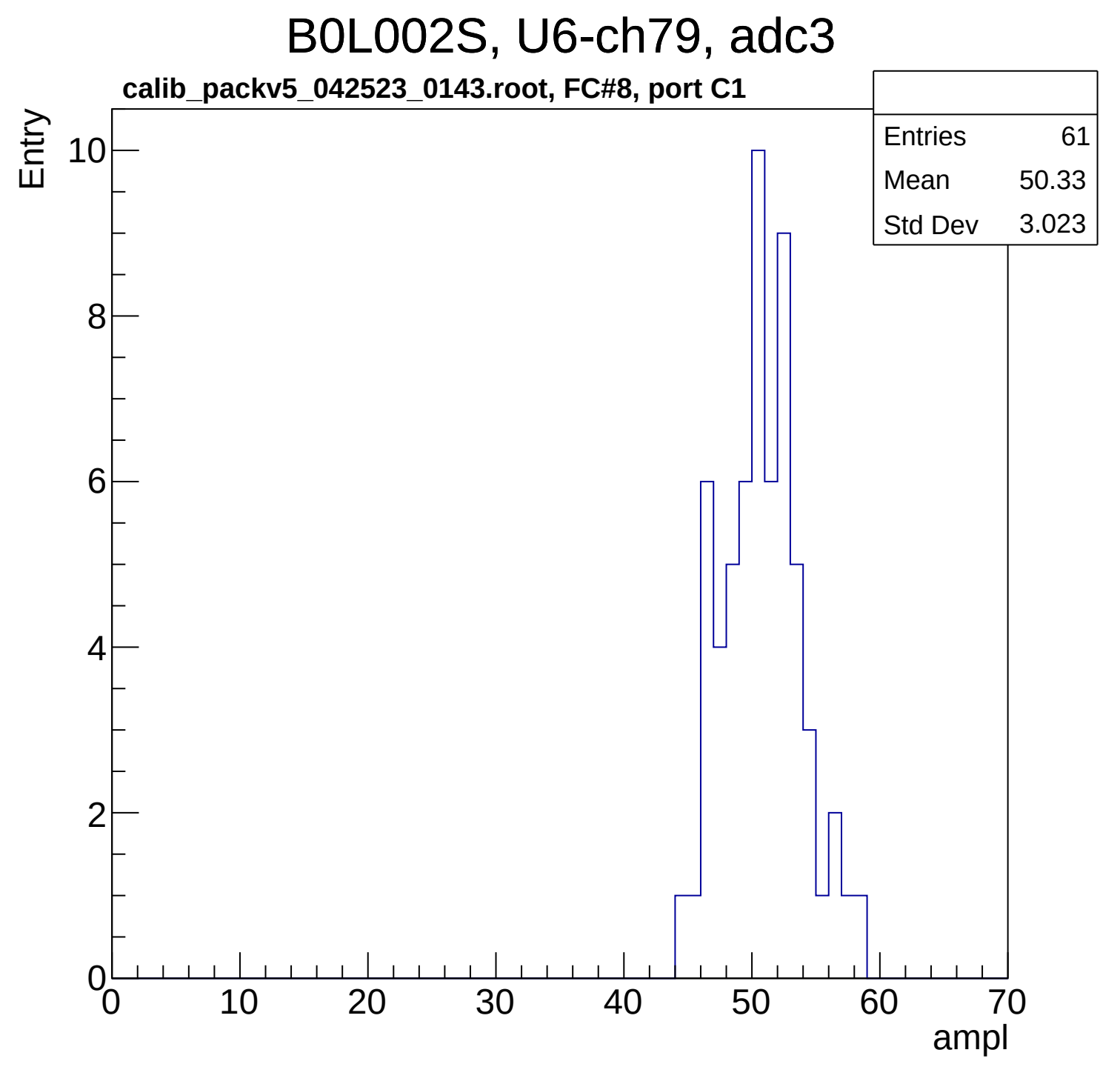
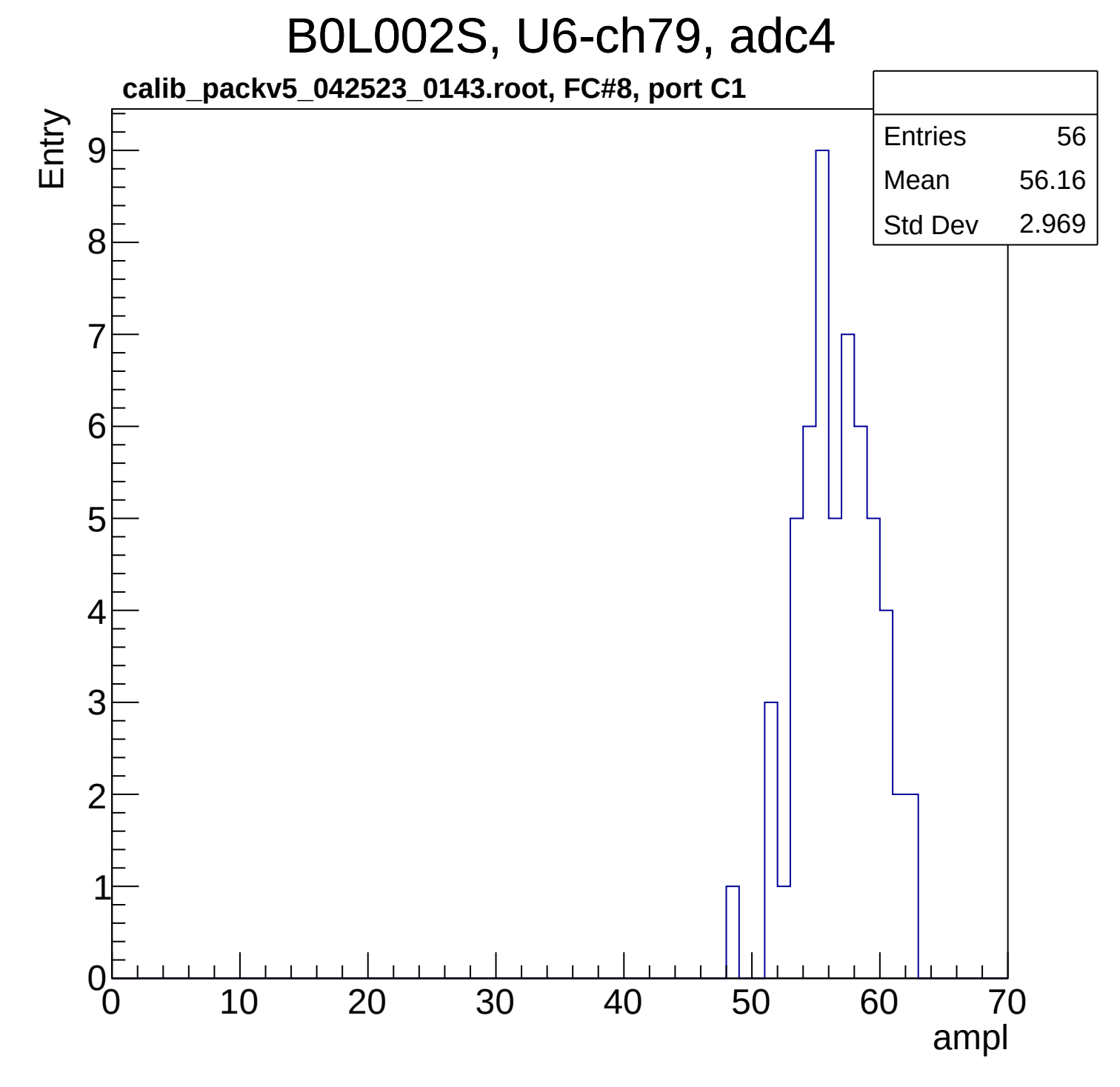

B0L002S, U6-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.33
Std Dev 3.023
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.16
Std Dev 2.969
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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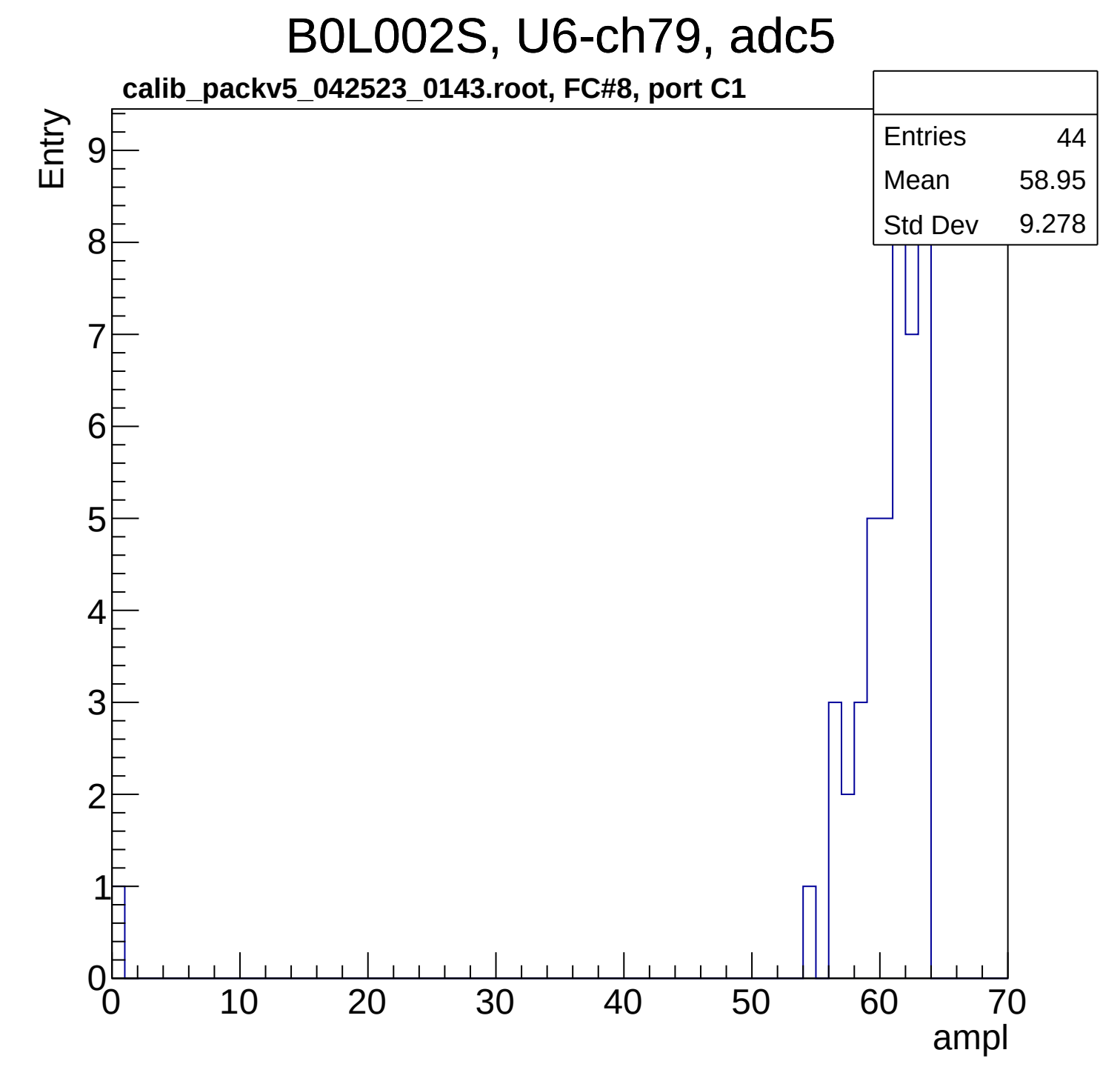
40
50
60
70


B0L002S, U6-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.95
Std Dev 9.278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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50
60
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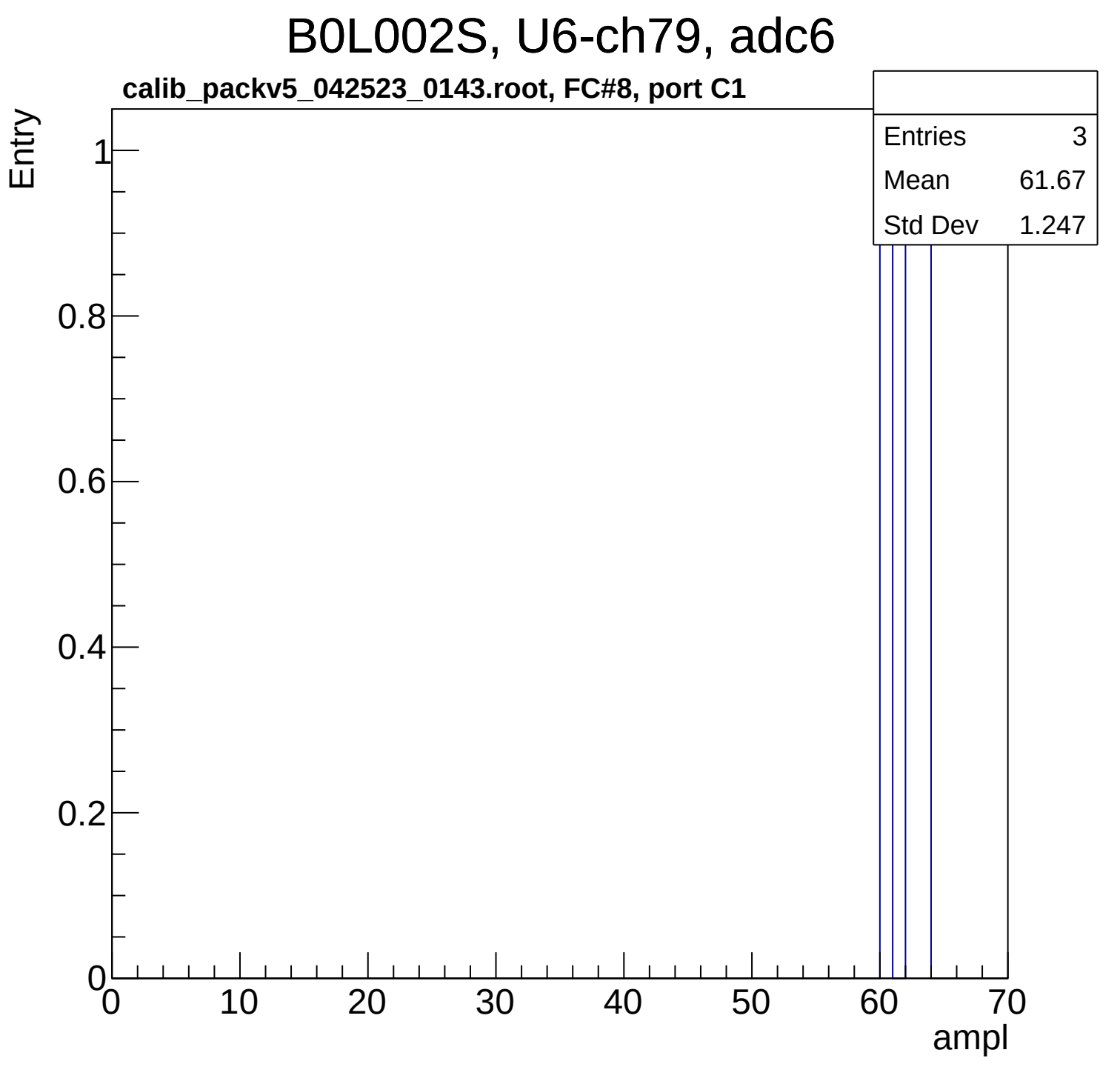
B0L002S, U6-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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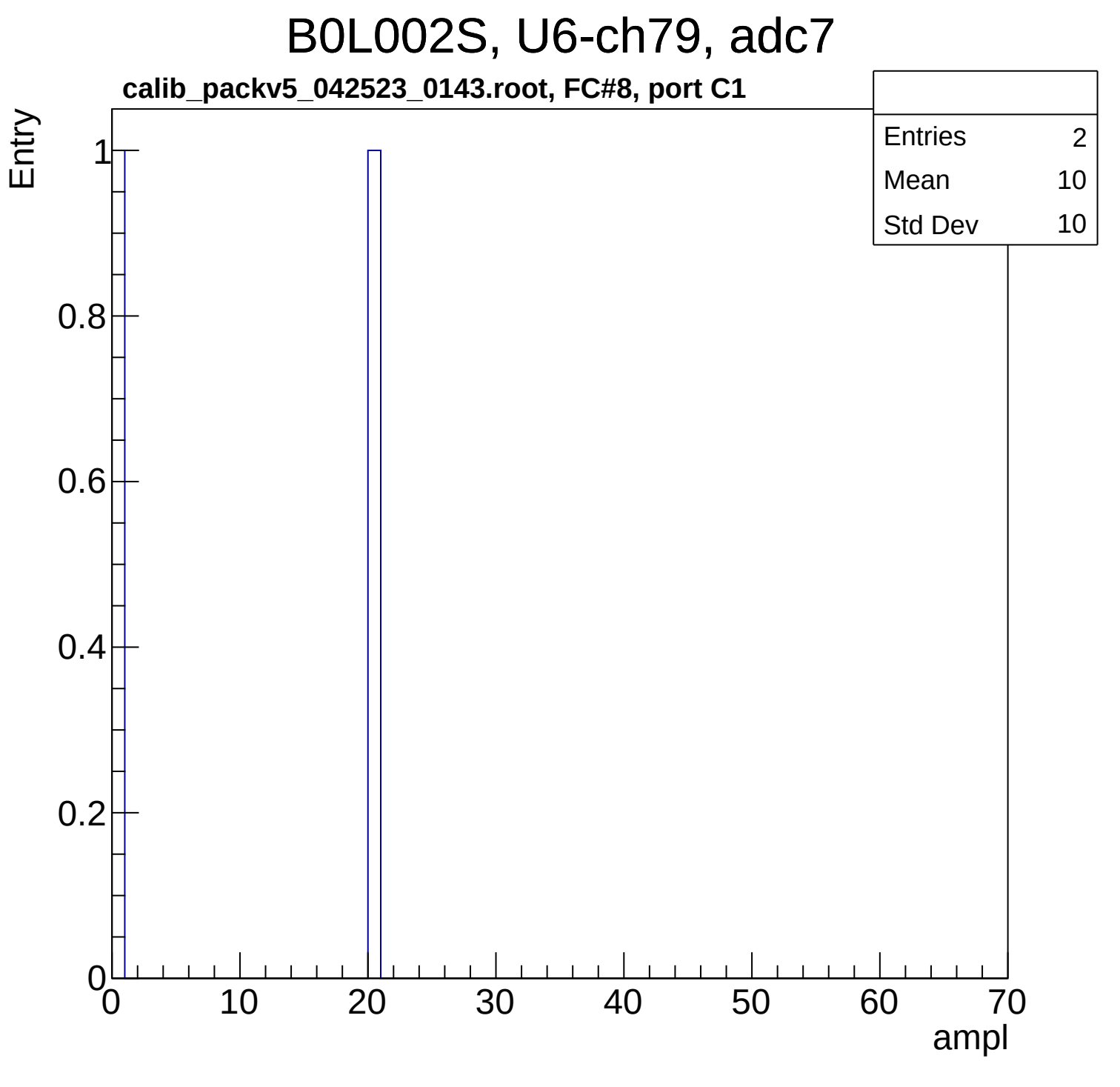
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10
Std Dev 10
Entry
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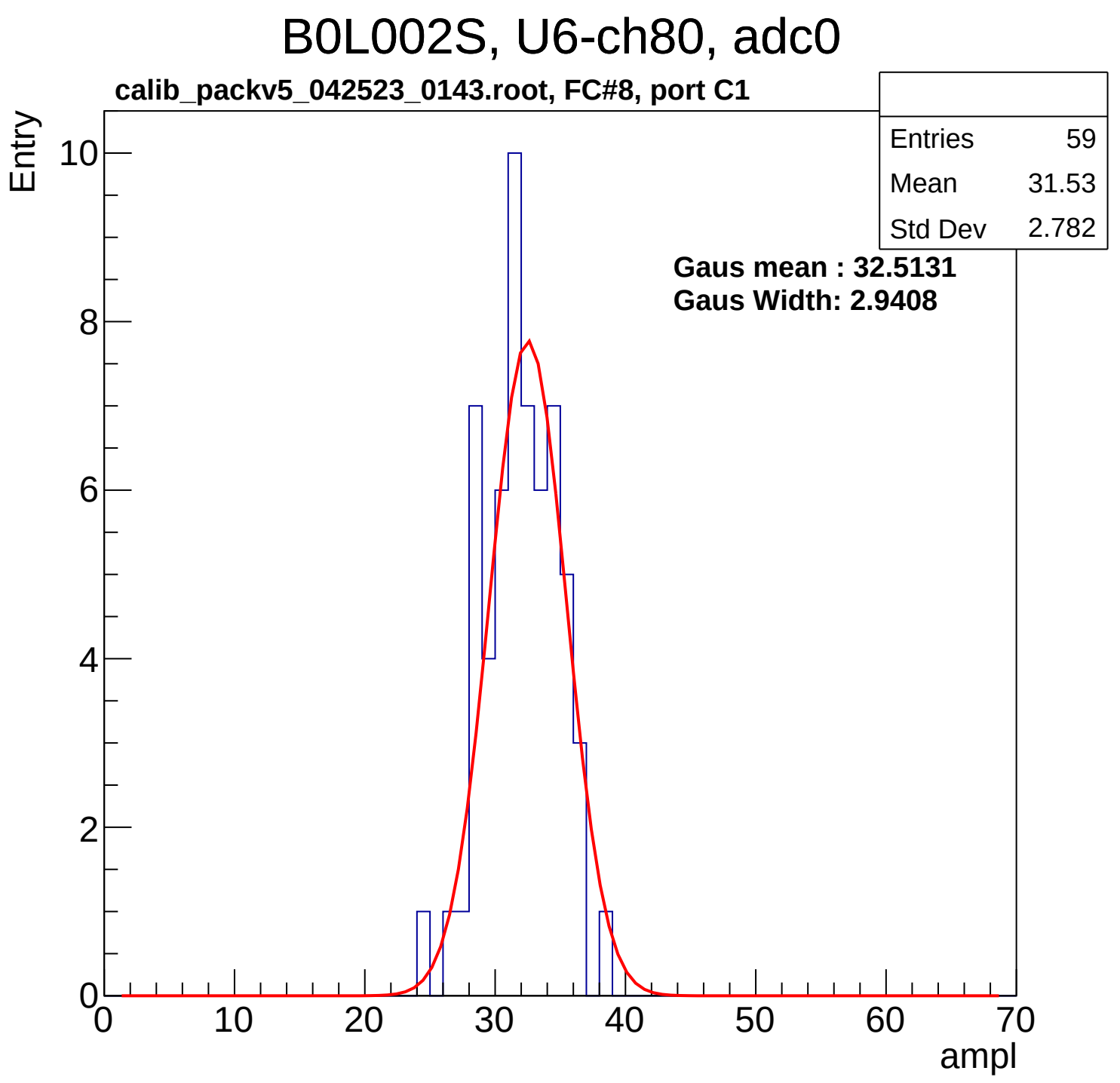
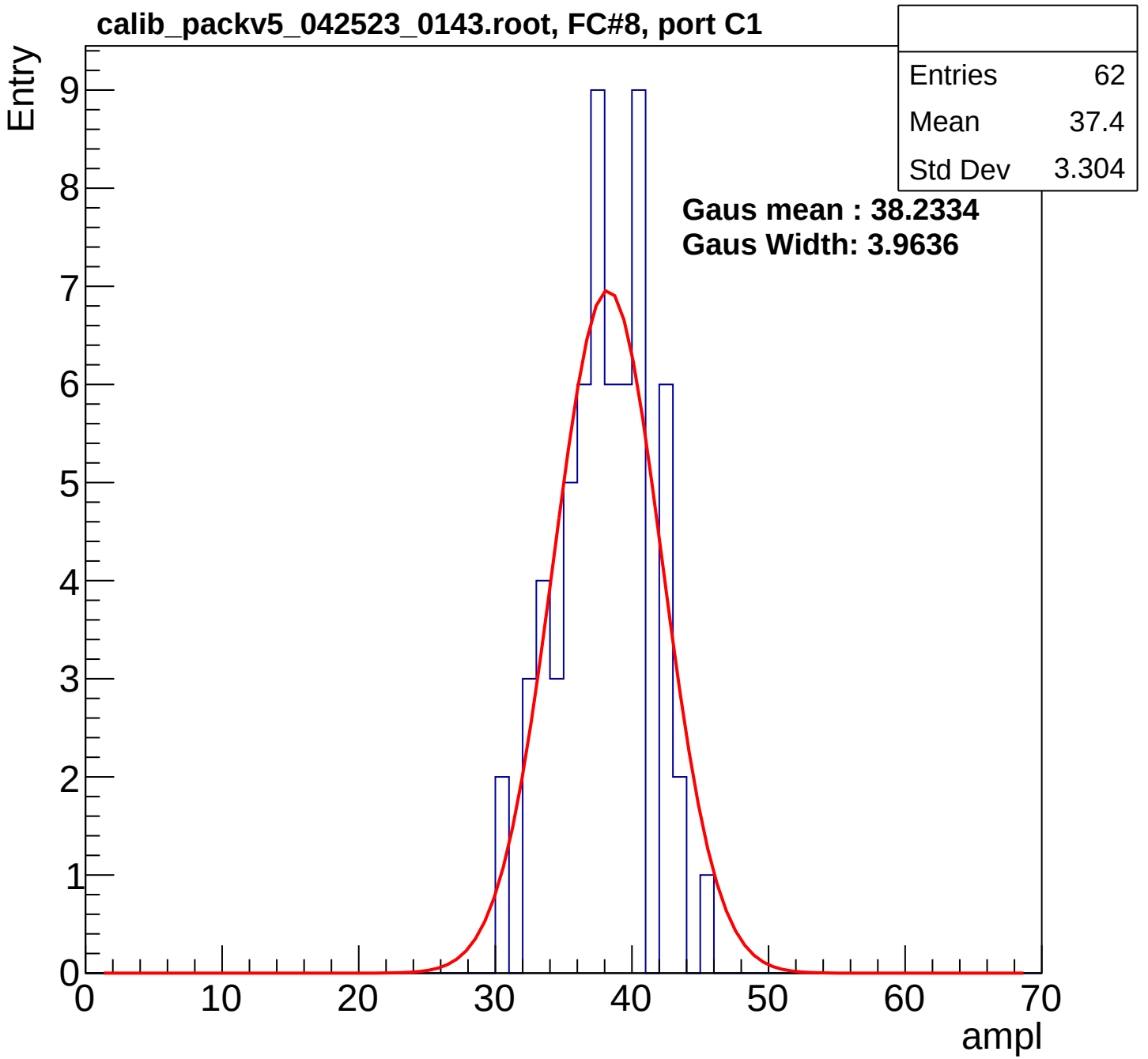
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 31.53
Std Dev 2.782
Gaus mean : 32.5131
Gaus Width: 2.9408
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.4
Std Dev 3.304
Gaus mean : 38.2334
Gaus Width: 3.9636
Entry
ampl
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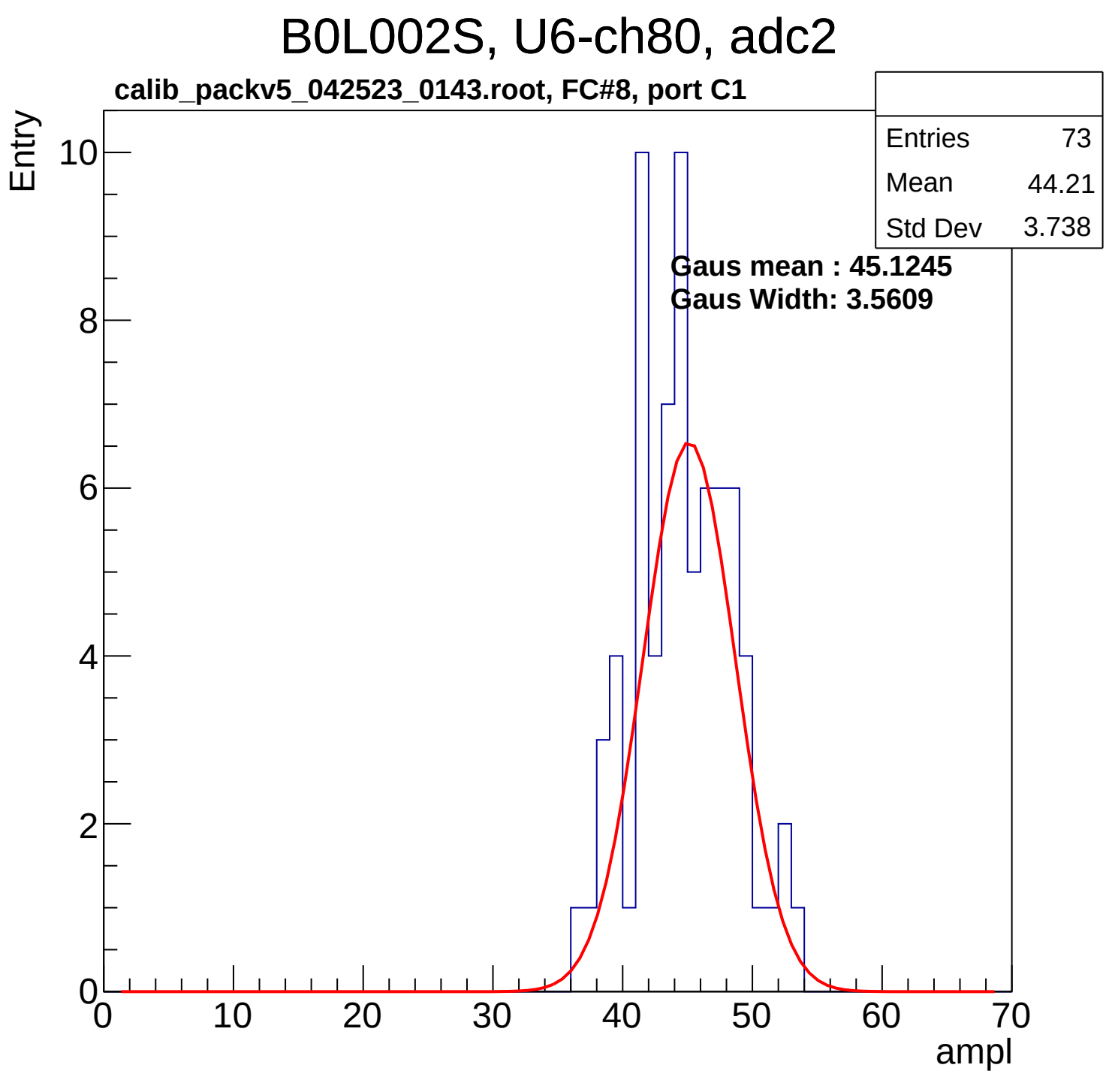
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.21
Std Dev 3.738
Gaus mean : 45.1245
Gaus Width: 3.5609
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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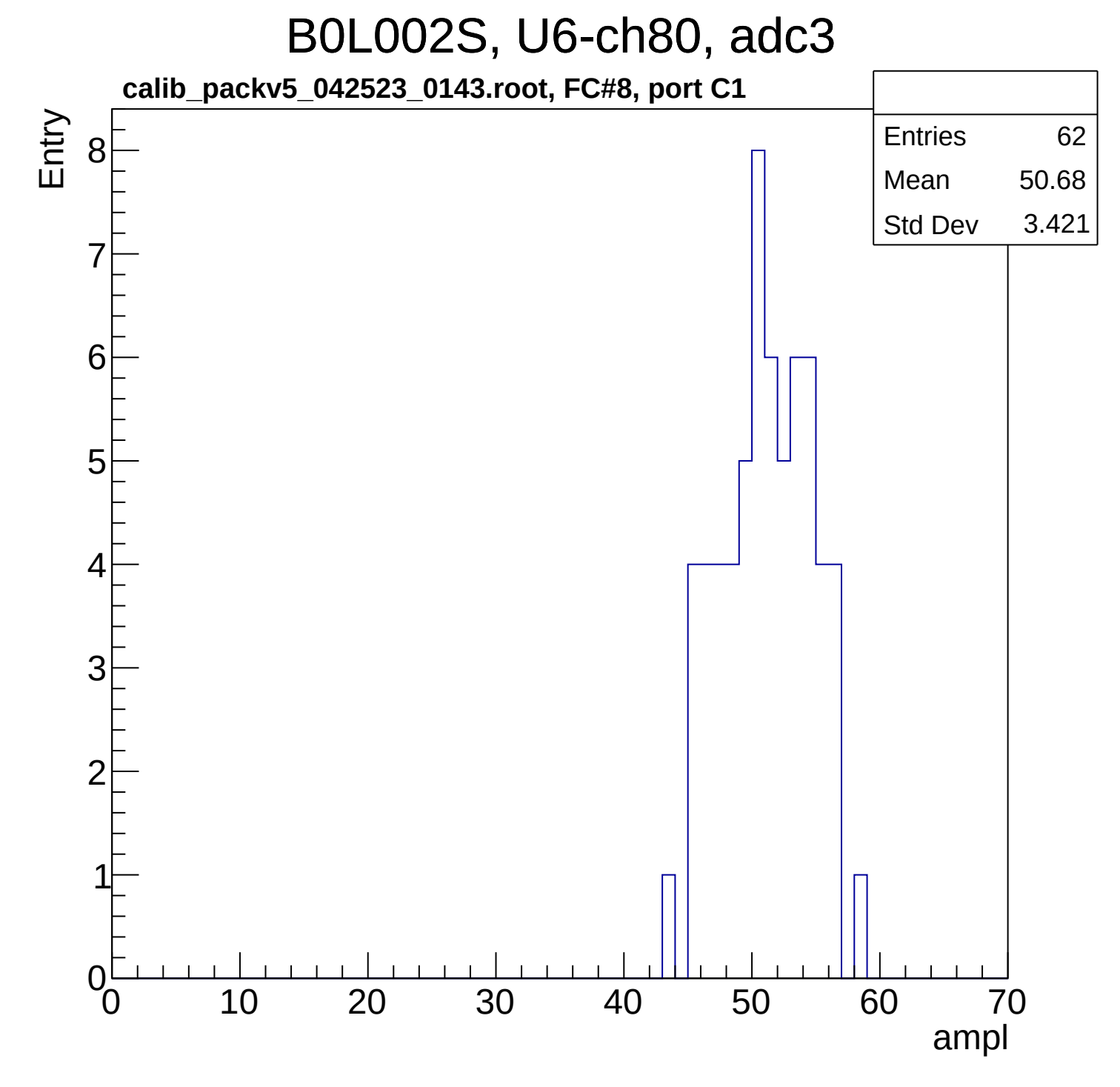
B0L002S, U6-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.68
Std Dev 3.421
Entry
ampl
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4
5
6
7
8
0
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60
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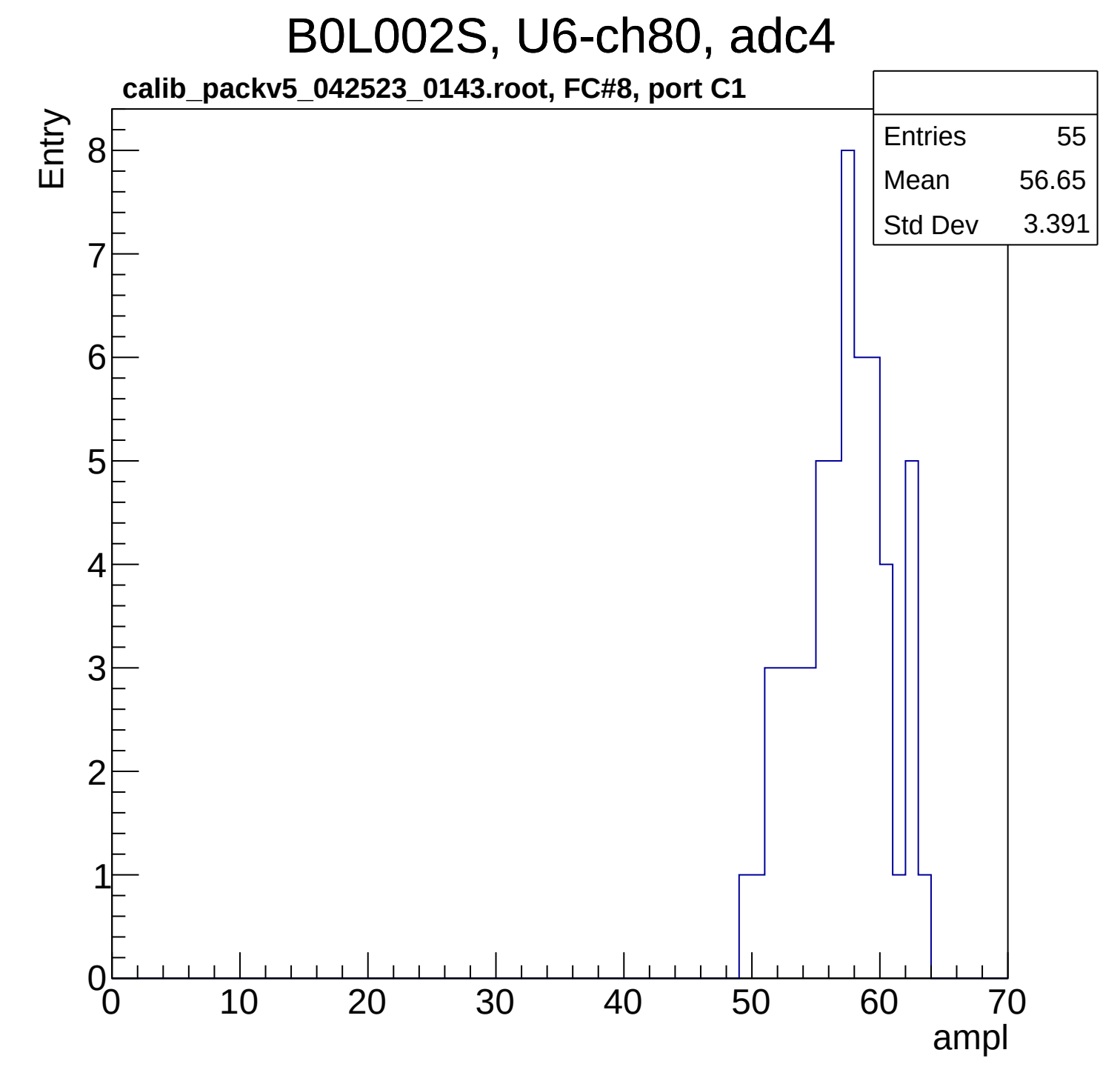
B0L002S, U6-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.65
Std Dev 3.391
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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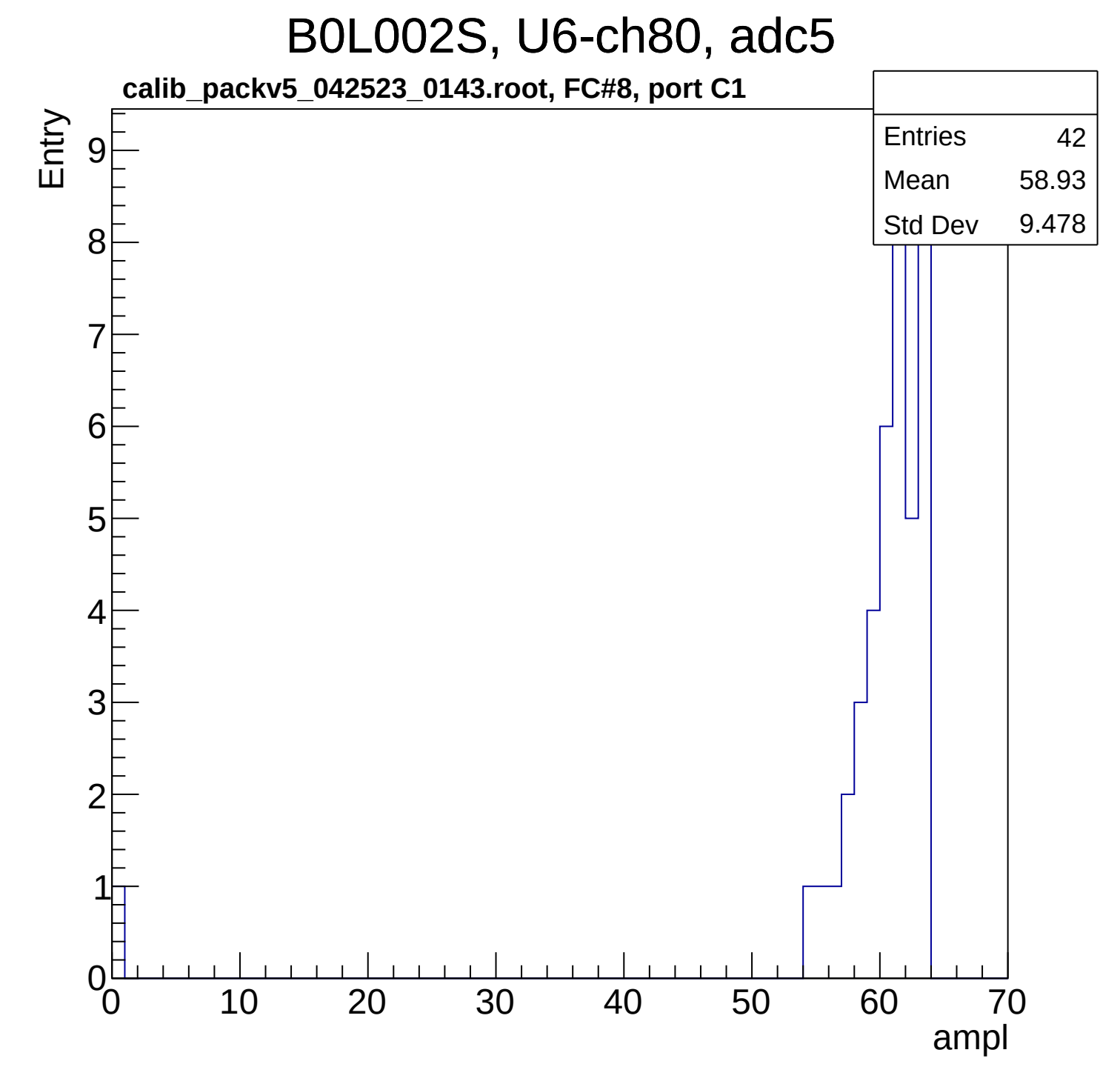
B0L002S, U6-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.93
Std Dev 9.478
Entry
ampl
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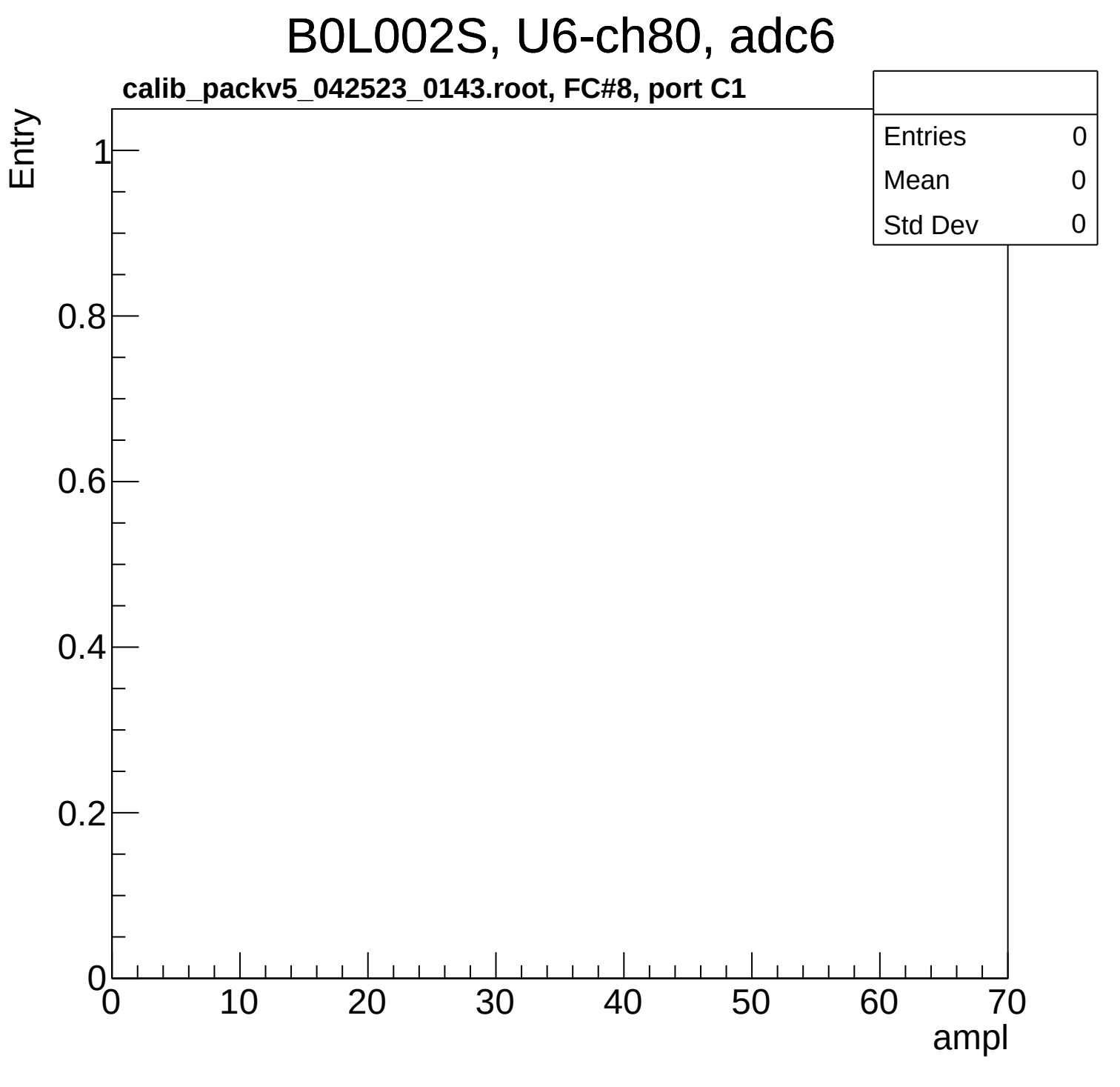
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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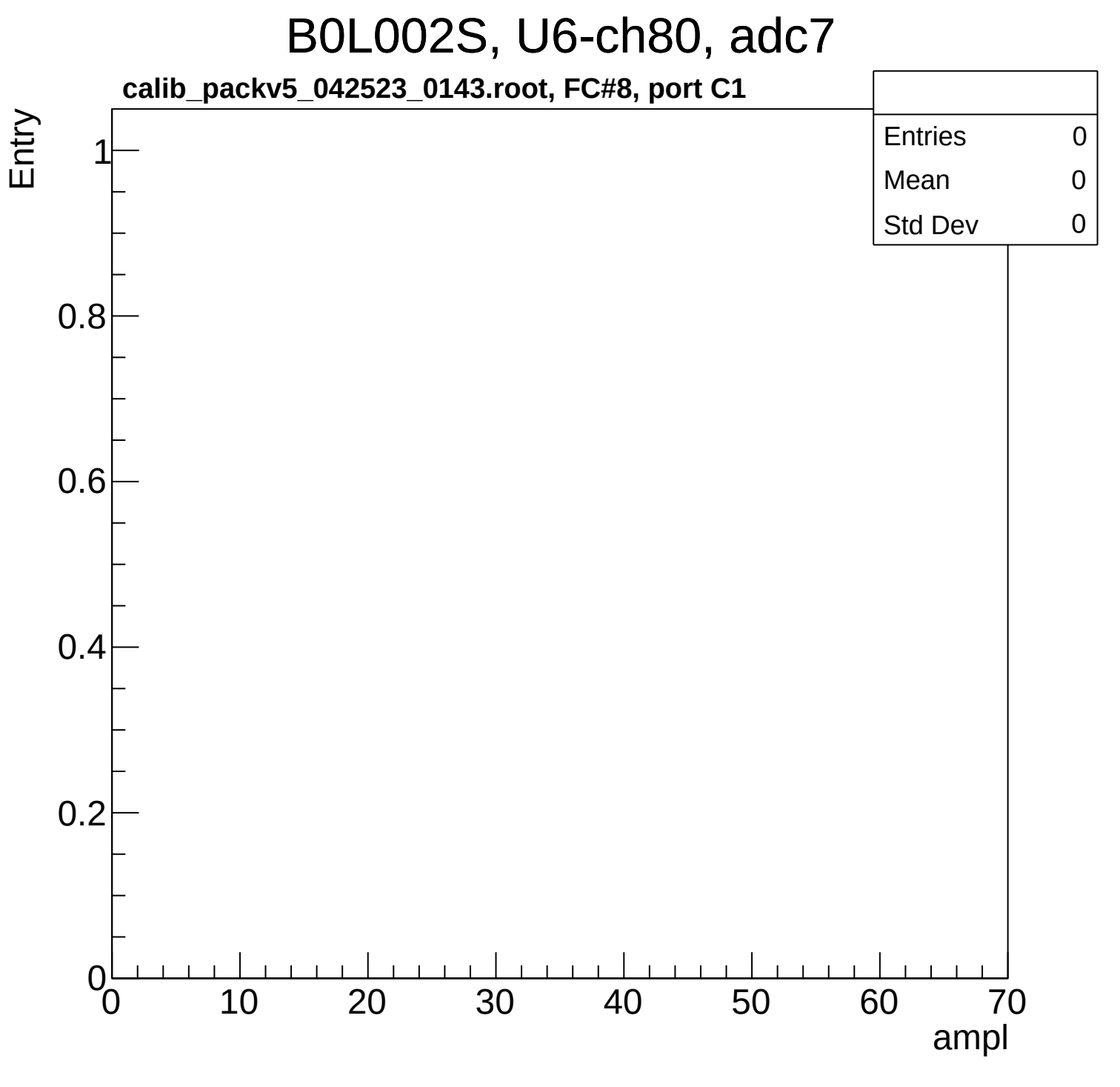
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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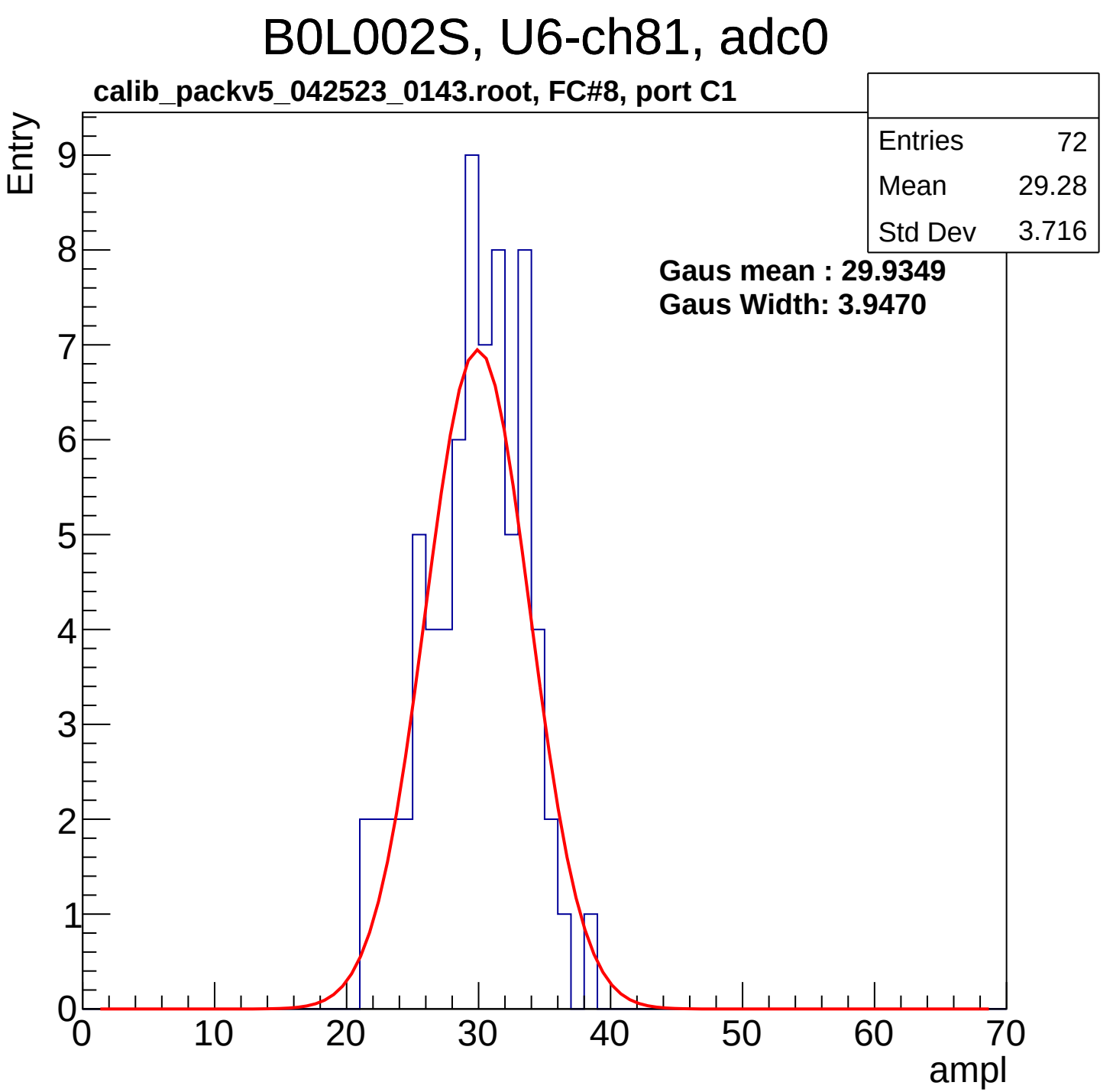
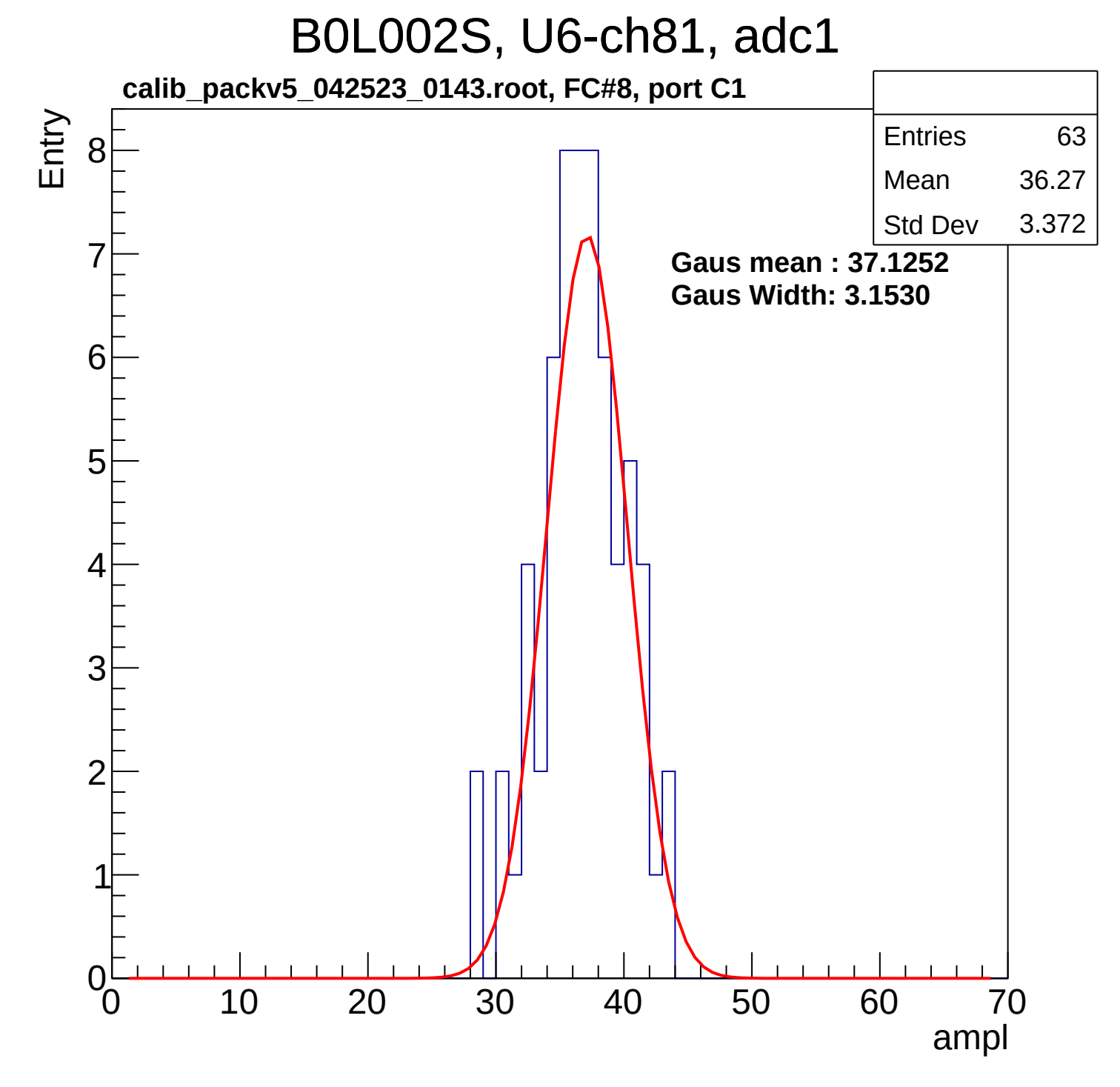
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.28
Std Dev 3.716
Gaus mean : 29.9349
Gaus Width: 3.9470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 36.27
Std Dev 3.372
Gaus mean : 37.1252
Gaus Width: 3.1530
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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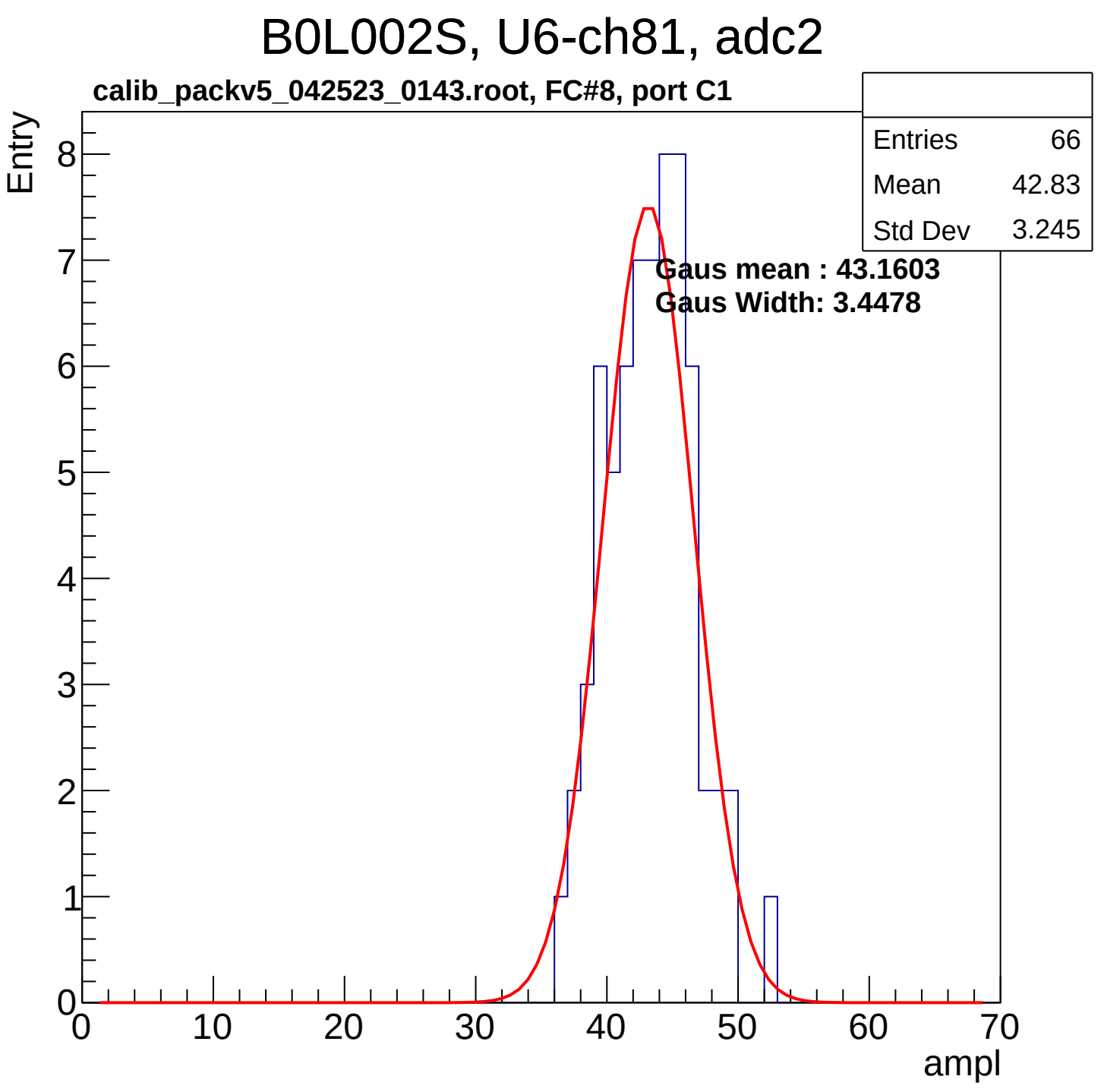
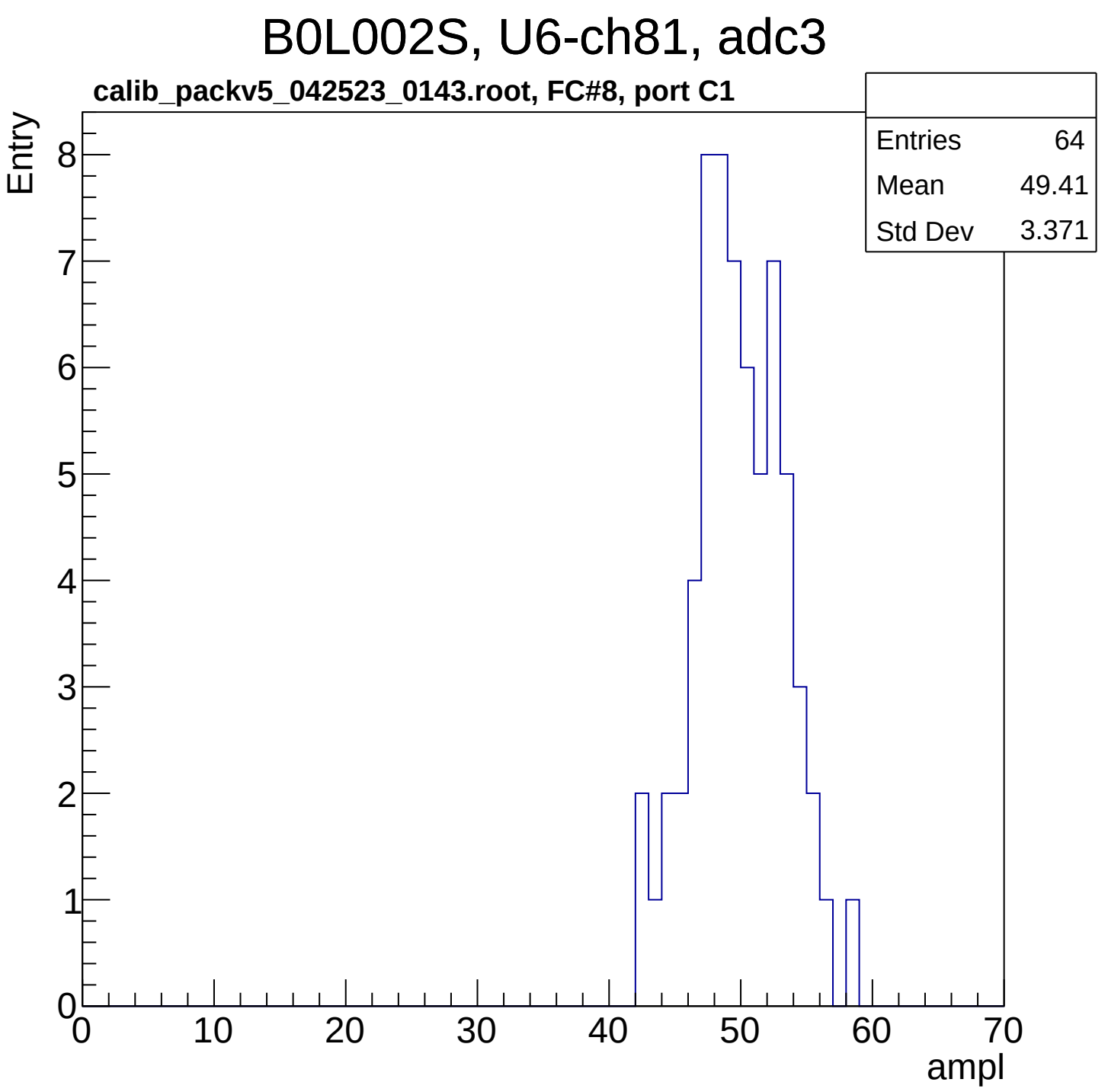
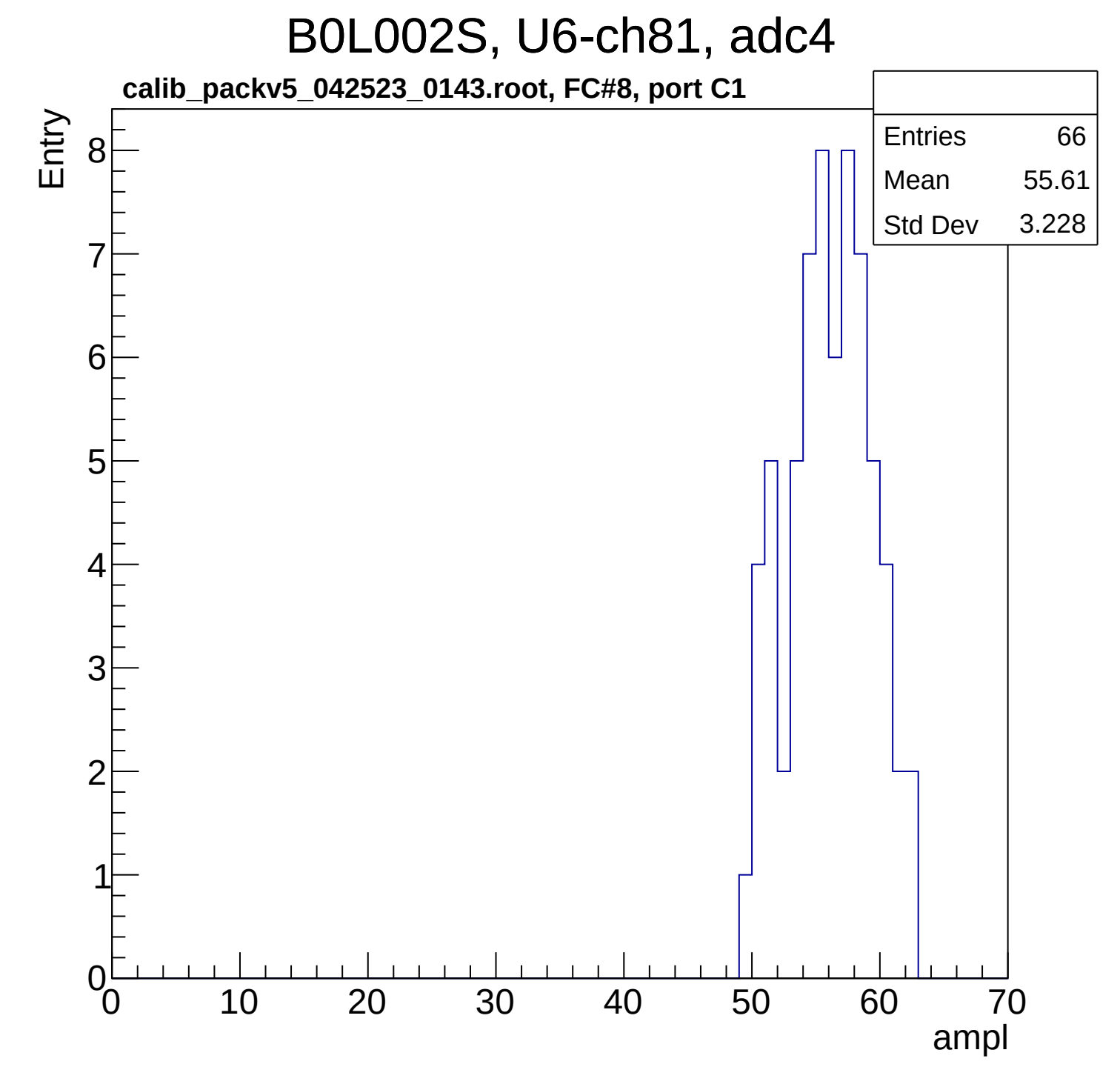
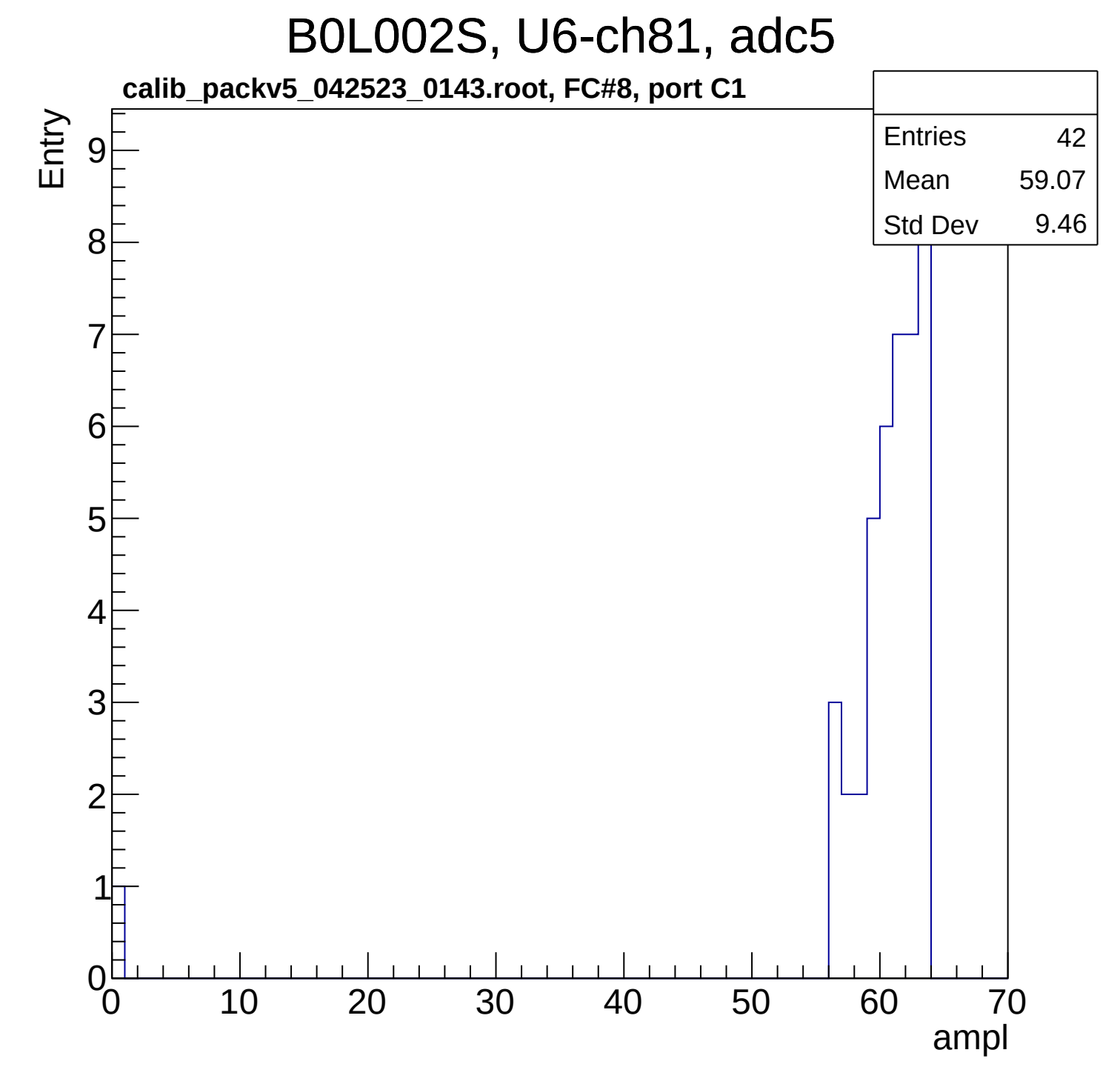
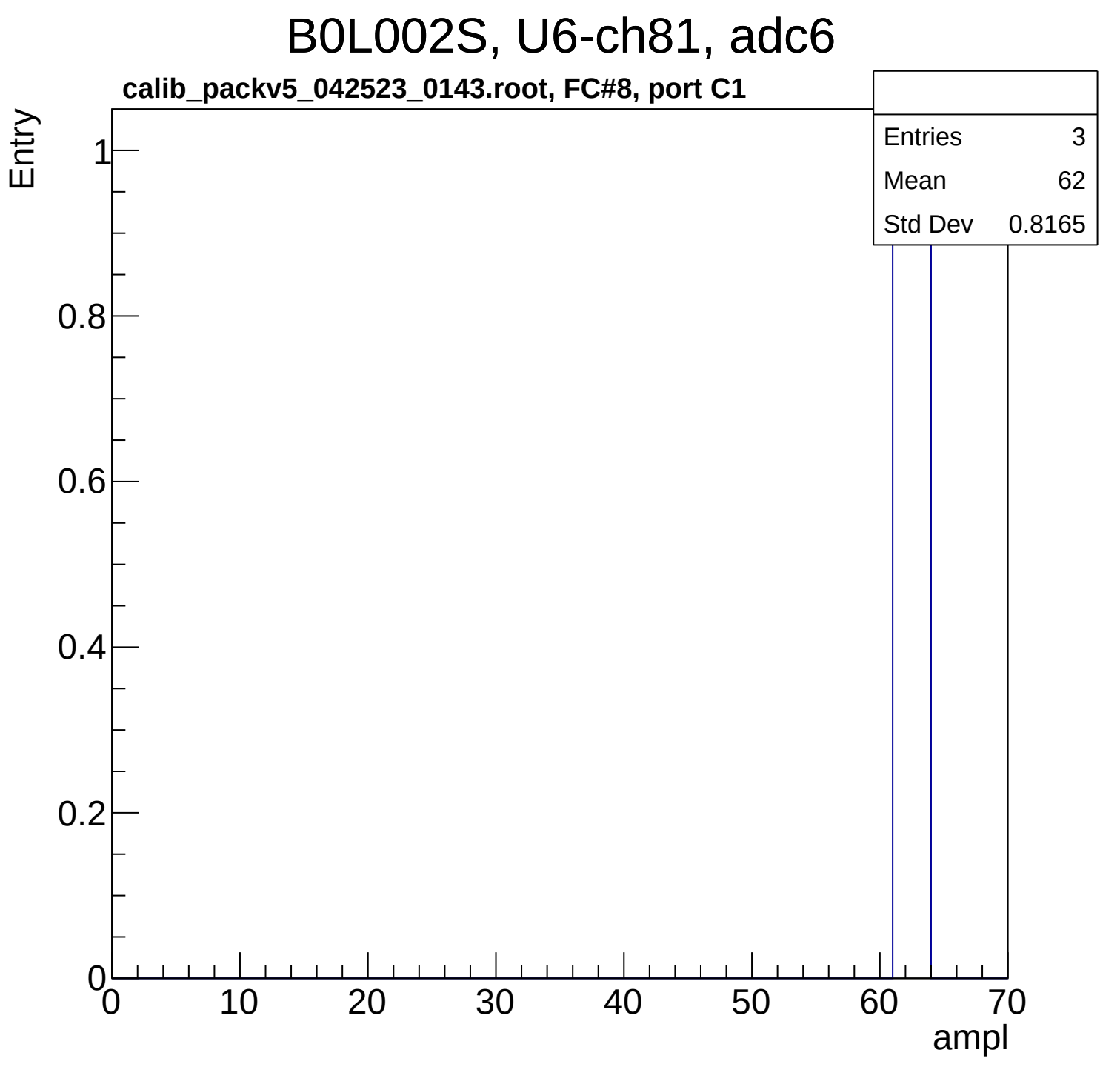
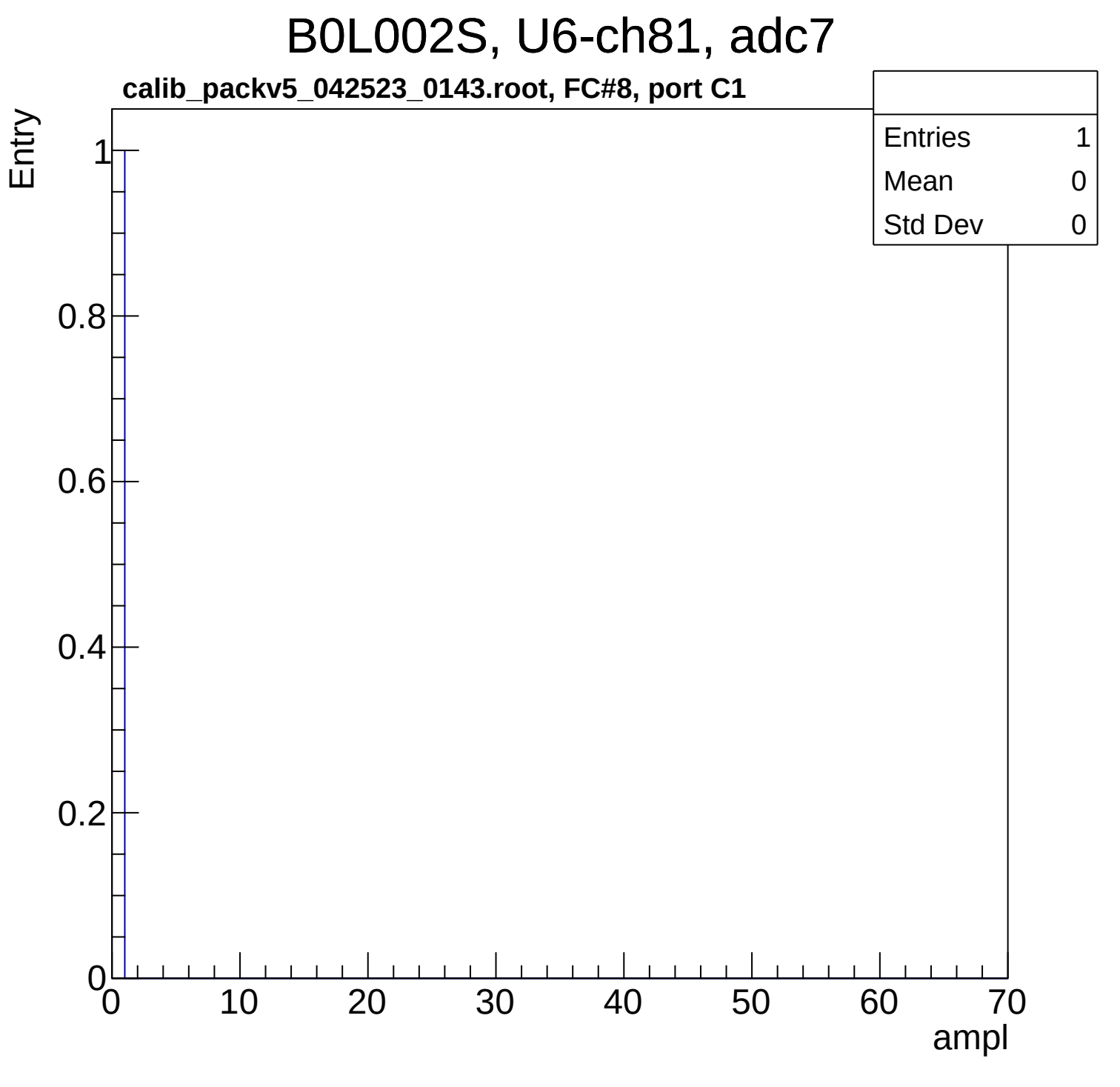

B0L002S, U6-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.83
Std Dev 3.245
Gaus mean : 43.1603
Gaus Width: 3.4478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

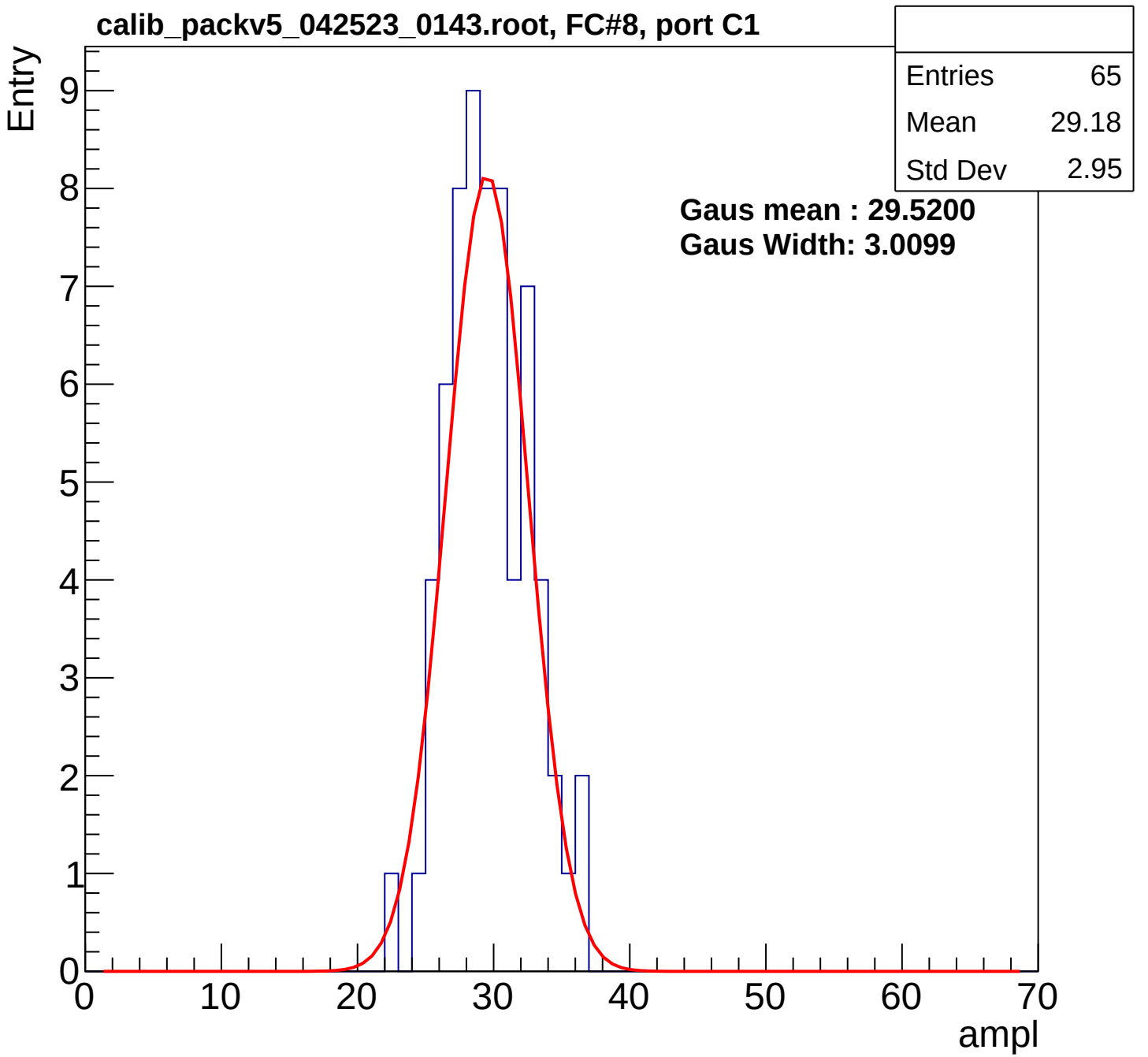

B0L002S, U6-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.41
Std Dev 3.371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 55.61
Std Dev 3.228
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.07
Std Dev 9.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.18
Std Dev 2.95
Gaus mean : 29.5200
Gaus Width: 3.0099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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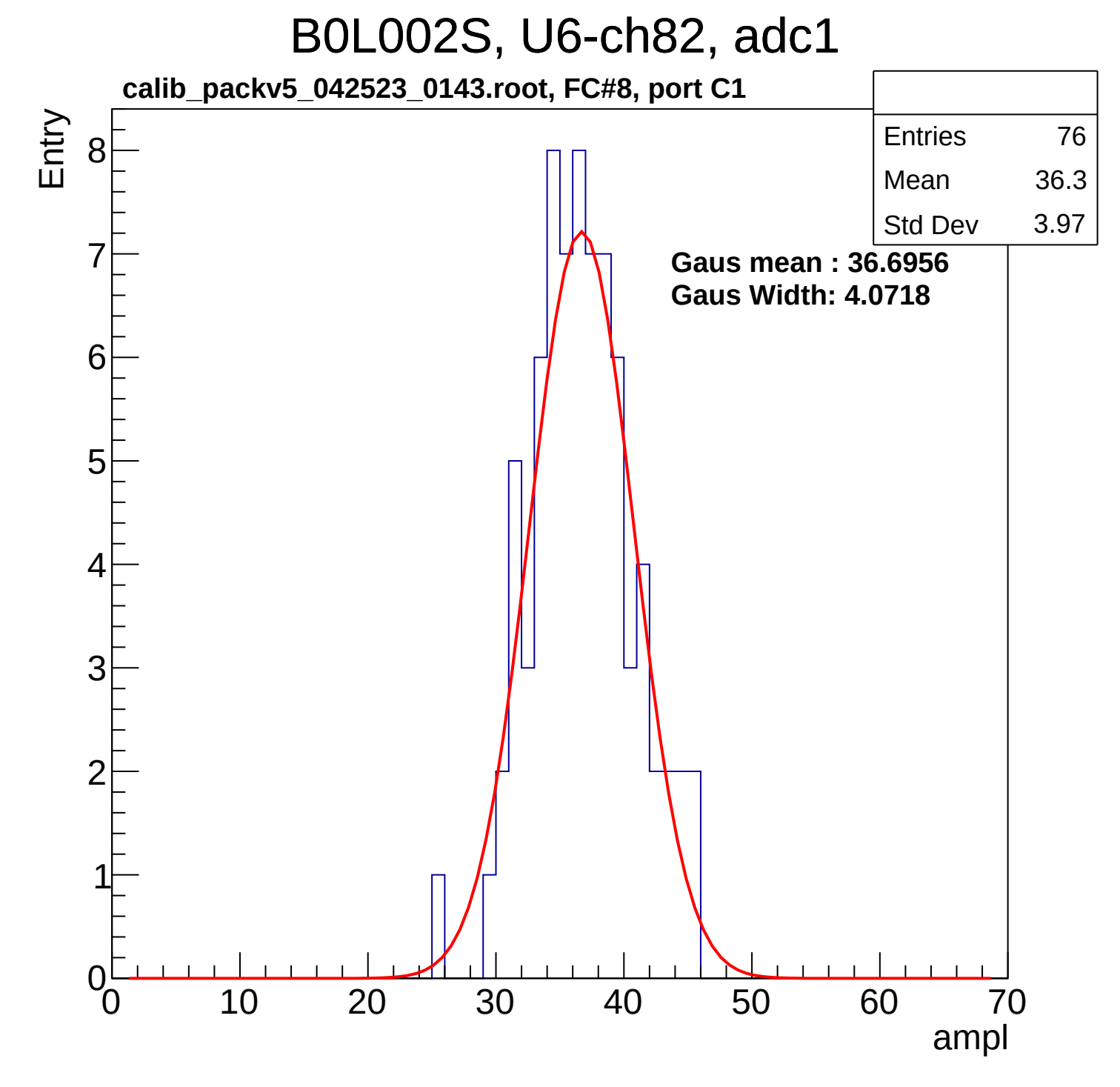
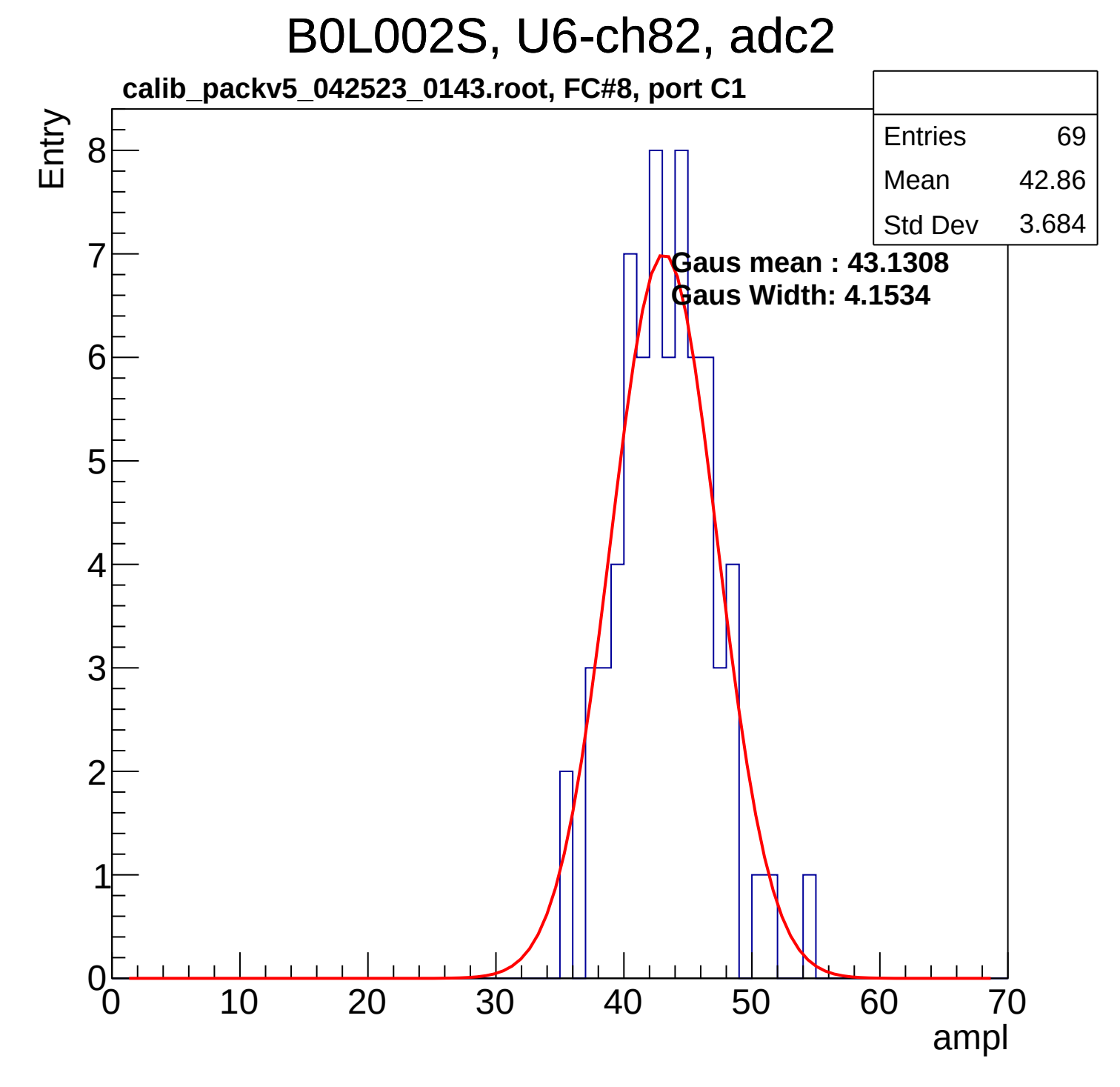
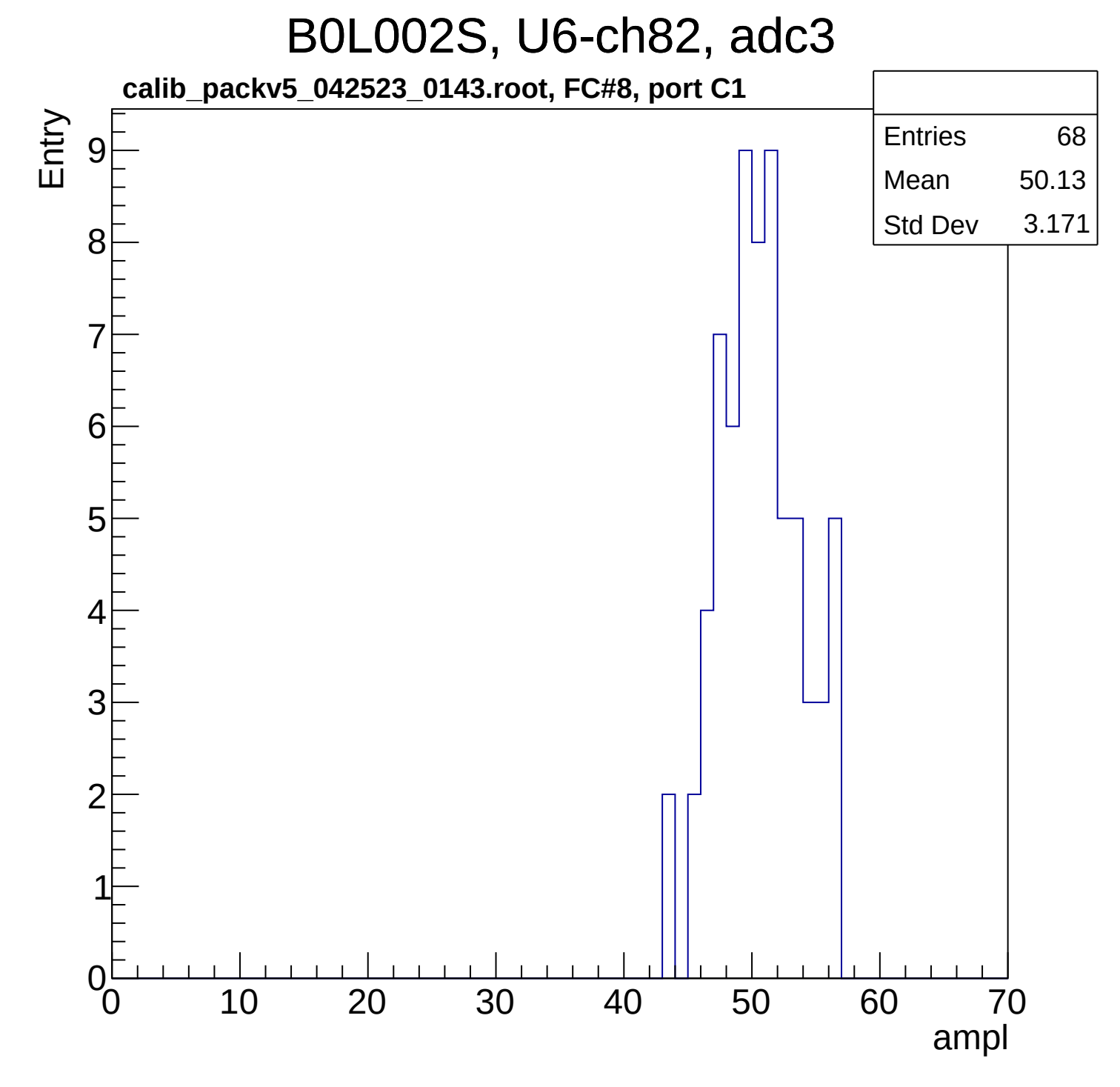
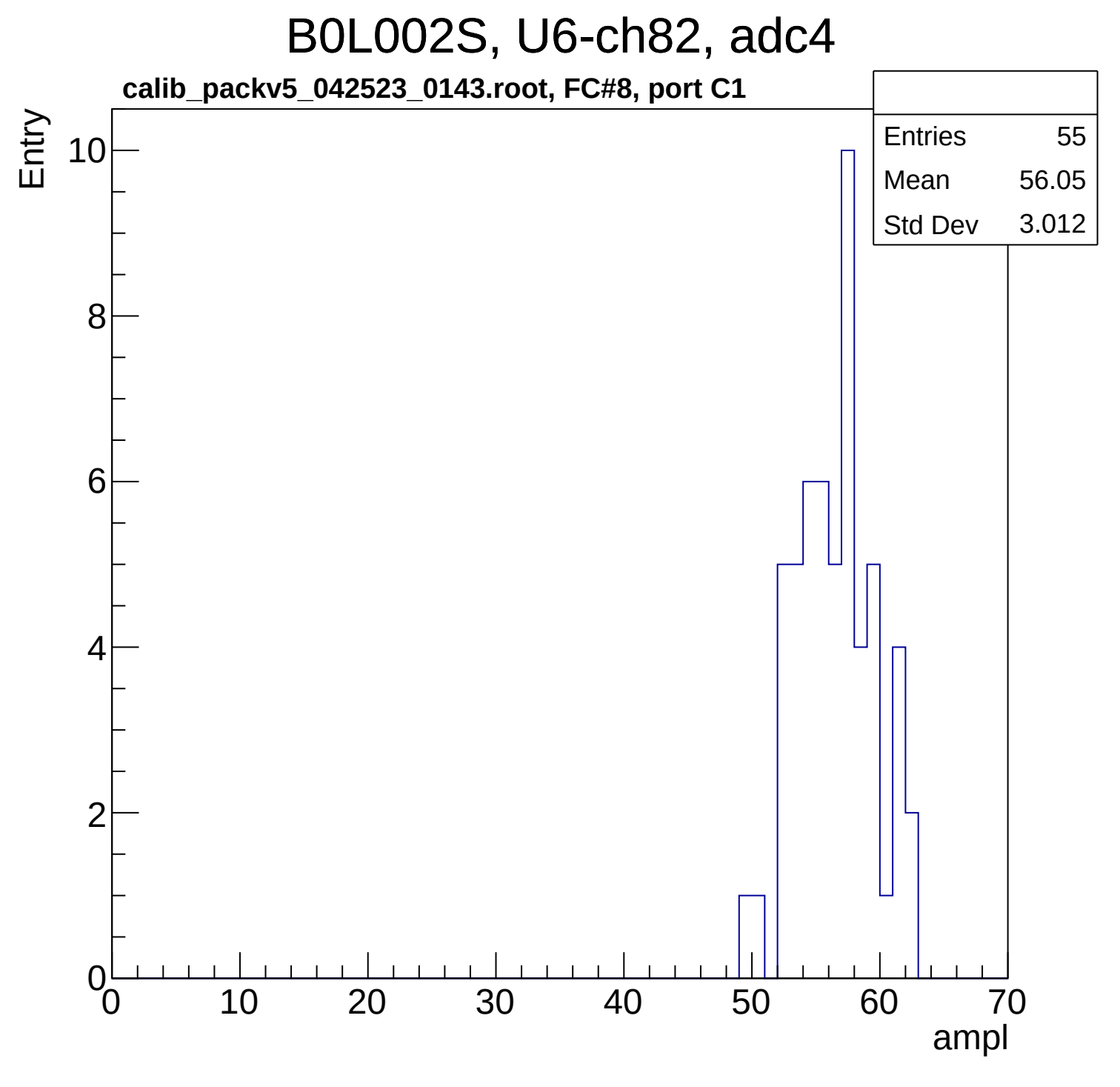
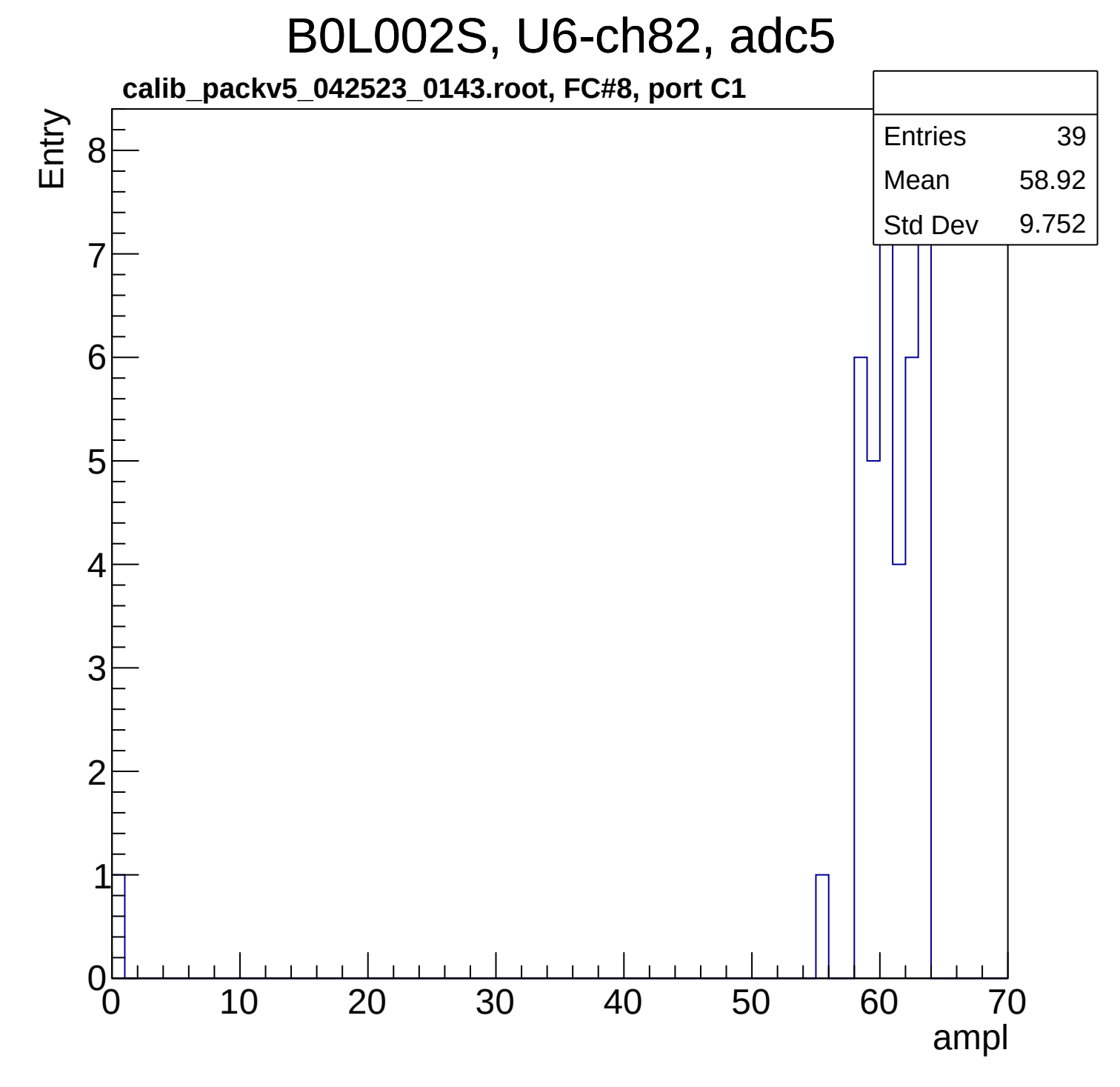
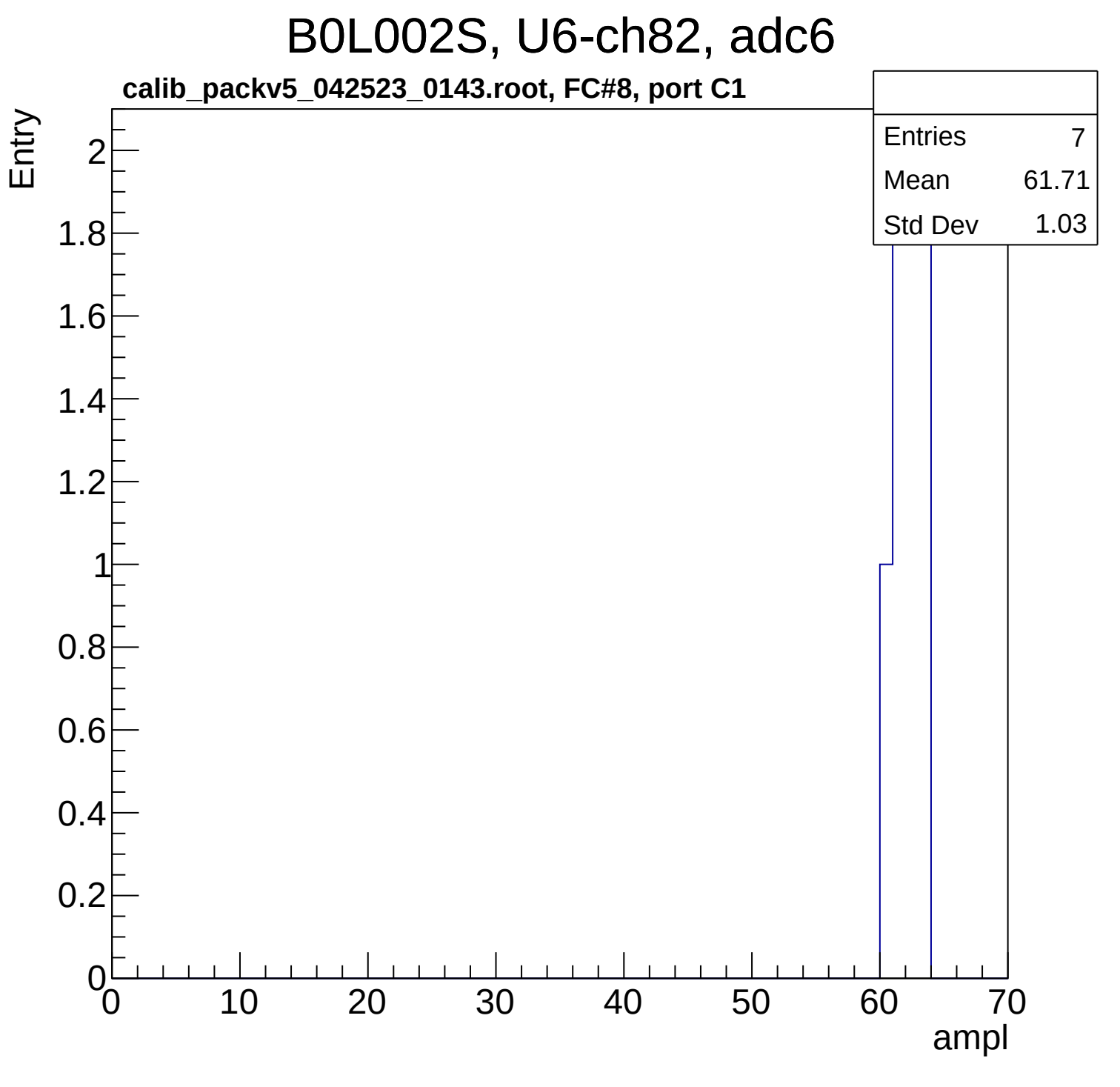

B0L002S, U6-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.3
Std Dev 3.97
Gaus mean : 36.6956
Gaus Width: 4.0718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

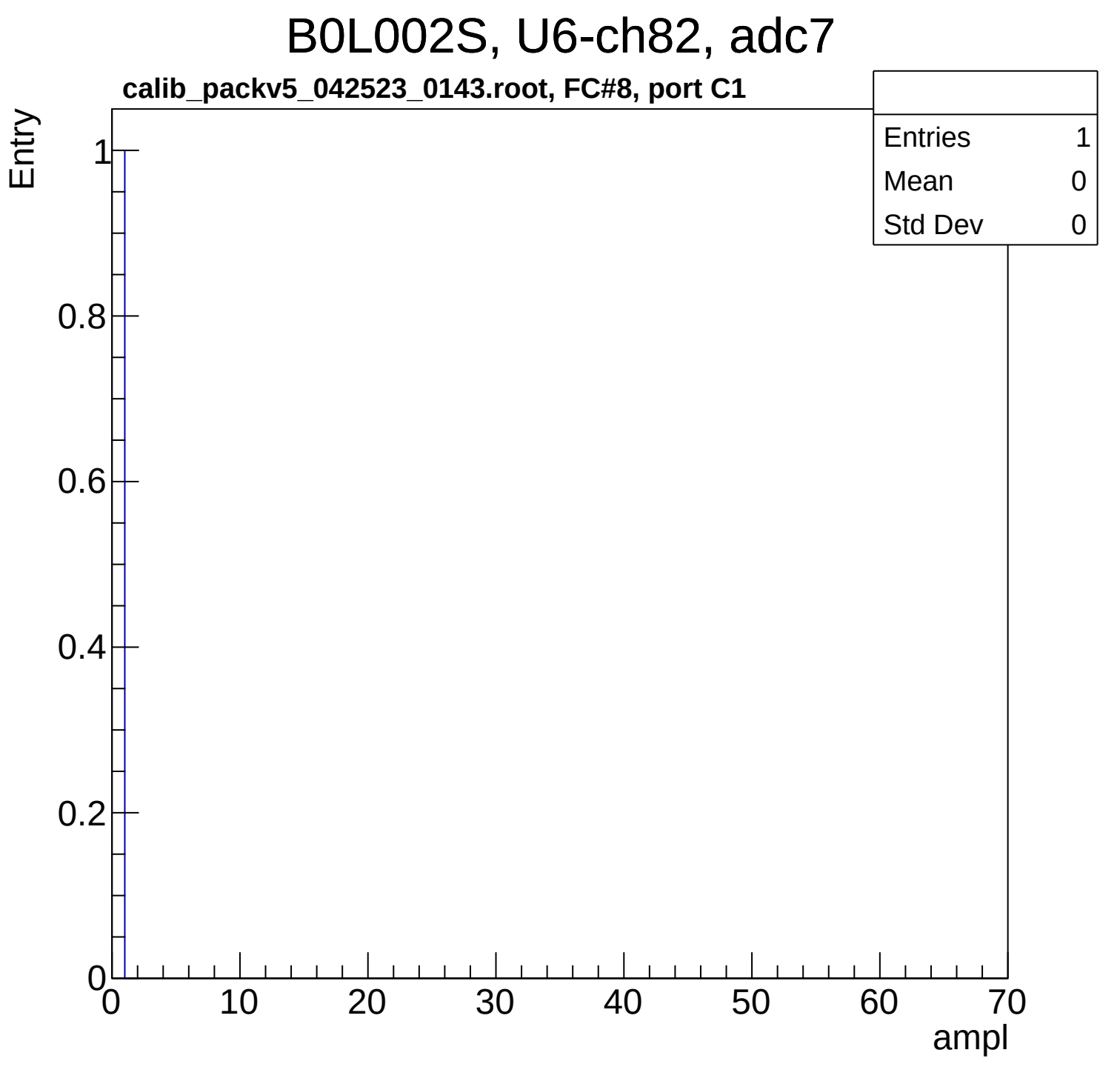

B0L002S, U6-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.86
Std Dev 3.684
Gaus mean : 43.1308
Gaus Width: 4.1534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

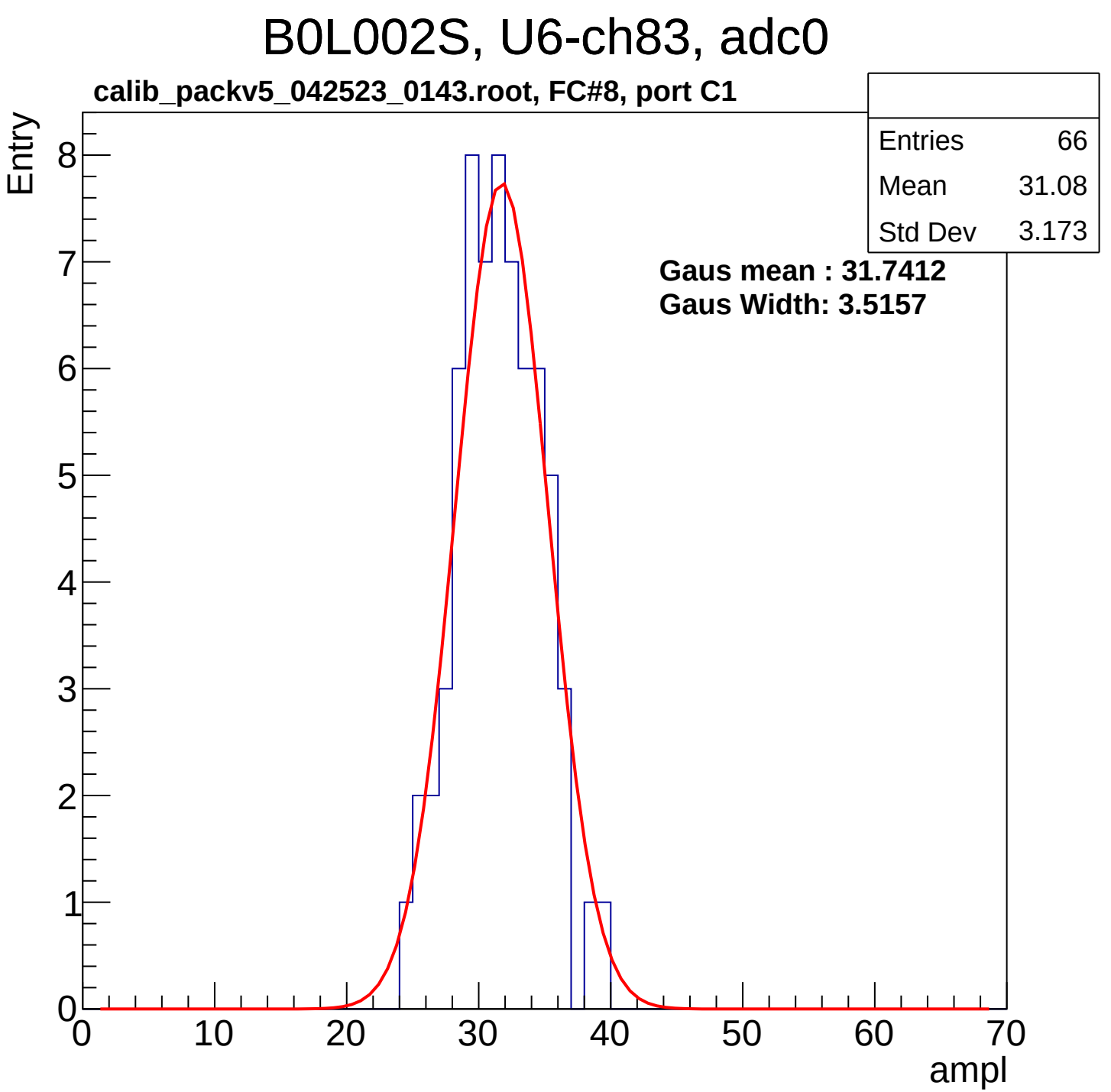

B0L002S, U6-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.13
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

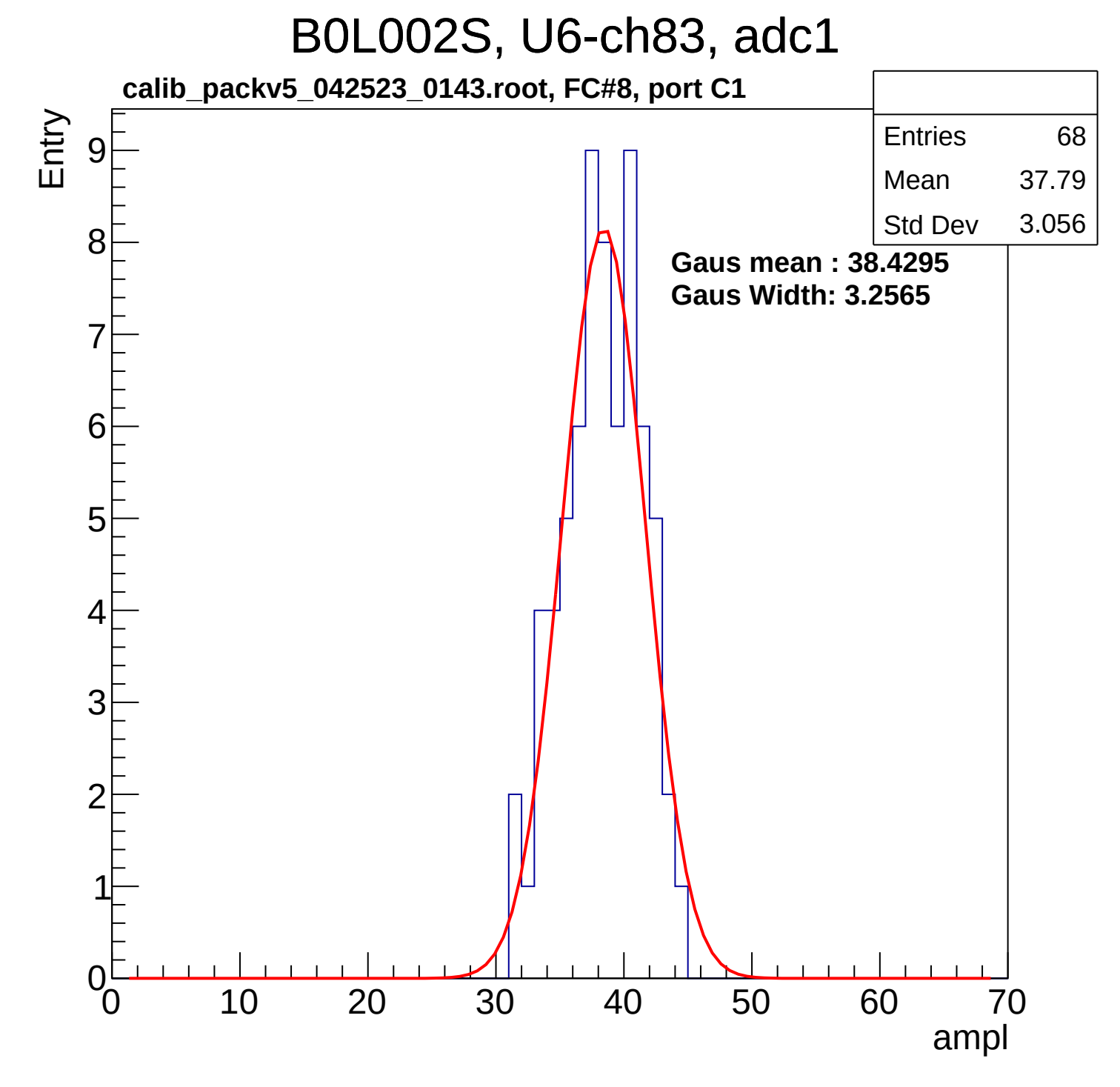

B0L002S, U6-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.05
Std Dev 3.012
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

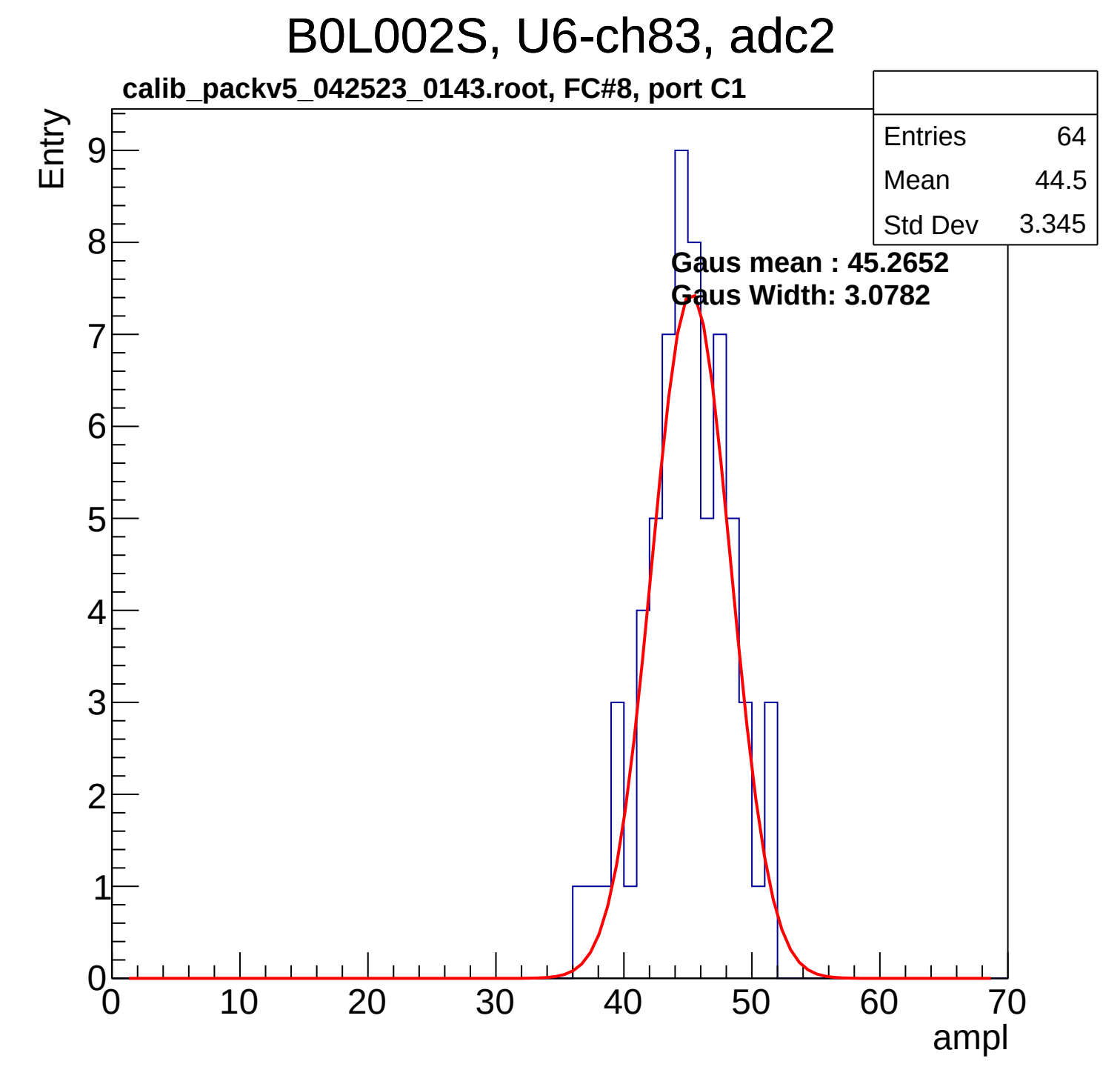

B0L002S, U6-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.92
Std Dev 9.752
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

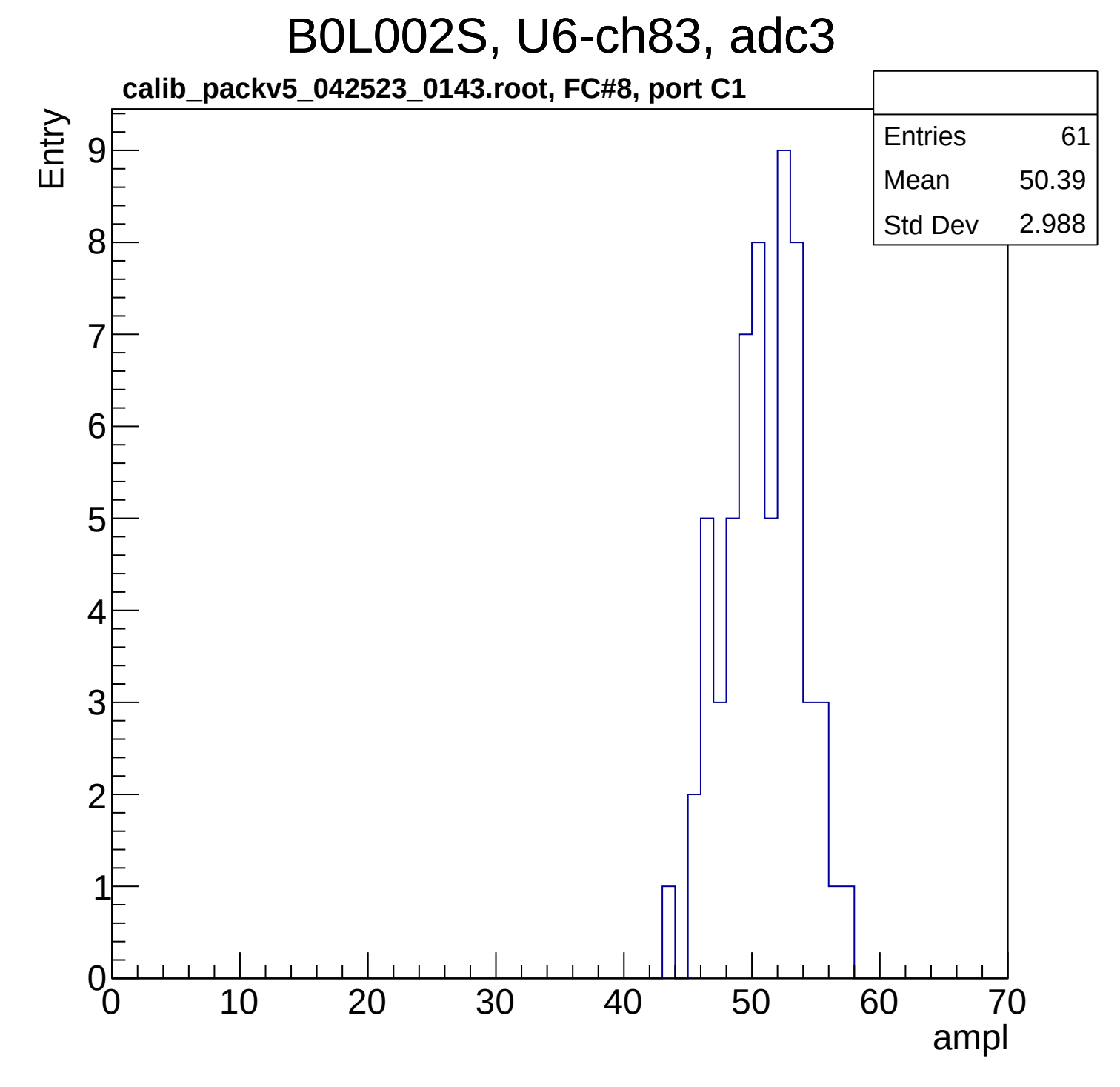

B0L002S, U6-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.08
Std Dev 3.173
Gaus mean : 31.7412
Gaus Width: 3.5157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.79
Std Dev 3.056
Gaus mean : 38.4295
Gaus Width: 3.2565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.5
Std Dev 3.345
Gaus mean : 45.2652
Gaus Width: 3.0782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.39
Std Dev 2.988
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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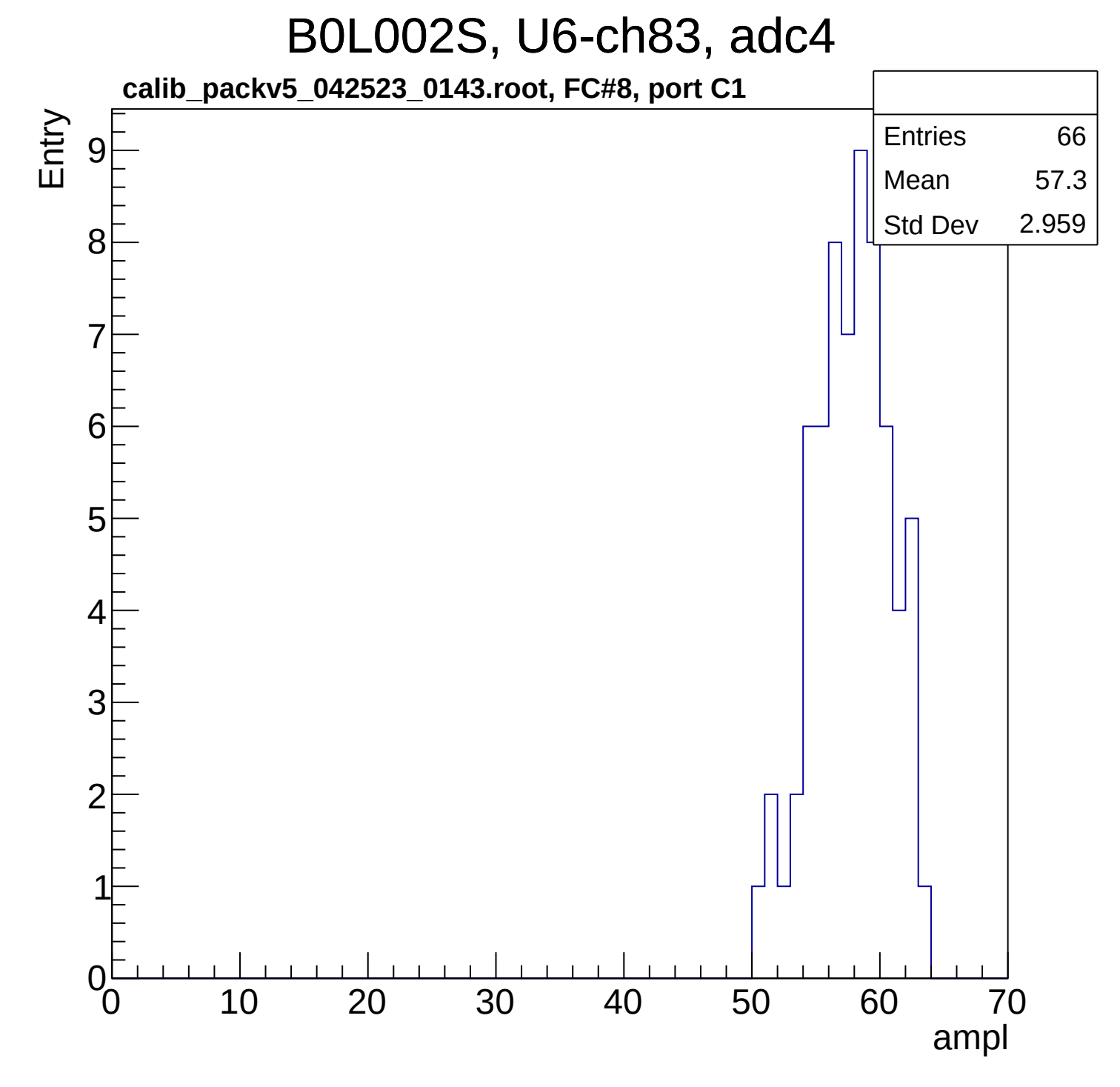
B0L002S, U6-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.3
Std Dev 2.959
Entry
ampl
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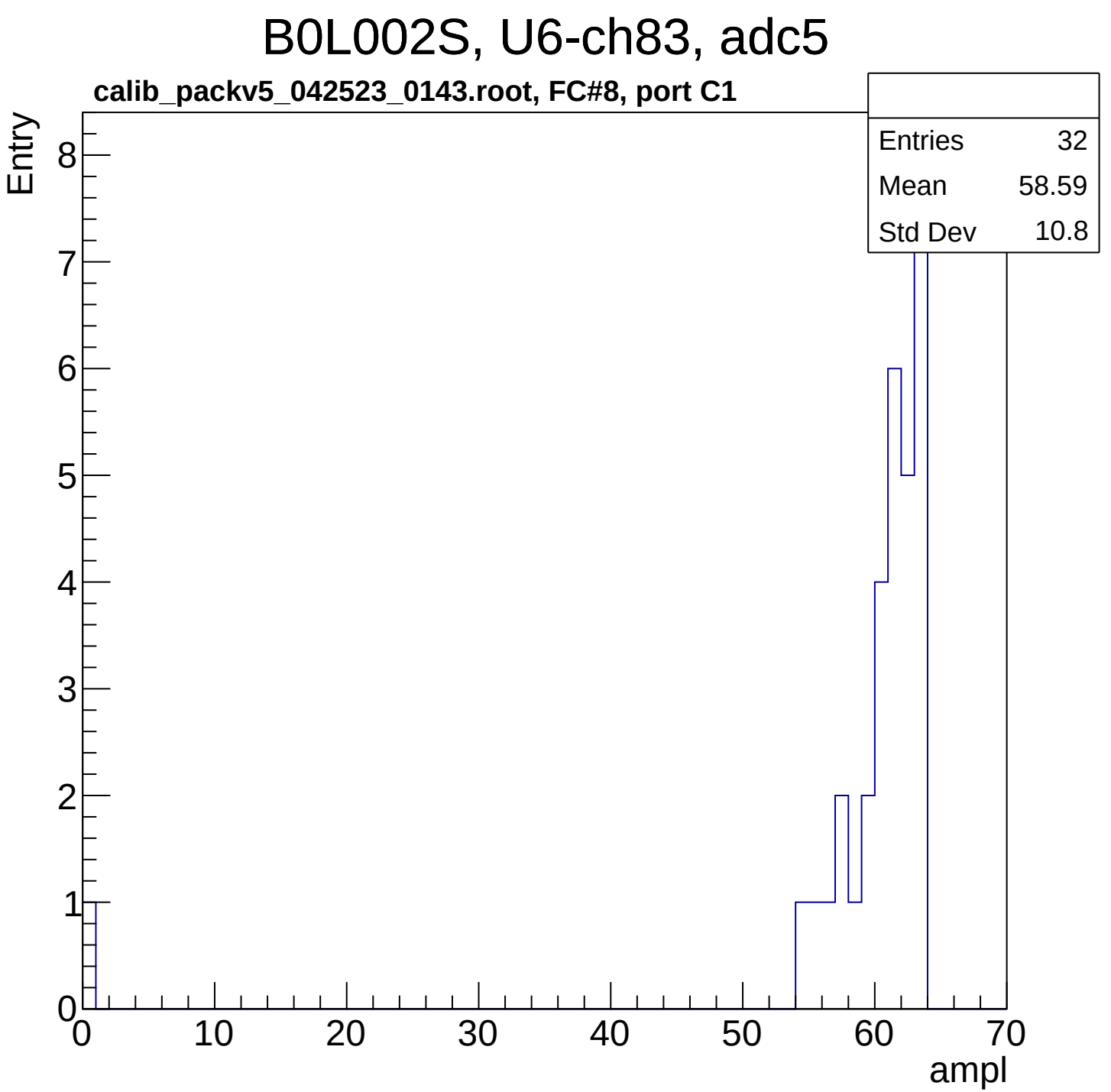
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.59
Std Dev 10.8
Entry
ampl
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1
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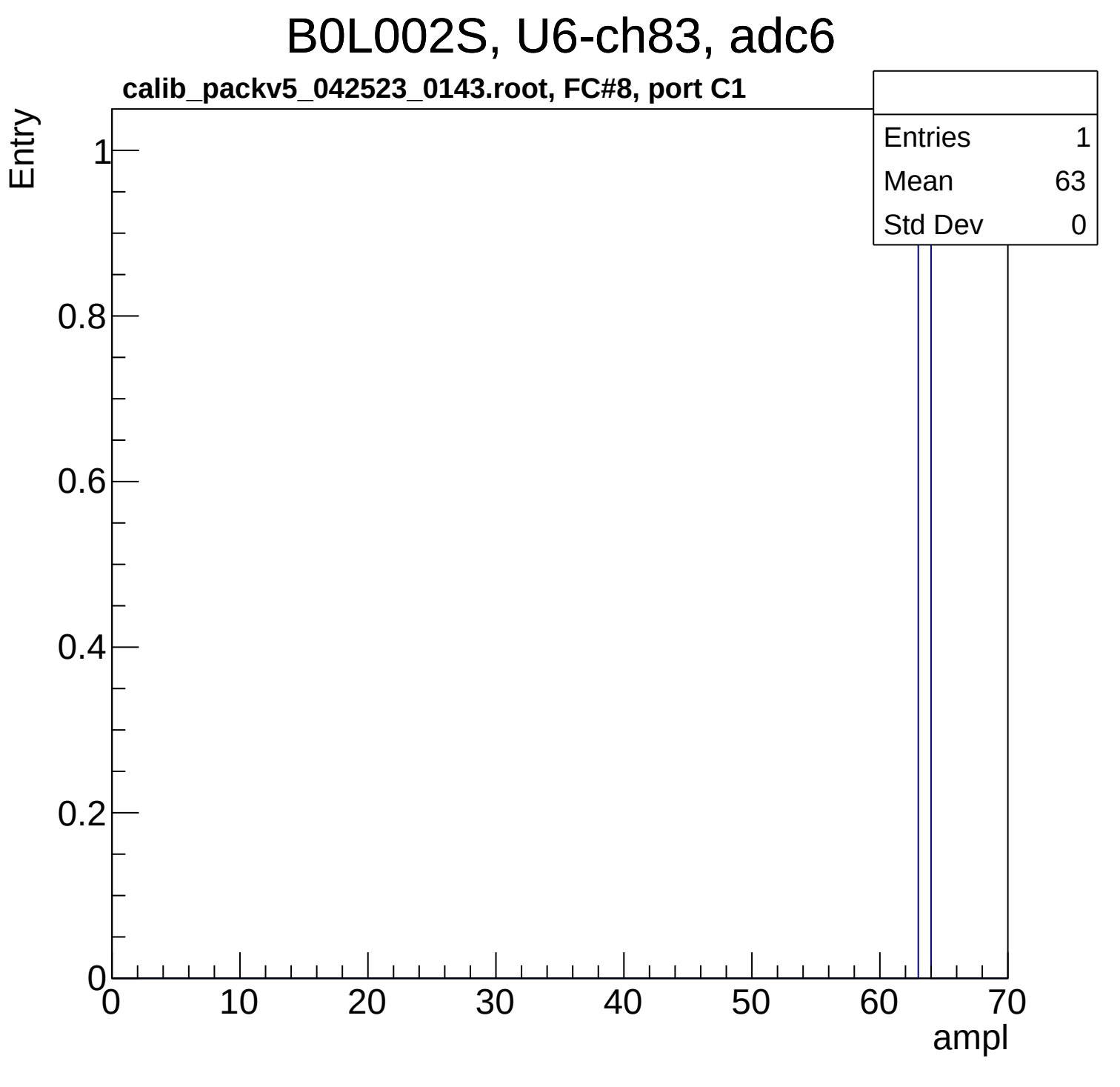
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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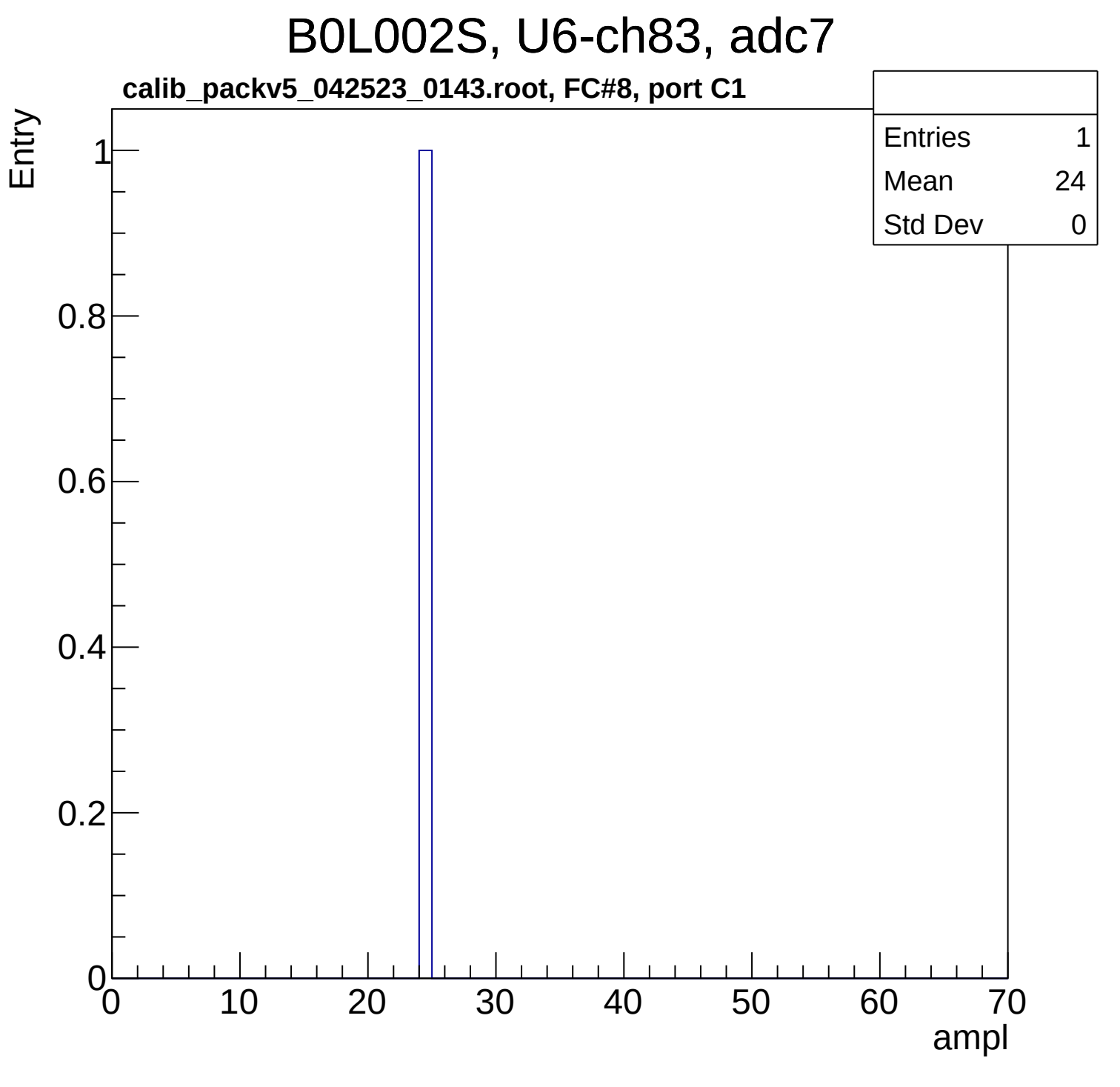
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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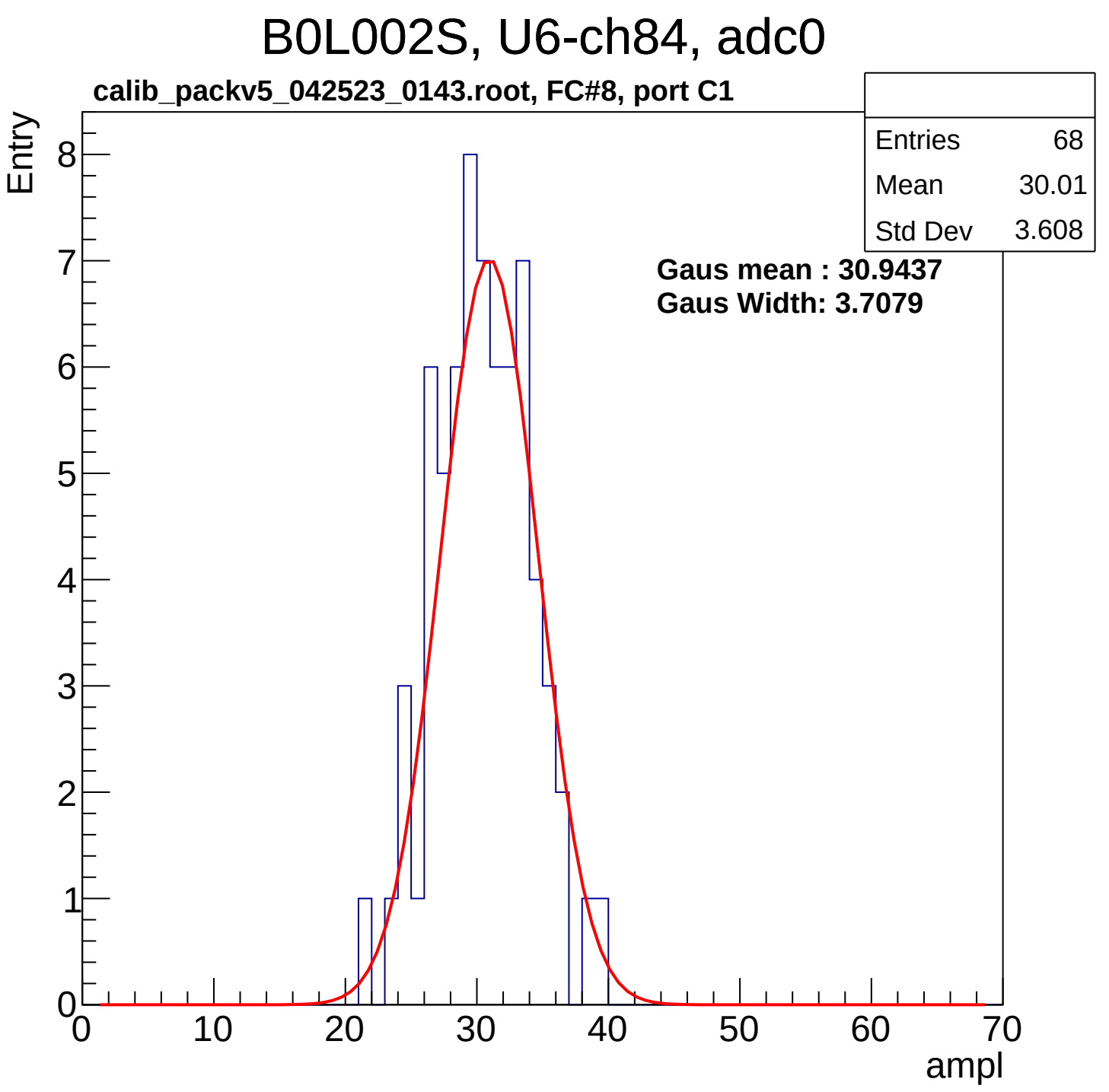
30
40
50
60
70
ampl


B0L002S, U6-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.01
Std Dev 3.608
Gaus mean : 30.9437
Gaus Width: 3.7079
Entry
ampl
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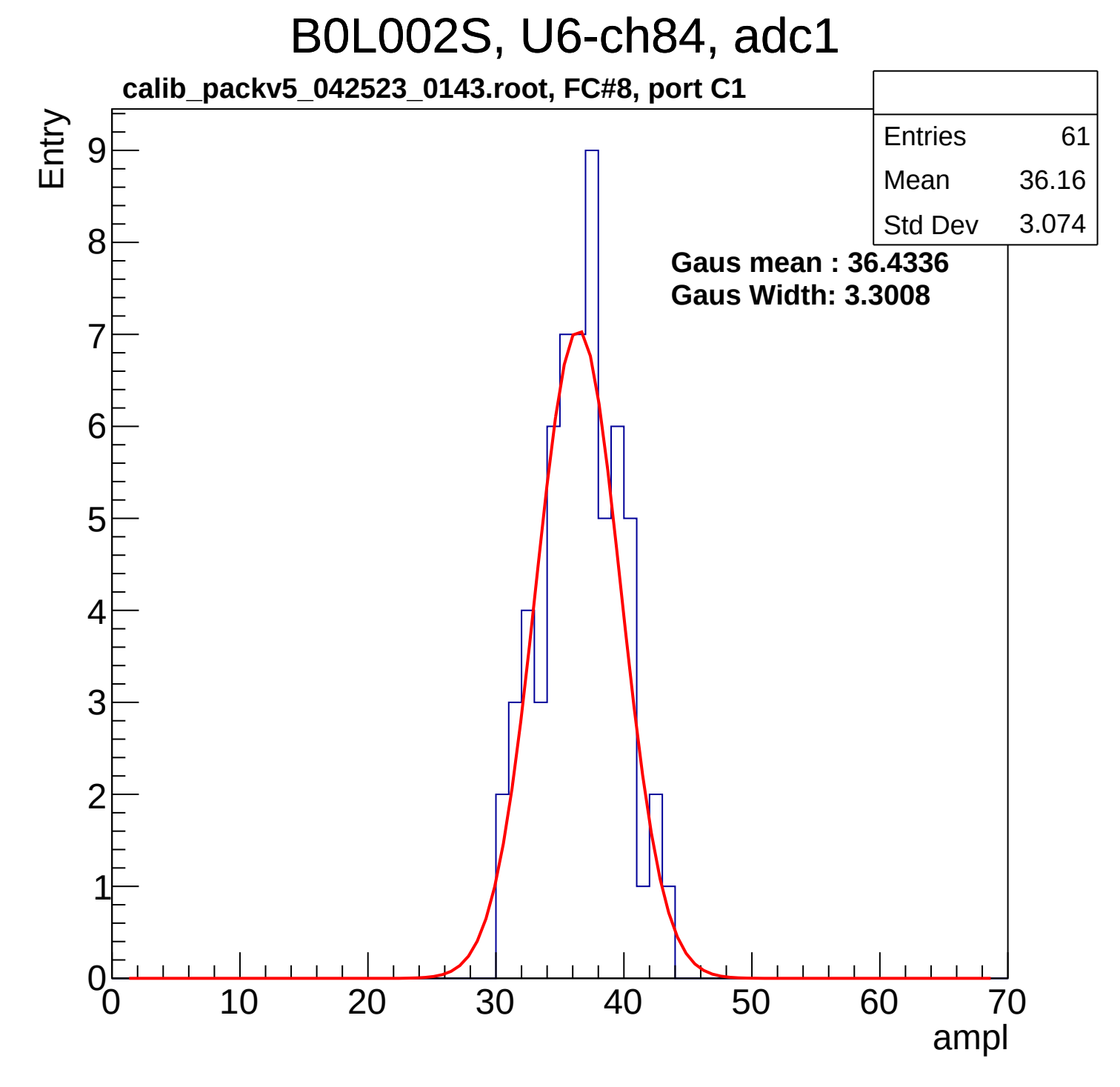
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.16
Std Dev 3.074
Gaus mean : 36.4336
Gaus Width: 3.3008
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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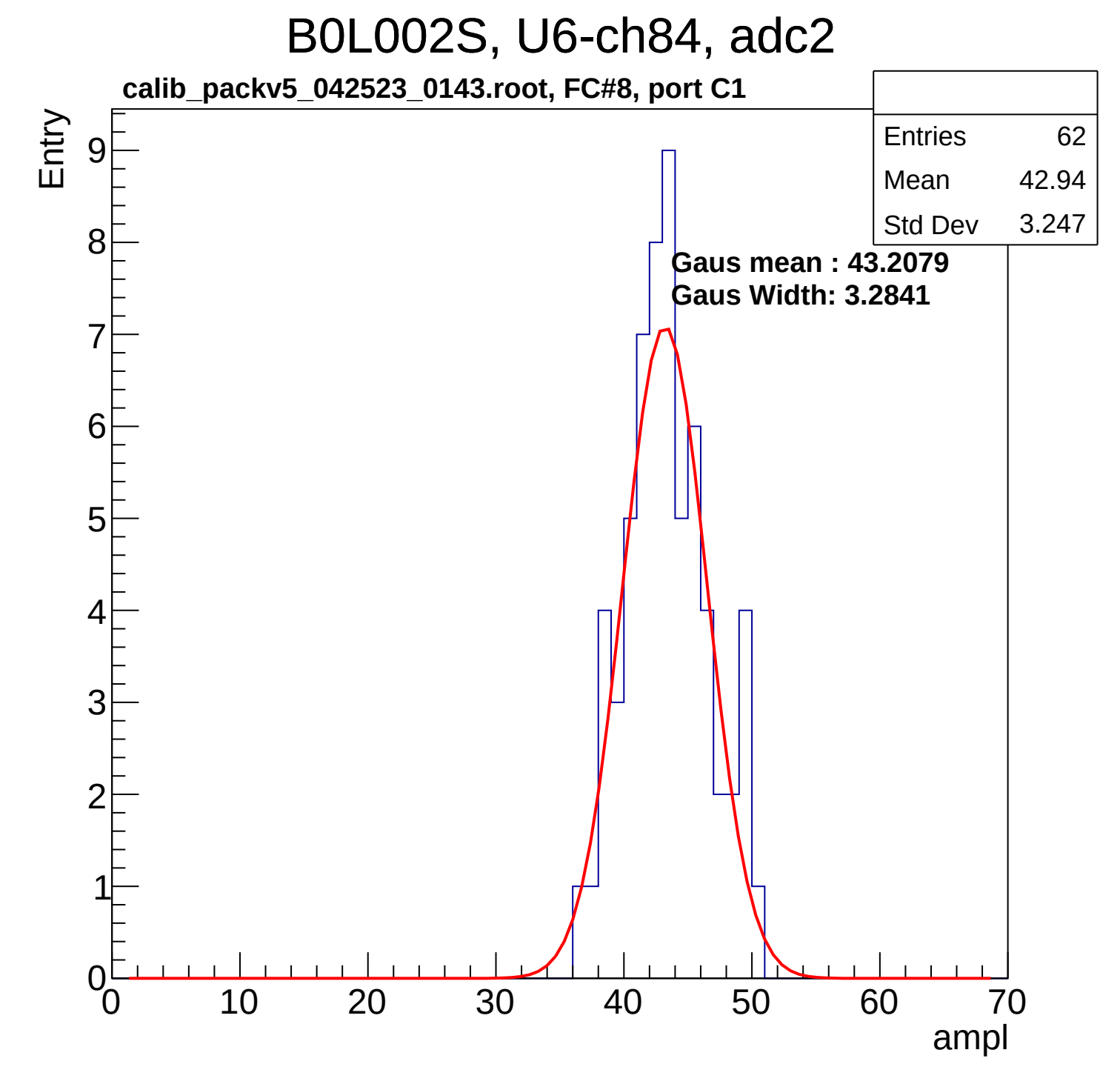
B0L002S, U6-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 42.94
Std Dev 3.247
Gaus mean : 43.2079
Gaus Width: 3.2841
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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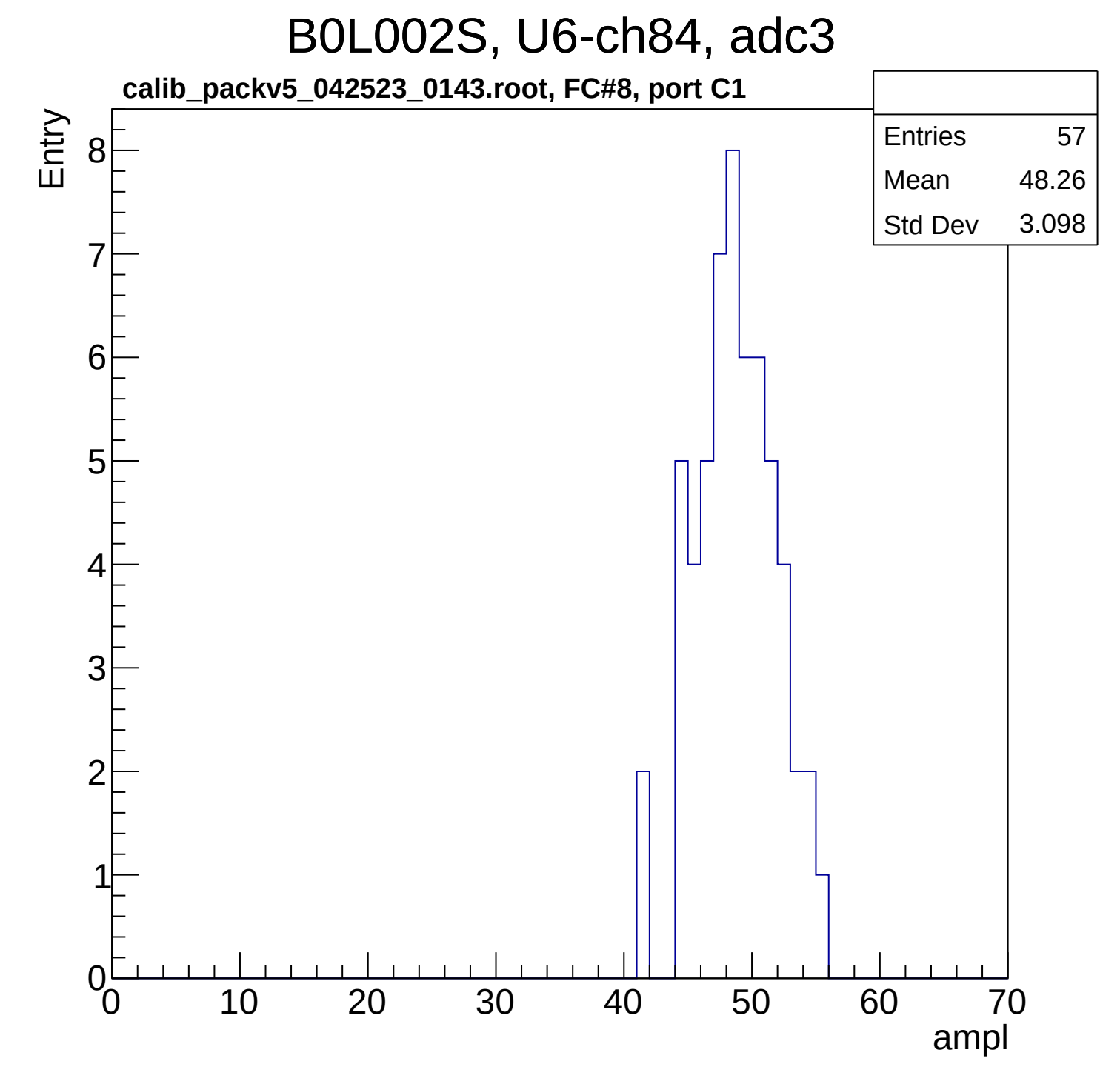
B0L002S, U6-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 48.26
Std Dev 3.098
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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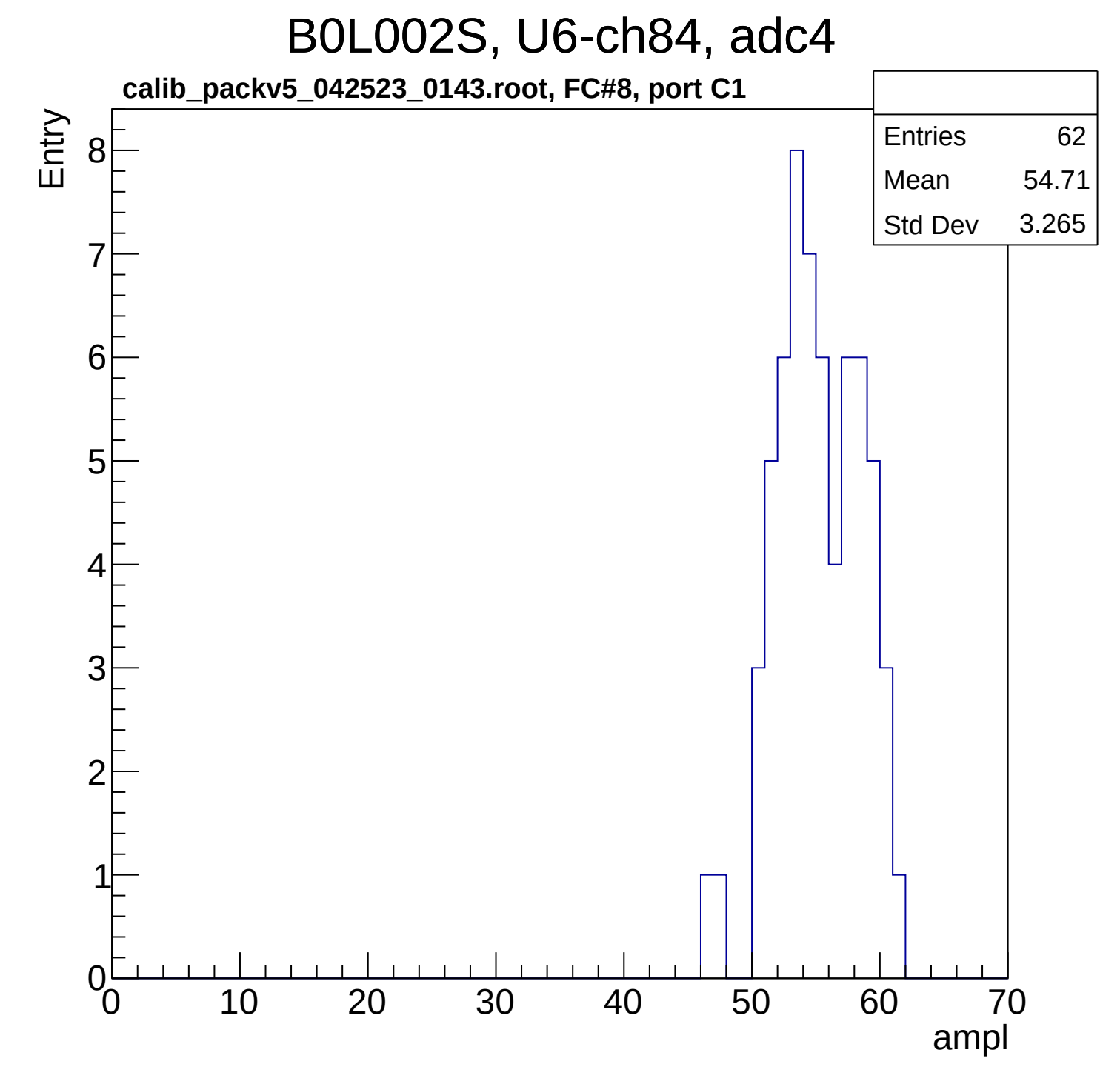
B0L002S, U6-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 54.71
Std Dev 3.265
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
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60
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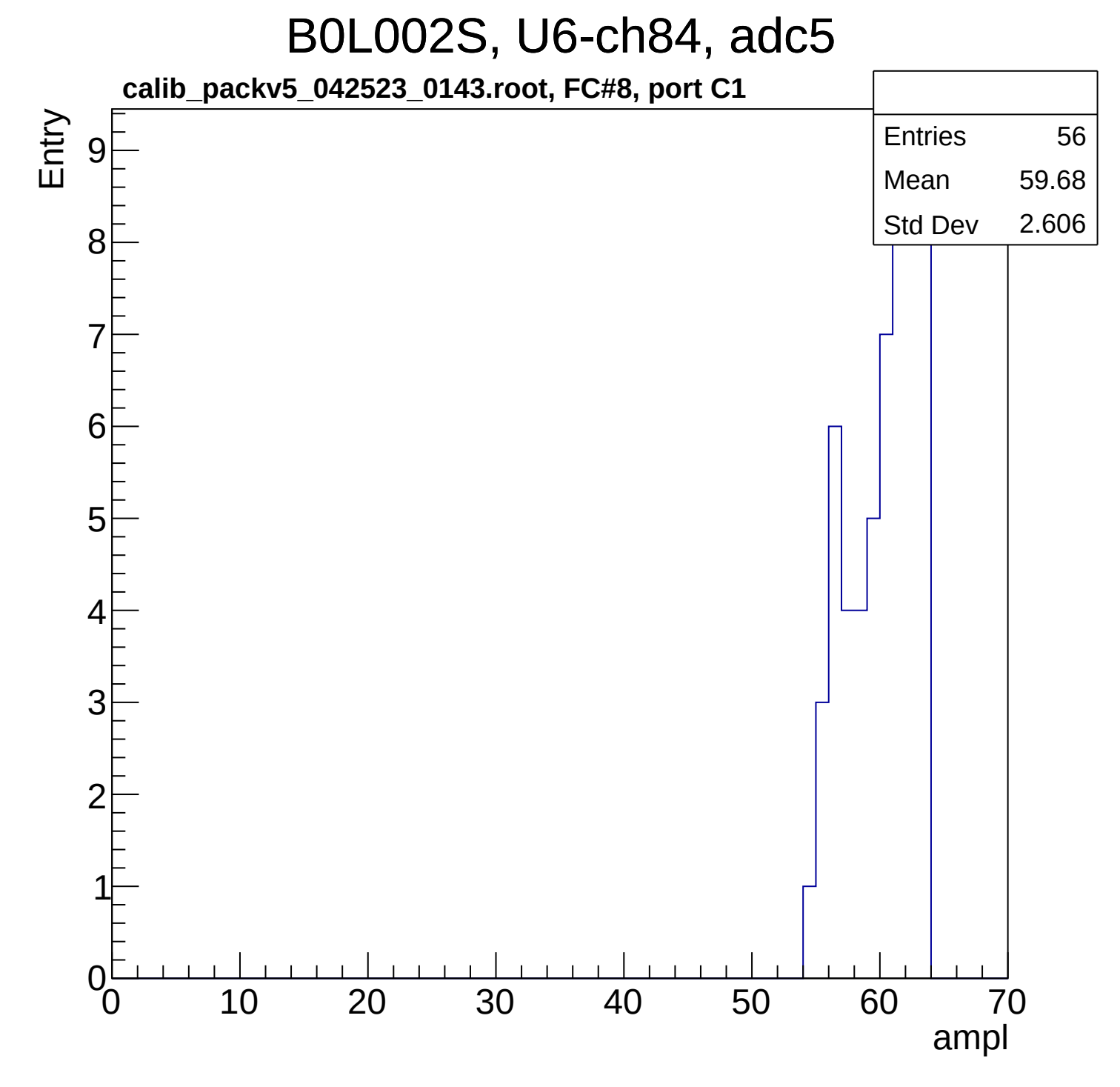
B0L002S, U6-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 59.68
Std Dev 2.606
Entry
ampl
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2
3
4
5
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7
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0
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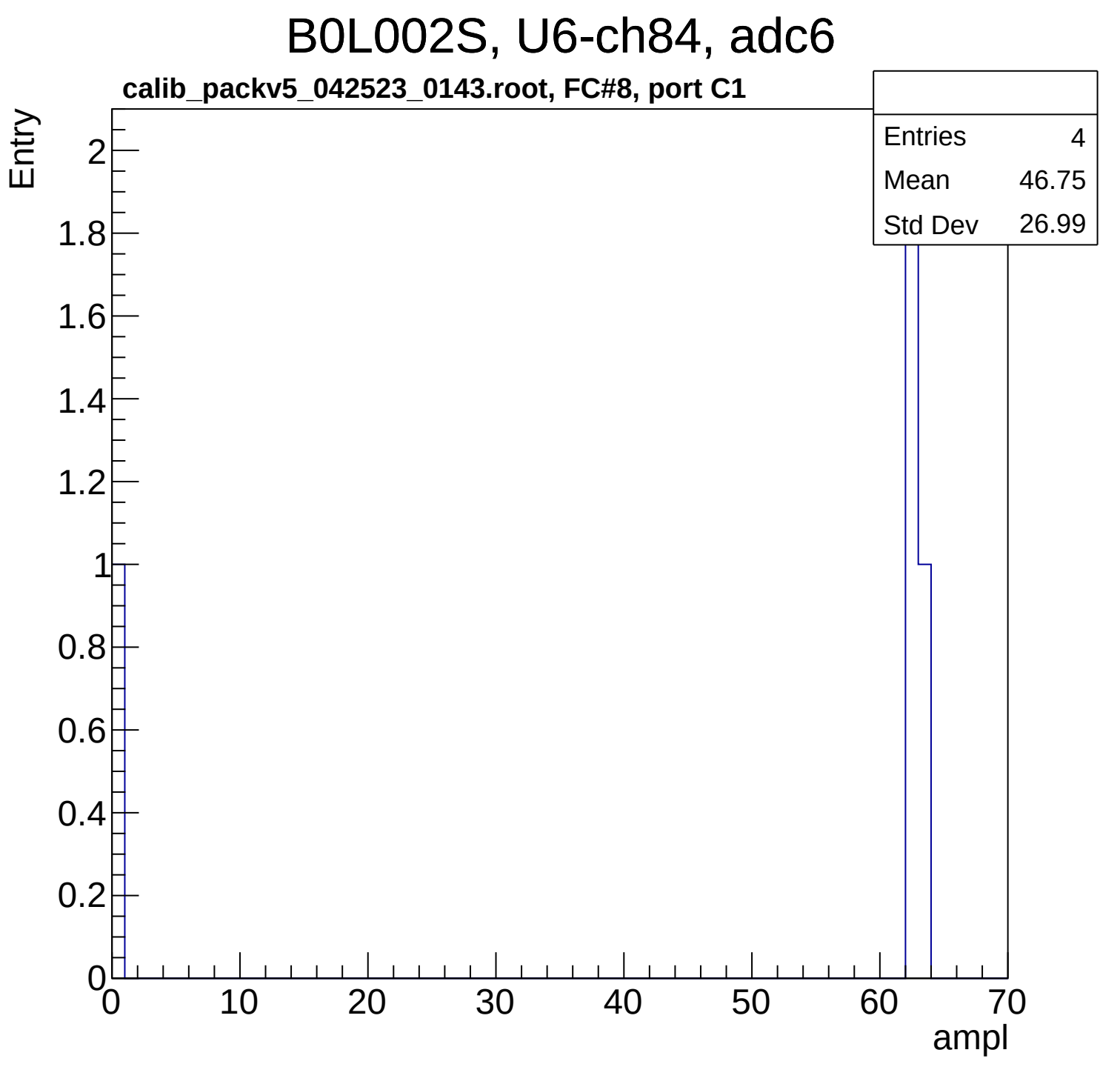
B0L002S, U6-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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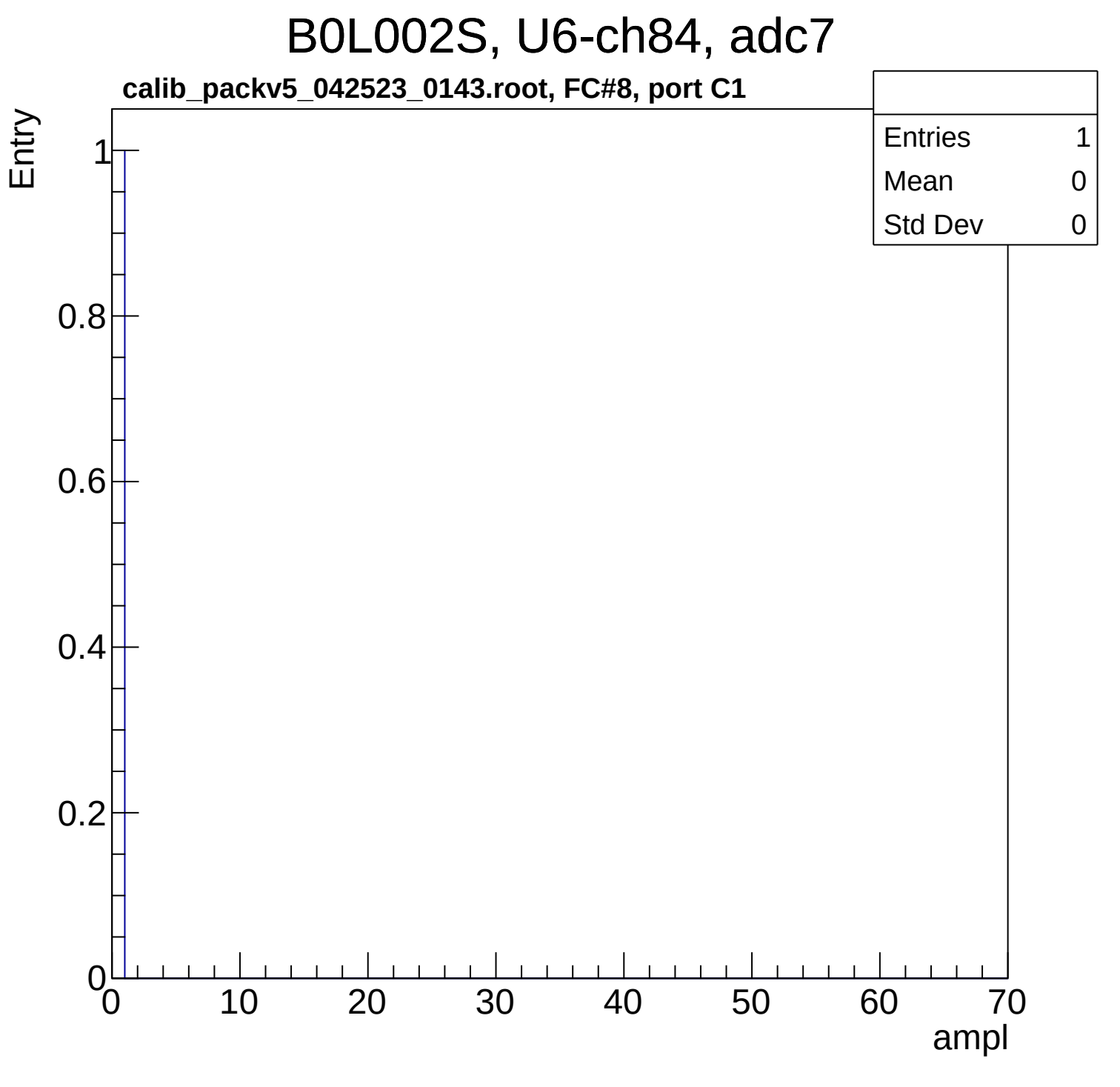
B0L002S, U6-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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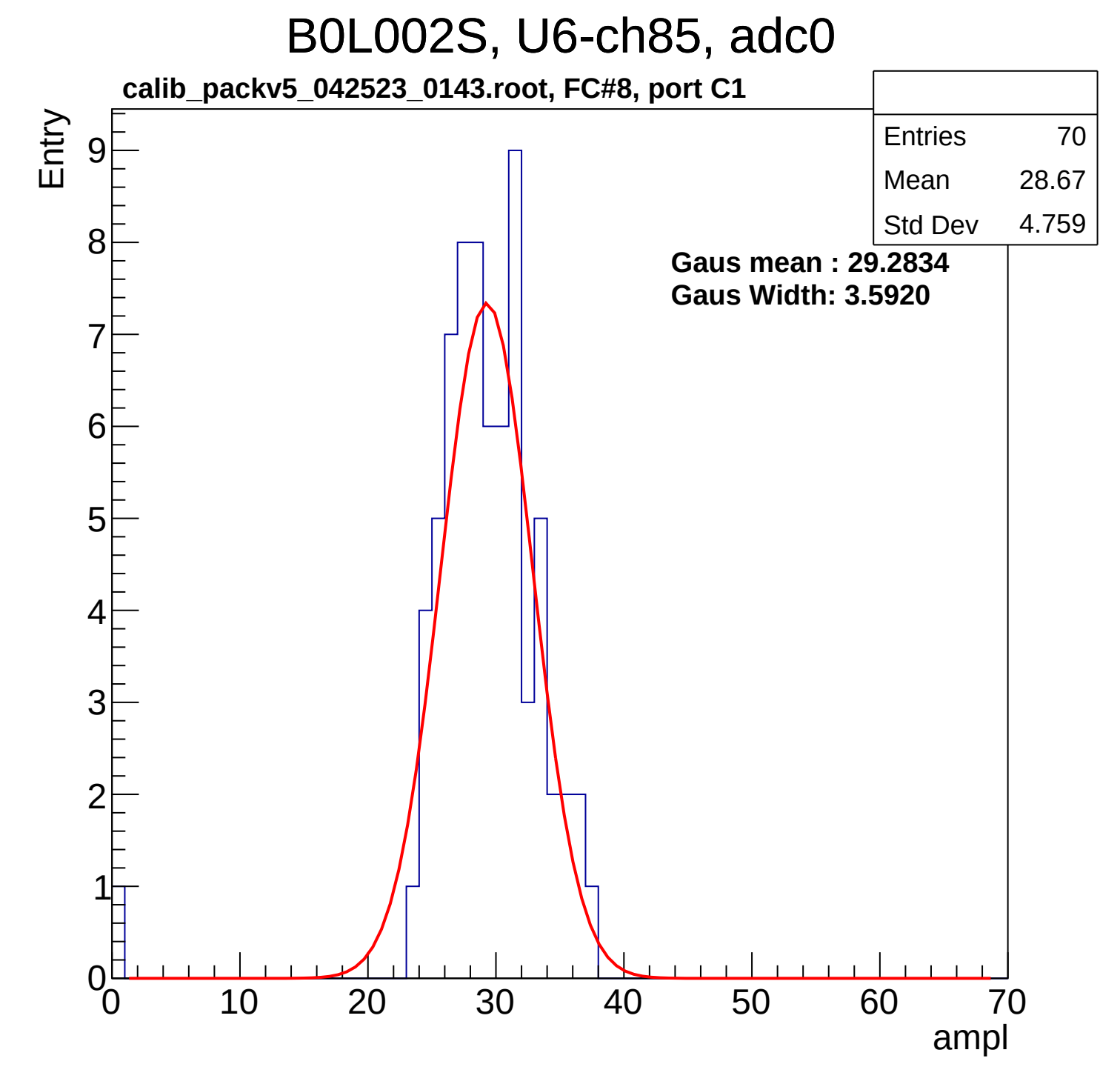
40
50
60
70
ampl


B0L002S, U6-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 28.67
Std Dev 4.759
Gaus mean : 29.2834
Gaus Width: 3.5920
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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40
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60
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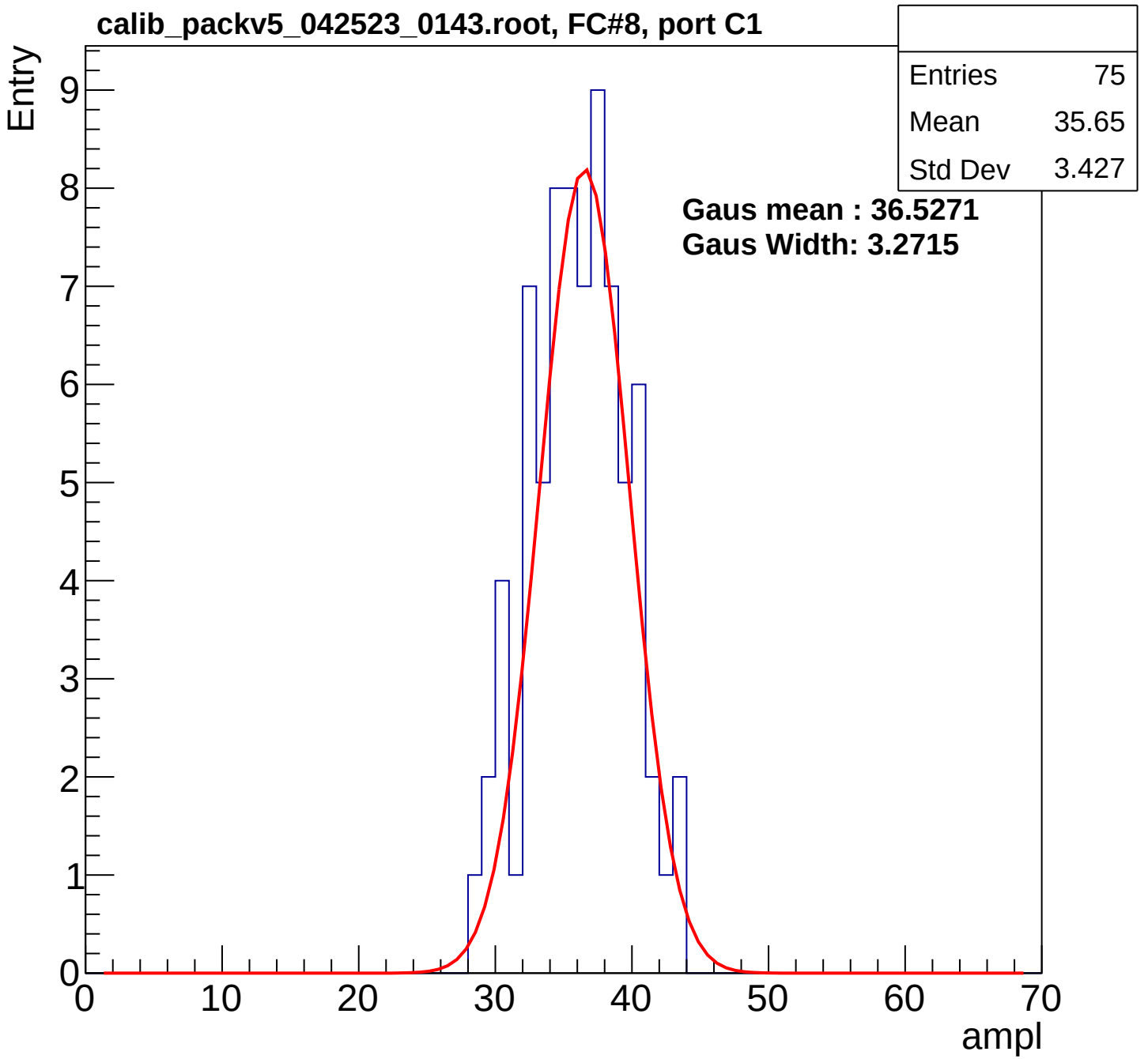
B0L002S, U6-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 35.65
Std Dev 3.427
Gaus mean : 36.5271
Gaus Width: 3.2715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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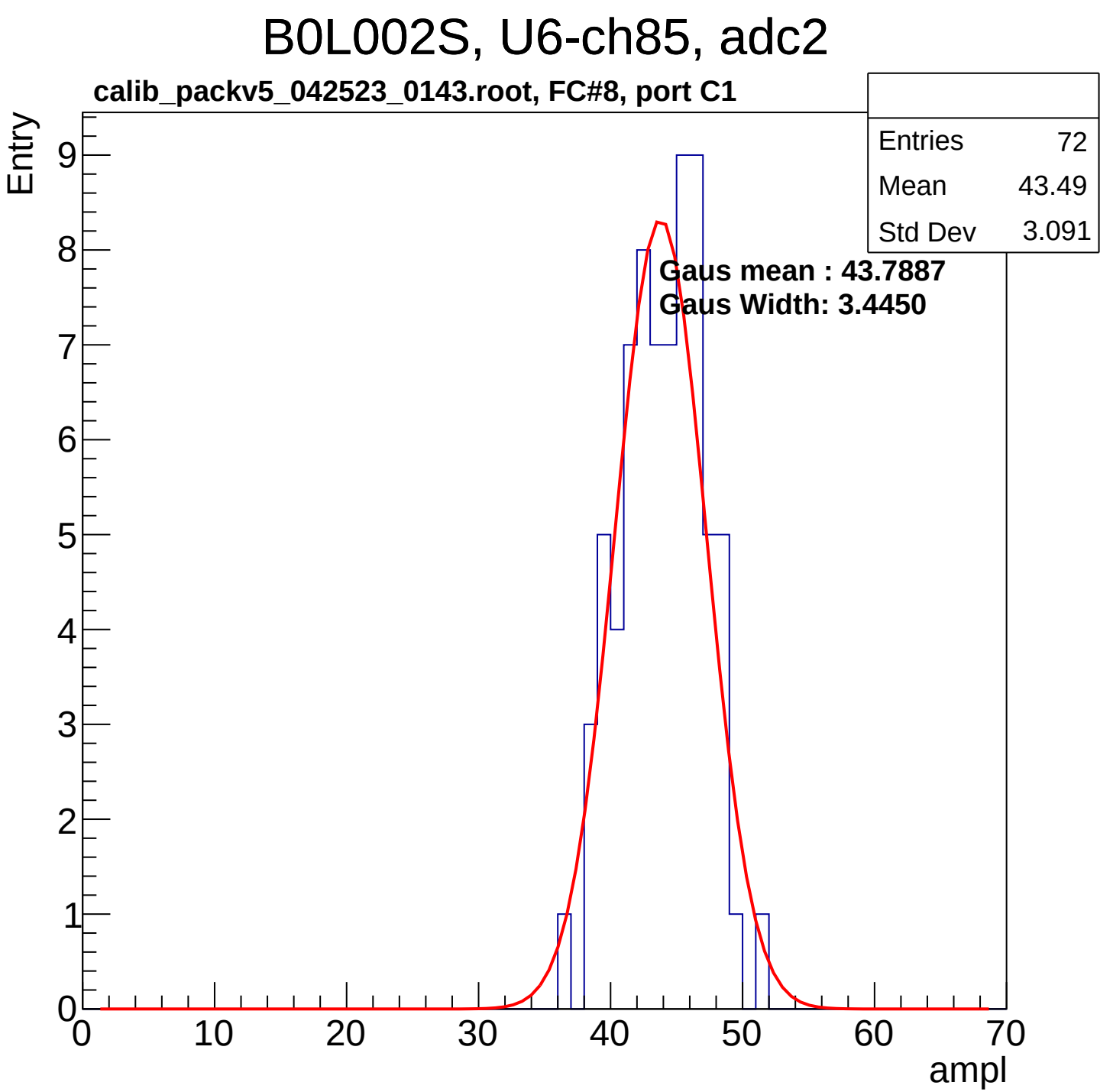
B0L002S, U6-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.49
Std Dev 3.091
Gaus mean : 43.7887
Gaus Width: 3.4450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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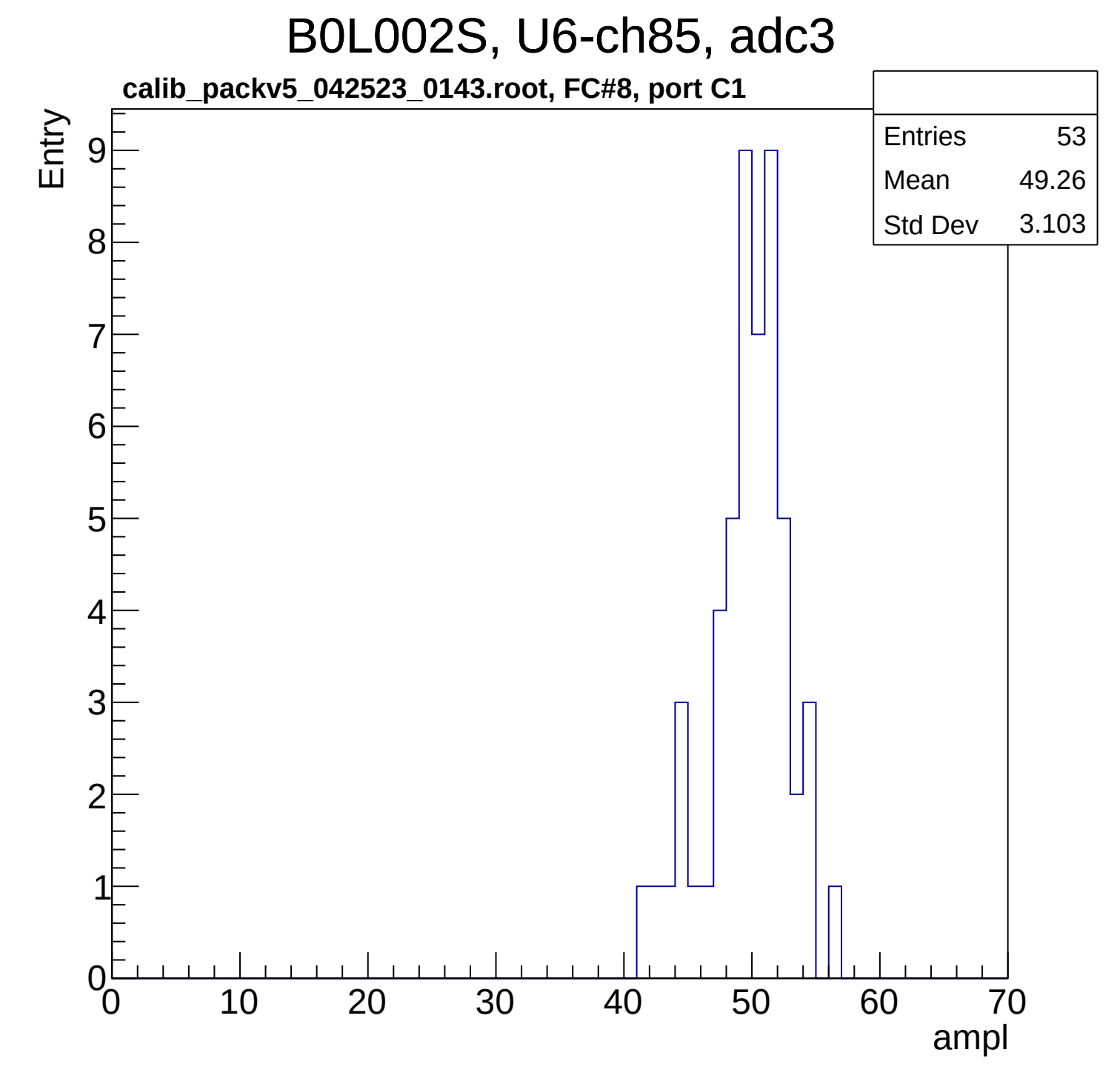
B0L002S, U6-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 49.26
Std Dev 3.103
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
50
60
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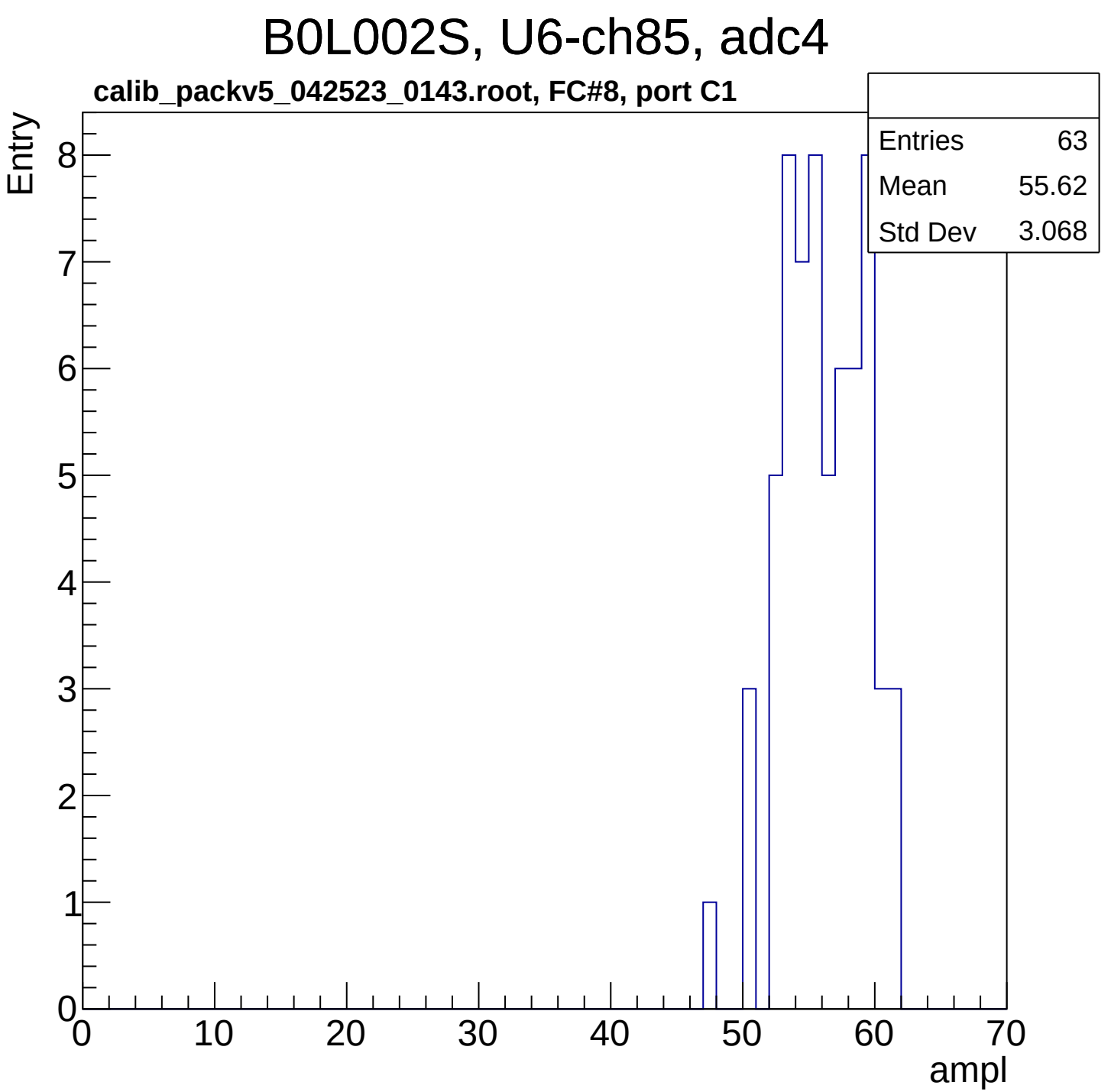
B0L002S, U6-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.62
Std Dev 3.068
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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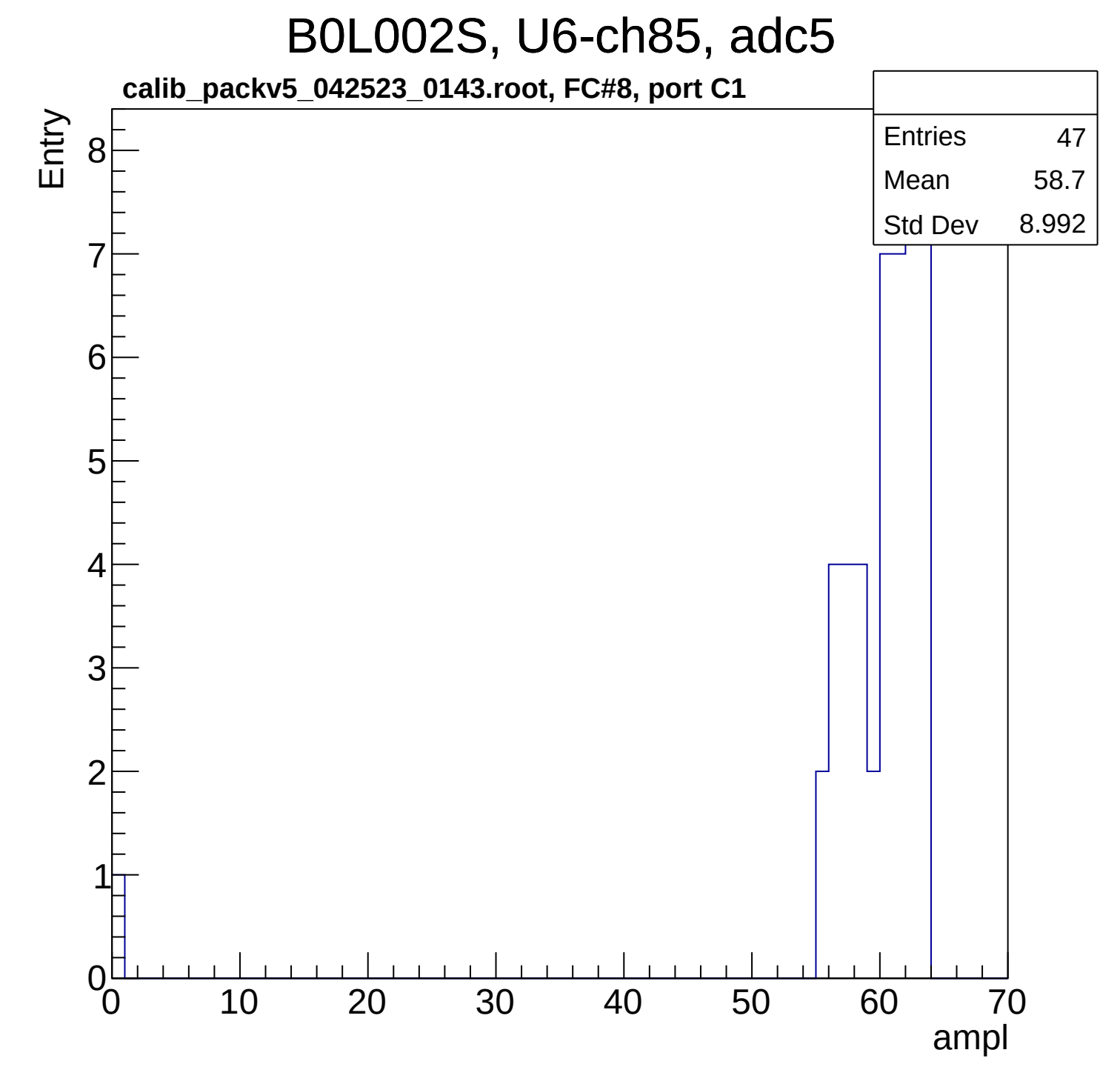
B0L002S, U6-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.7
Std Dev 8.992
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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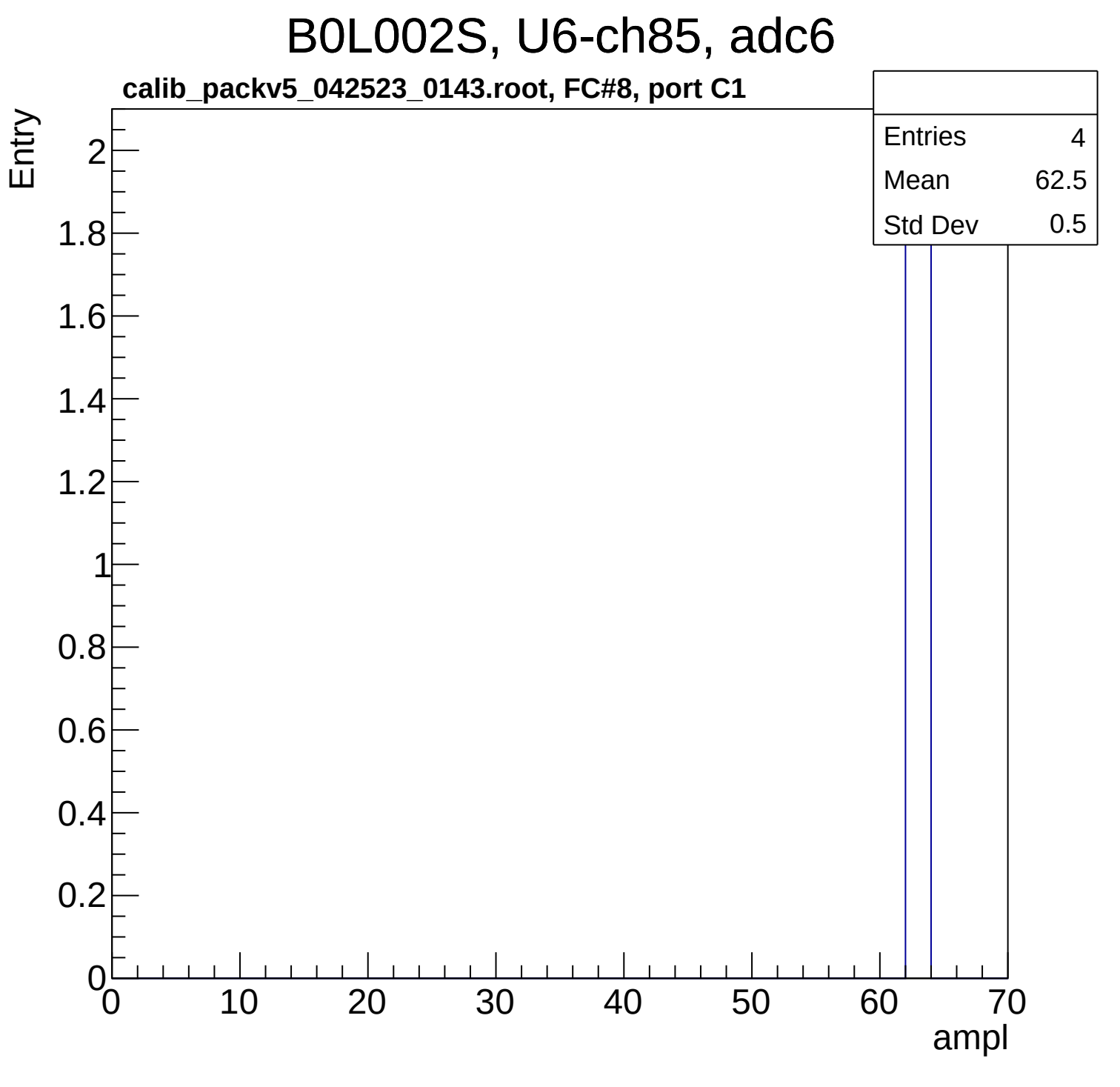
B0L002S, U6-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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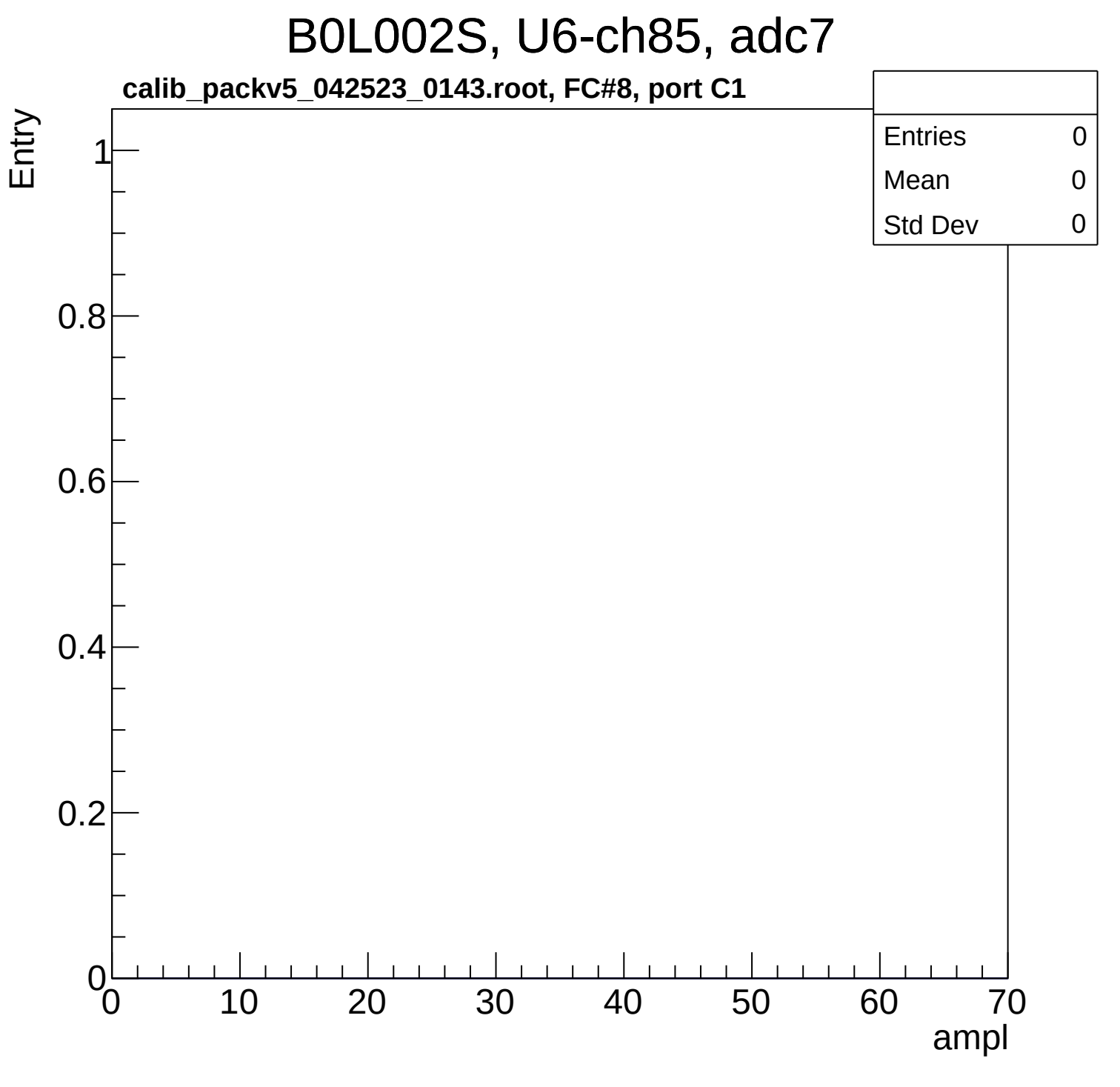
B0L002S, U6-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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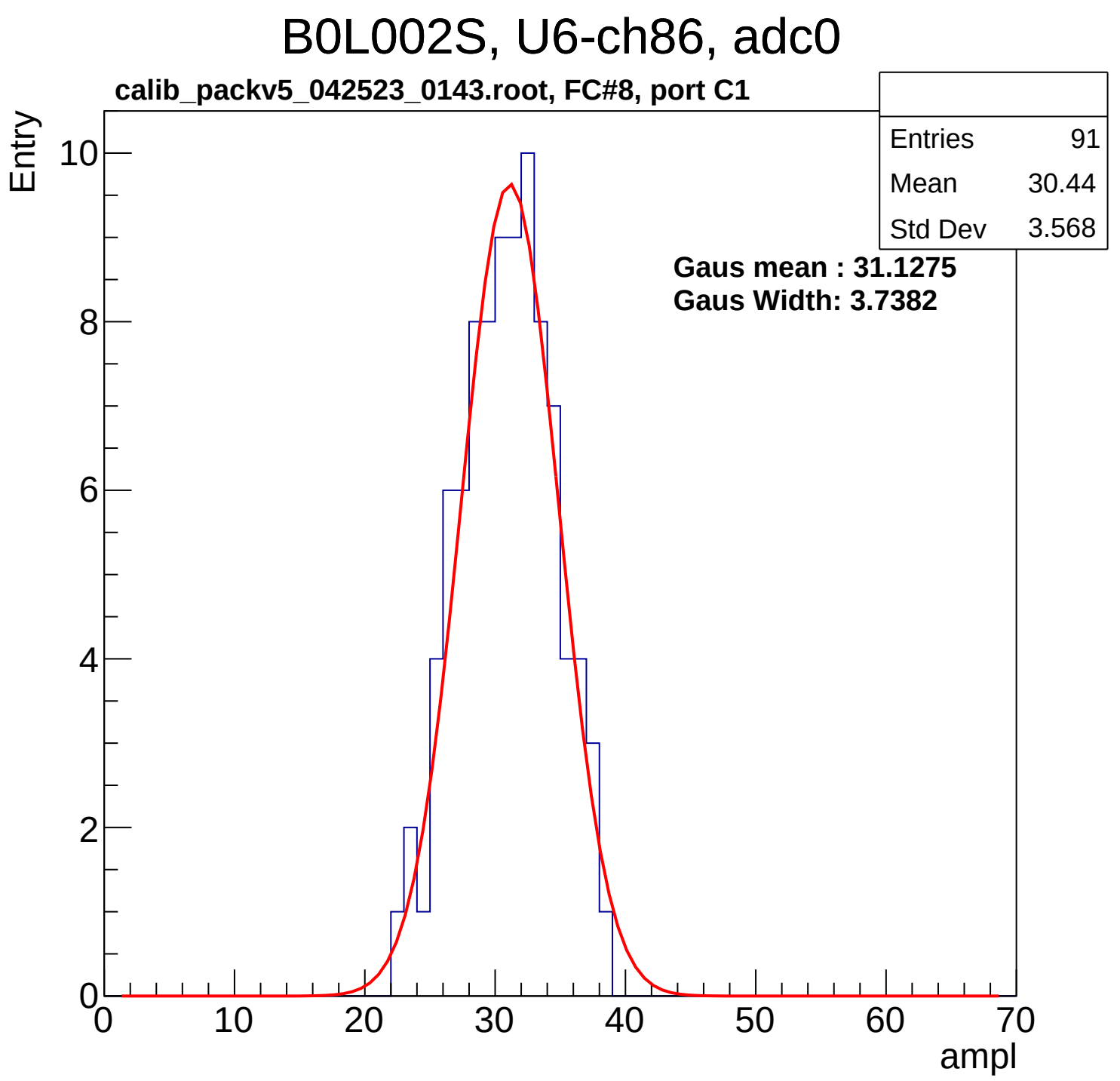
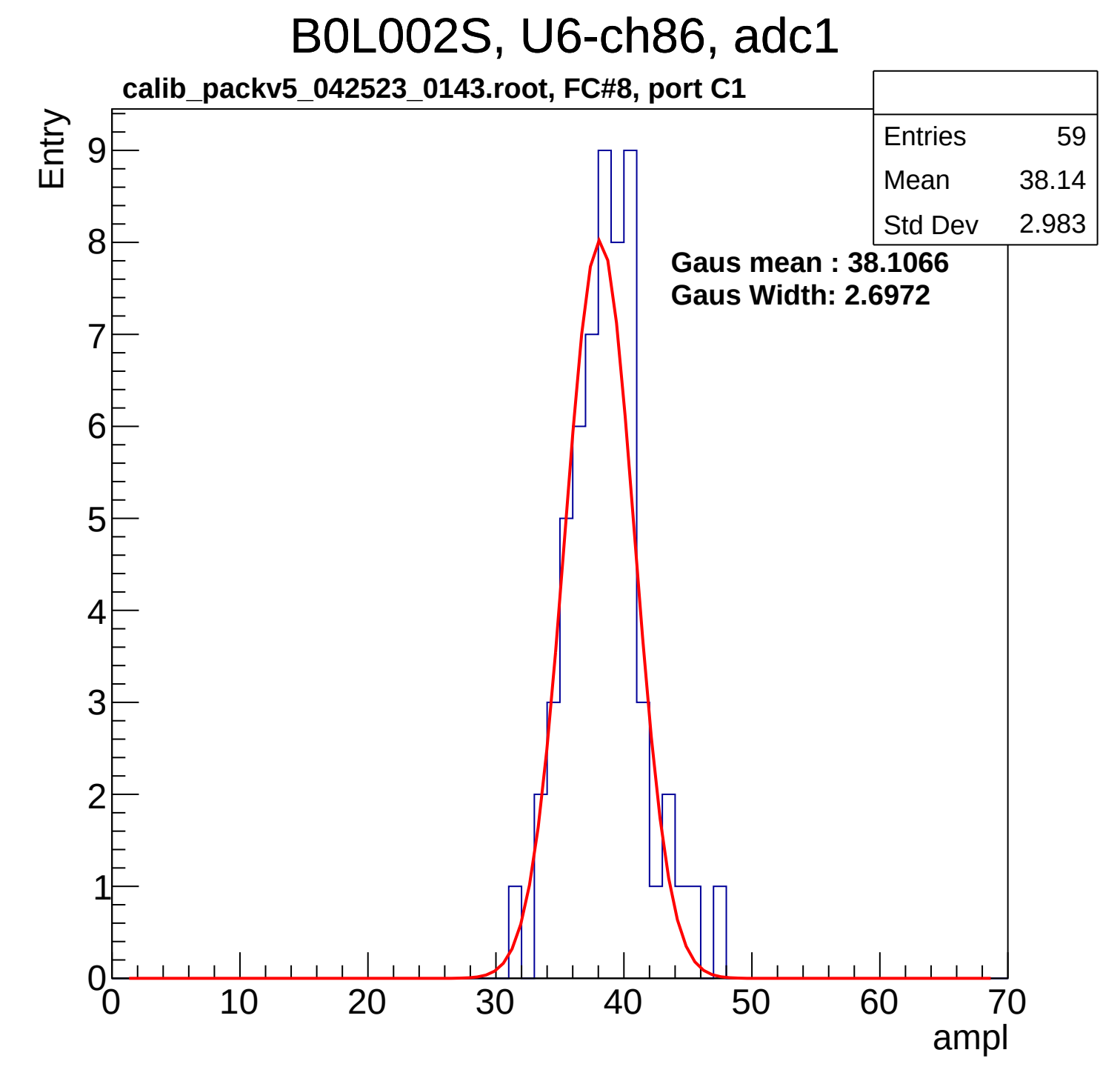
40
50
60
70
ampl


B0L002S, U6-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 30.44
Std Dev 3.568
Gaus mean : 31.1275
Gaus Width: 3.7382
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 38.14
Std Dev 2.983
Gaus mean : 38.1066
Gaus Width: 2.6972
Entry
ampl
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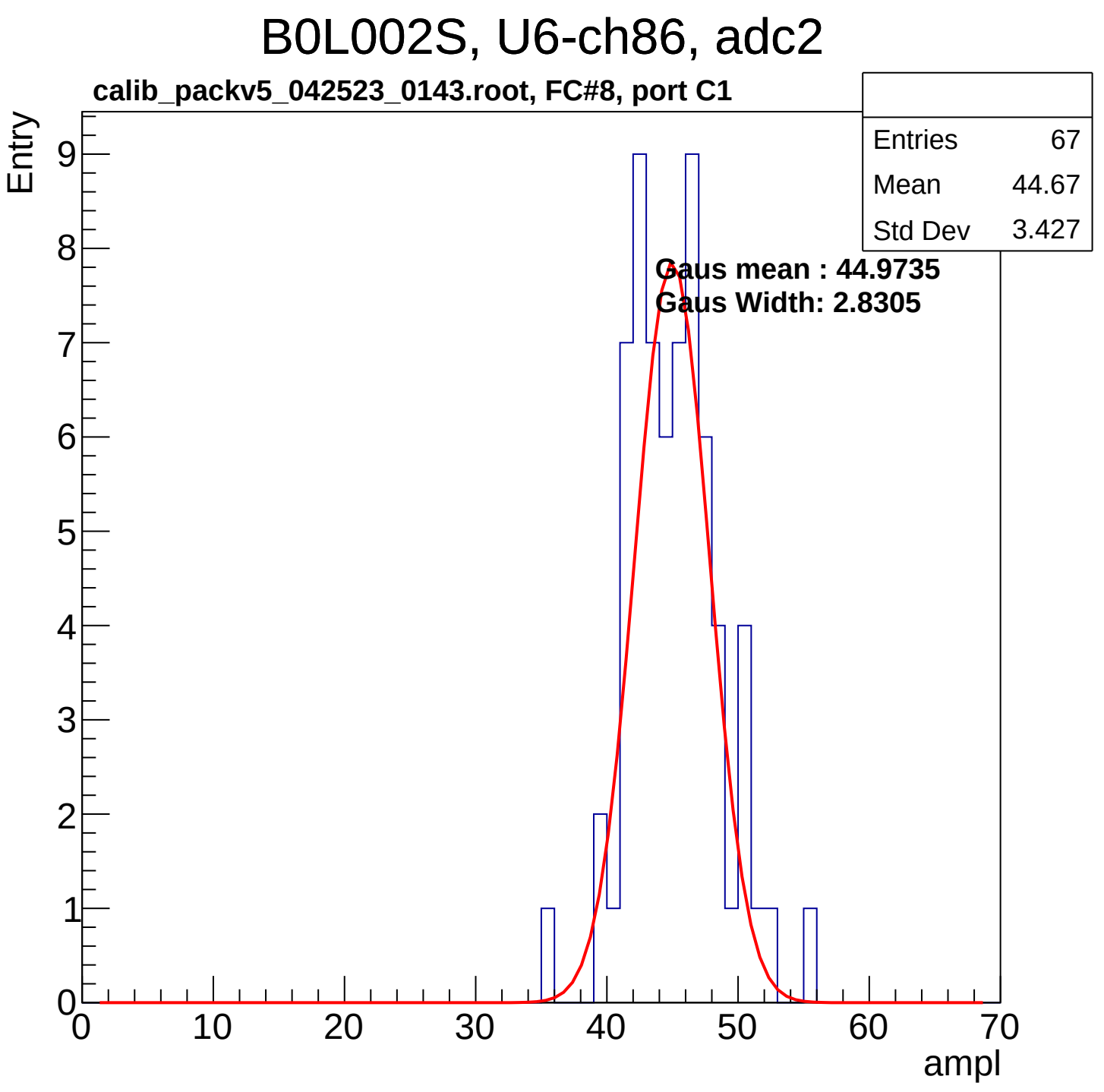
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U6-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.67
Std Dev 3.427
Gaus mean : 44.9735
Gaus Width: 2.8305
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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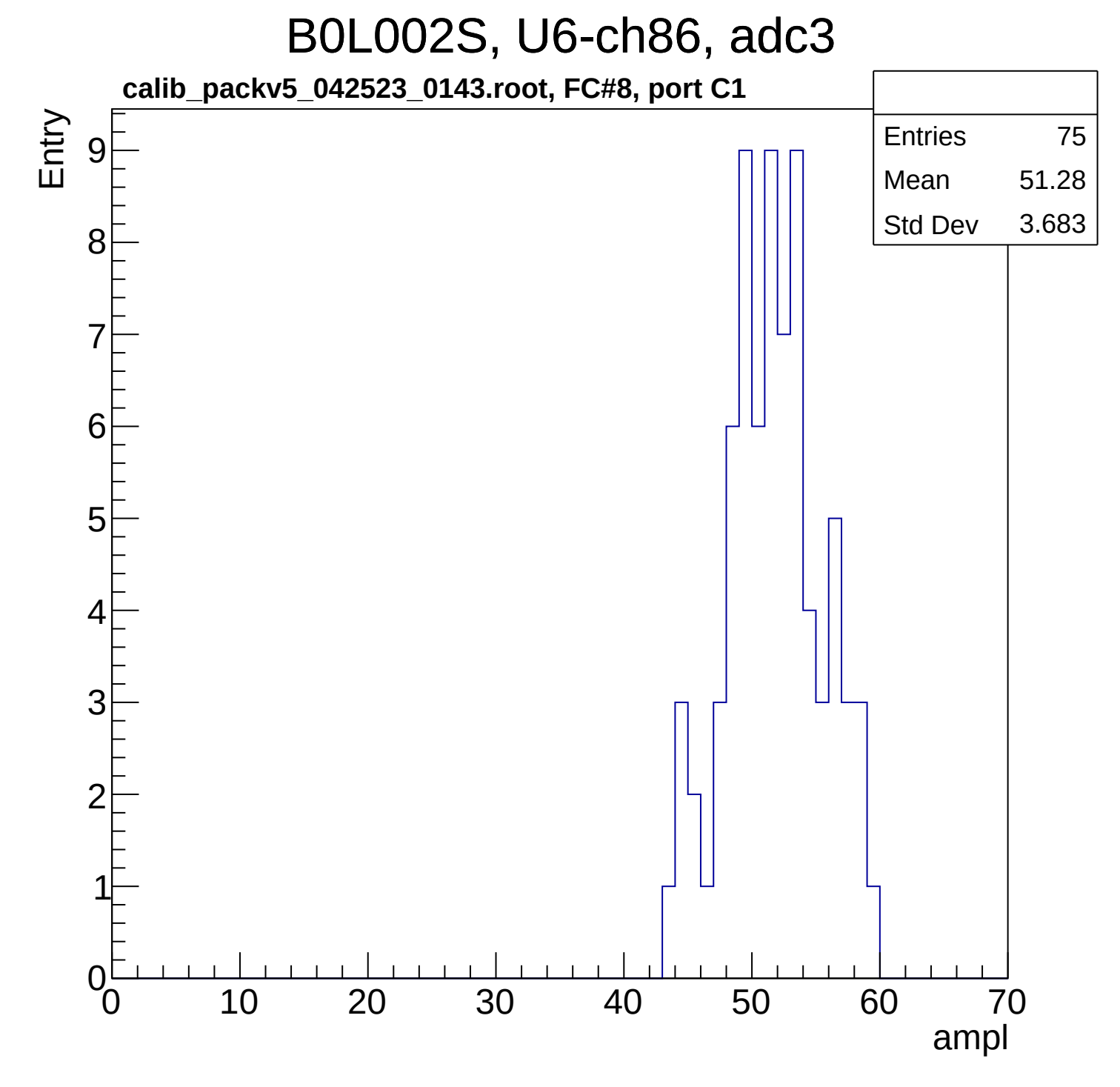
B0L002S, U6-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.28
Std Dev 3.683
Entry
ampl
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3
4
5
6
7
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0
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60
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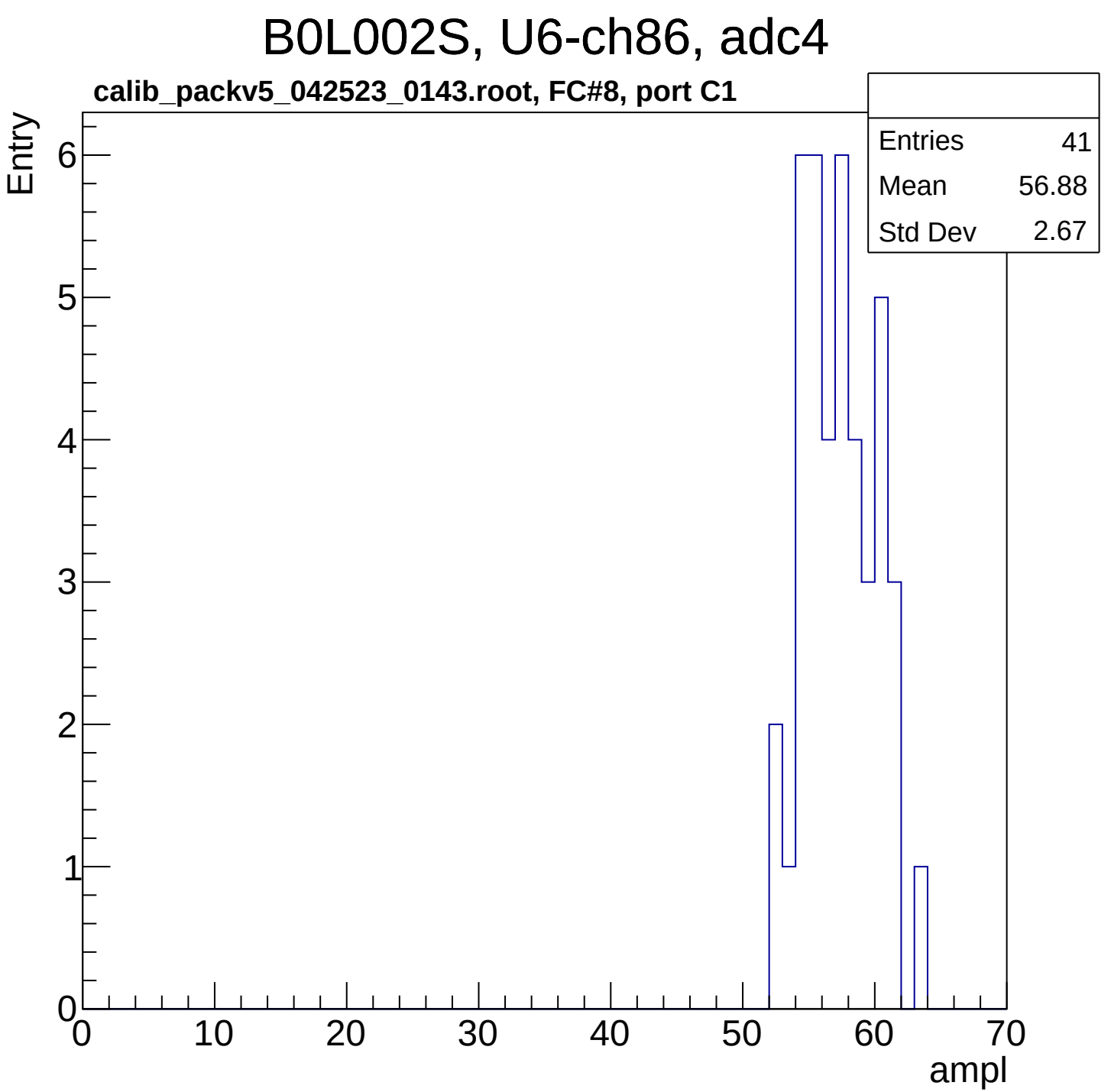
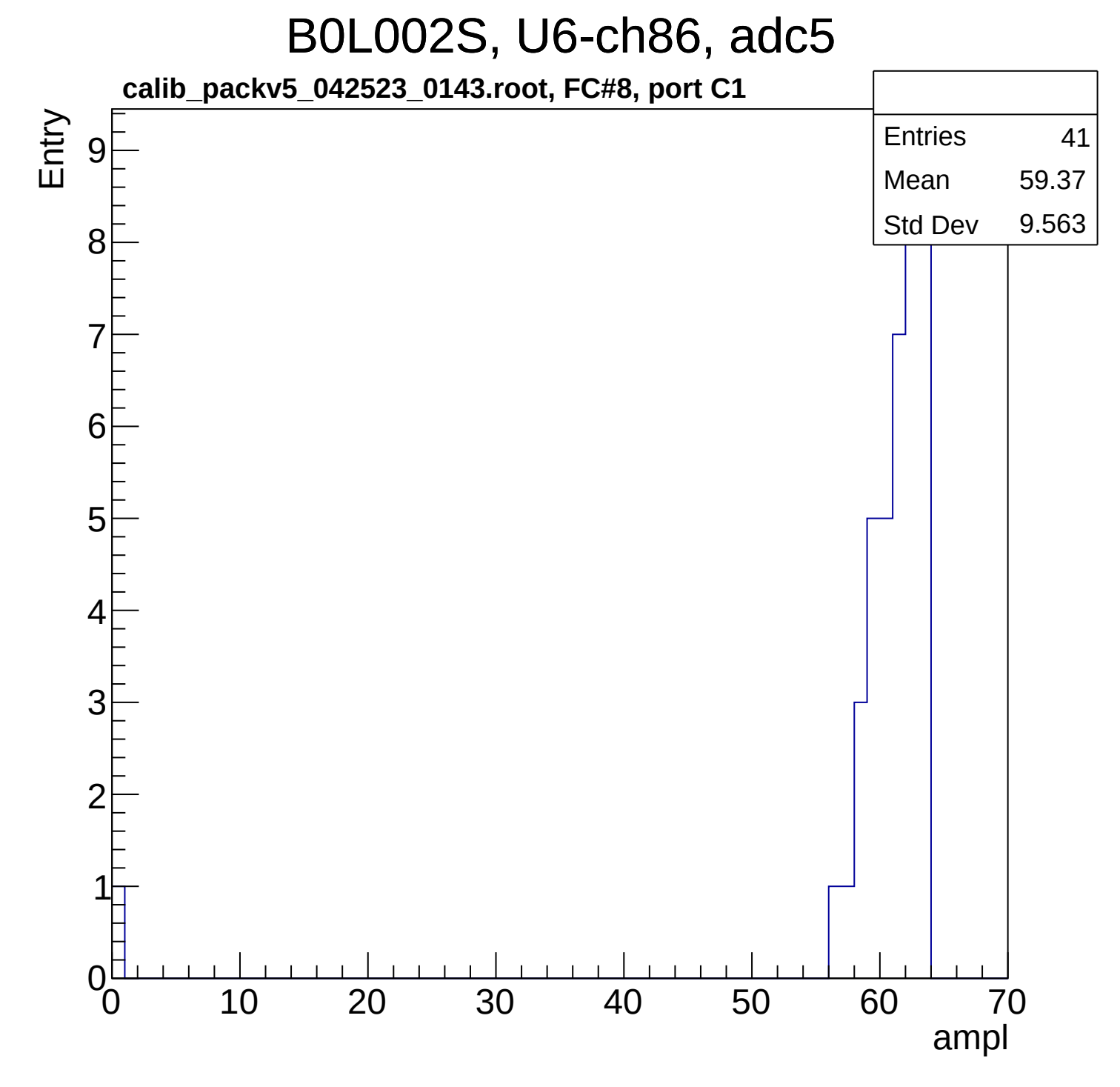

B0L002S, U6-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 56.88
Std Dev 2.67
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.37
Std Dev 9.563
Entry
ampl
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2
3
4
5
6
7
8
9
0
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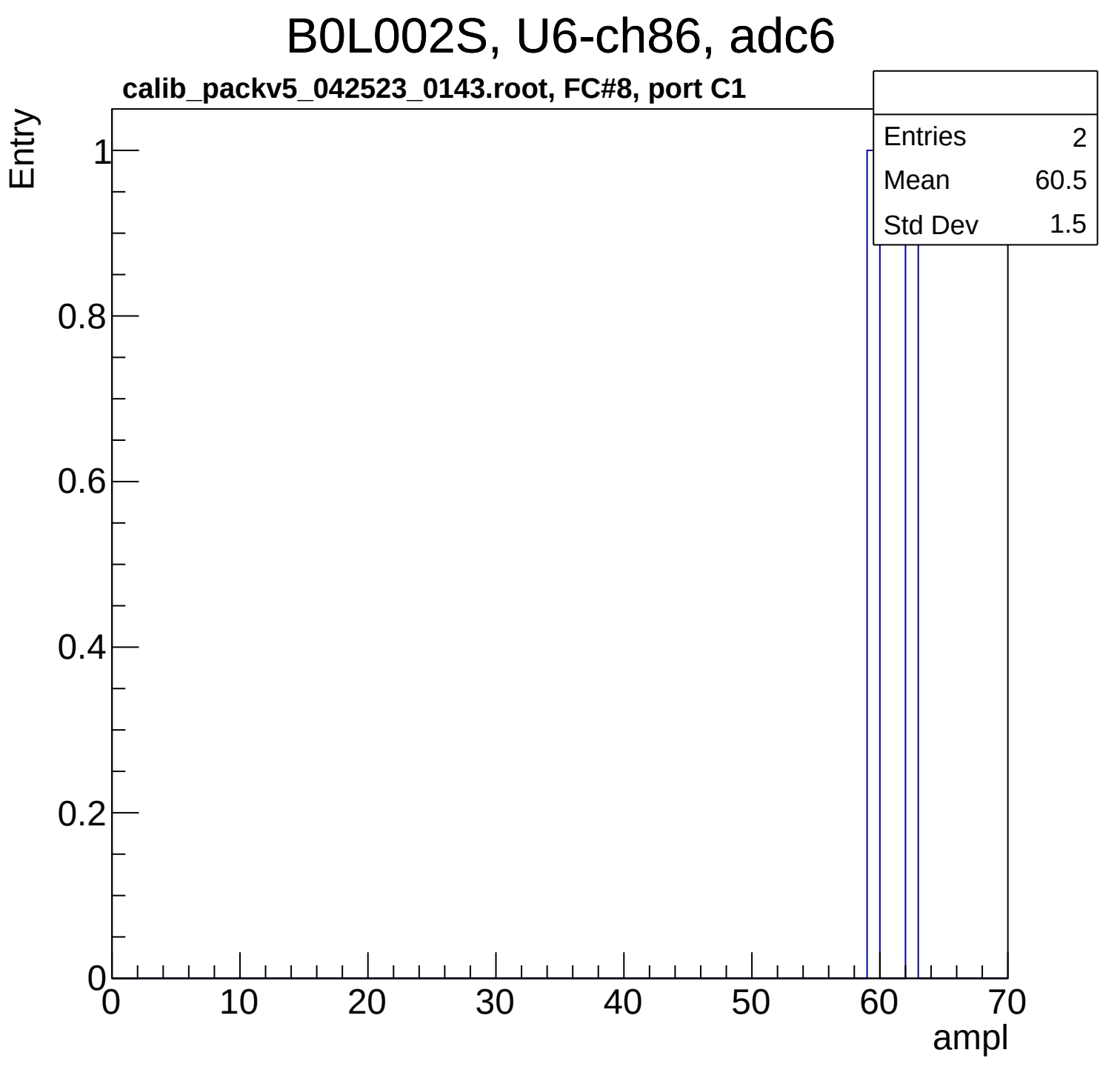
60
70


B0L002S, U6-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
ampl
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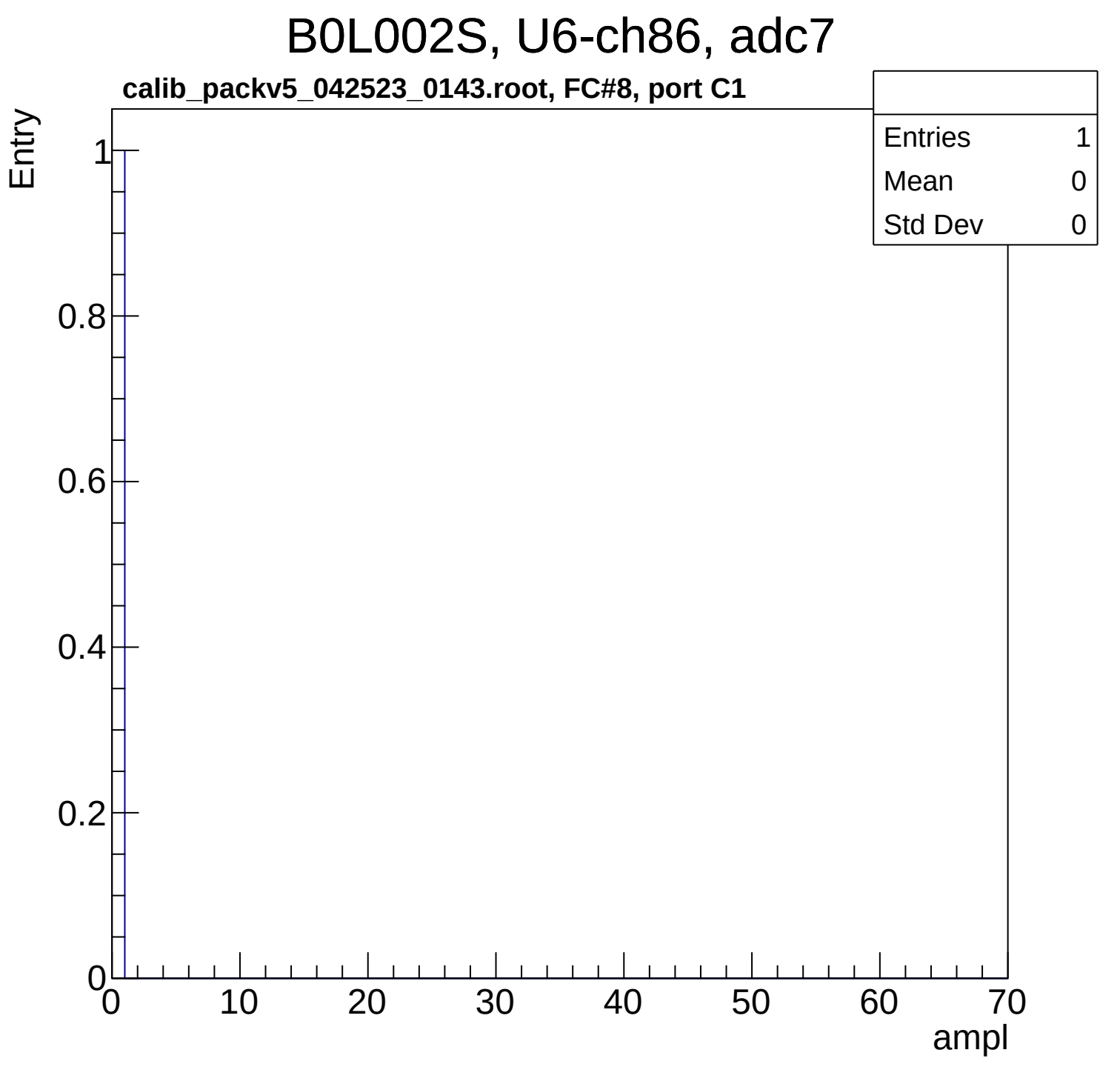
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L002S, U6-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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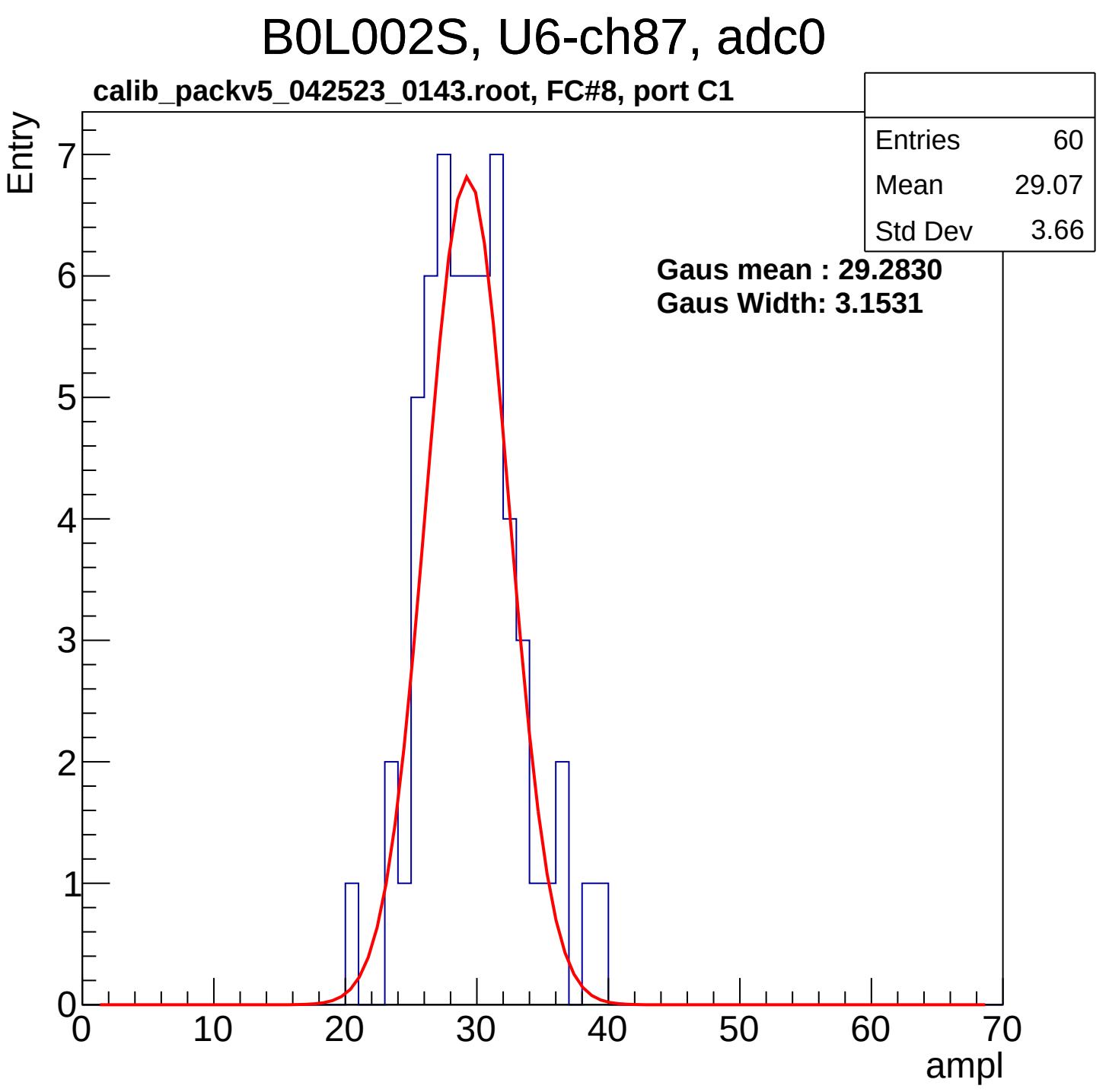
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 29.07
Std Dev 3.66
Gaus mean : 29.2830
Gaus Width: 3.1531
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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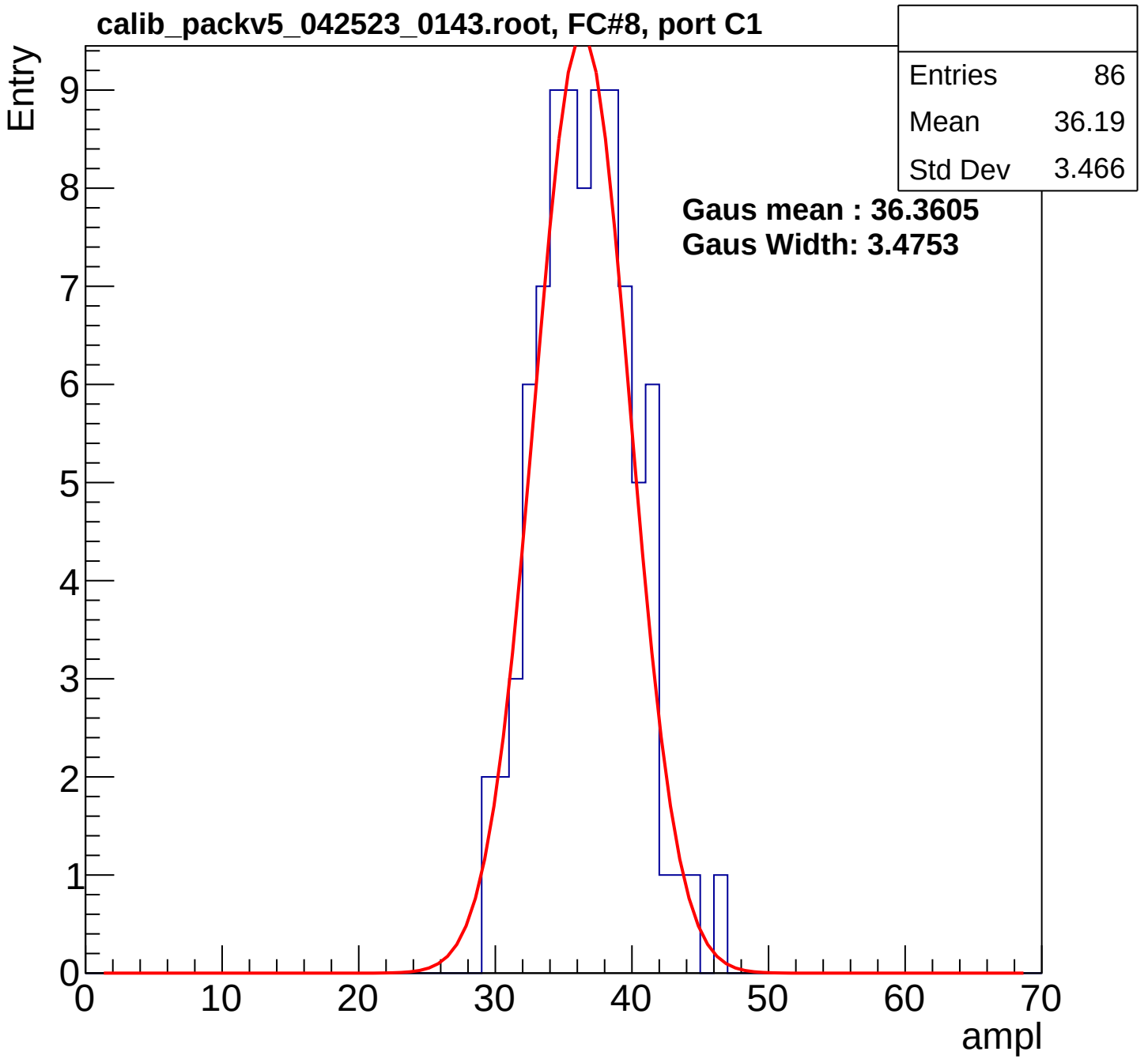
B0L002S, U6-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 36.19
Std Dev 3.466
Gaus mean : 36.3605
Gaus Width: 3.4753
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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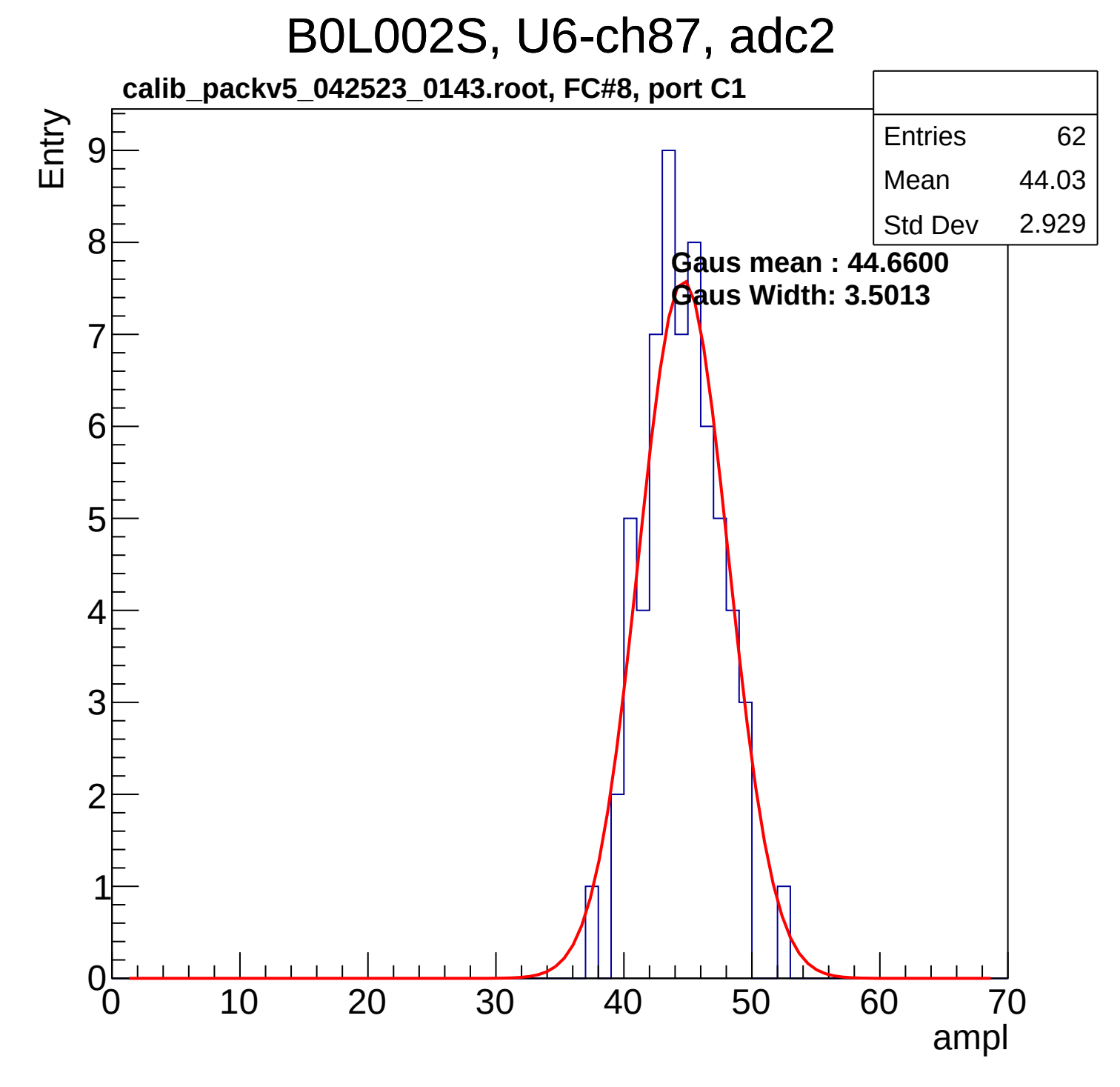
B0L002S, U6-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.03
Std Dev 2.929
Gaus mean : 44.6600
Gaus Width: 3.5013
Entry
ampl
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2
3
4
5
6
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0
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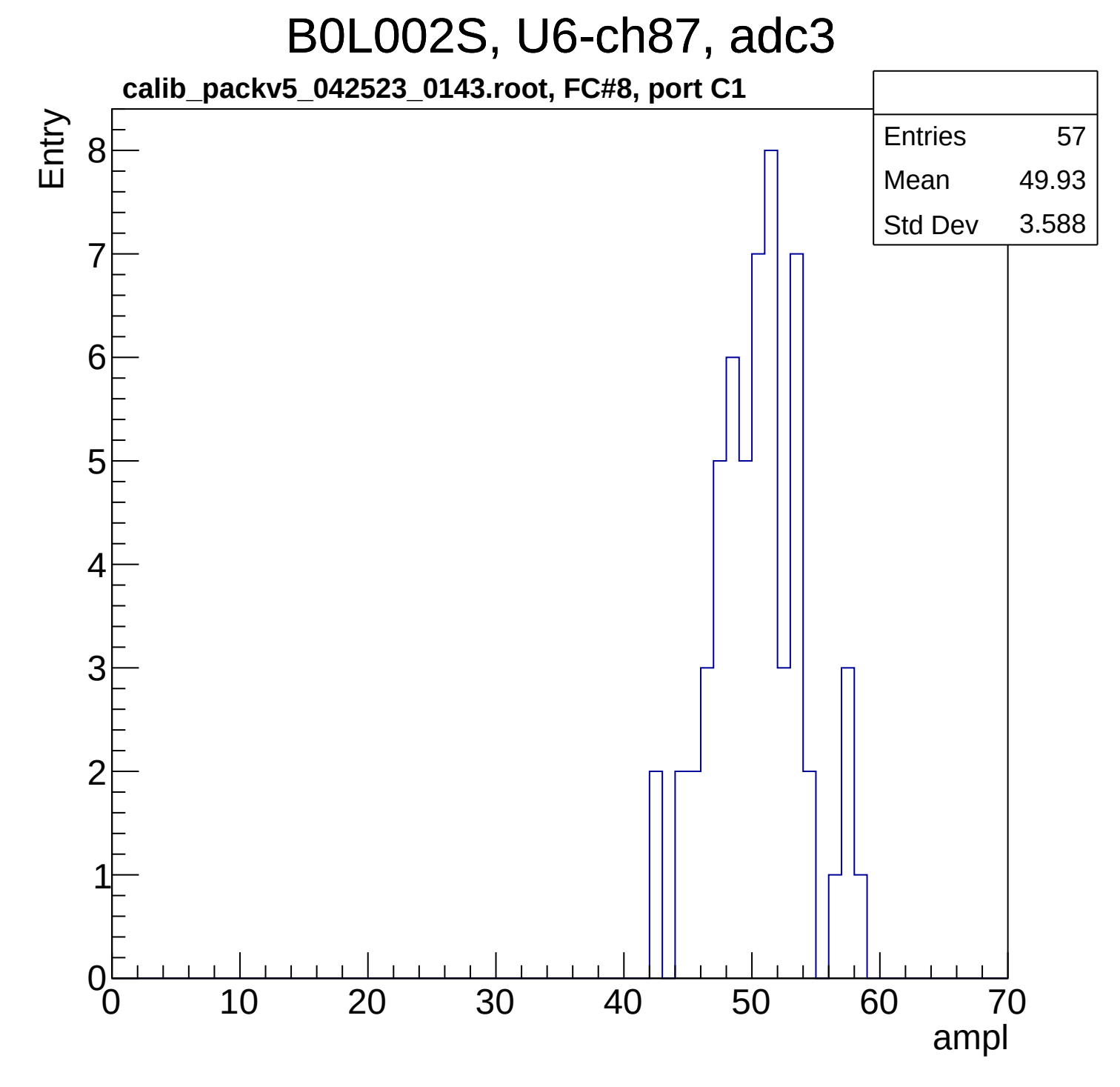
B0L002S, U6-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 49.93
Std Dev 3.588
Entry
ampl
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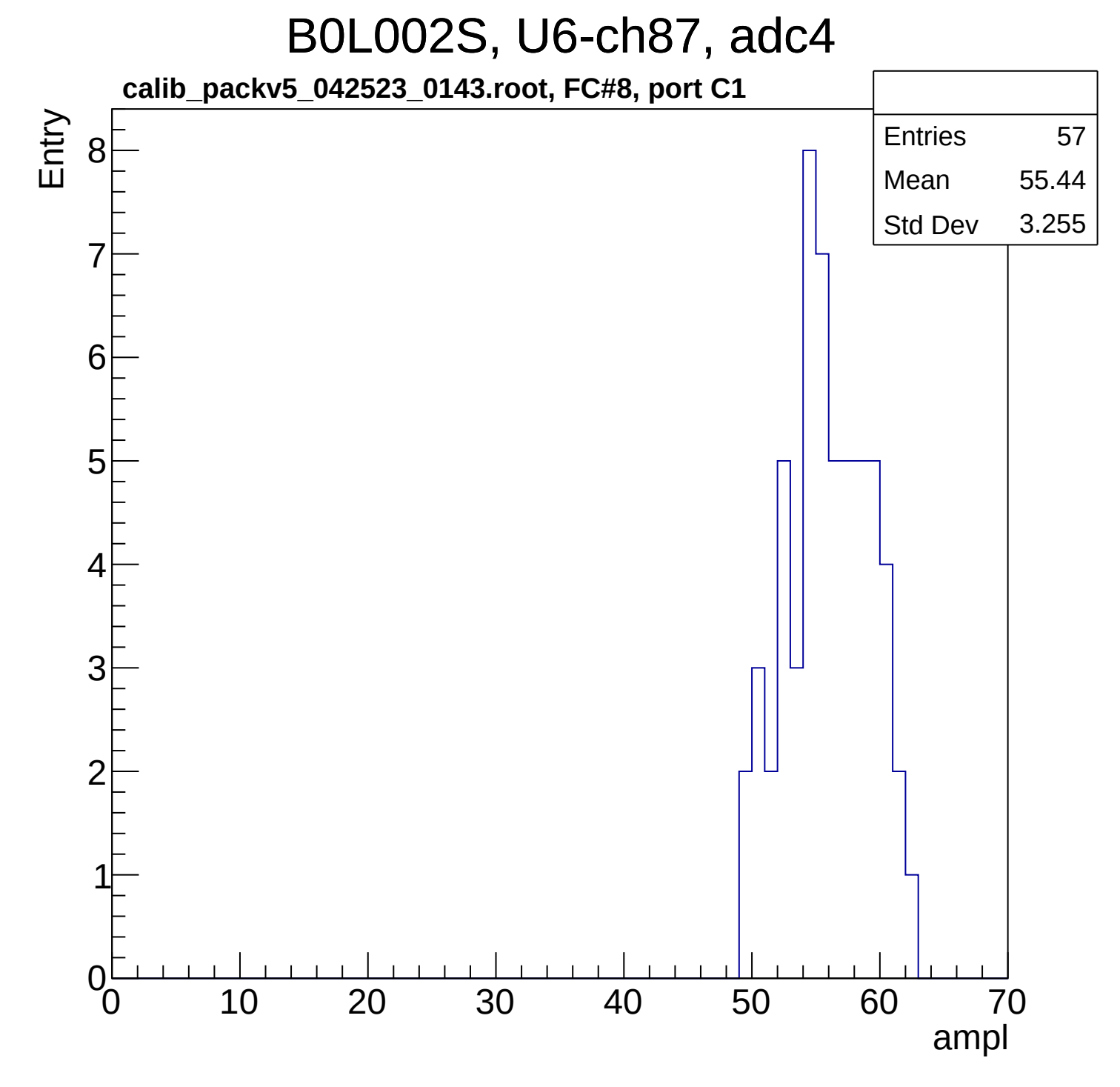
B0L002S, U6-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.44
Std Dev 3.255
Entry
ampl
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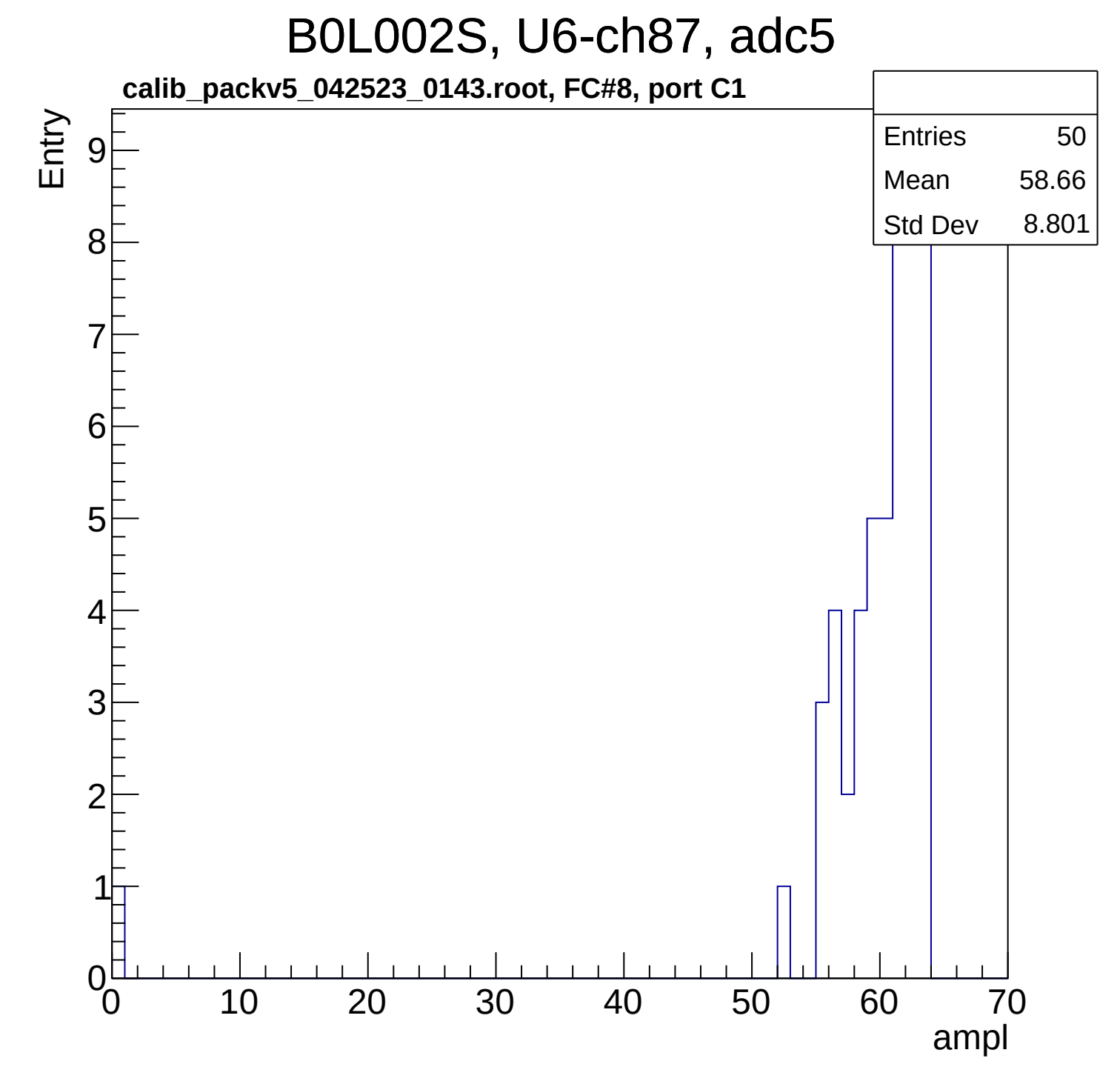
B0L002S, U6-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.66
Std Dev 8.801
Entry
ampl
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6
7
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60
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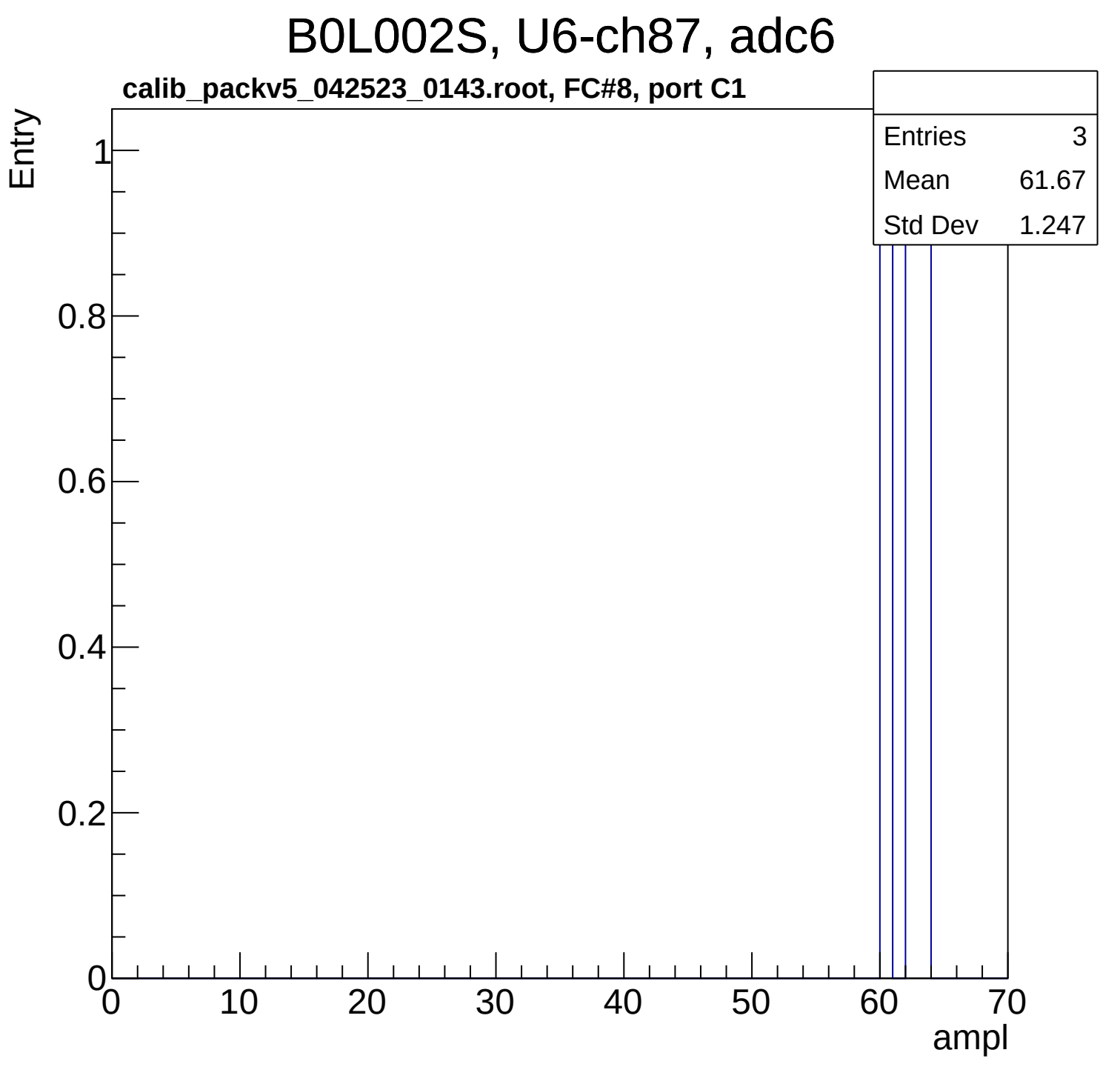
B0L002S, U6-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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0.6
0.4
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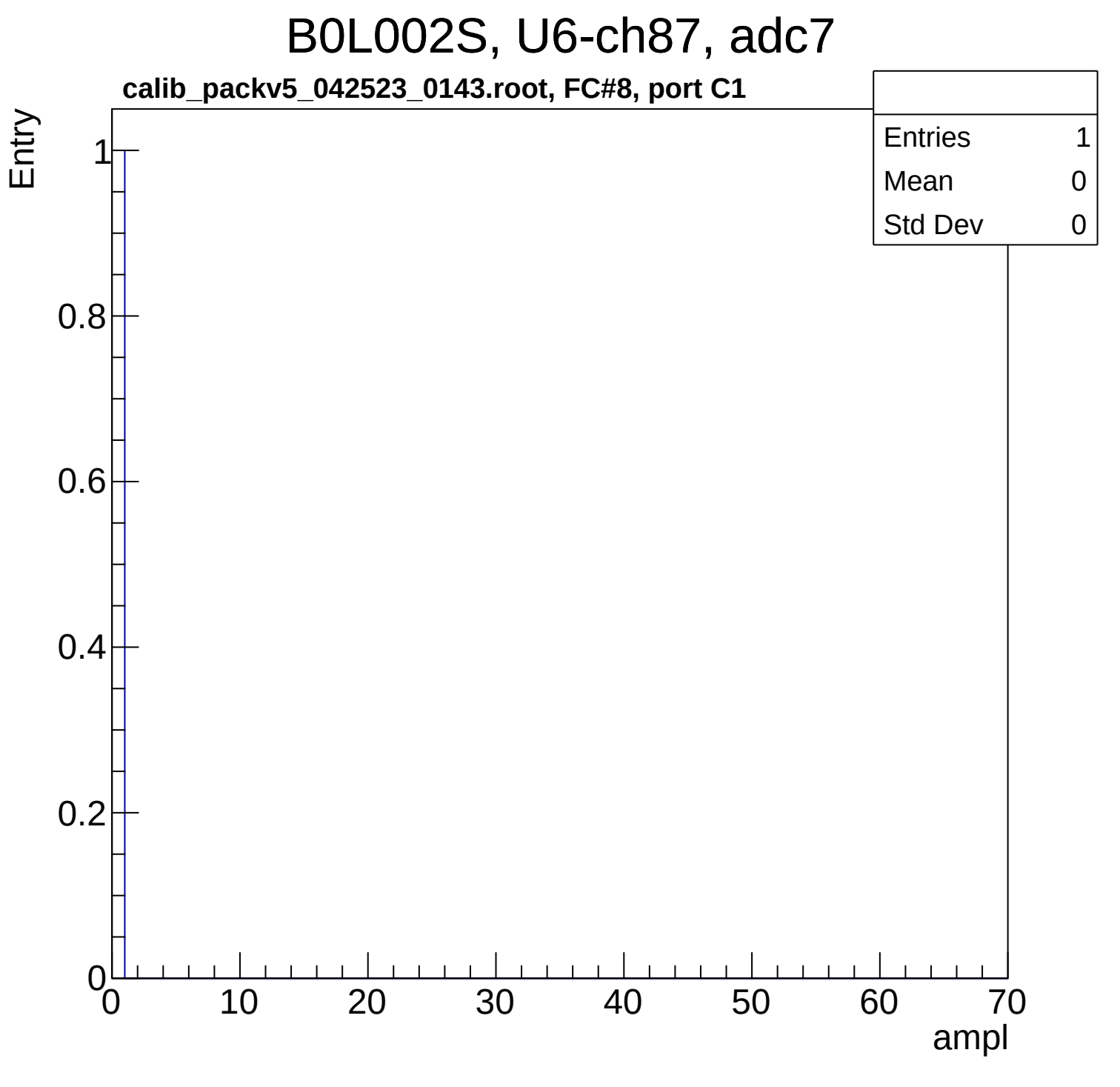
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.4
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10
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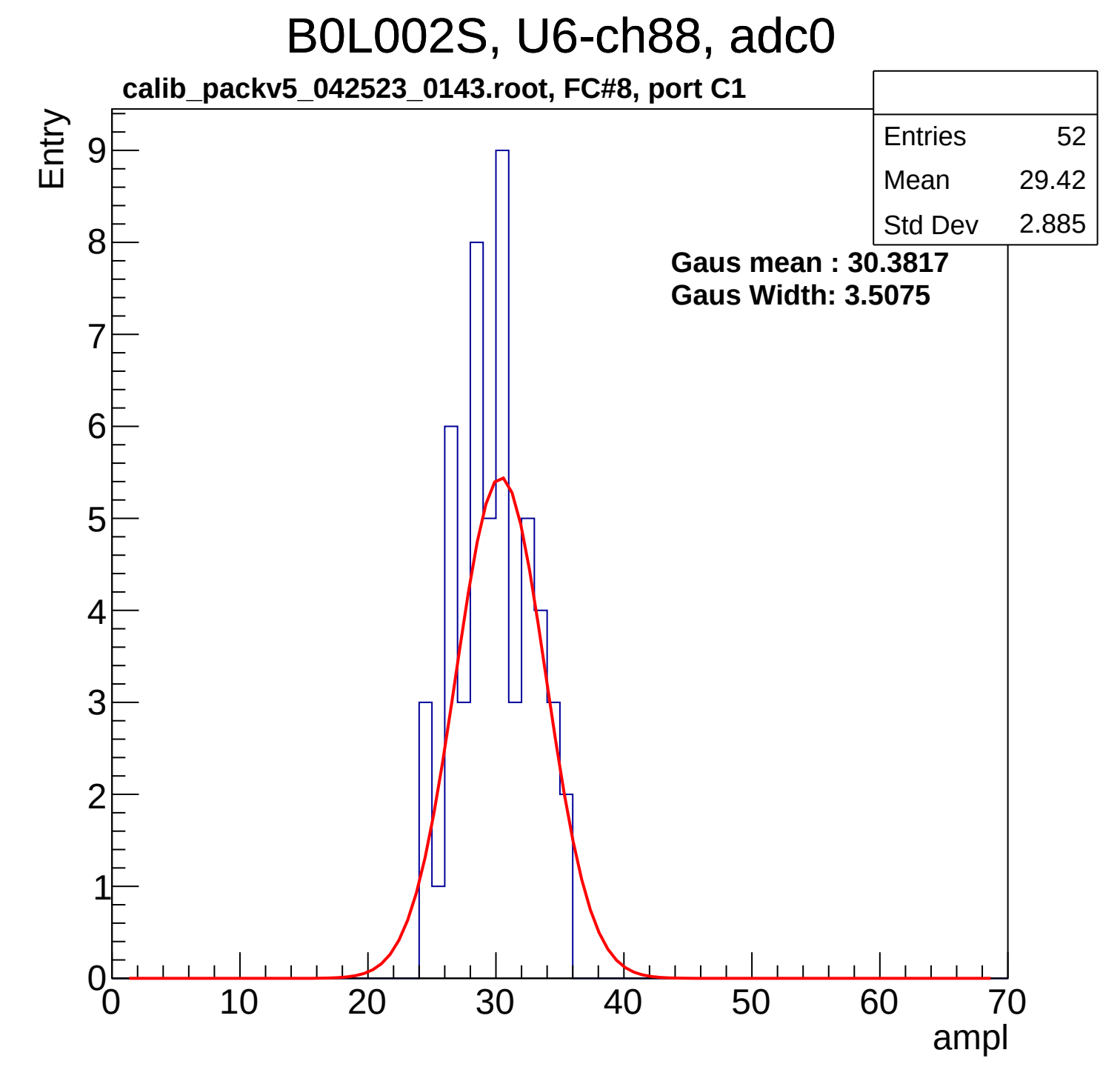
50
60
70
ampl


B0L002S, U6-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 29.42
Std Dev 2.885
Gaus mean : 30.3817
Gaus Width: 3.5075
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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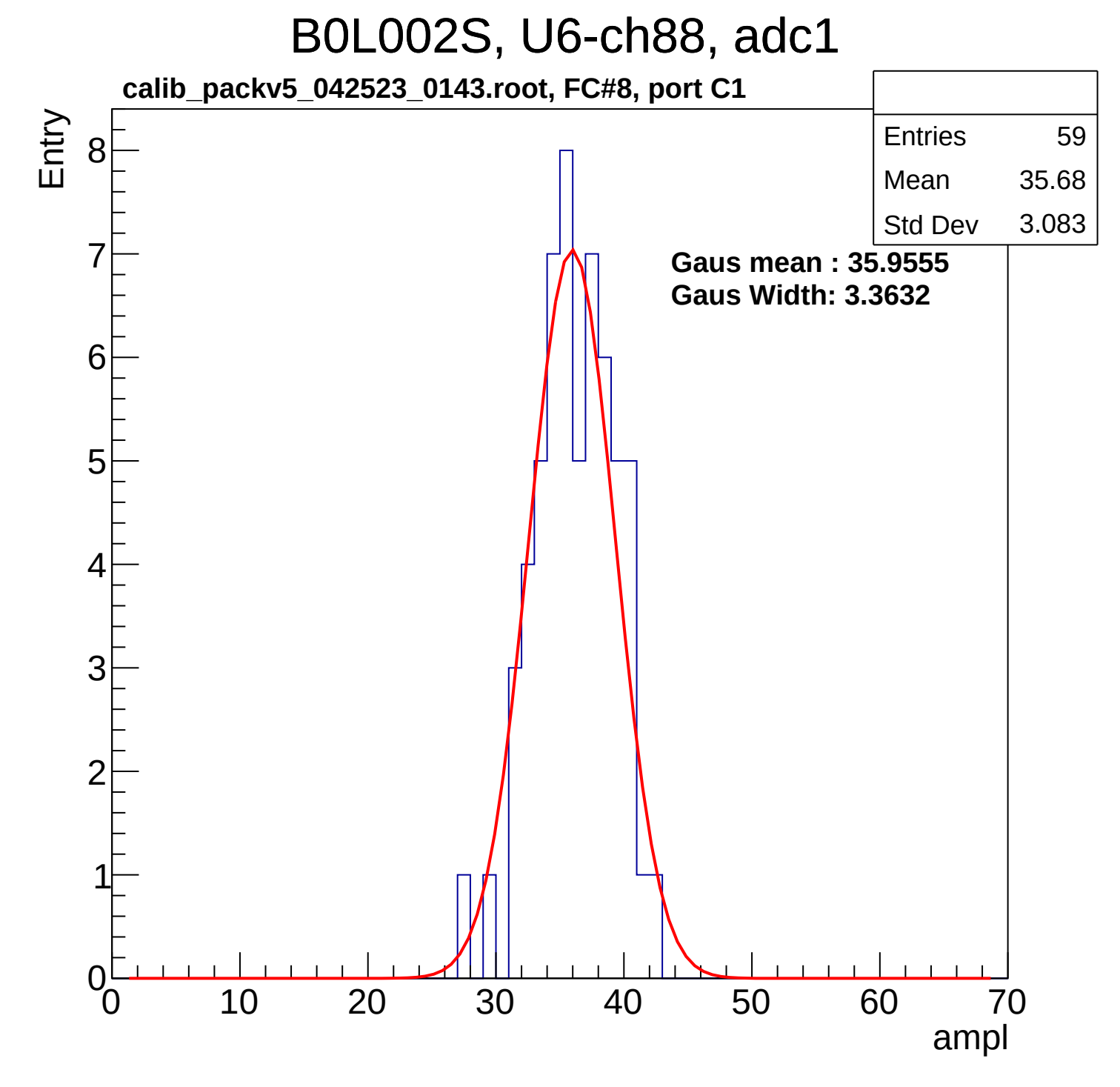
B0L002S, U6-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 35.68
Std Dev 3.083
Gaus mean : 35.9555
Gaus Width: 3.3632
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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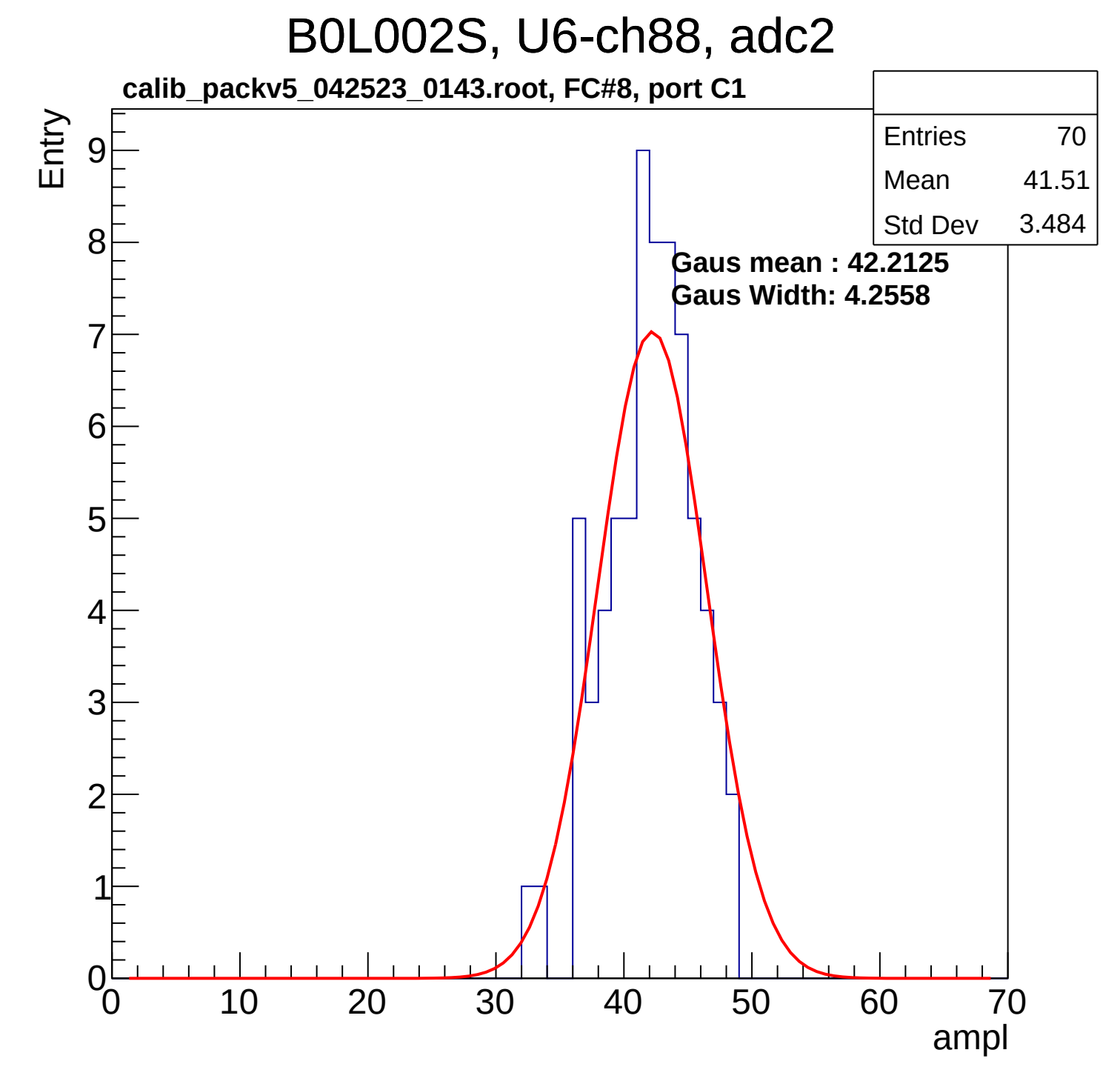
B0L002S, U6-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 41.51
Std Dev 3.484
Gaus mean : 42.2125
Gaus Width: 4.2558
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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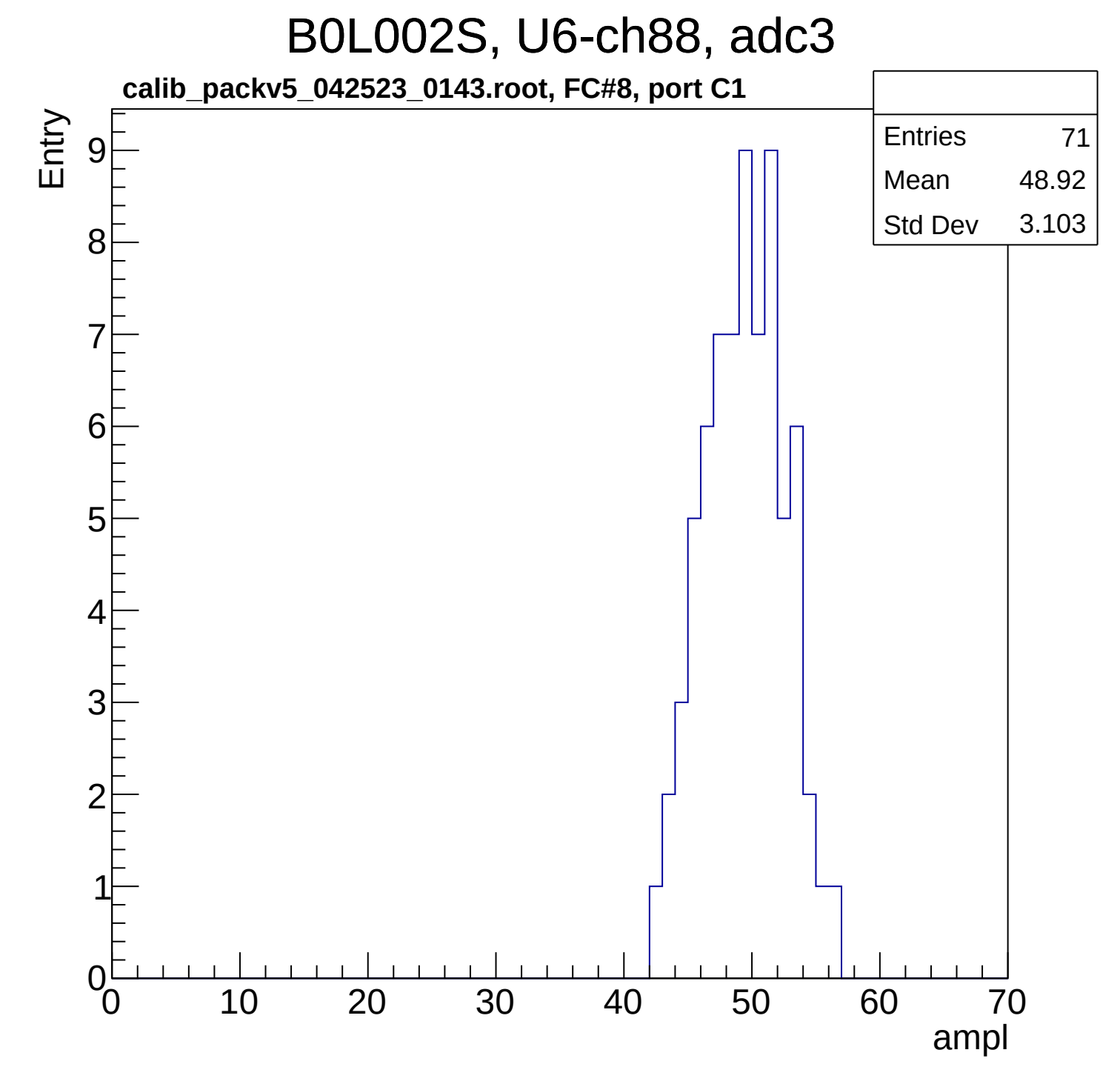
B0L002S, U6-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 48.92
Std Dev 3.103
Entry
ampl
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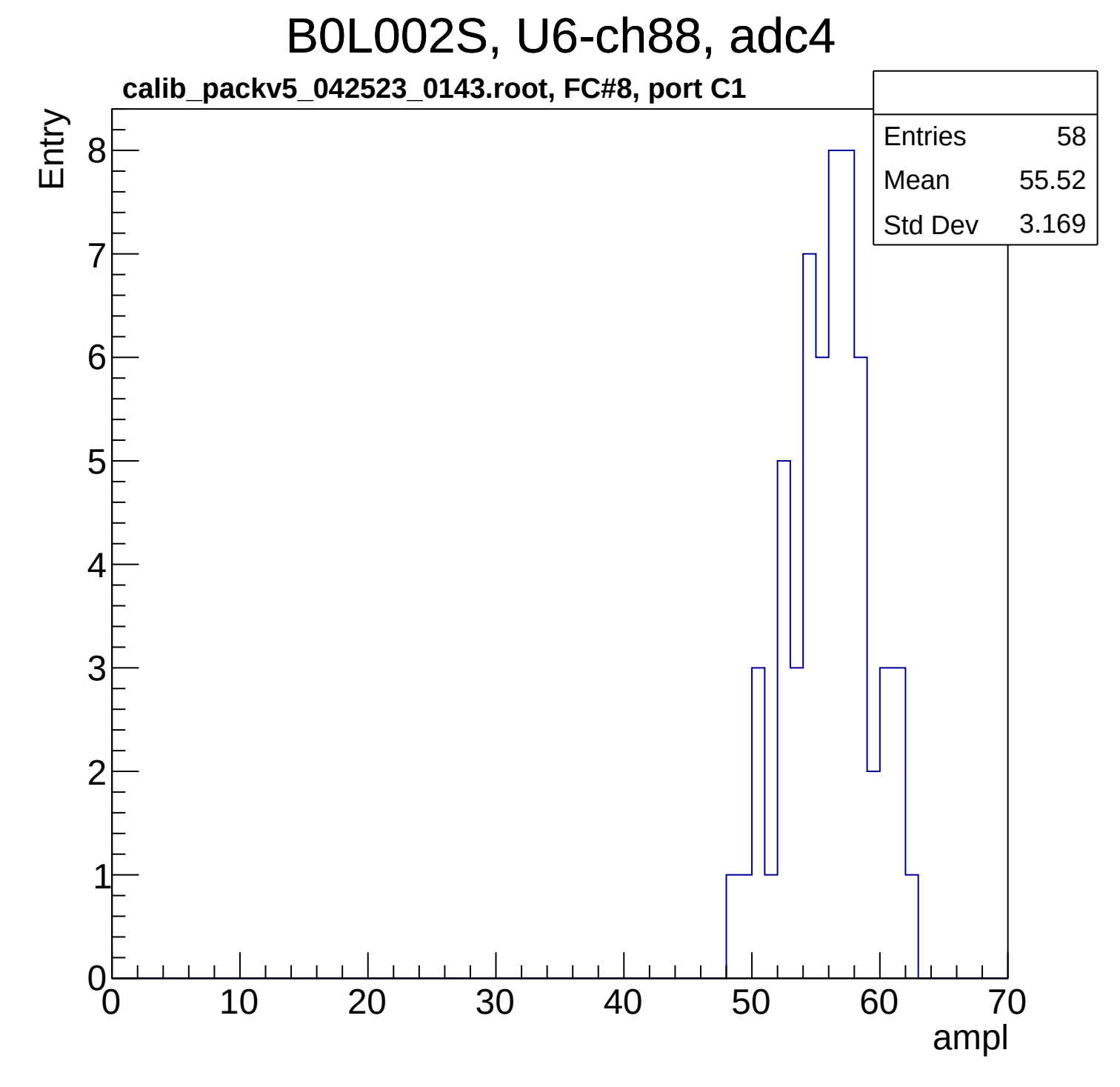
B0L002S, U6-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.52
Std Dev 3.169
Entry
ampl
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4
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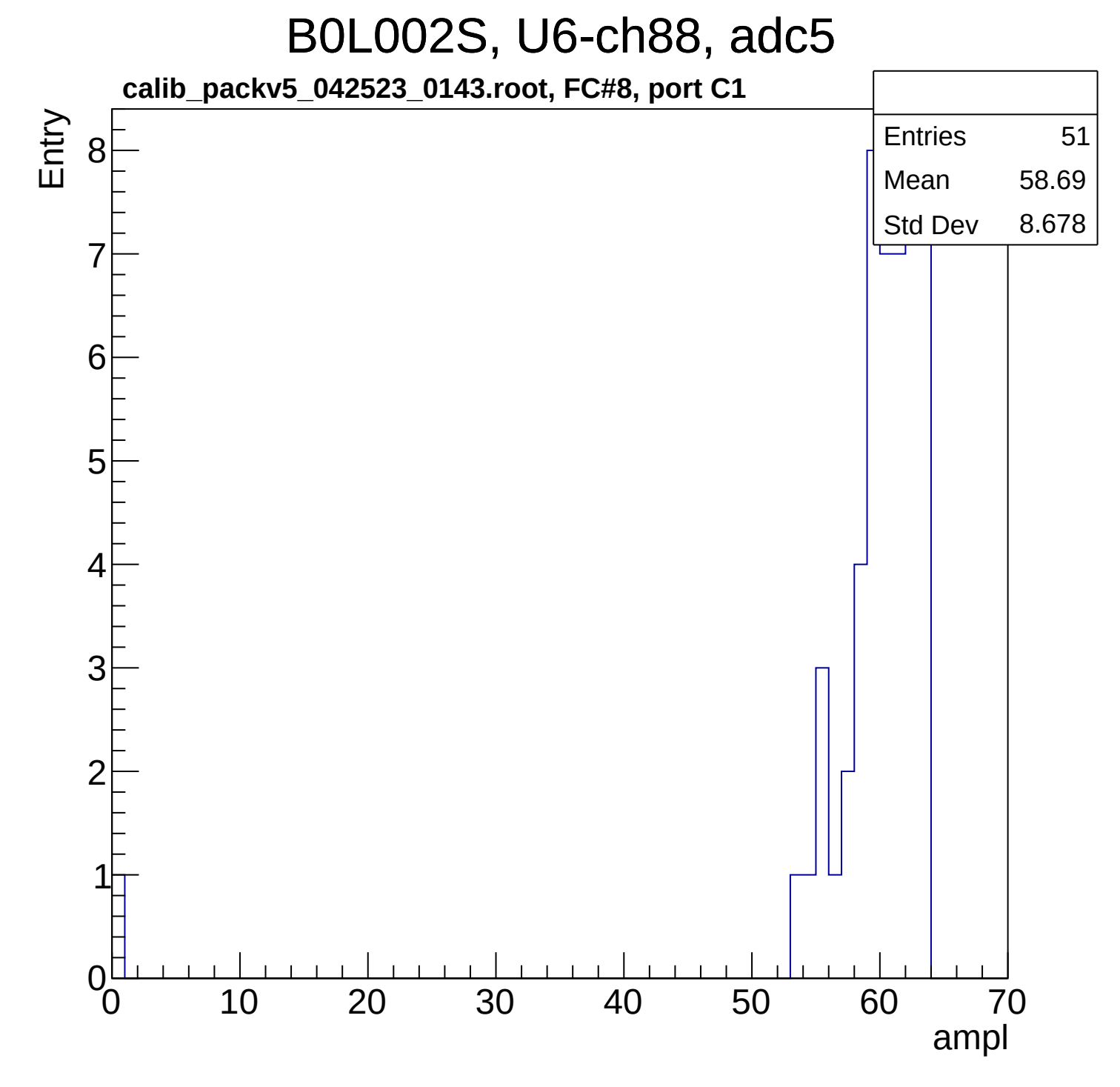
B0L002S, U6-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.69
Std Dev 8.678
Entry
ampl
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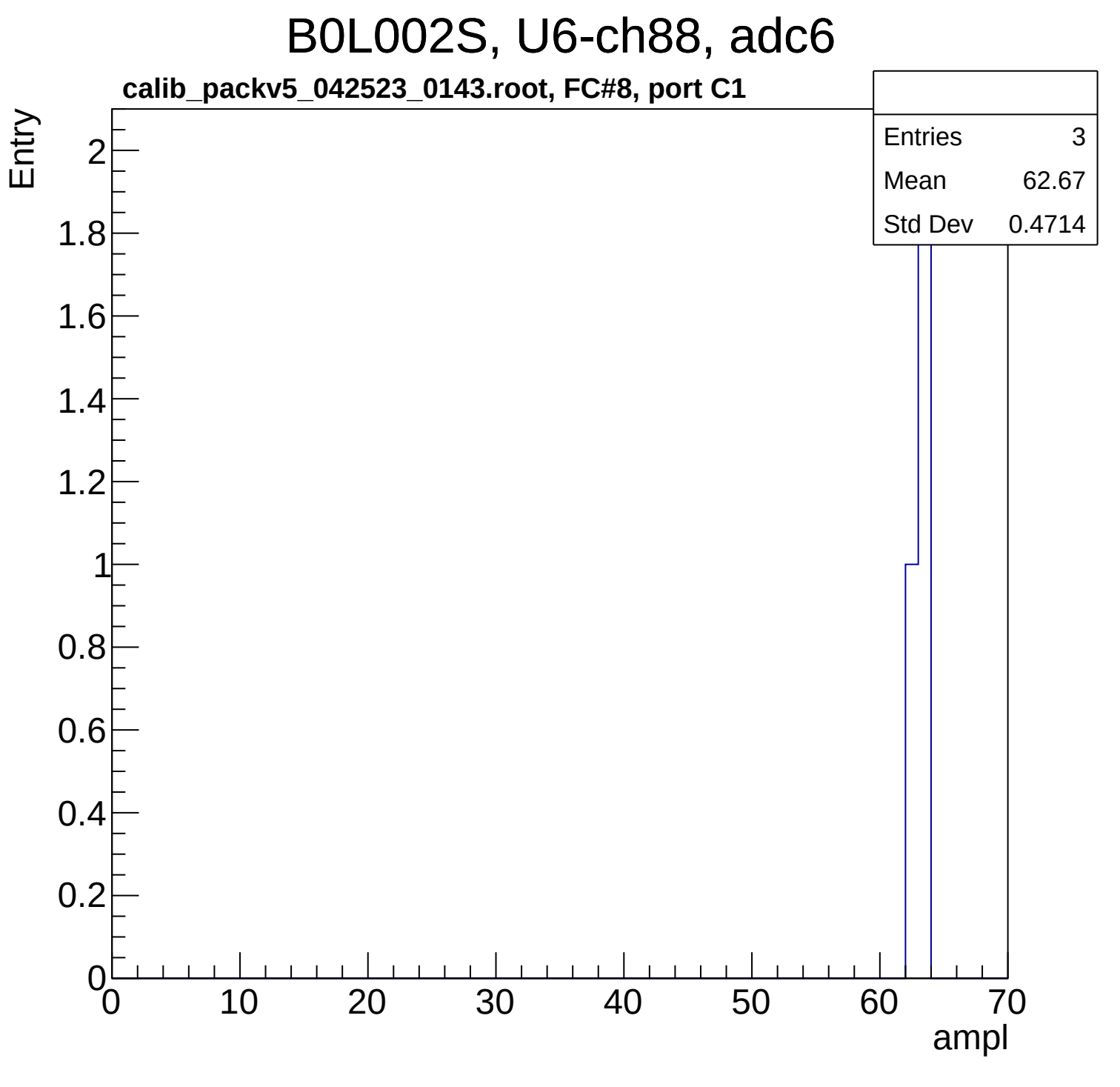
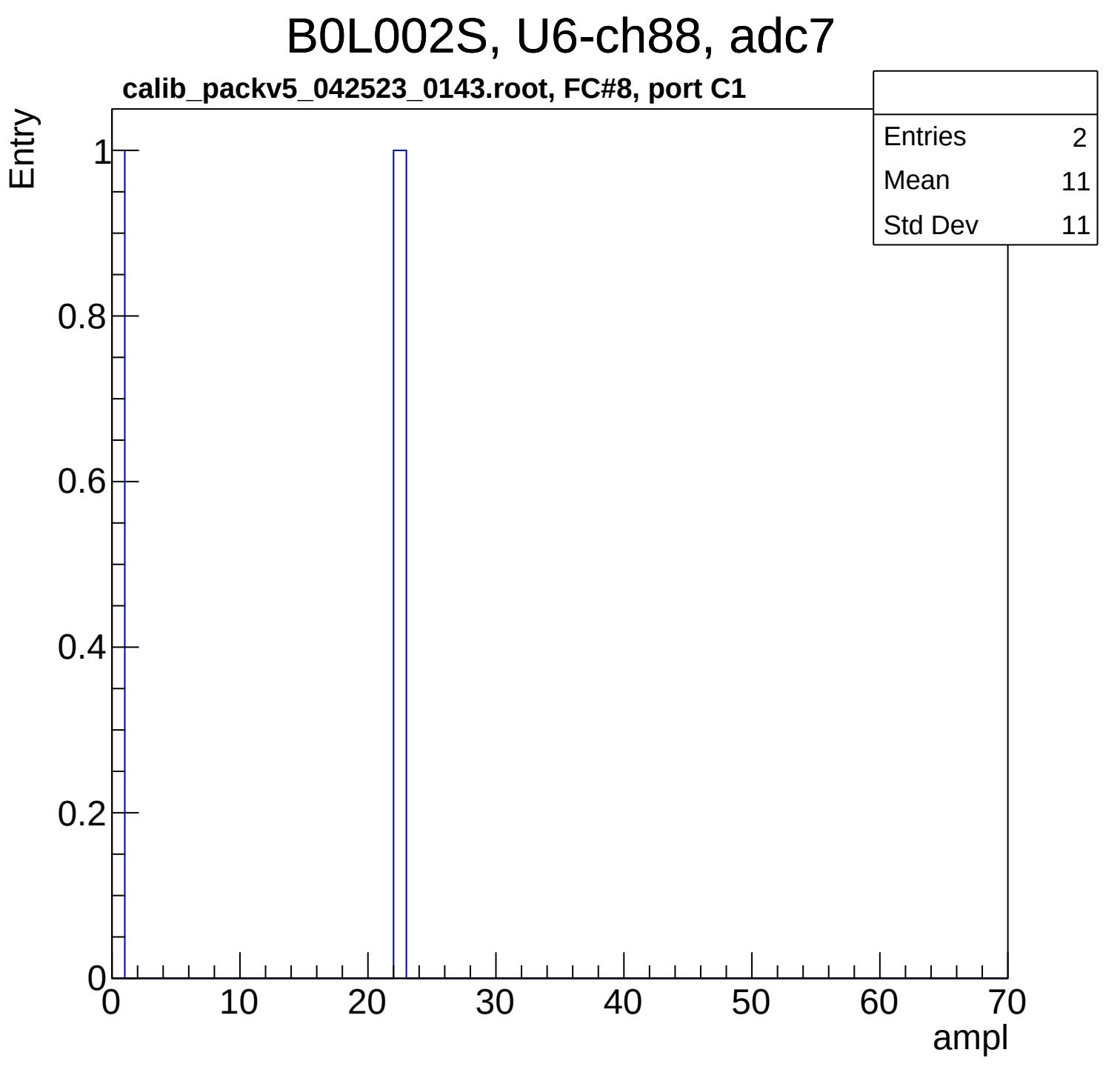
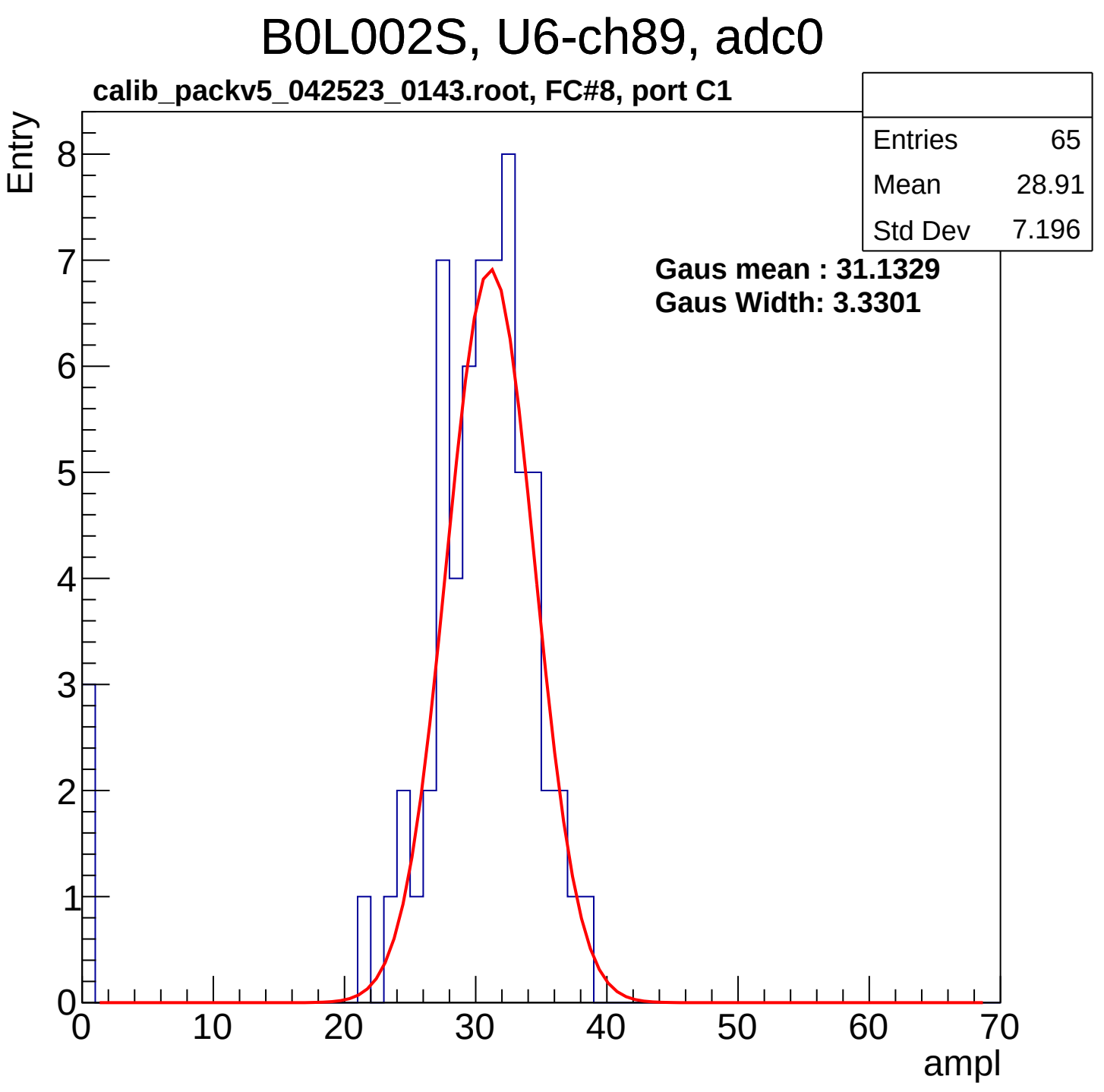
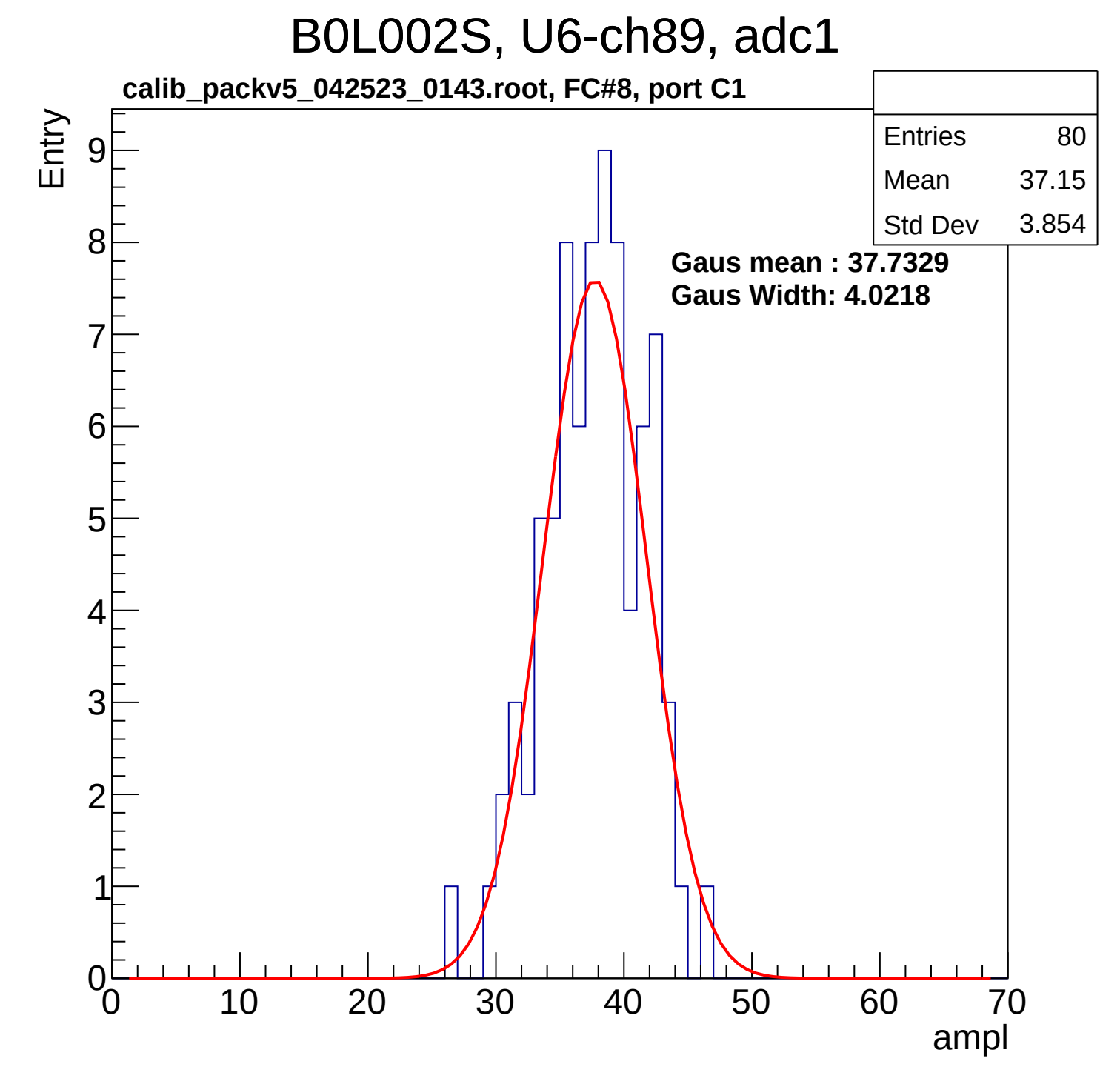
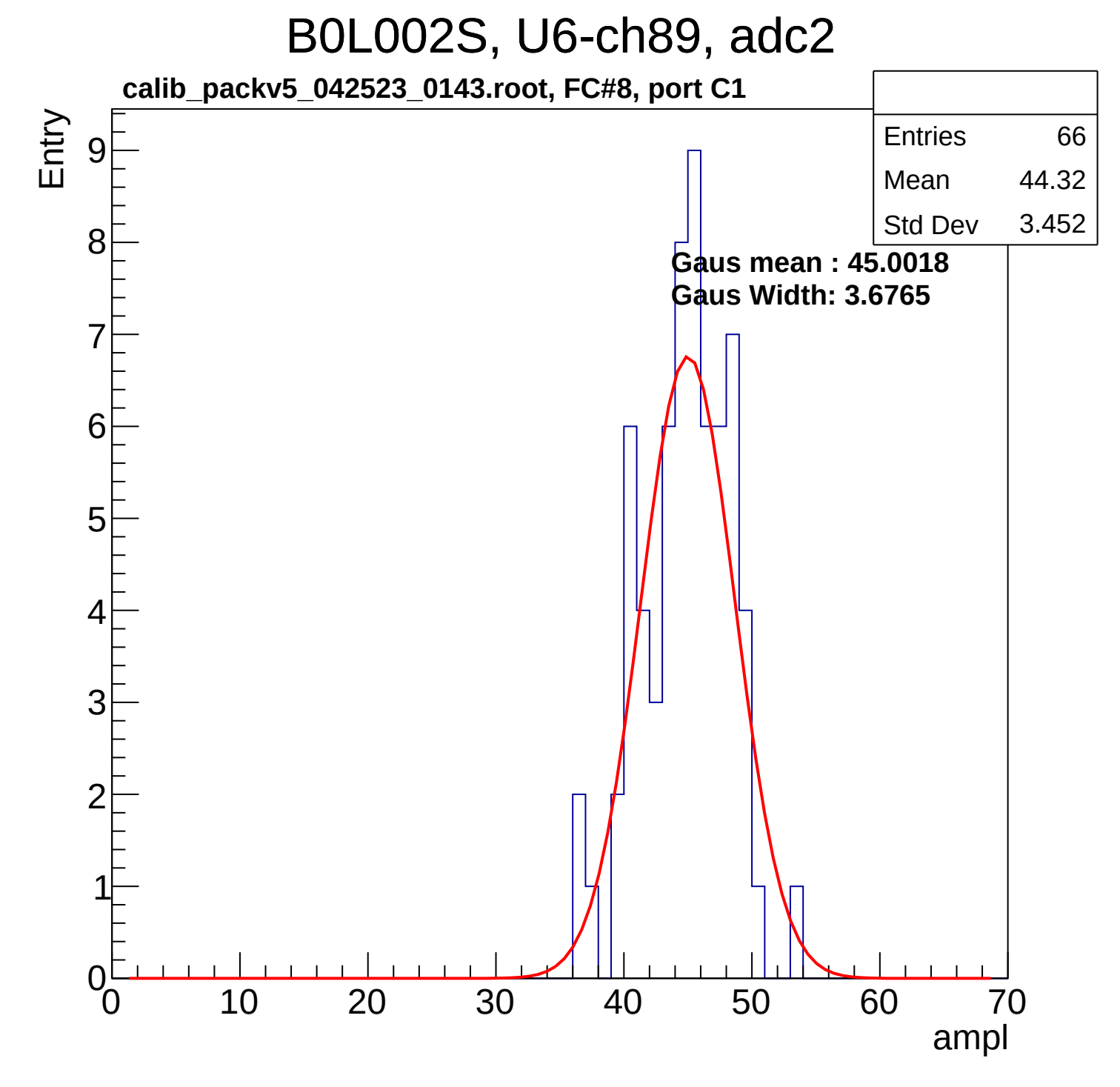

B0L002S, U6-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 28.91
Std Dev 7.196
Gaus mean : 31.1329
Gaus Width: 3.3301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.15
Std Dev 3.854
Gaus mean : 37.7329
Gaus Width: 4.0218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.32
Std Dev 3.452
Gaus mean : 45.0018
Gaus Width: 3.6765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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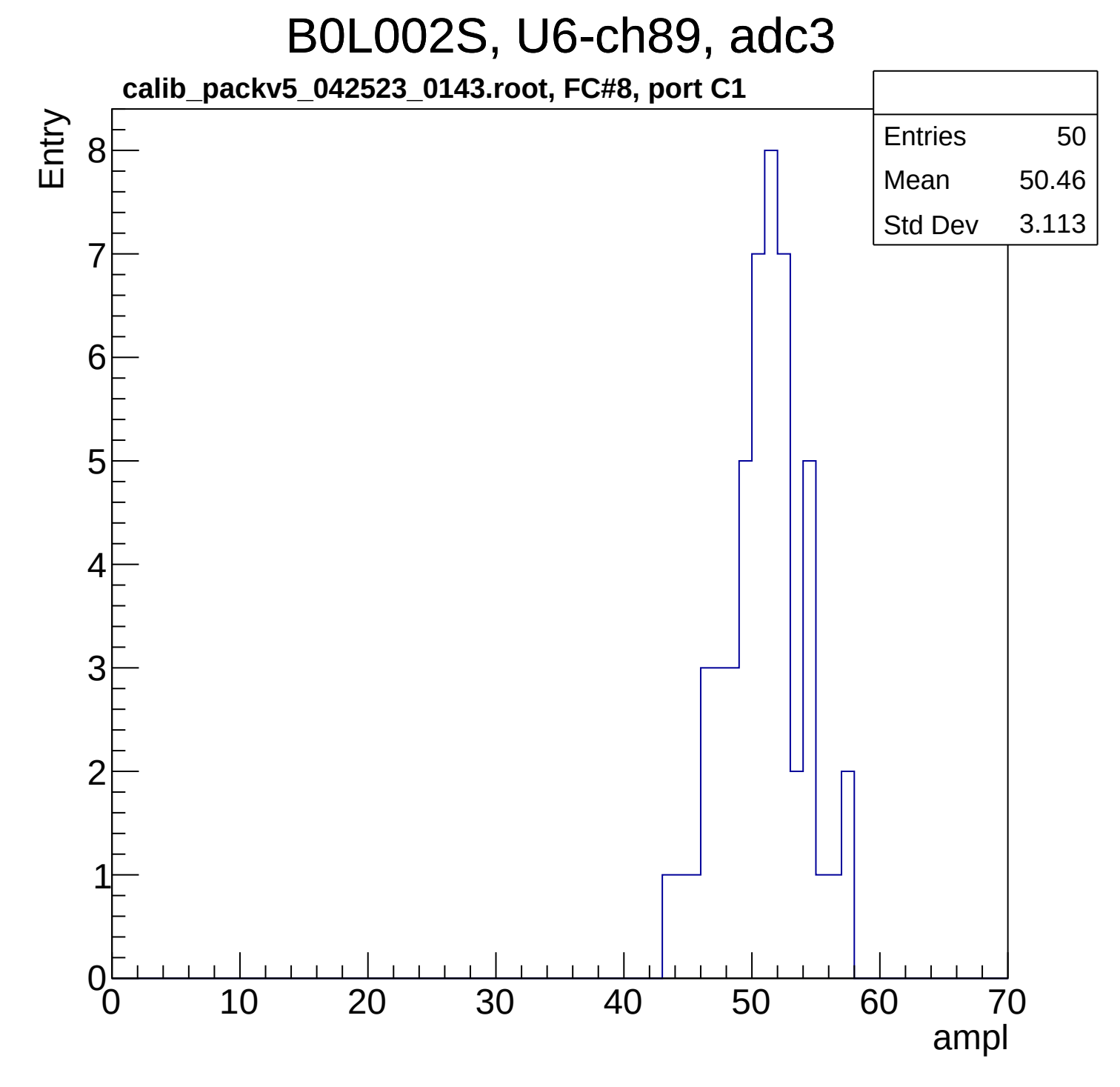
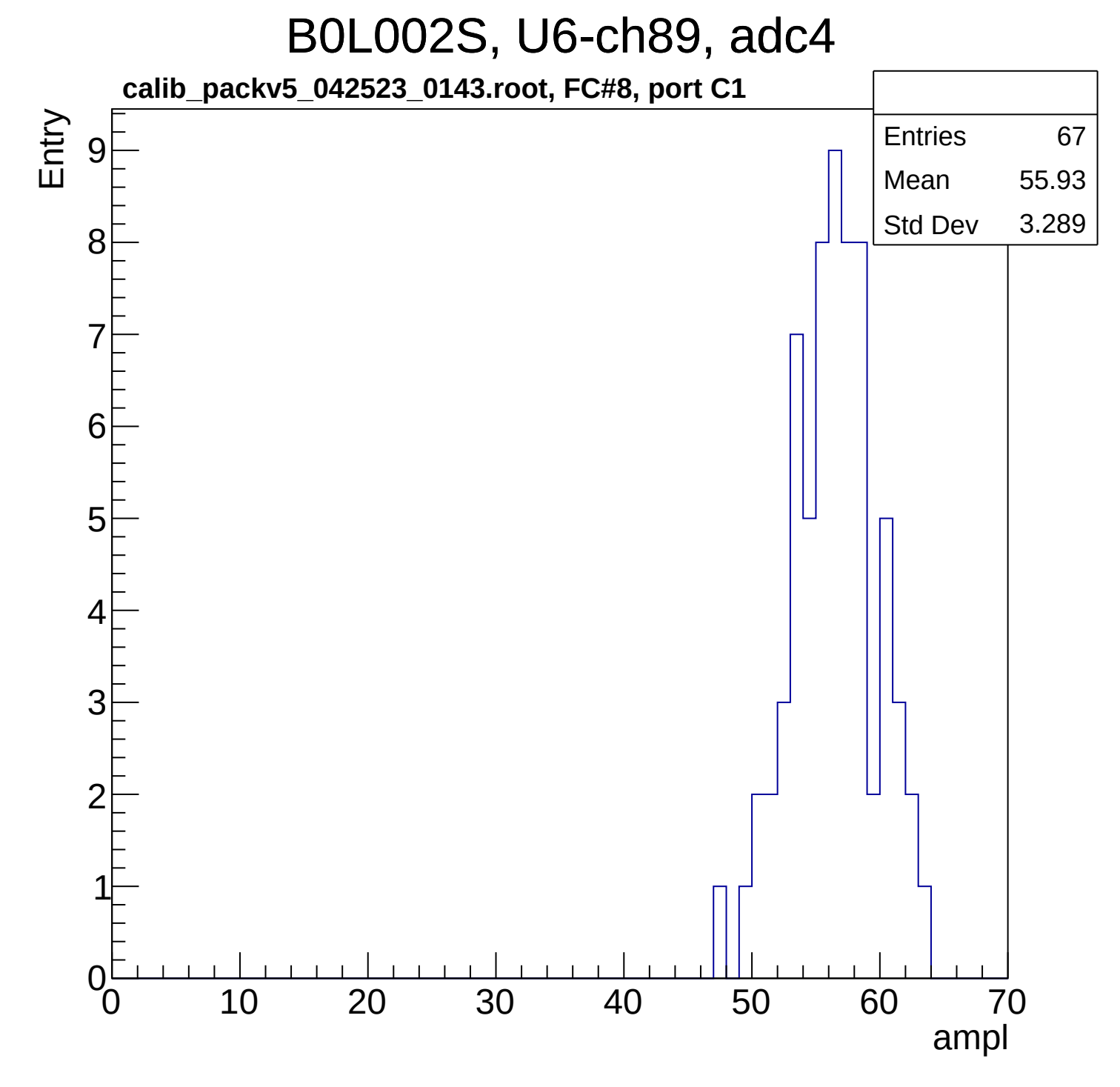
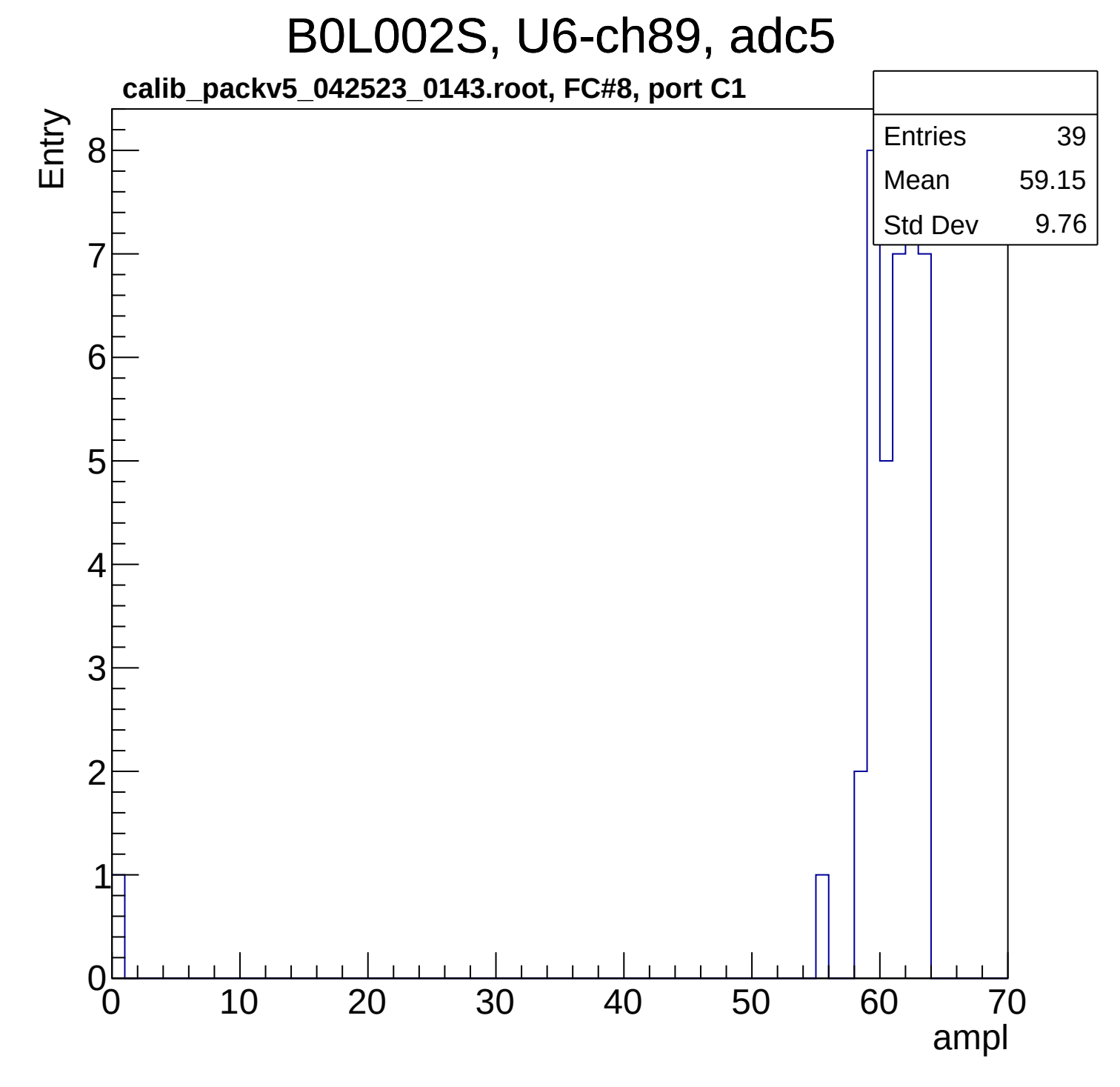
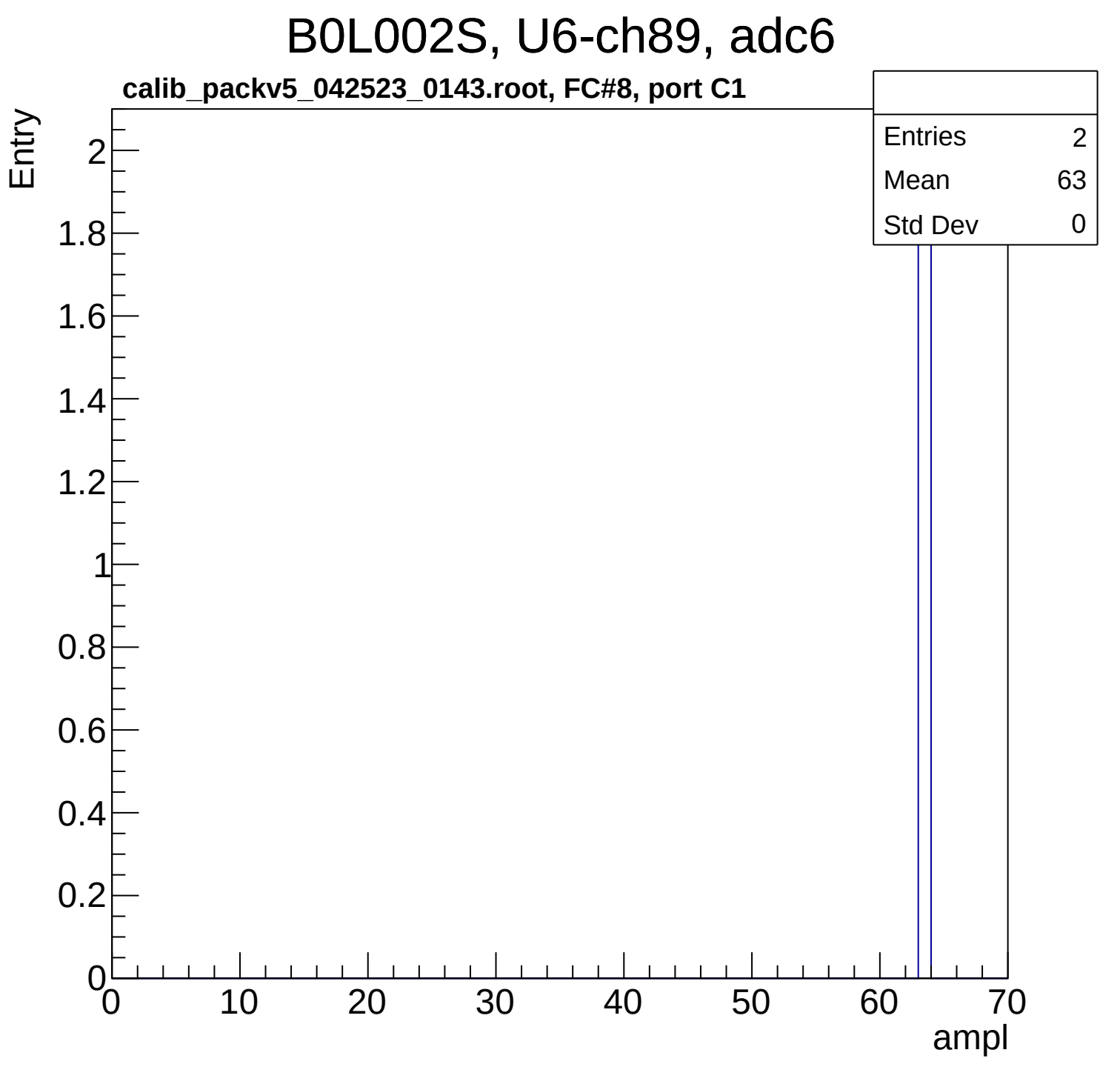
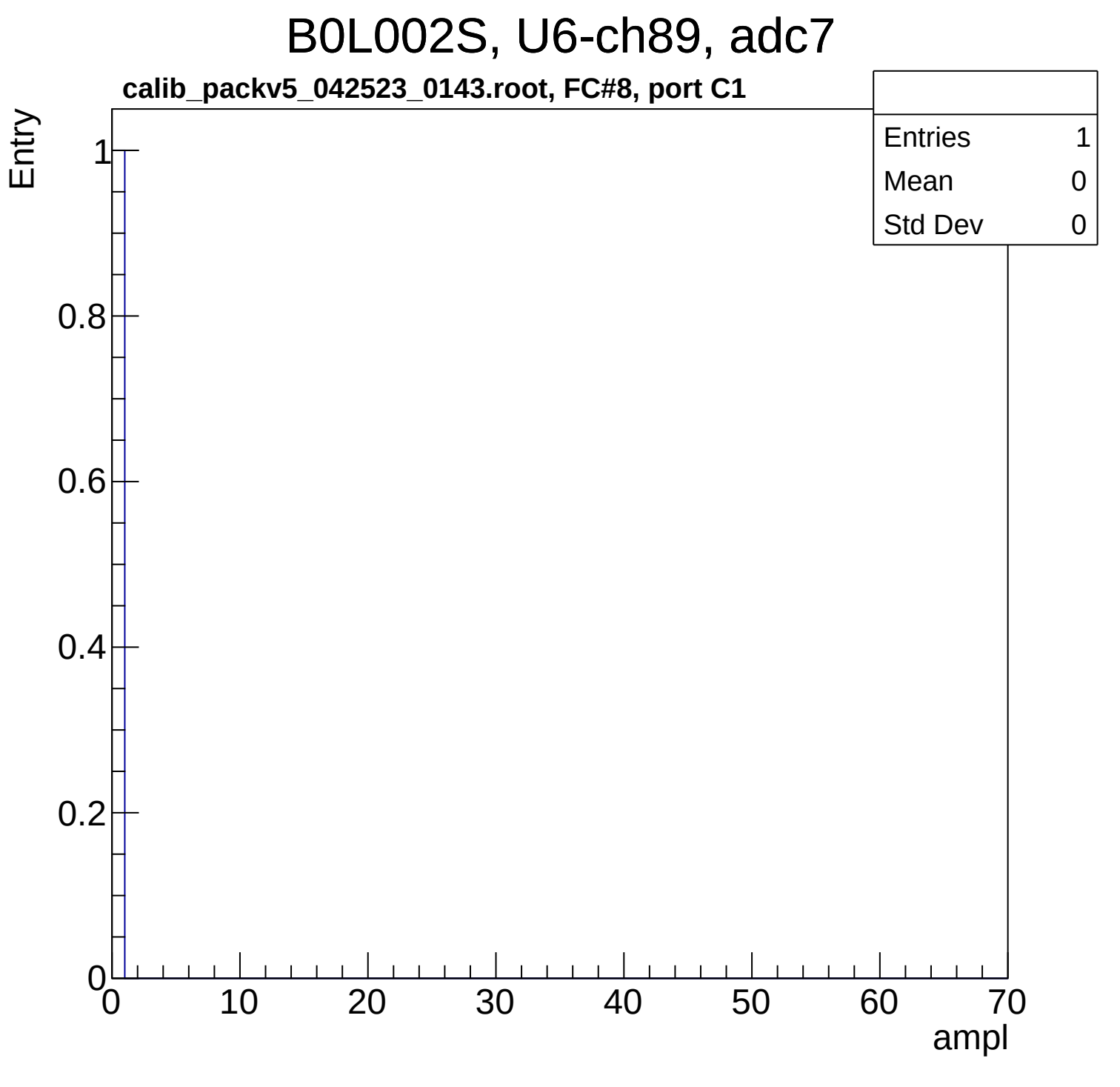
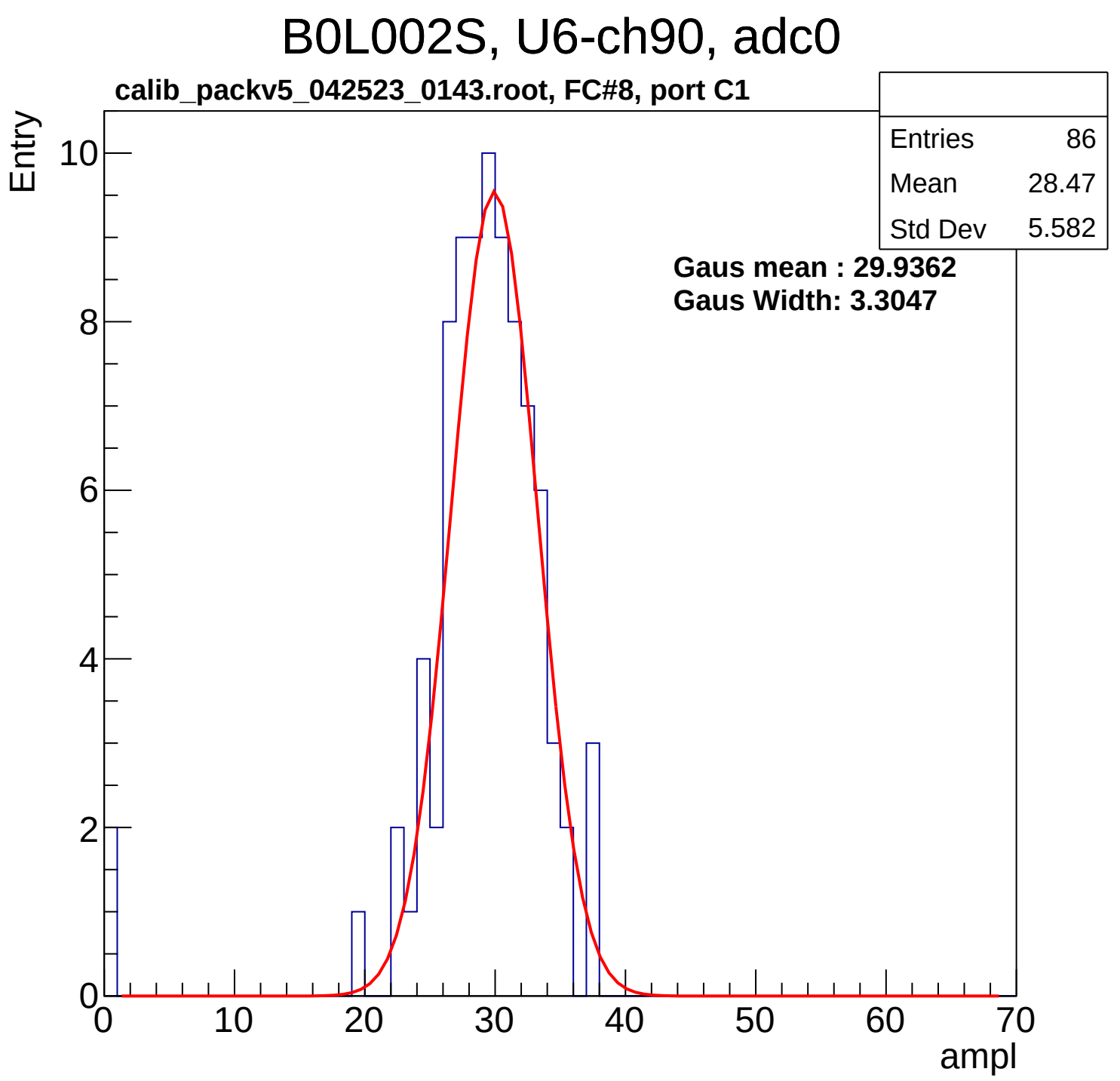

B0L002S, U6-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 50.46
Std Dev 3.113
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

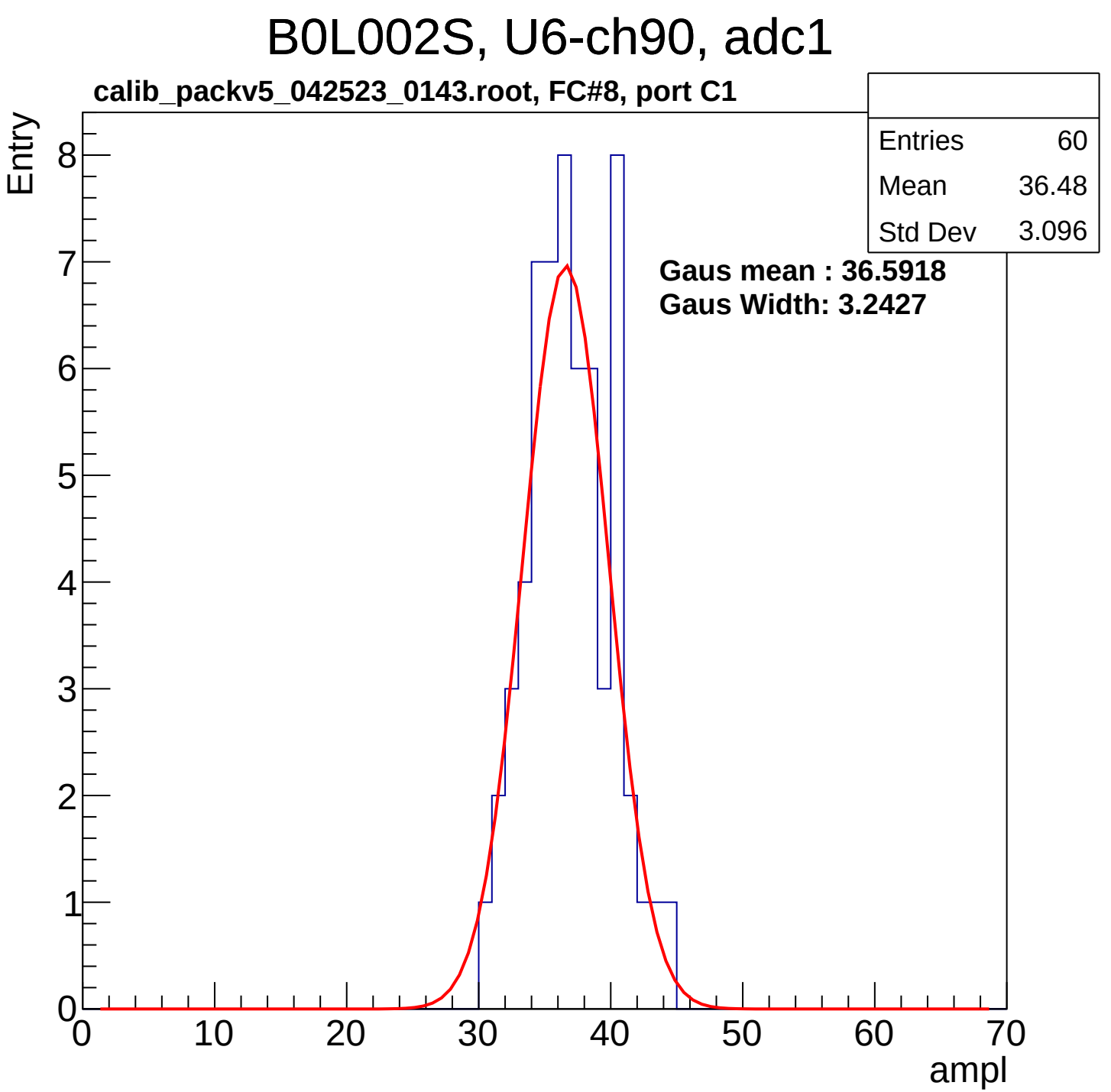

B0L002S, U6-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 55.93
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

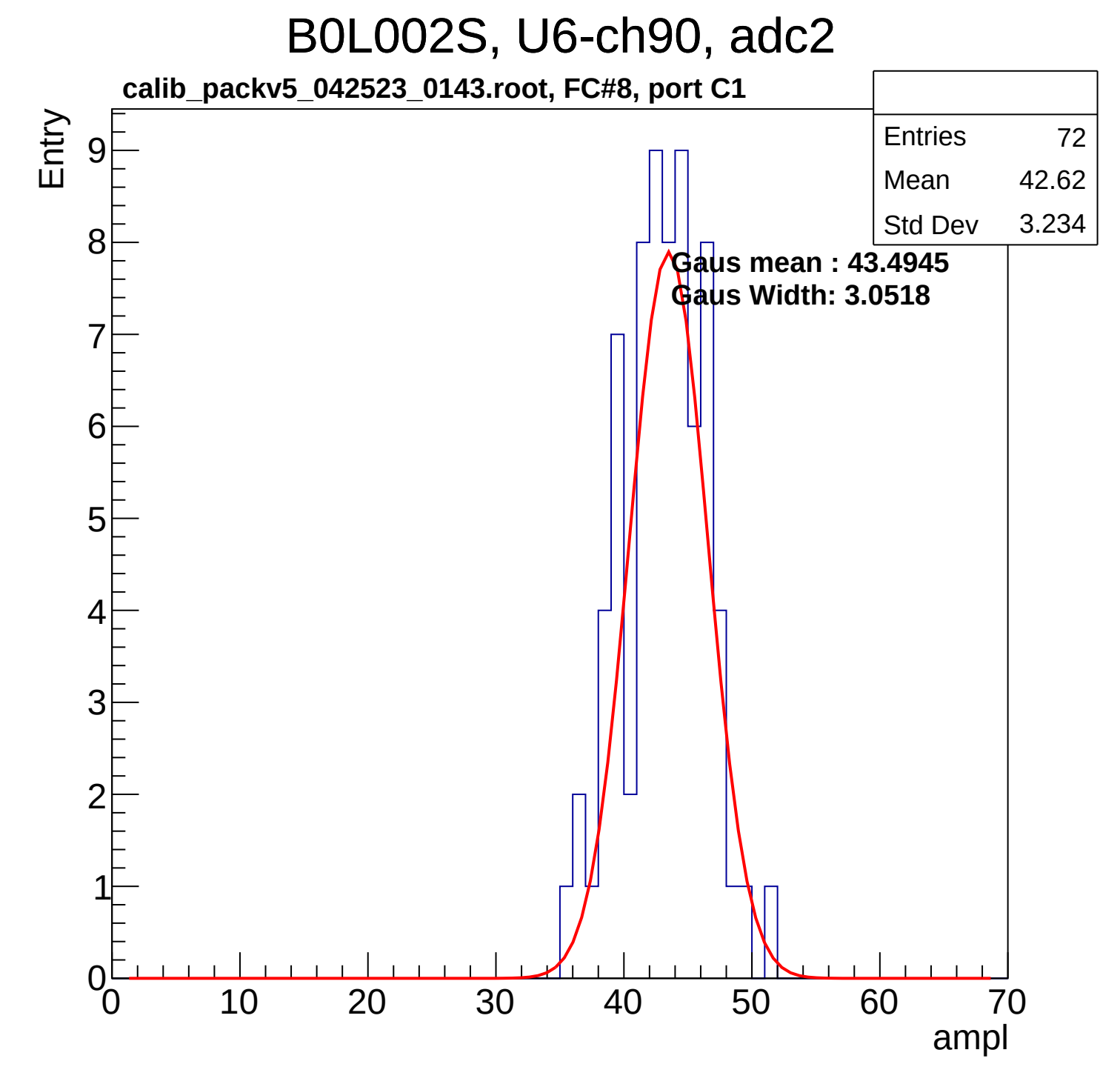

B0L002S, U6-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.15
Std Dev 9.76
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

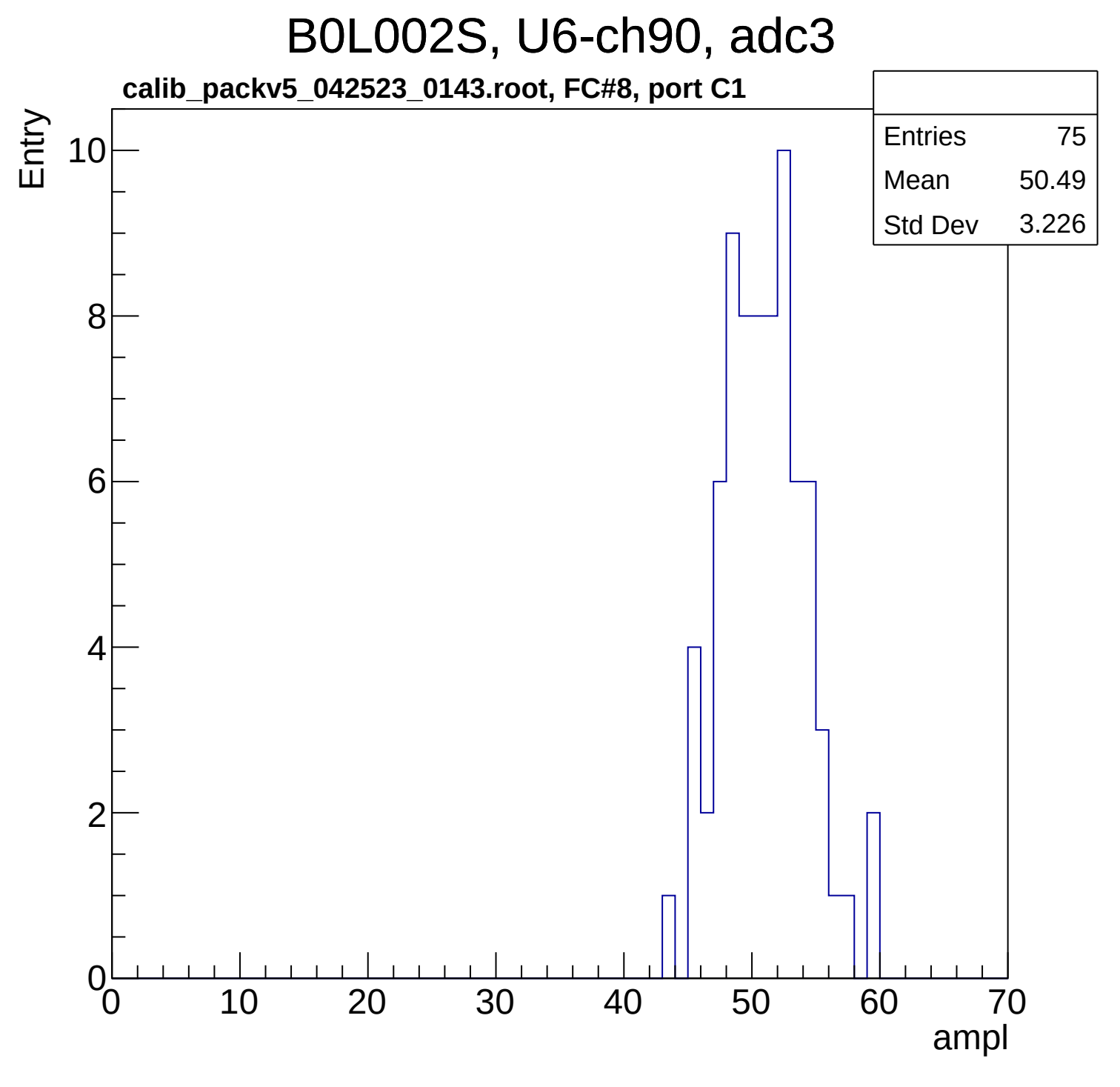

B0L002S, U6-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

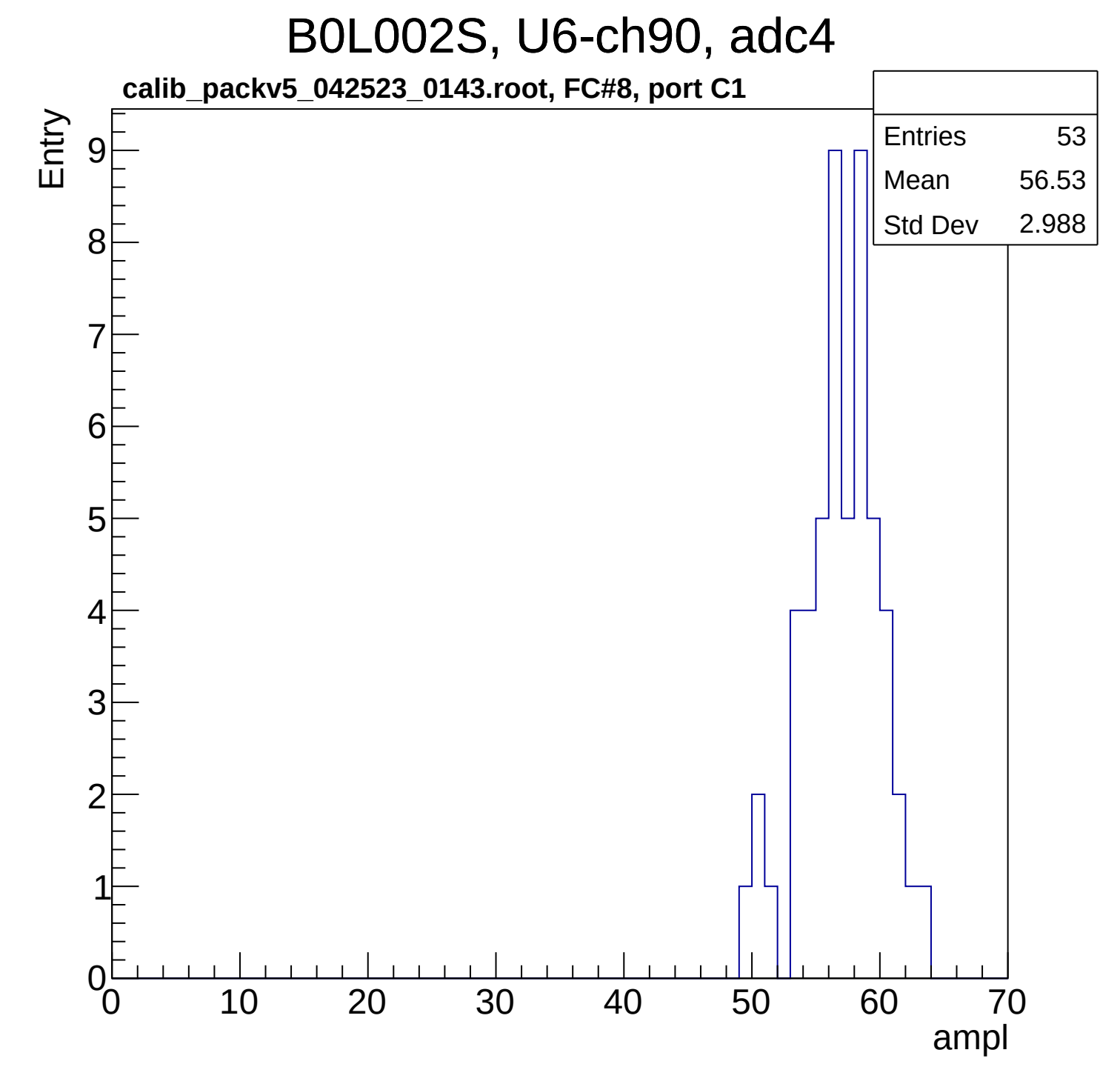

B0L002S, U6-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

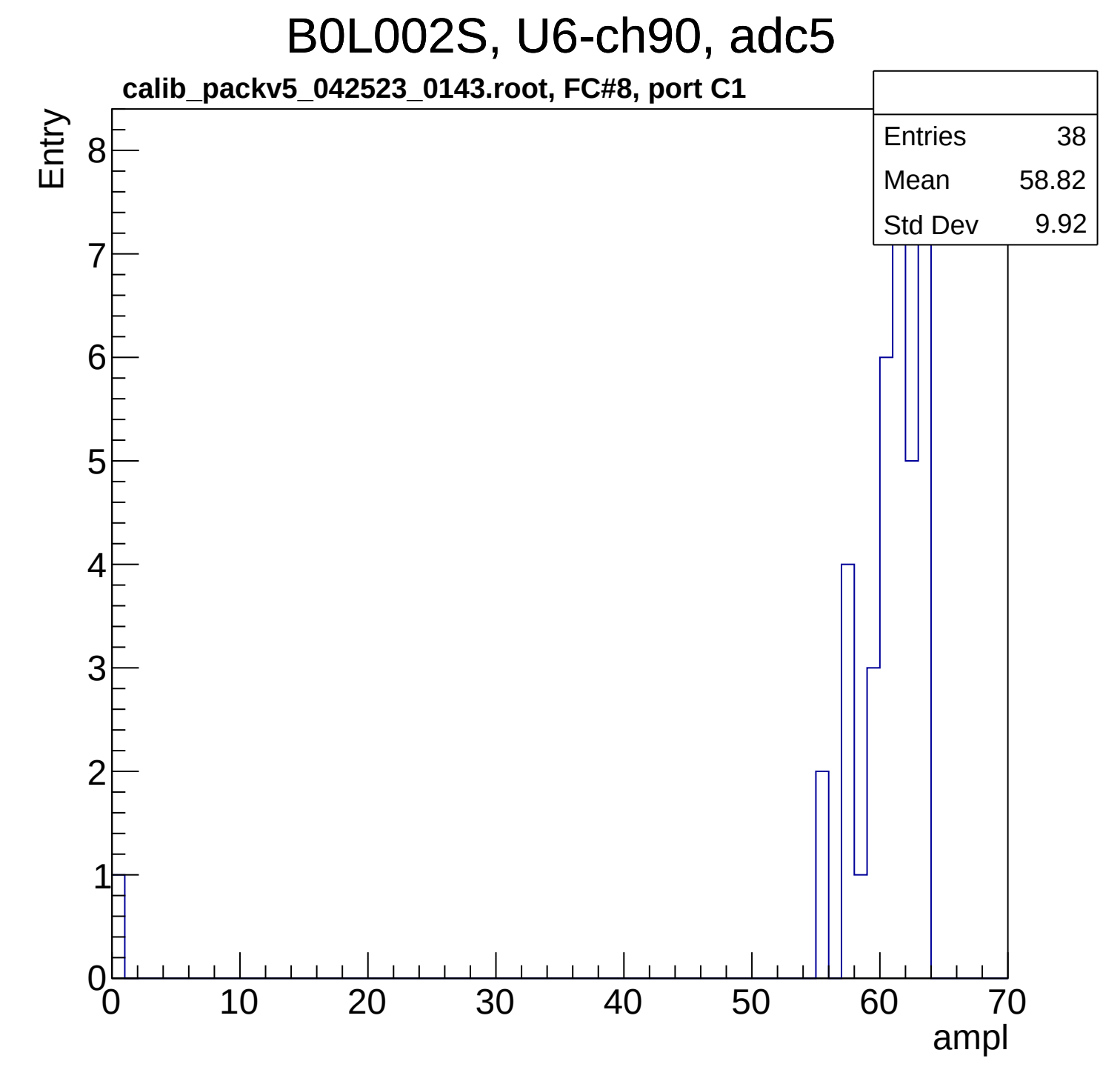

B0L002S, U6-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 28.47
Std Dev 5.582
Gaus mean : 29.9362
Gaus Width: 3.3047
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

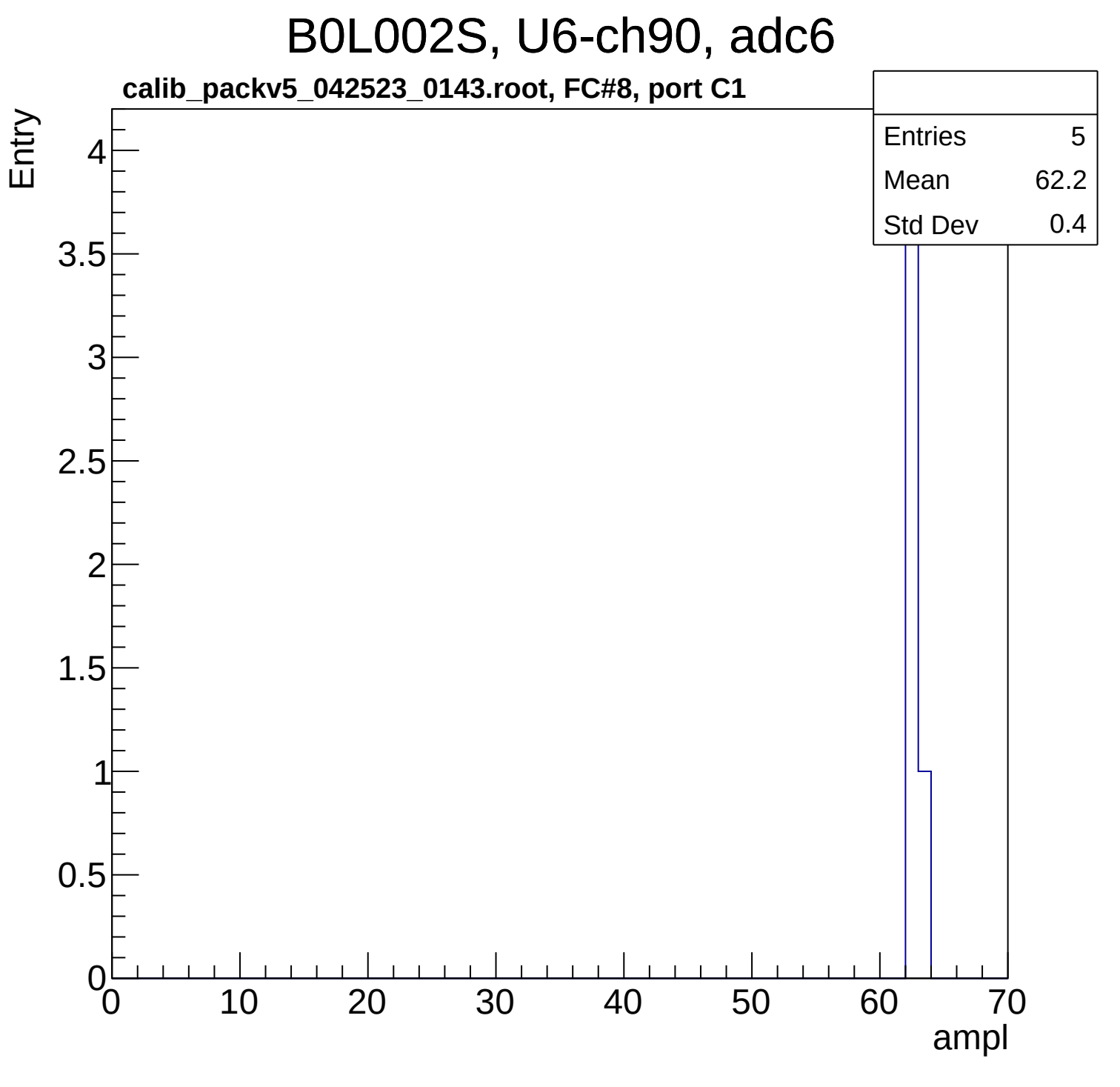

B0L002S, U6-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.48
Std Dev 3.096
Gaus mean : 36.5918
Gaus Width: 3.2427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

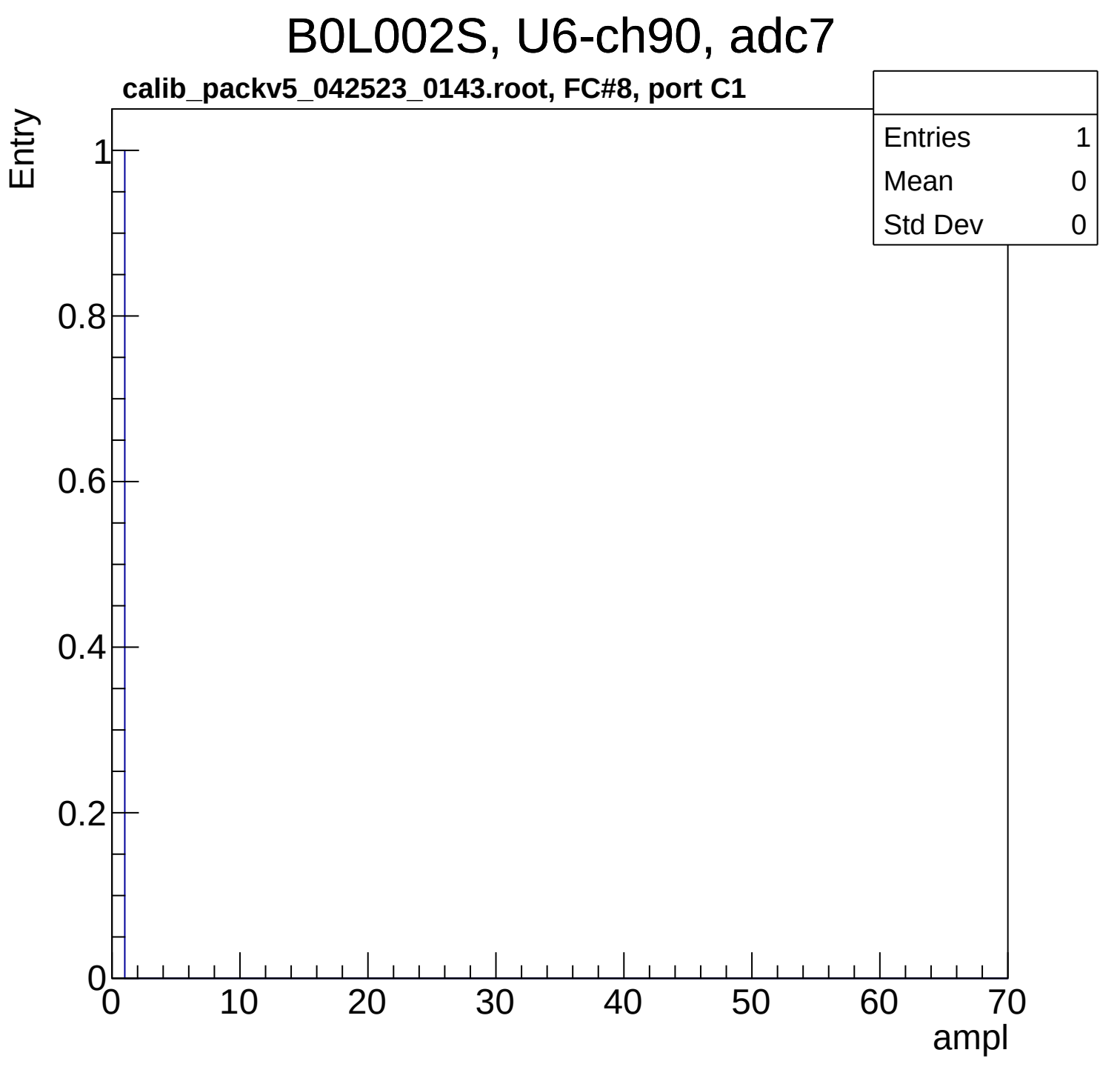

B0L002S, U6-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 42.62
Std Dev 3.234
Gaus mean : 43.4945
Gaus Width: 3.0518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

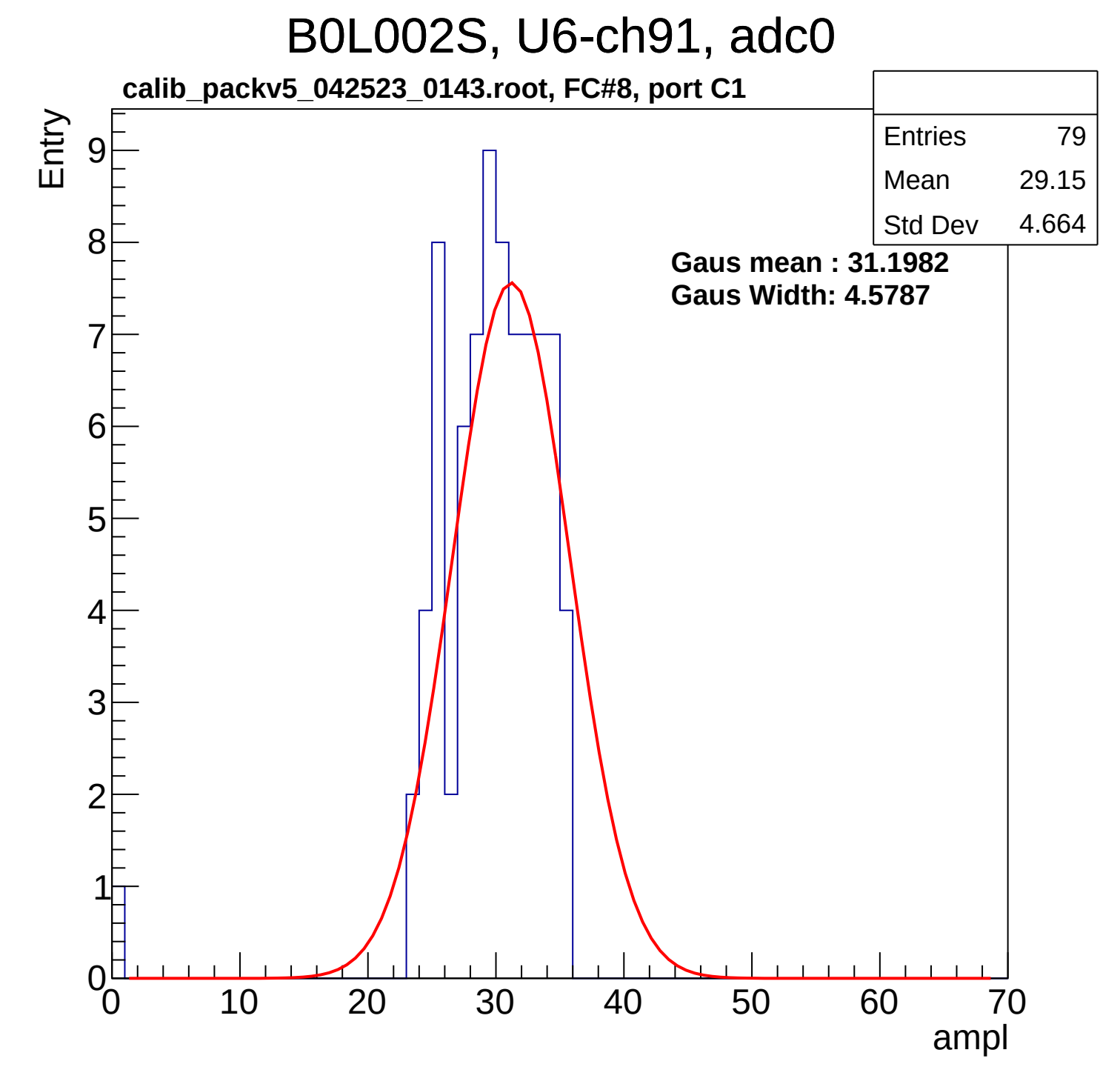

B0L002S, U6-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.49
Std Dev 3.226
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.53
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 58.82
Std Dev 9.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.2
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U6-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.15
Std Dev 4.664
Gaus mean : 31.1982
Gaus Width: 4.5787
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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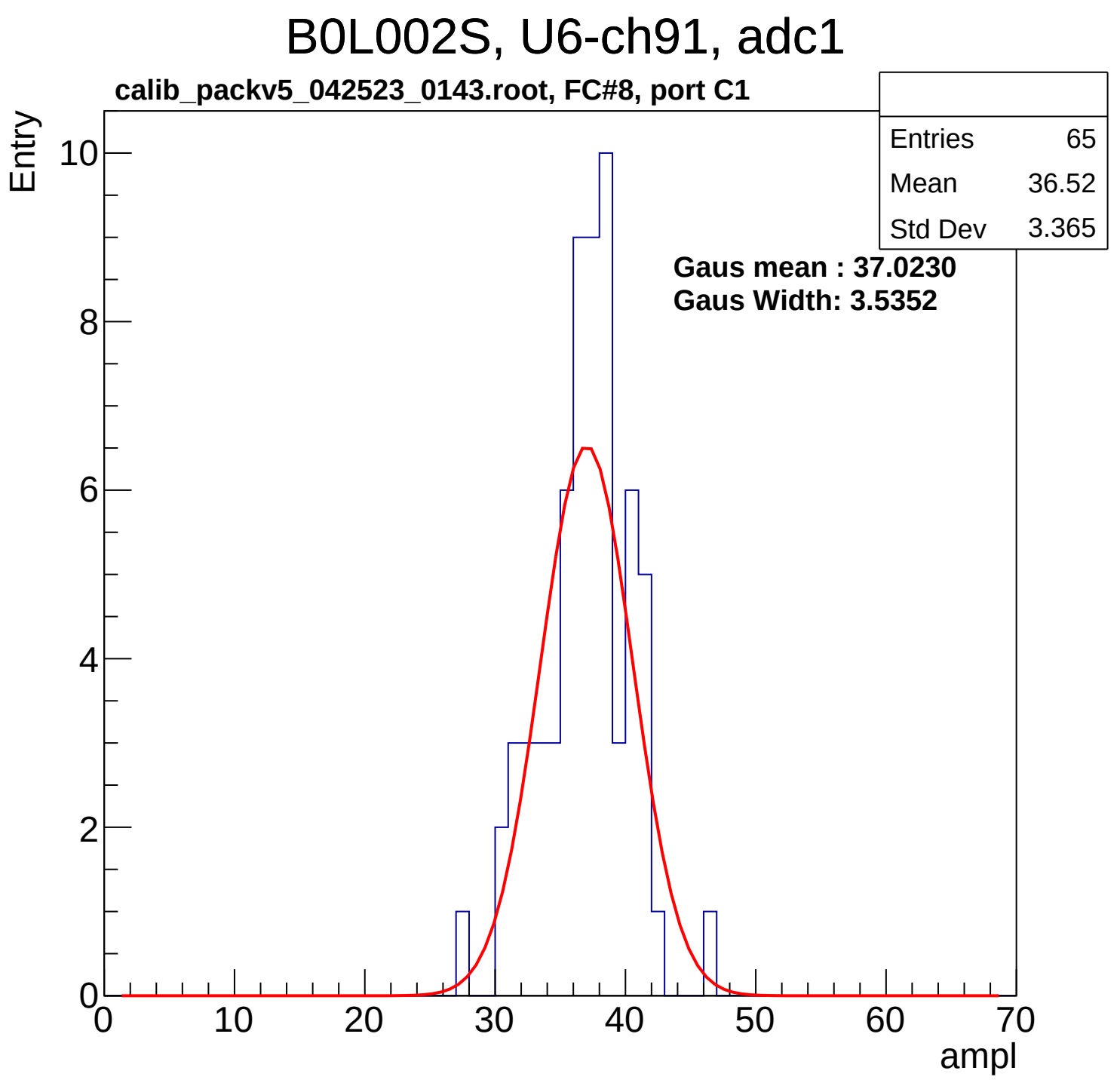
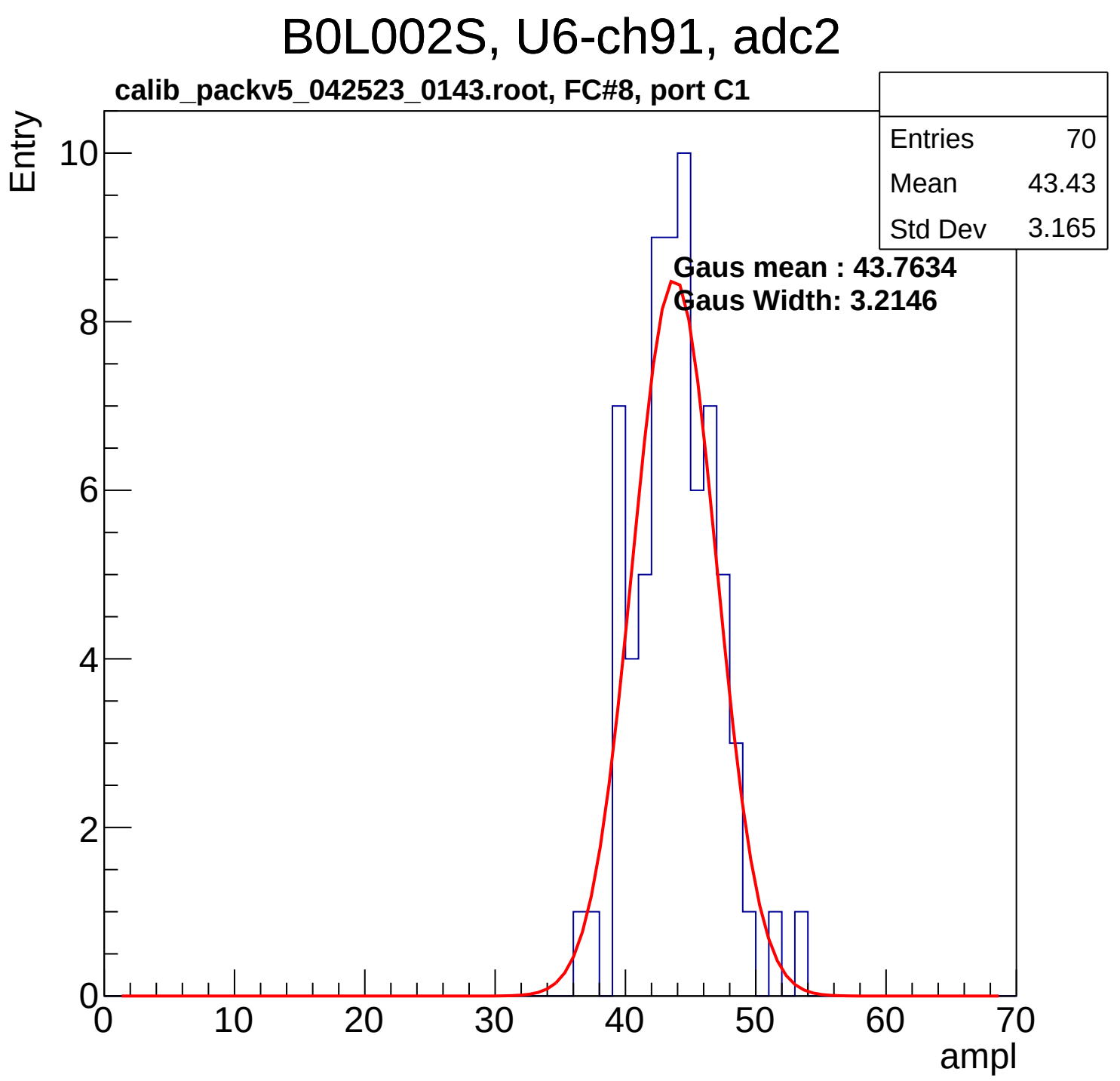
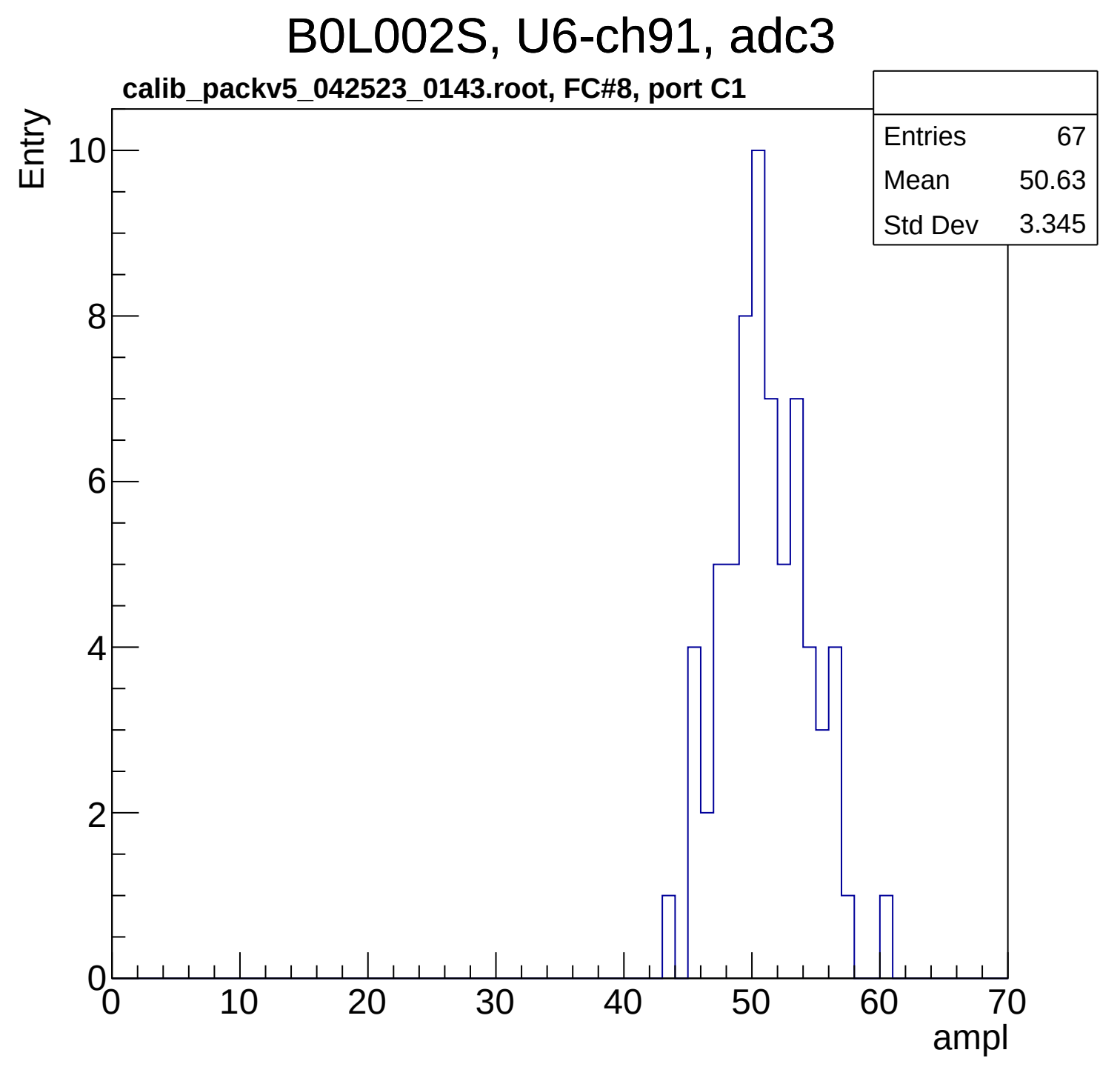
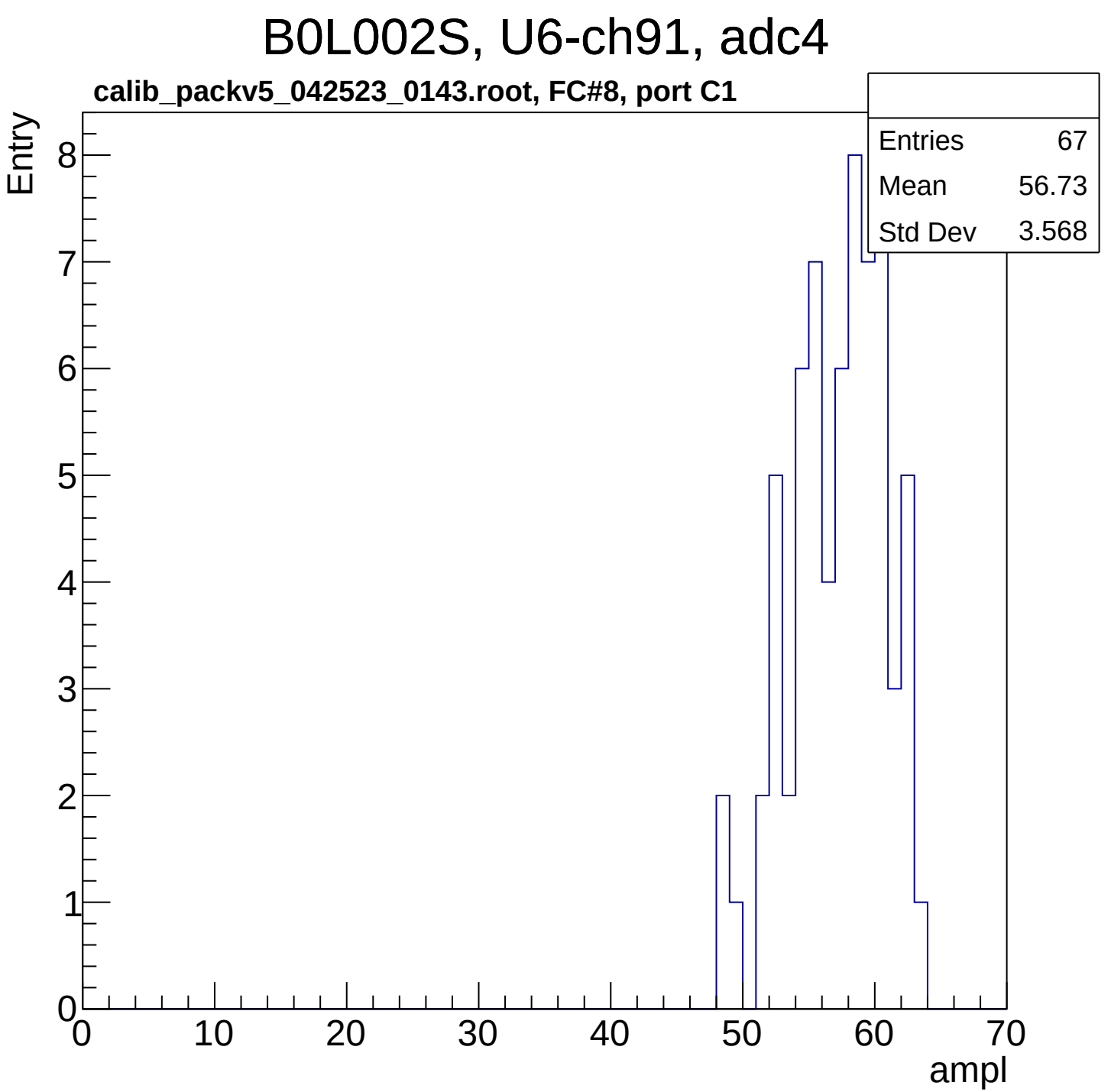

B0L002S, U6-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.52
Std Dev 3.365
Gaus mean : 37.0230
Gaus Width: 3.5352
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.43
Std Dev 3.165
Gaus mean : 43.7634
Gaus Width: 3.2146
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.63
Std Dev 3.345
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.73
Std Dev 3.568
Entry
ampl
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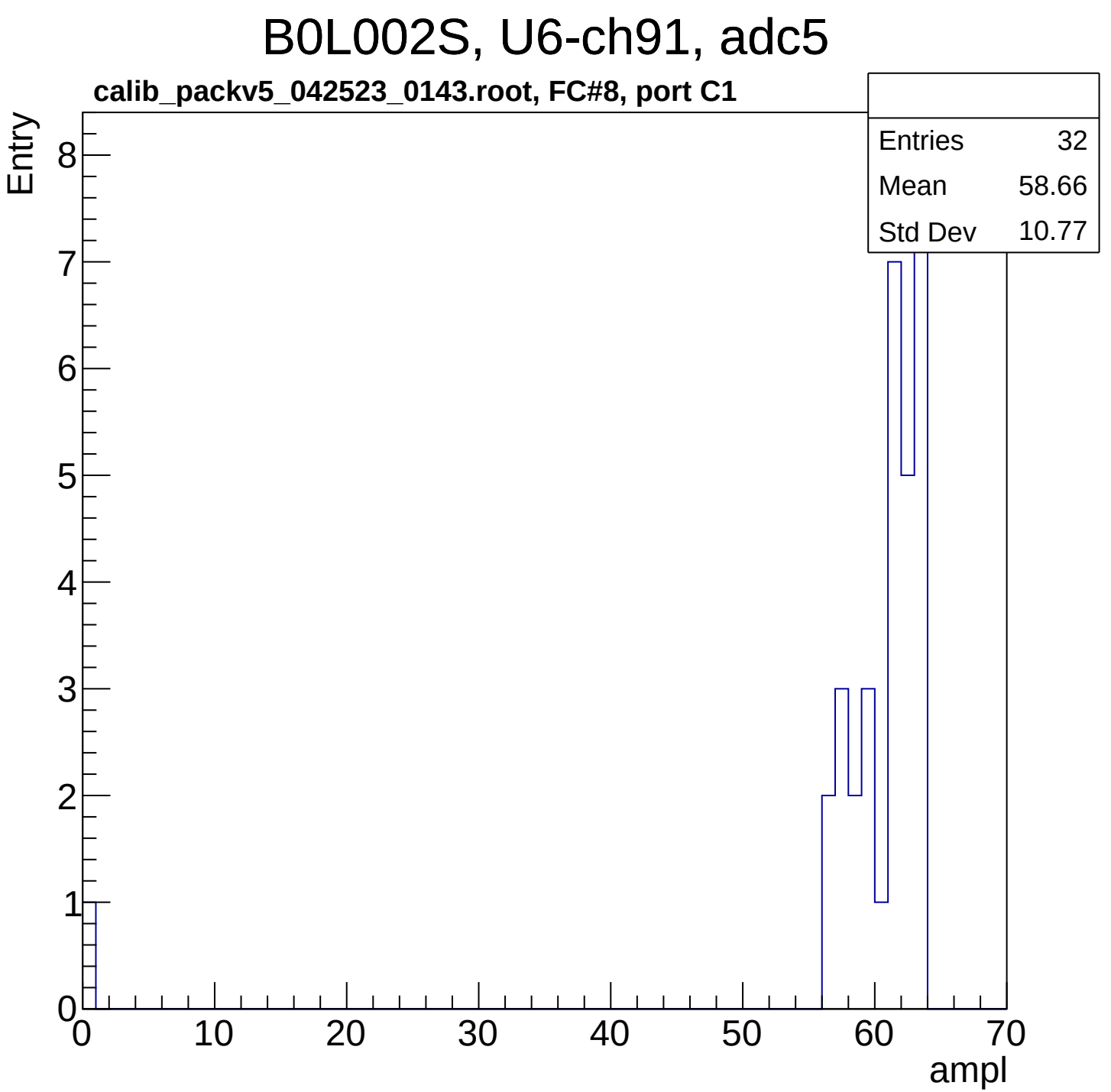
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.66
Std Dev 10.77
Entry
ampl
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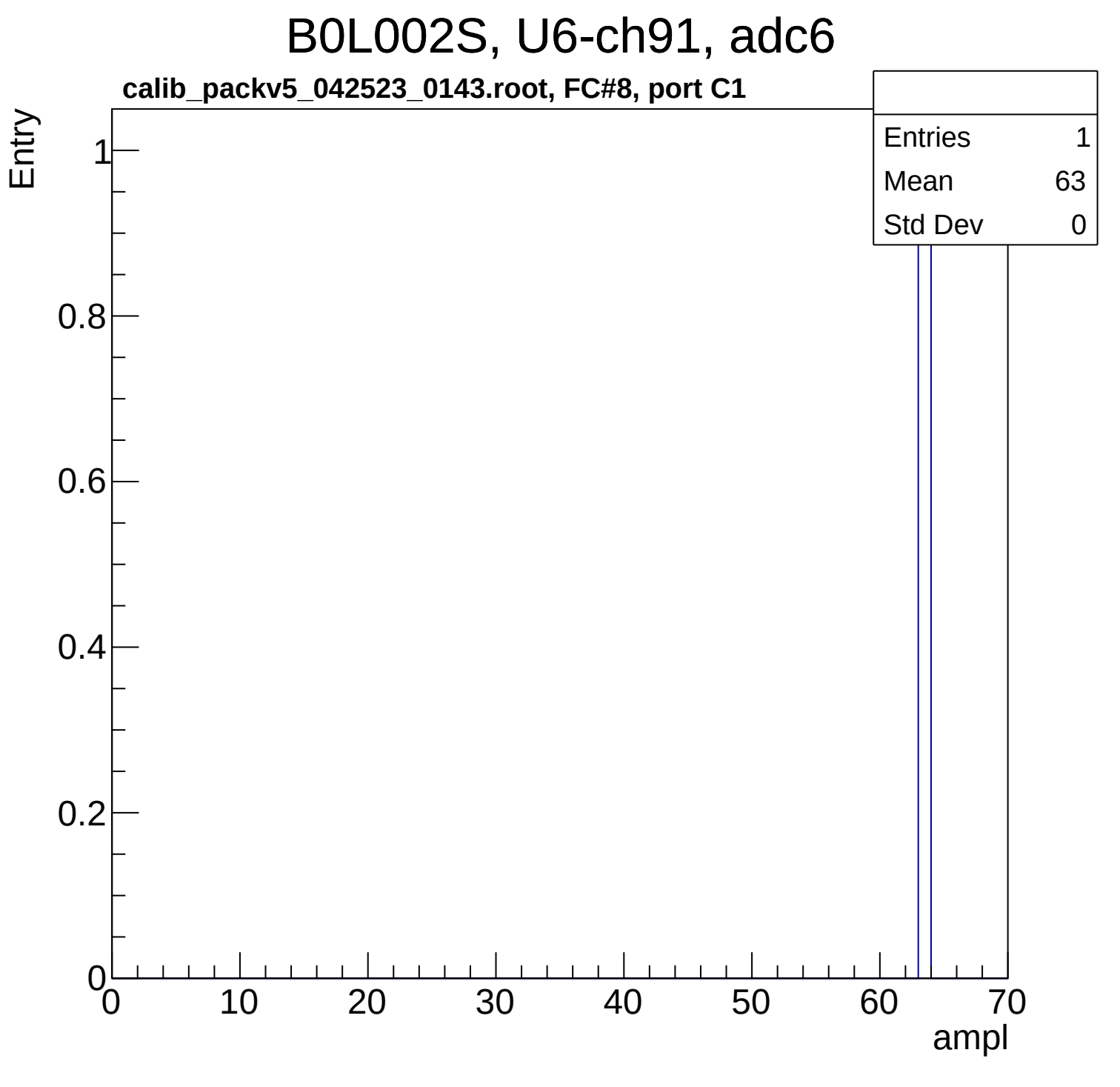
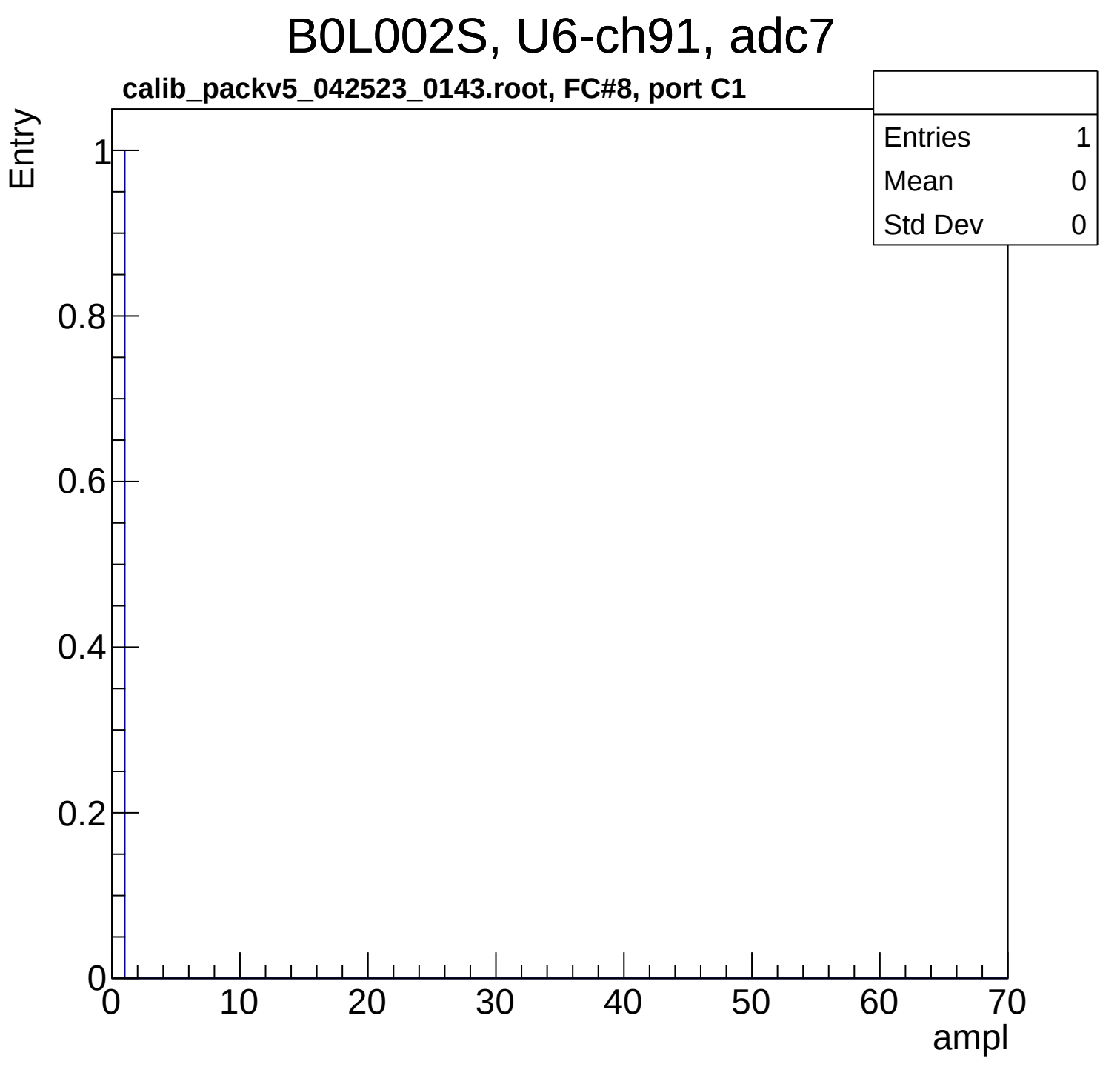
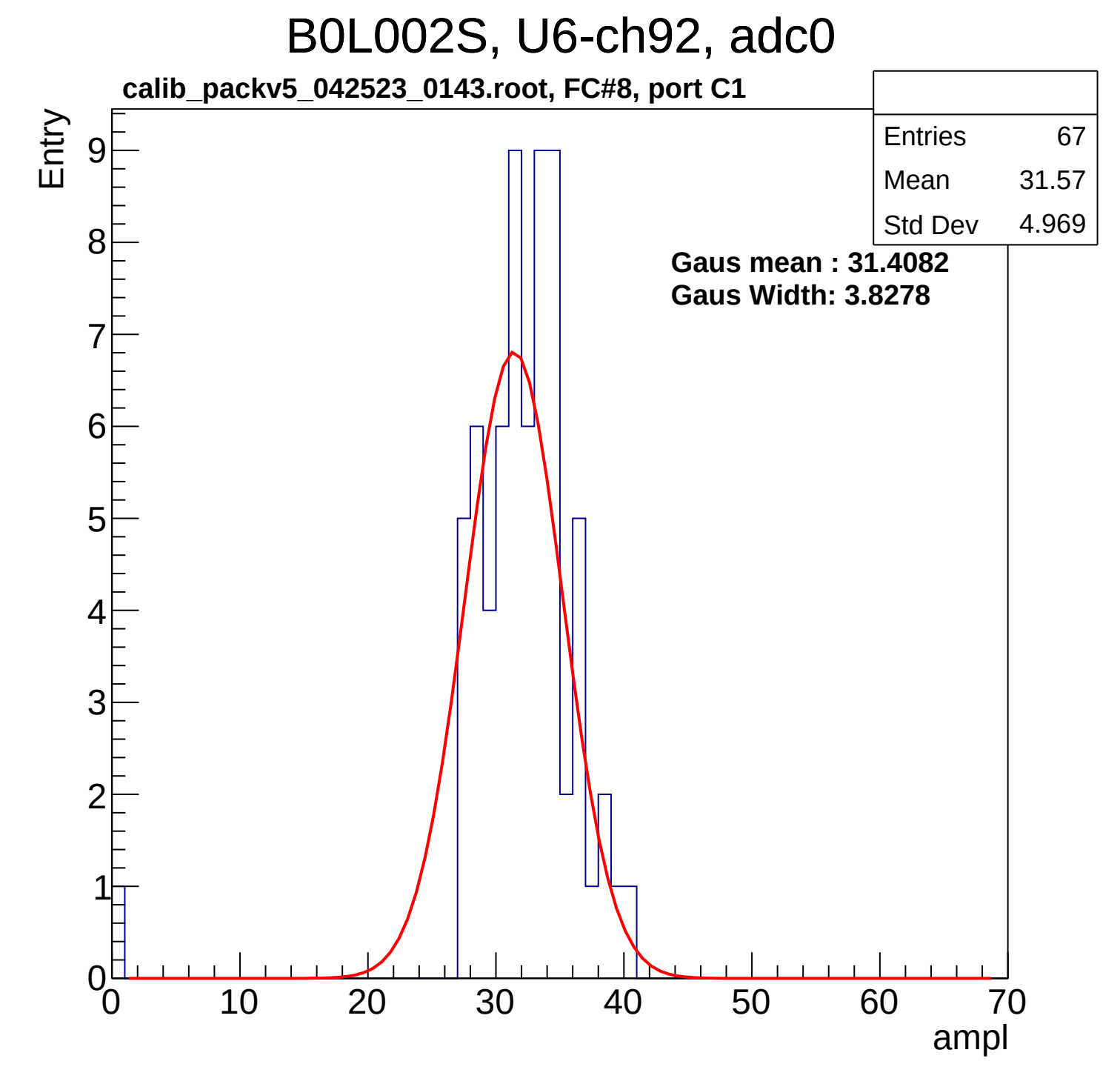
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.57
Std Dev 4.969
Gaus mean : 31.4082
Gaus Width: 3.8278
Entry
ampl
0
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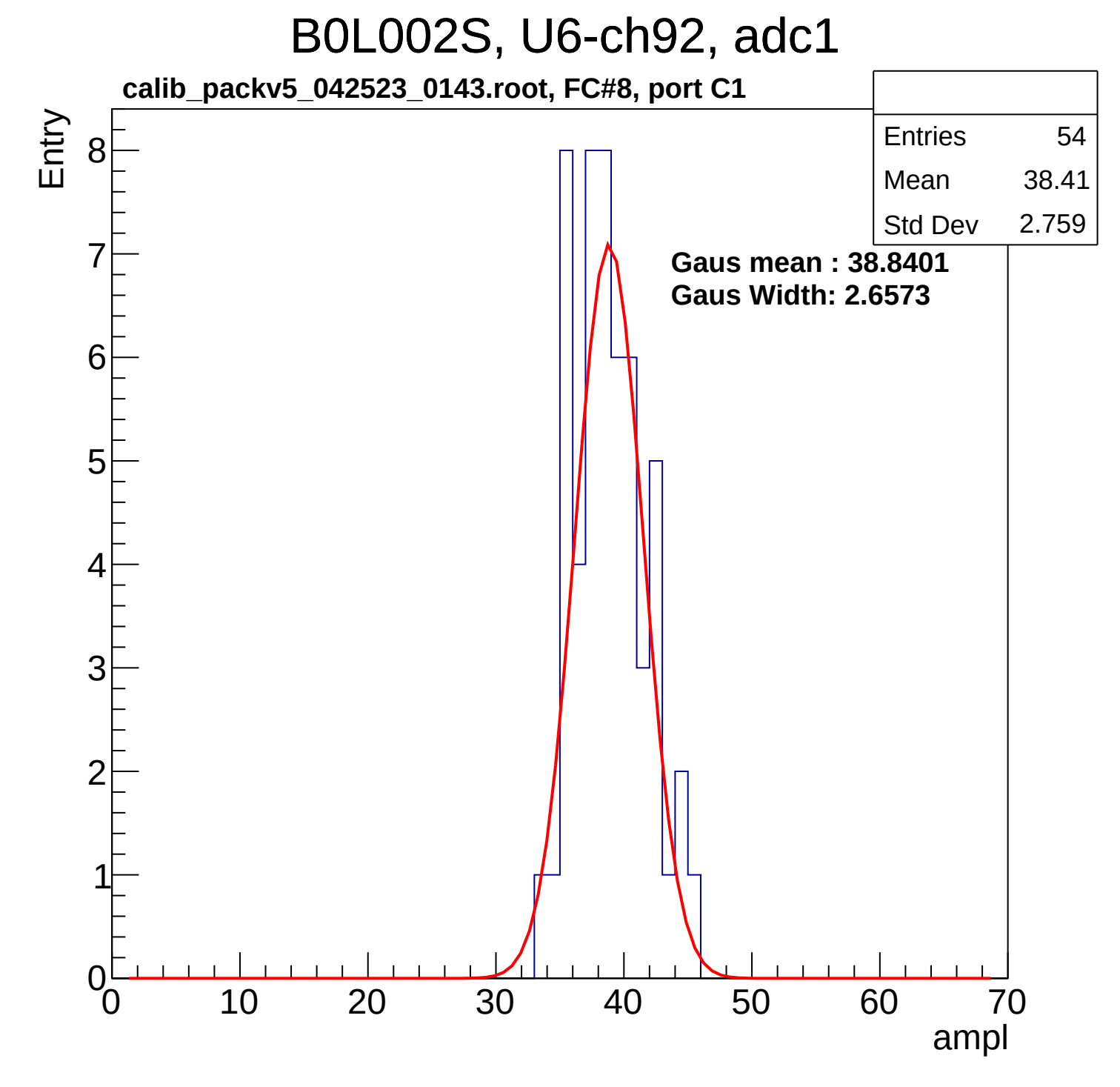
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 38.41
Std Dev 2.759
Gaus mean : 38.8401
Gaus Width: 2.6573
Entry
ampl
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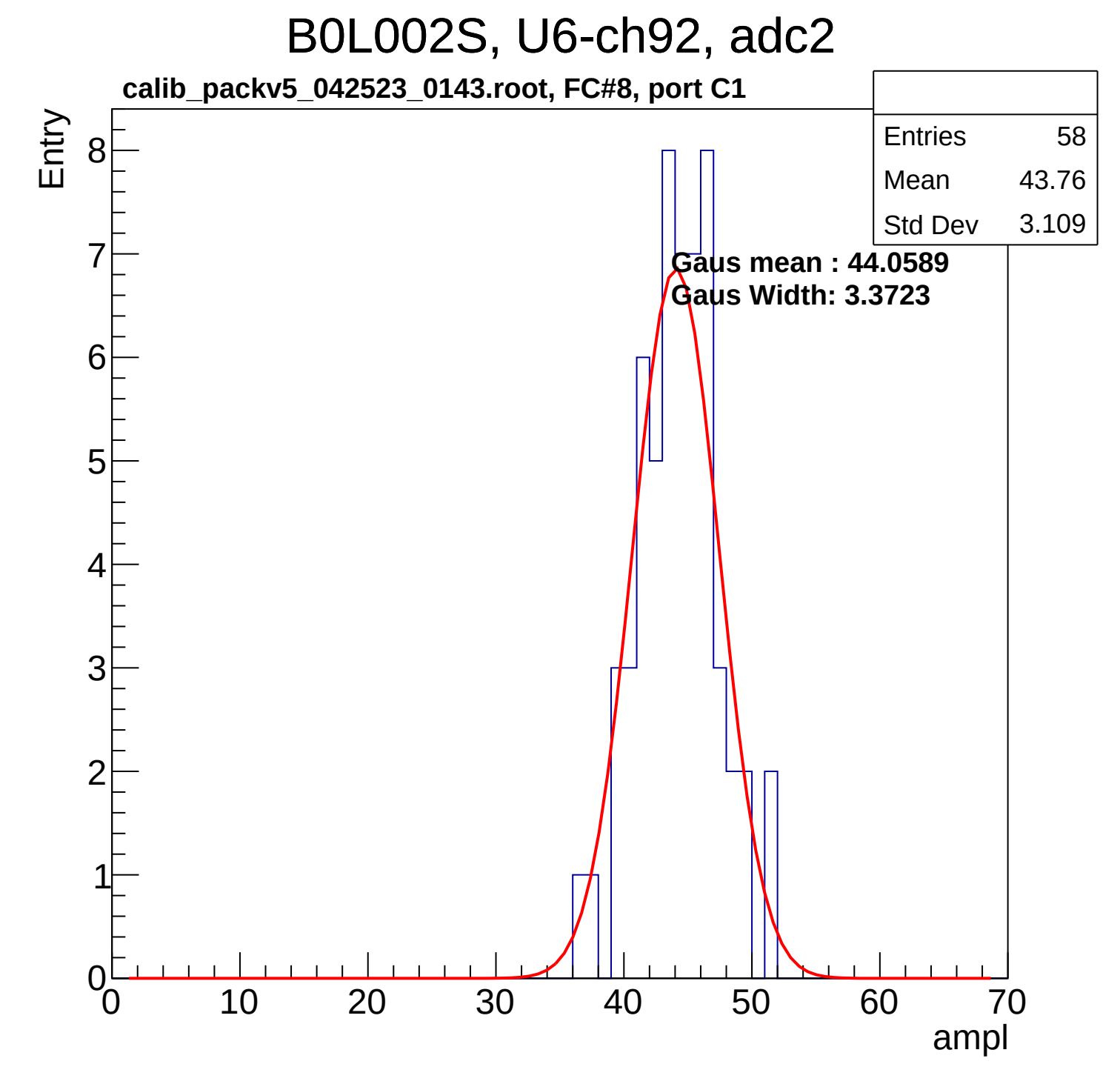
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.76
Std Dev 3.109
Gaus mean : 44.0589
Gaus Width: 3.3723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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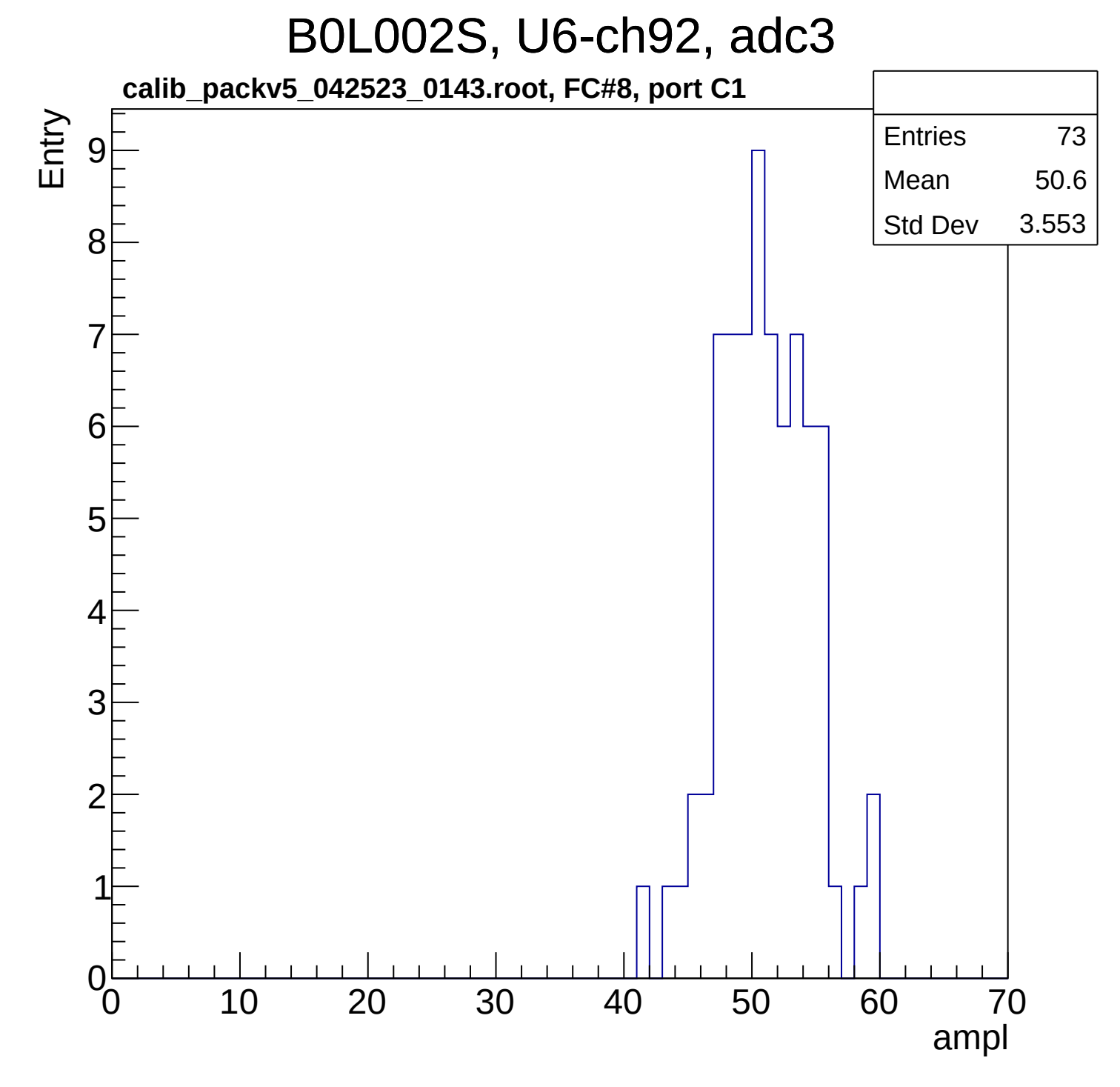
B0L002S, U6-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.6
Std Dev 3.553
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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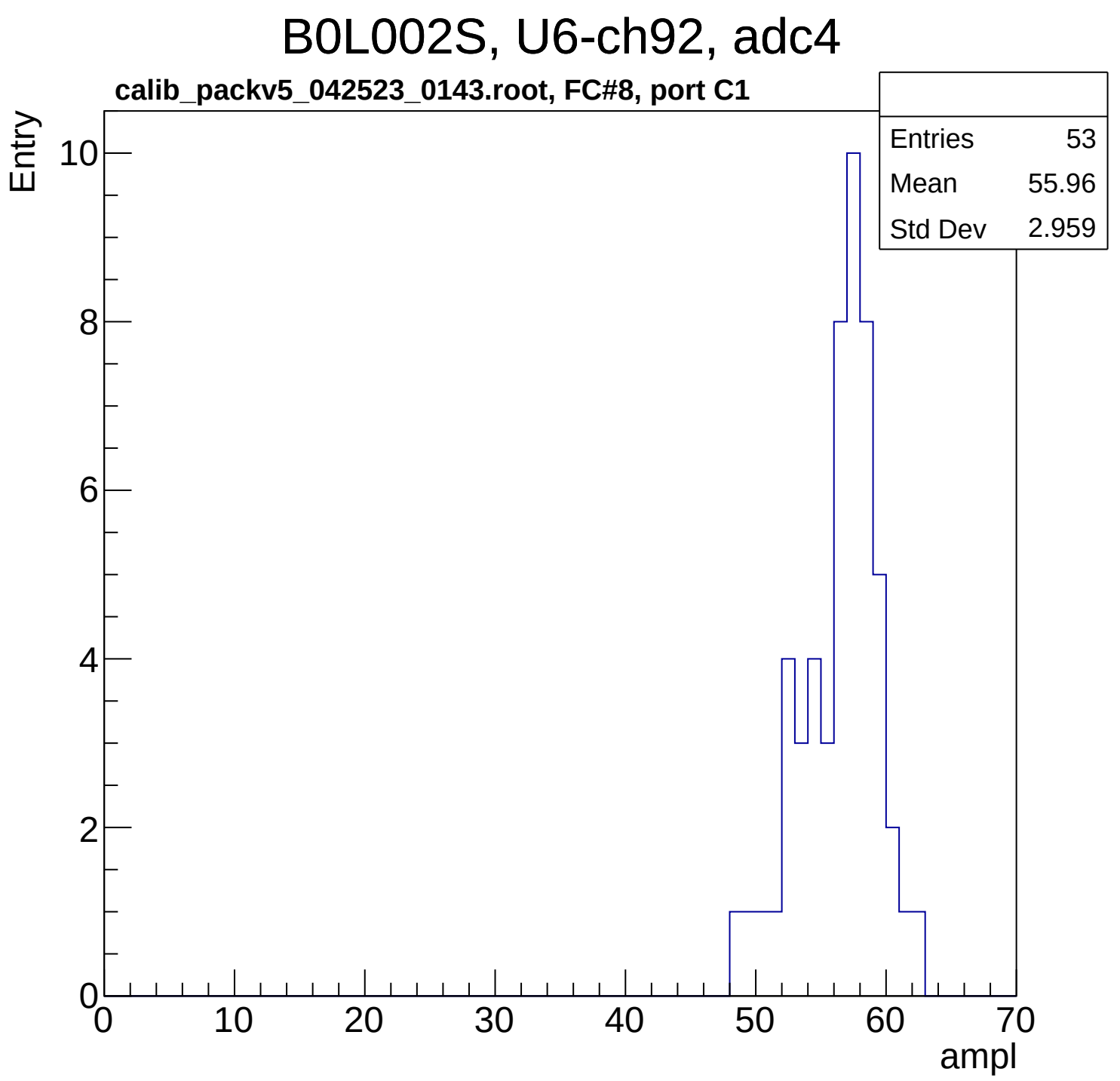
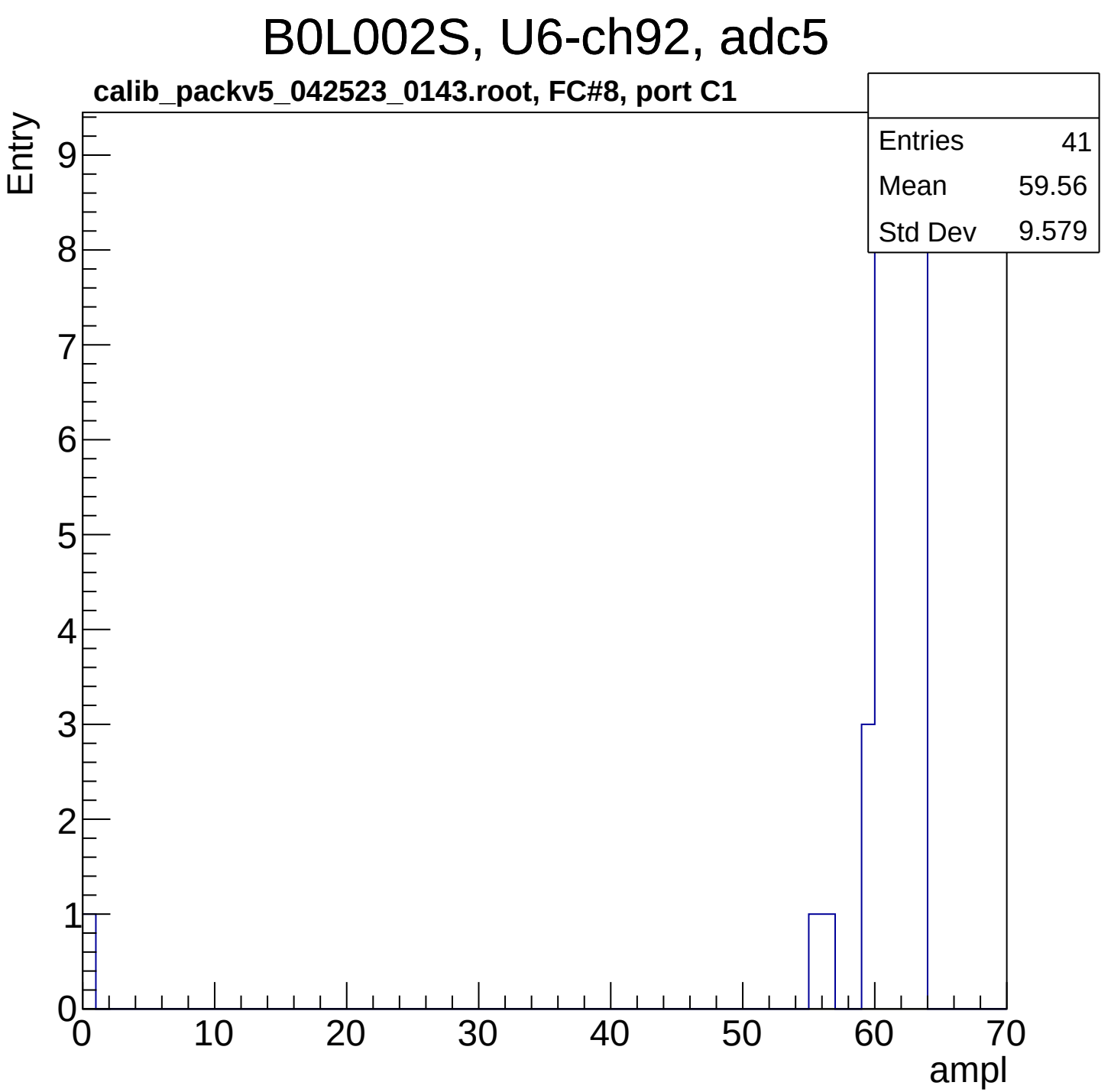
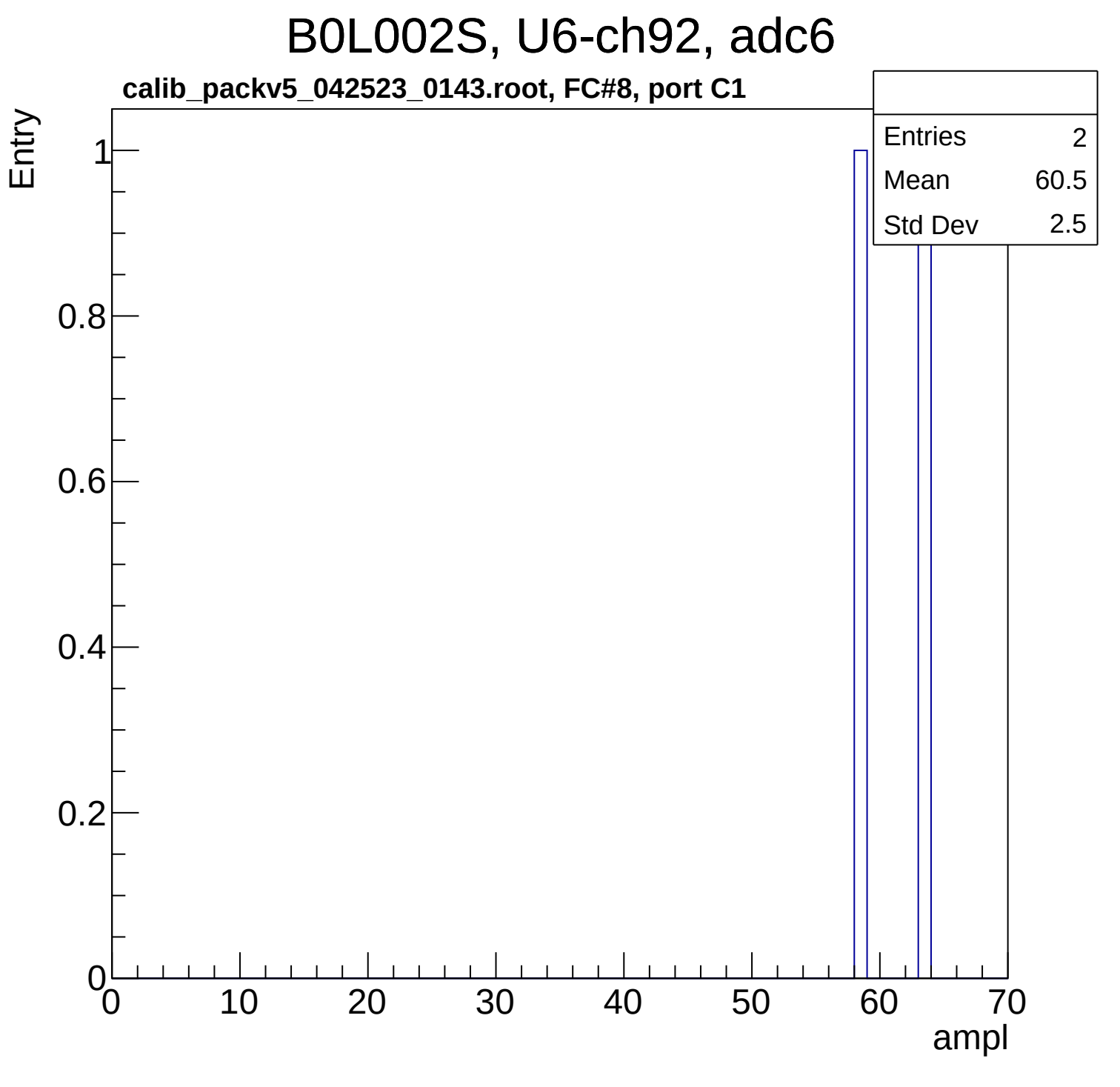
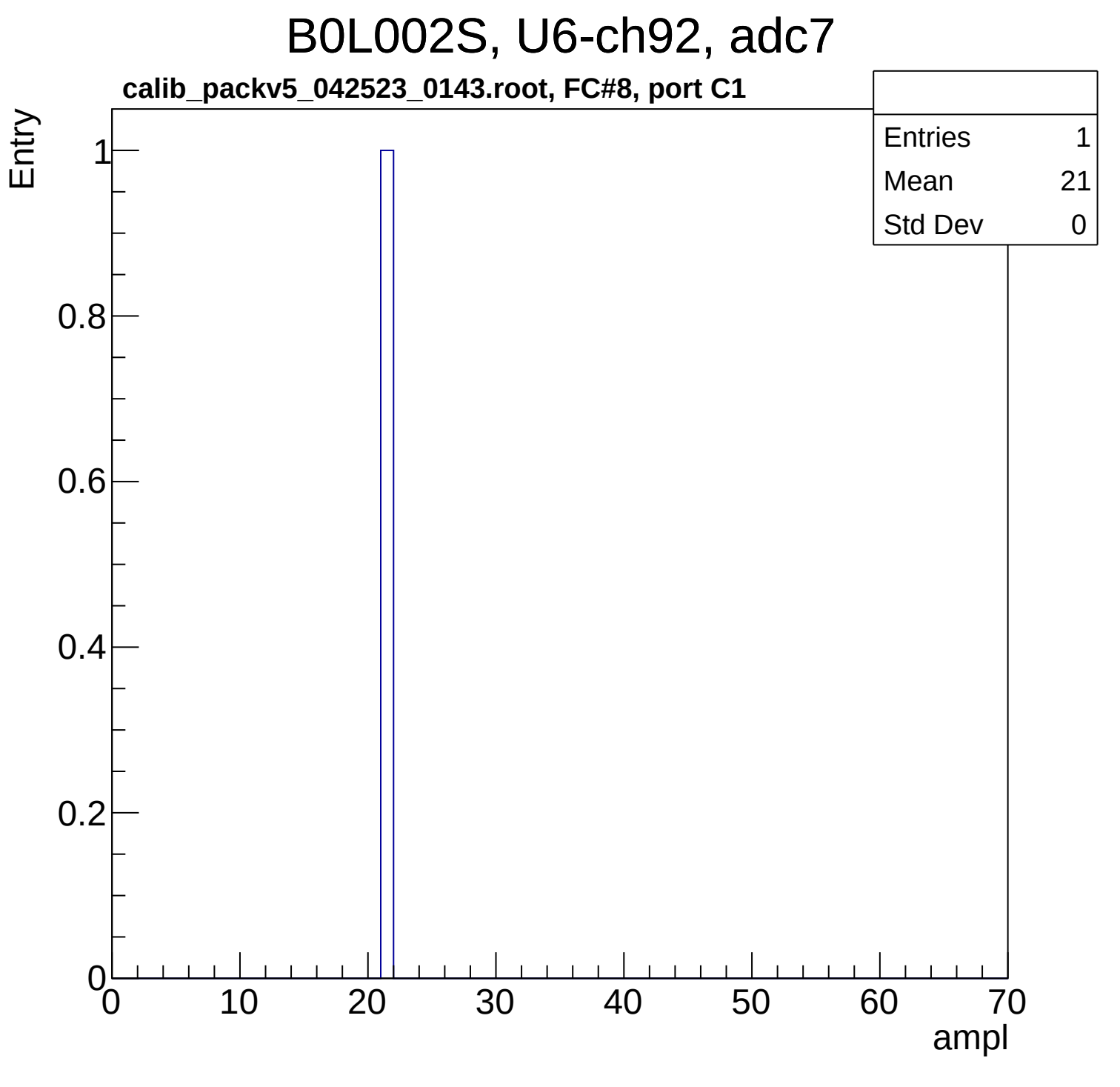
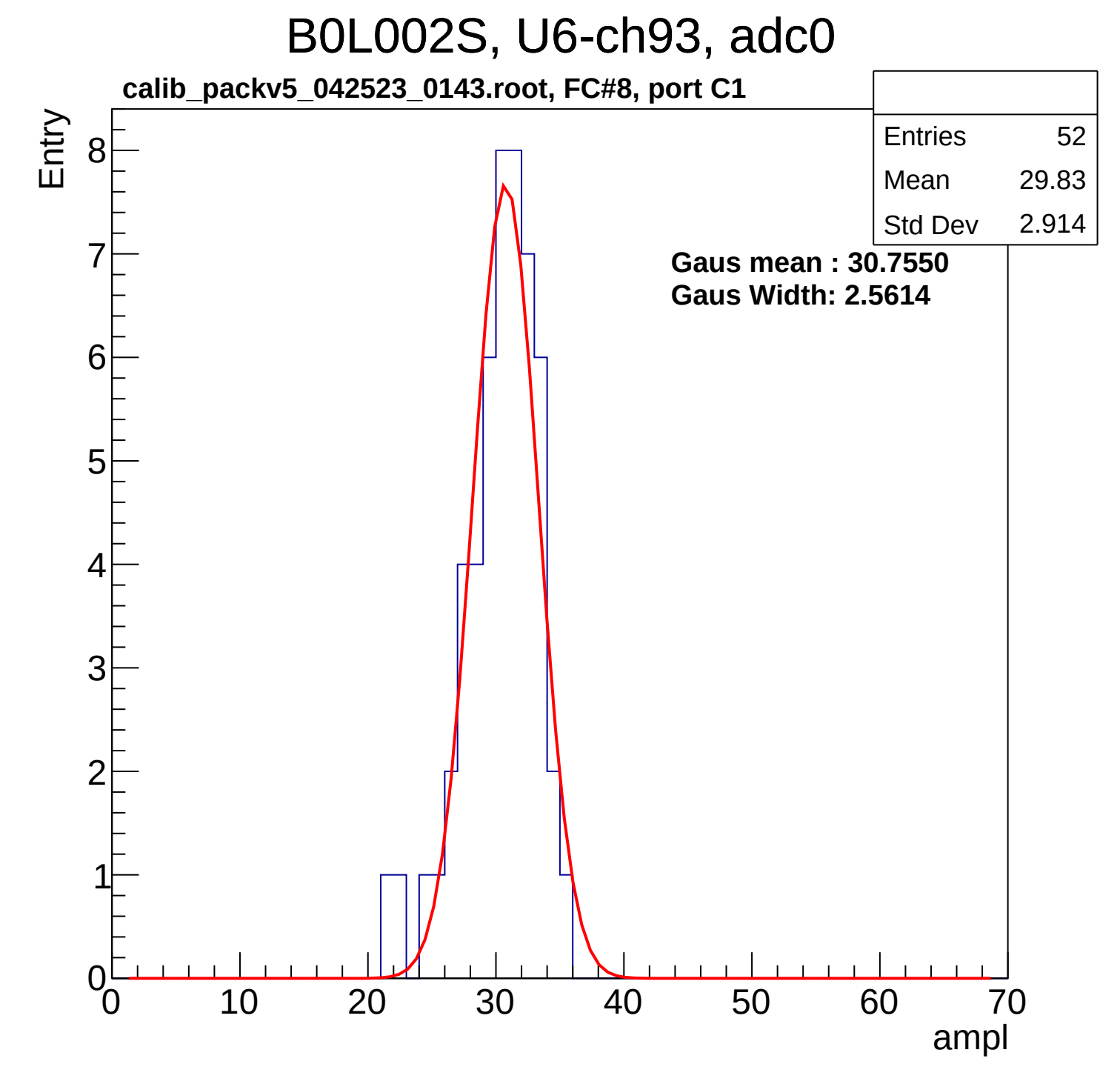
40
50
60
70

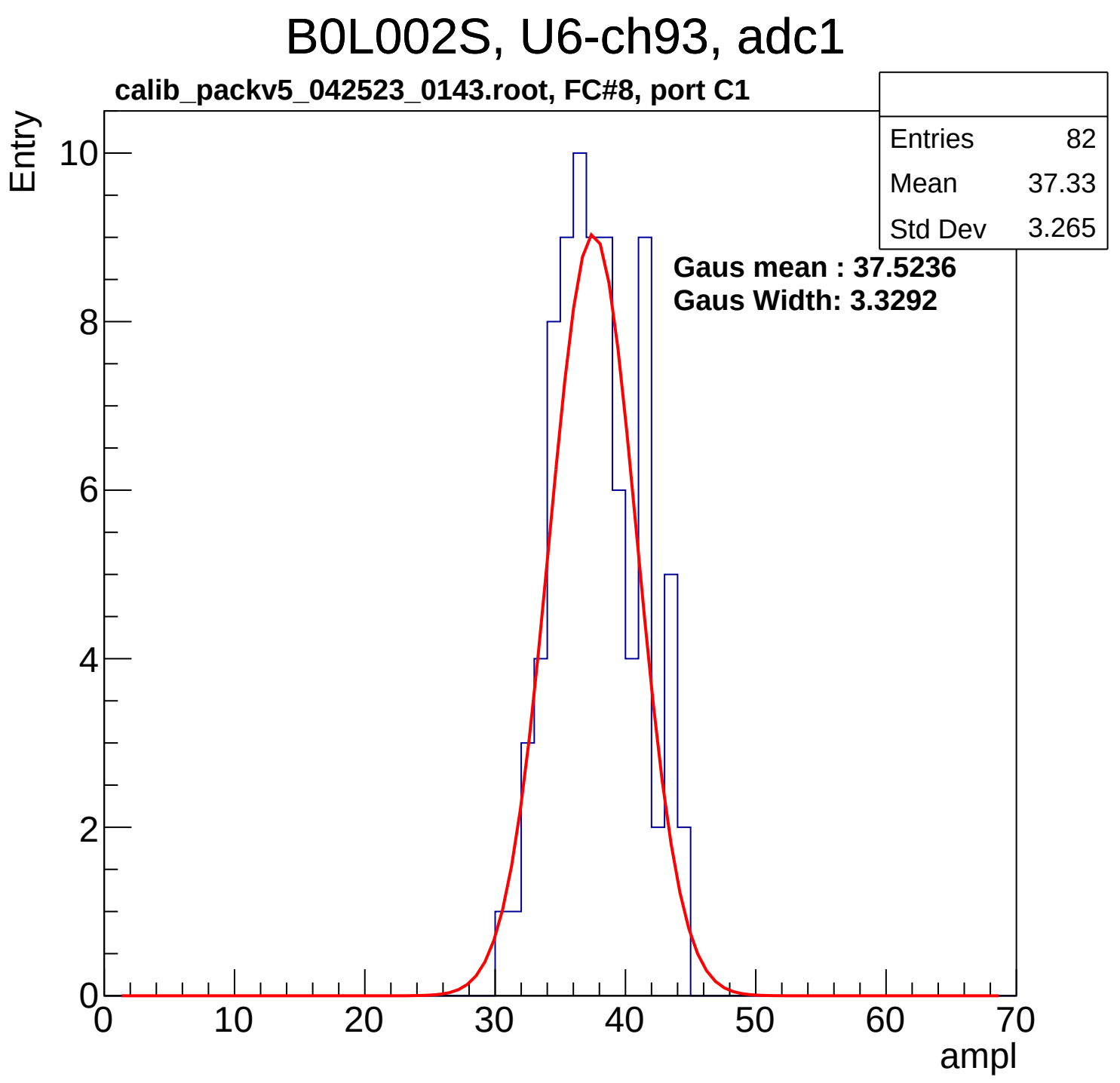

B0L002S, U6-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 55.96
Std Dev 2.959
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

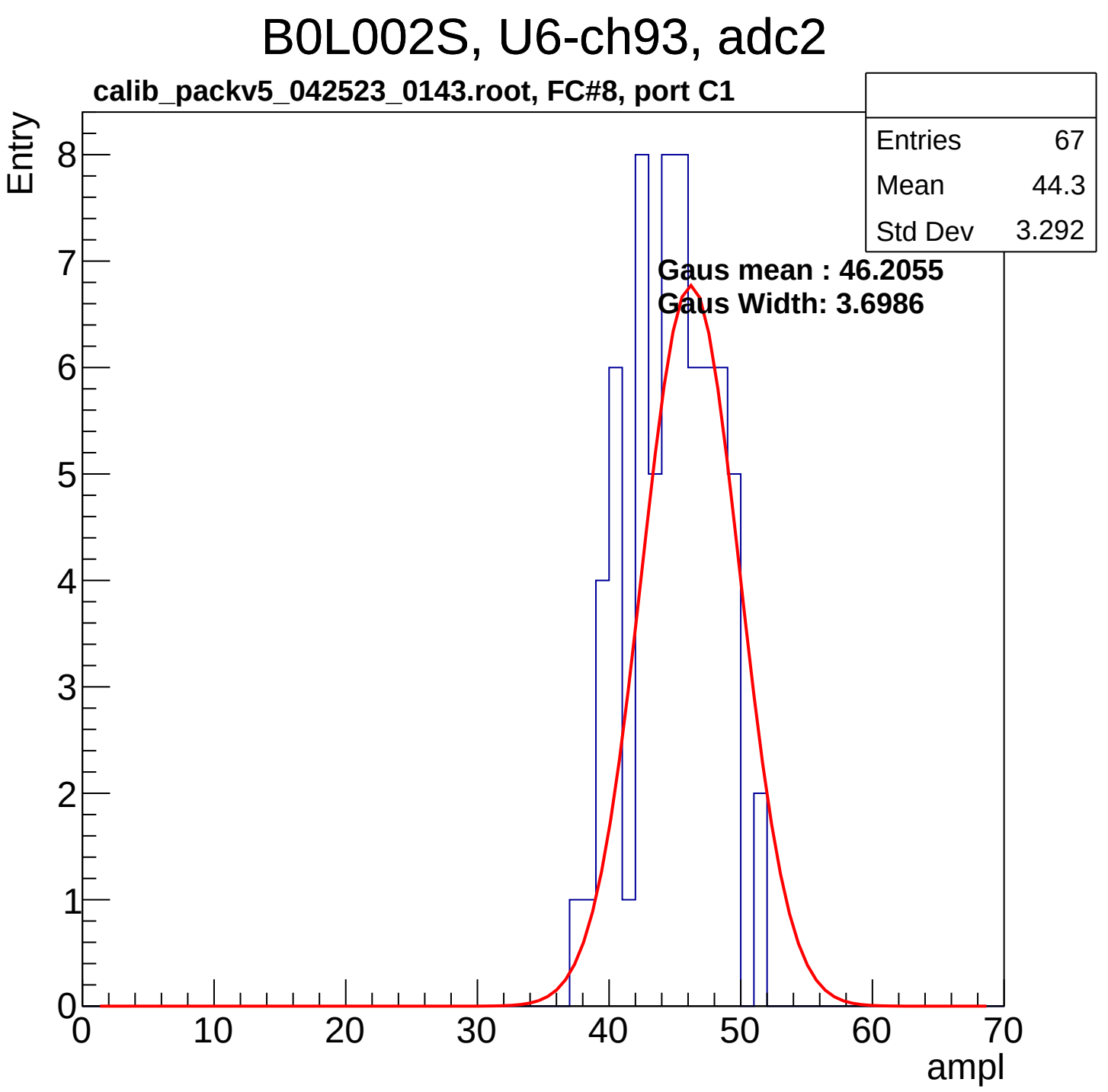

B0L002S, U6-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.56
Std Dev 9.579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 29.83
Std Dev 2.914
Gaus mean : 30.7550
Gaus Width: 2.5614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 37.33
Std Dev 3.265
Gaus mean : 37.5236
Gaus Width: 3.3292
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.3
Std Dev 3.292
Gaus mean : 46.2055
Gaus Width: 3.6986
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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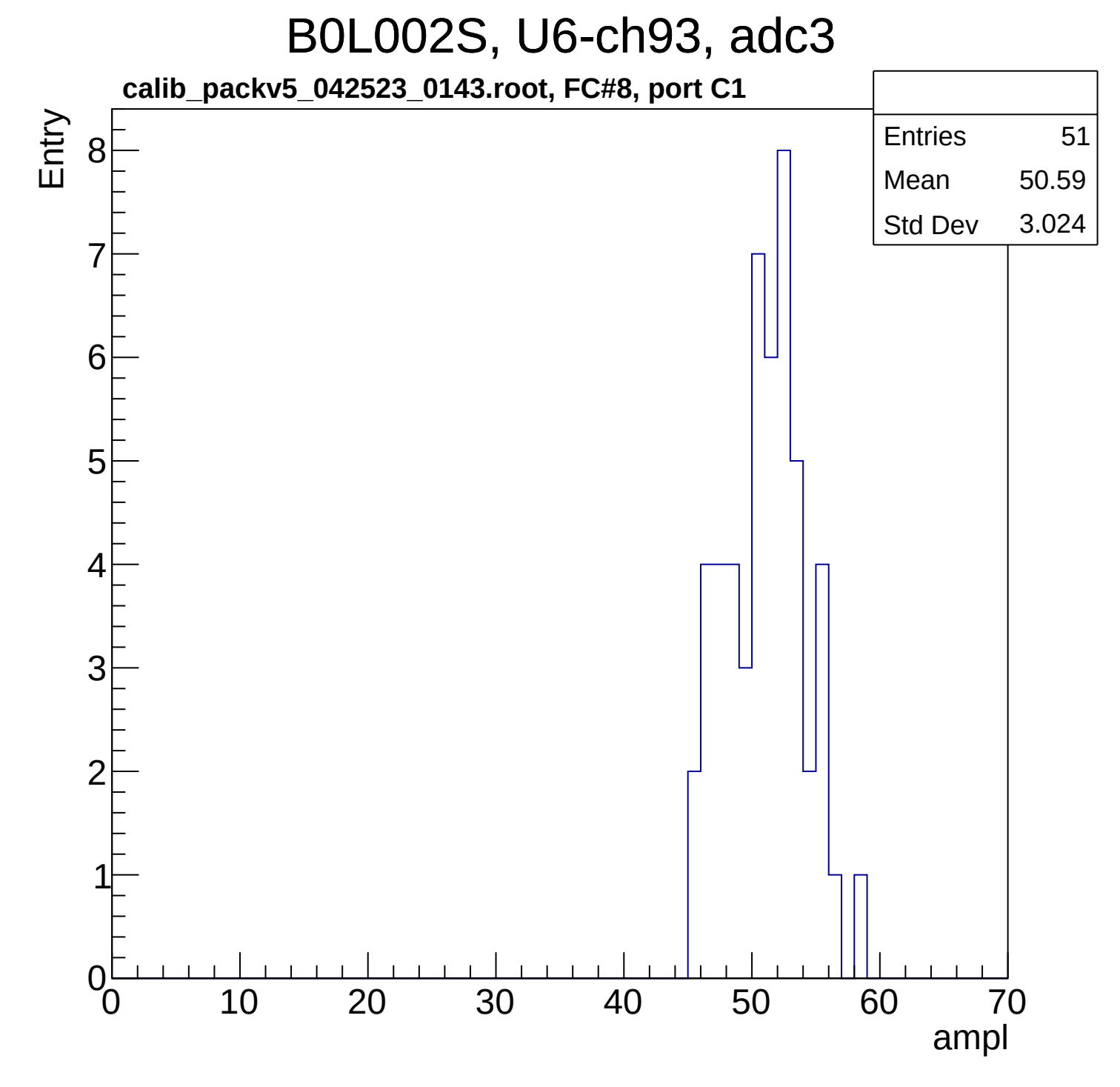
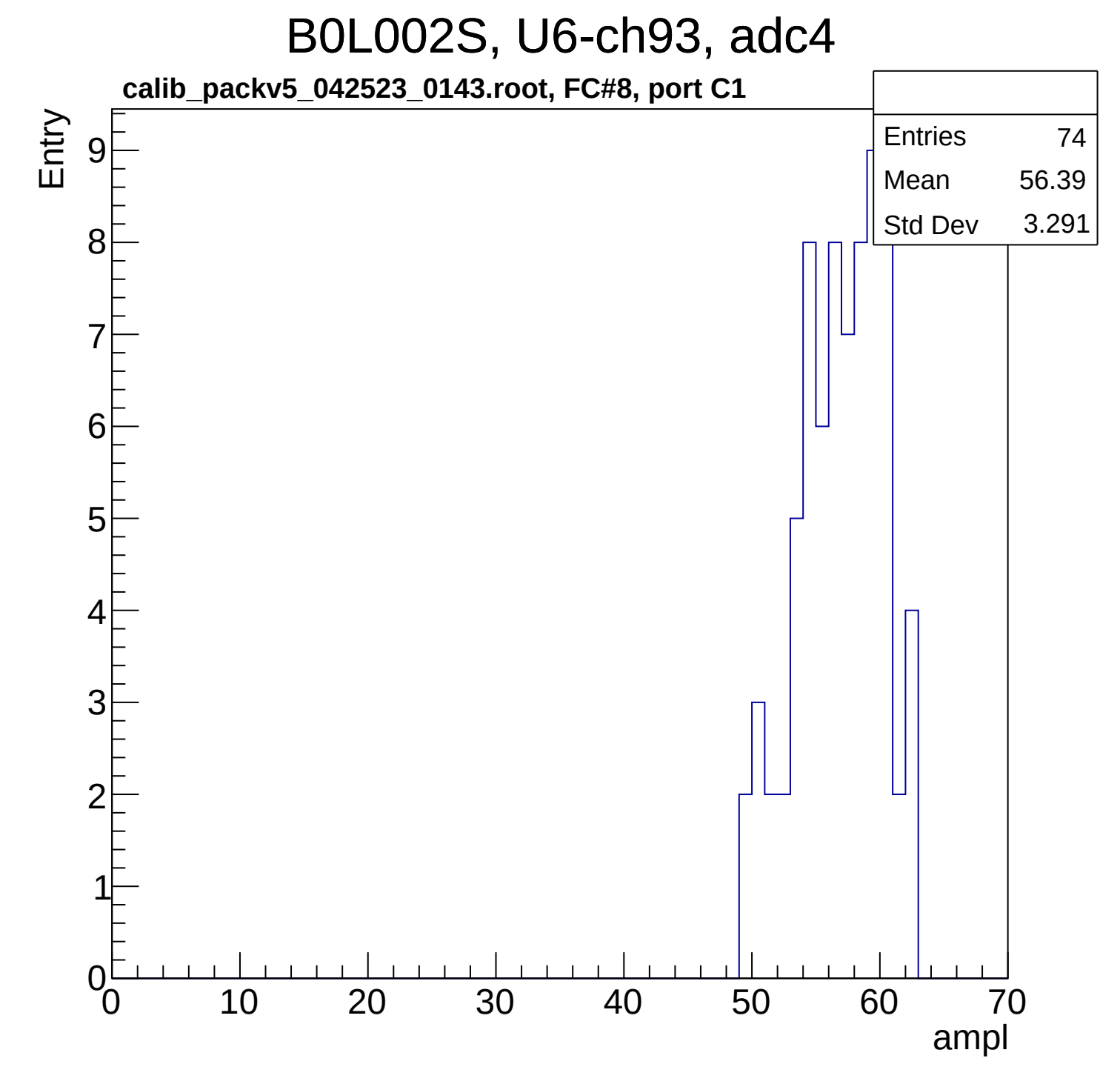
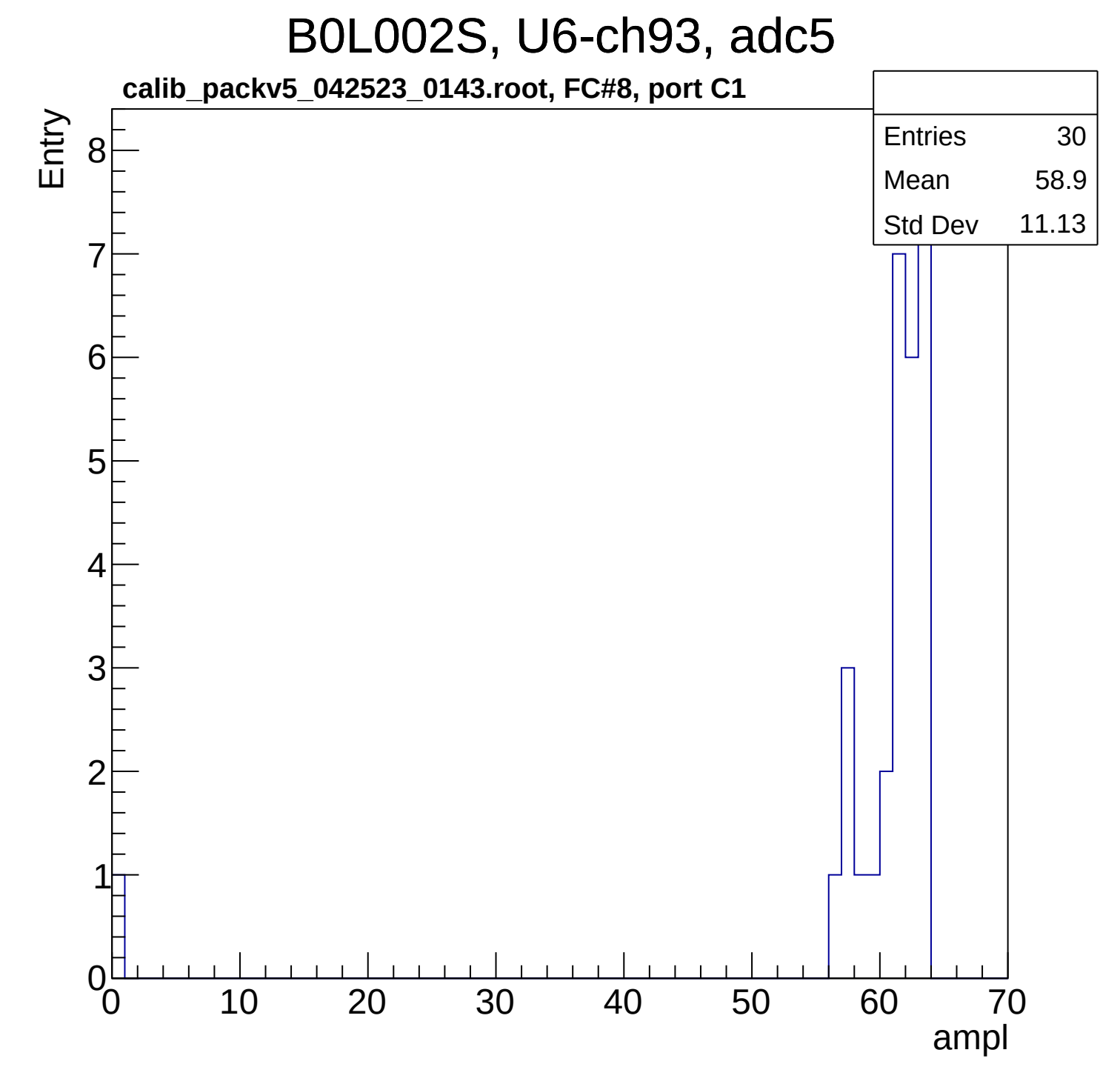
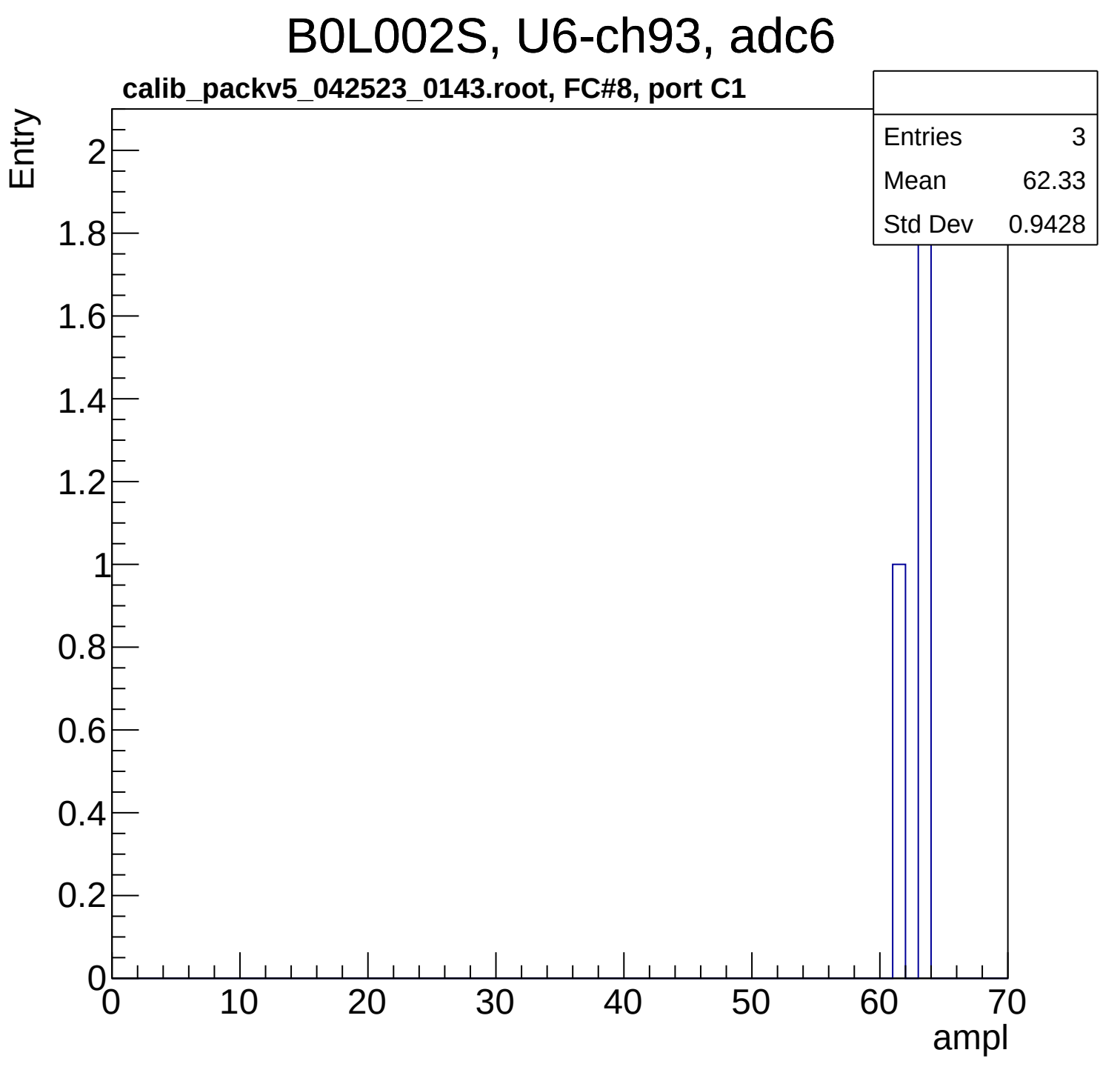
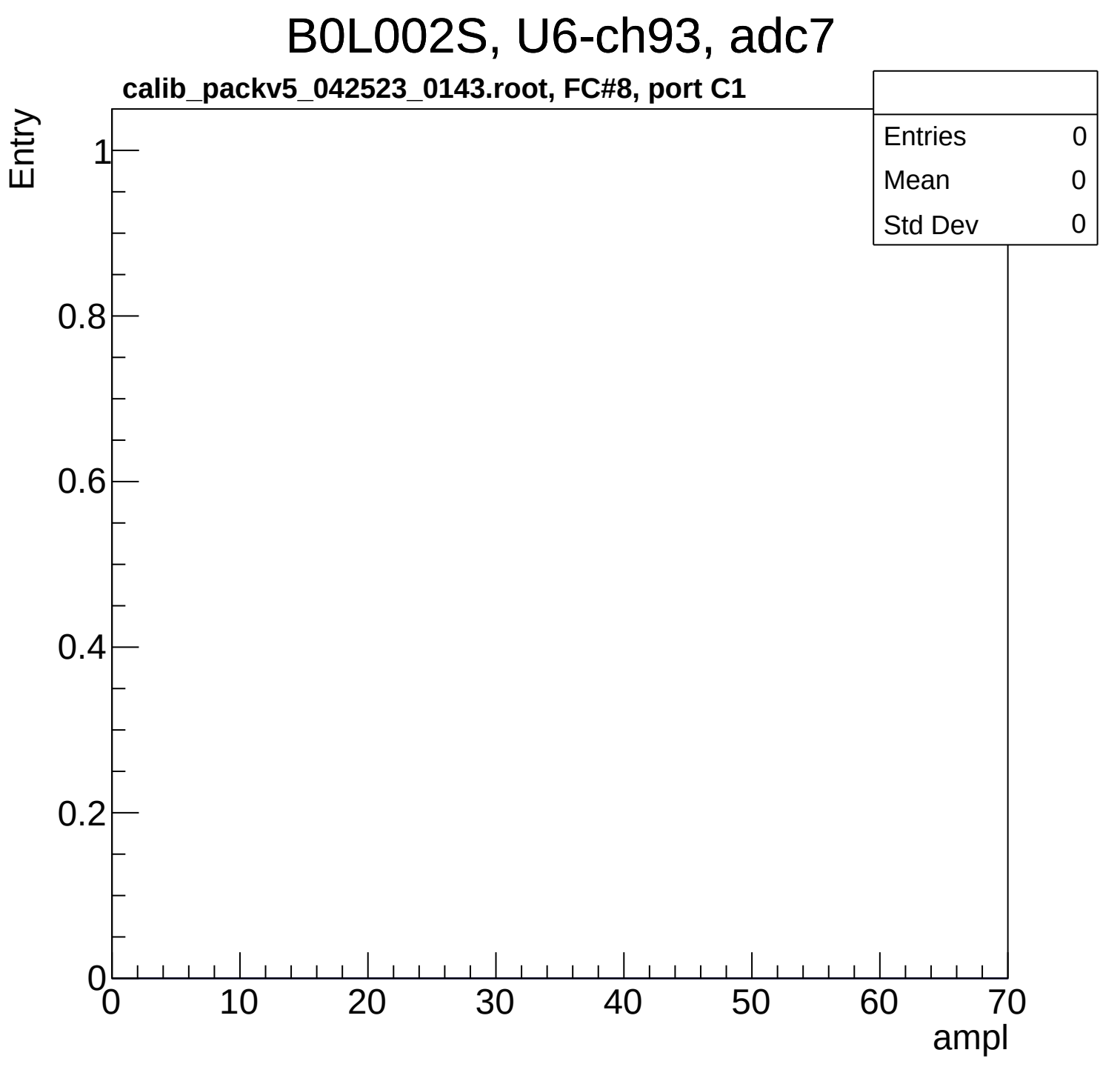
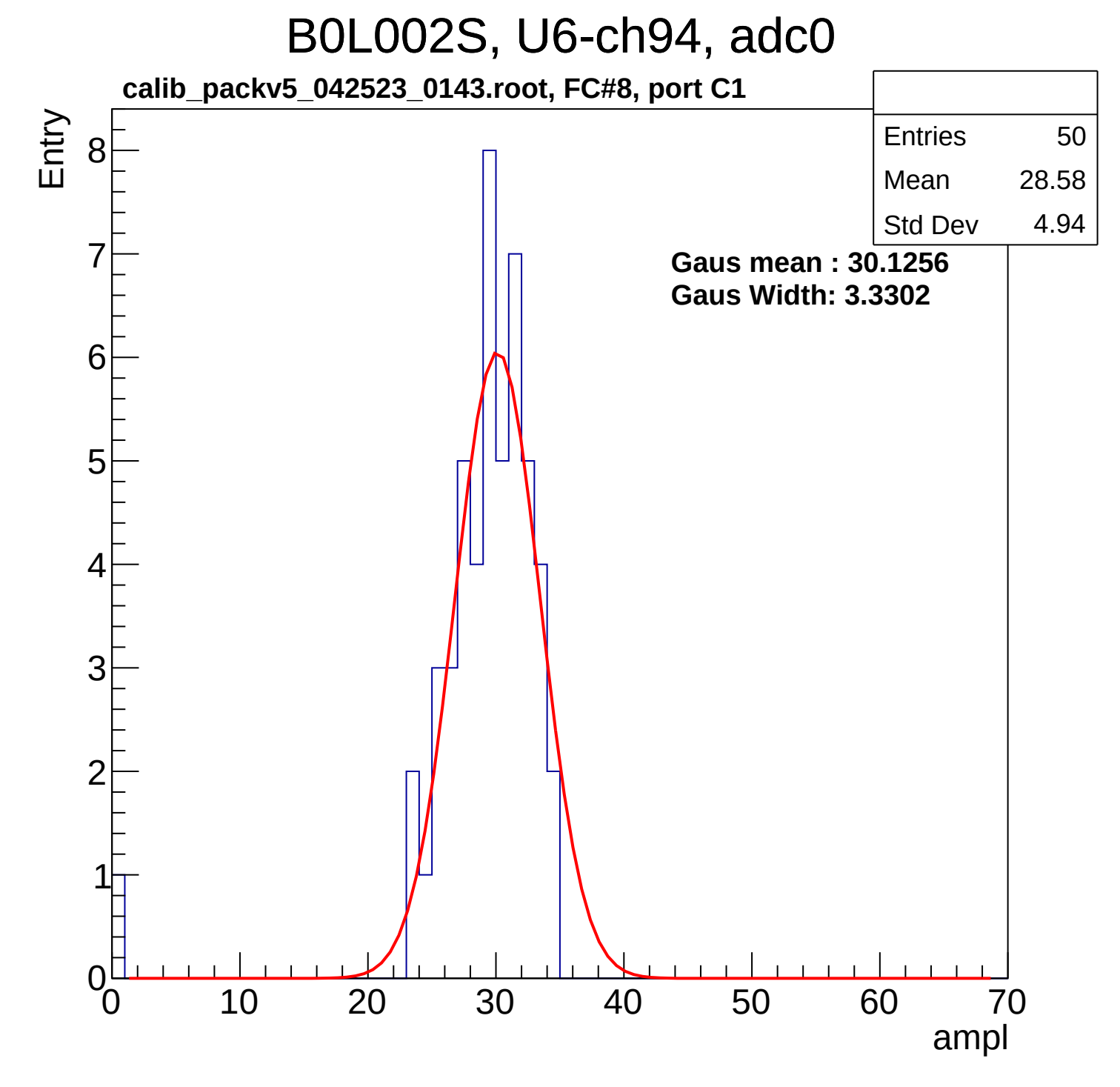

B0L002S, U6-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 50.59
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

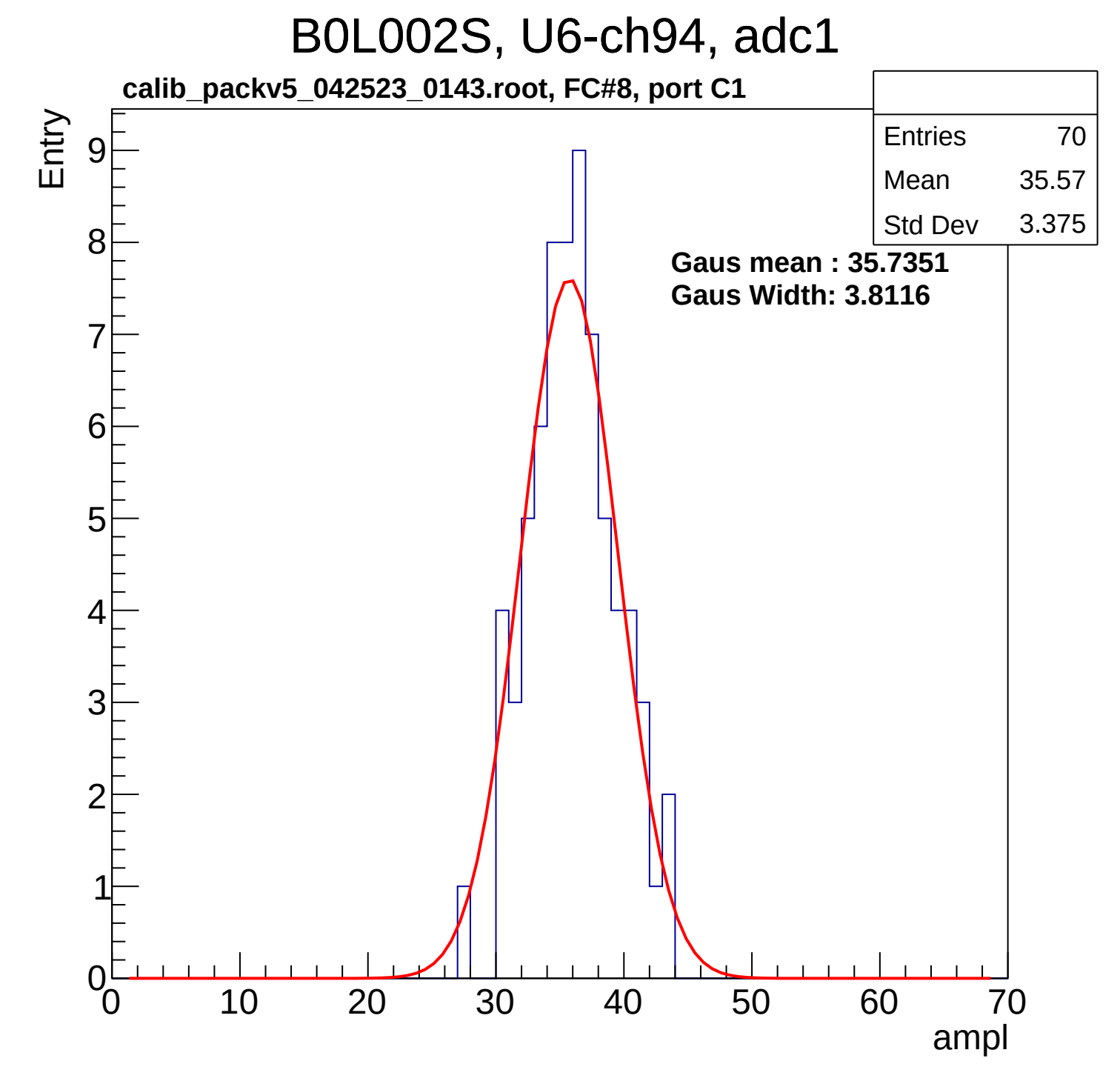

B0L002S, U6-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 56.39
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

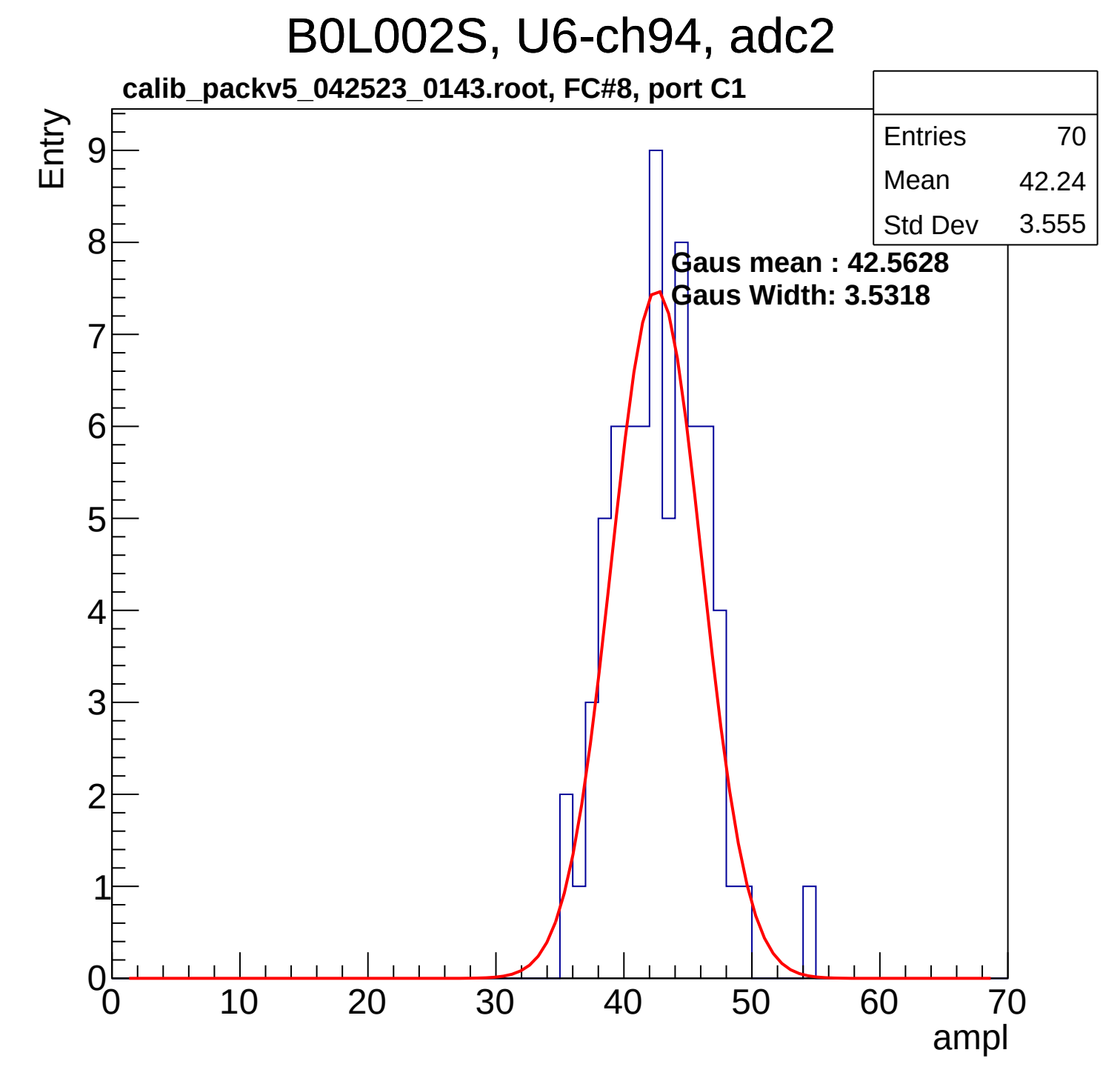

B0L002S, U6-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.9
Std Dev 11.13
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

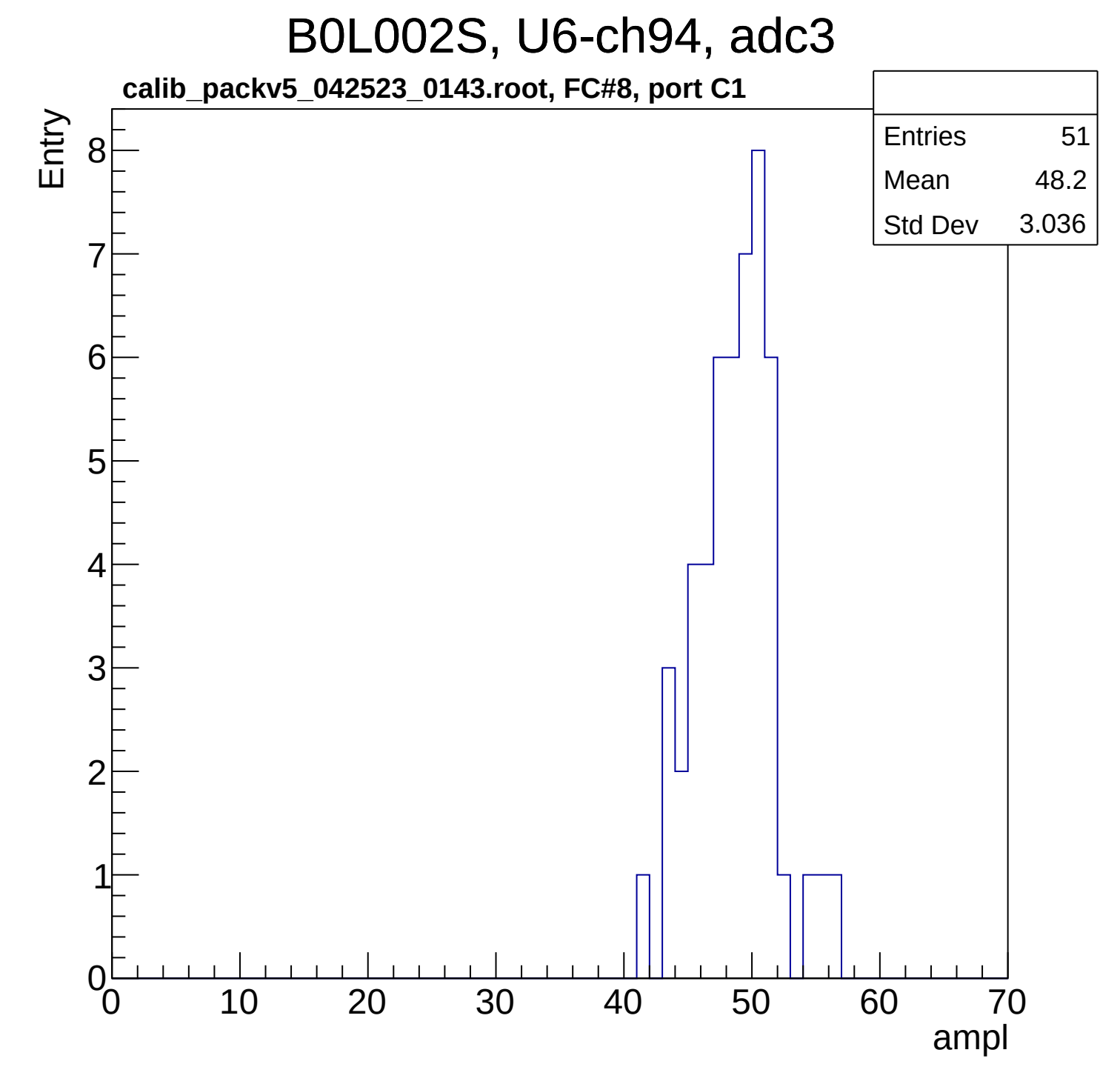

B0L002S, U6-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

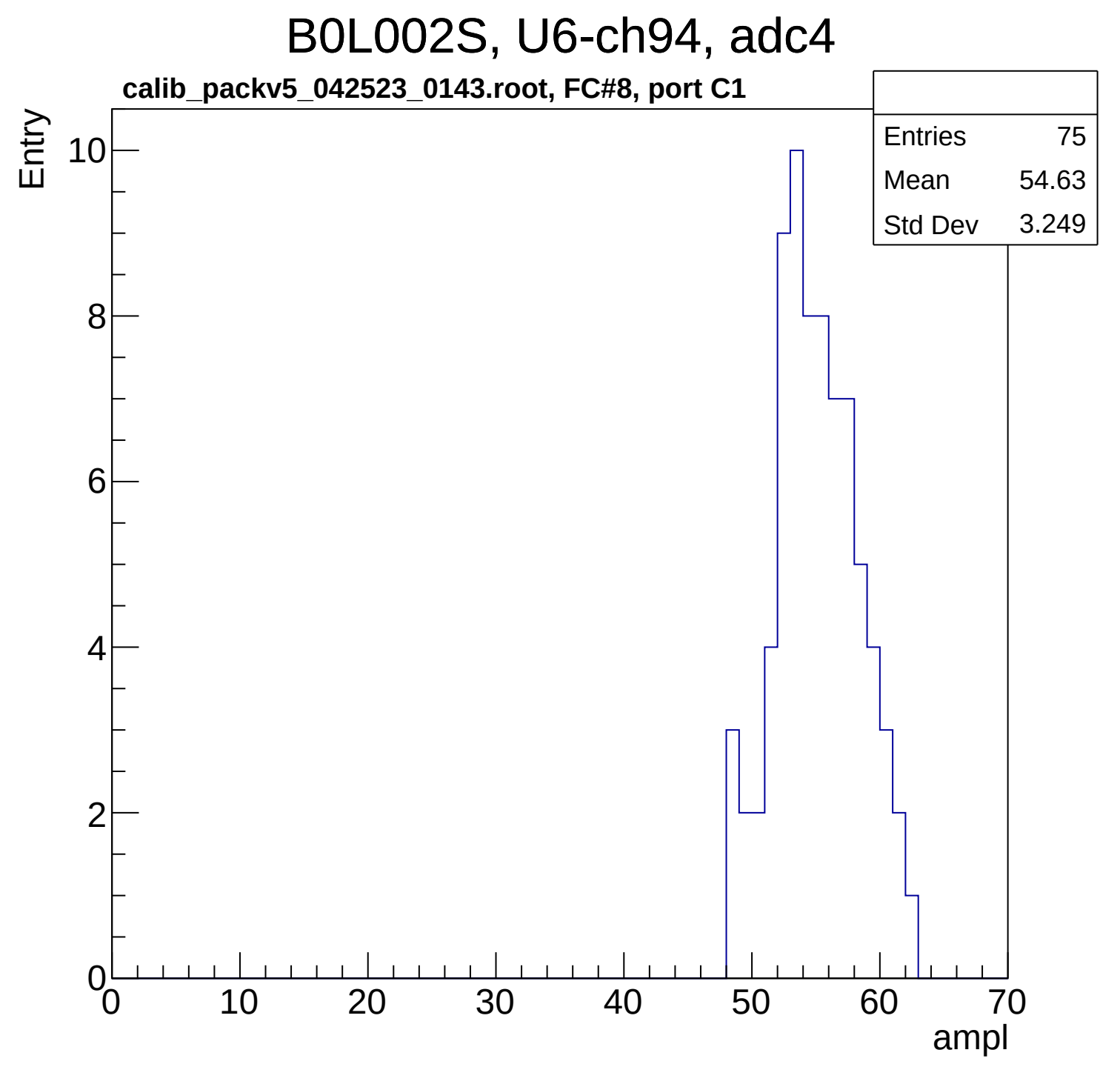

B0L002S, U6-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

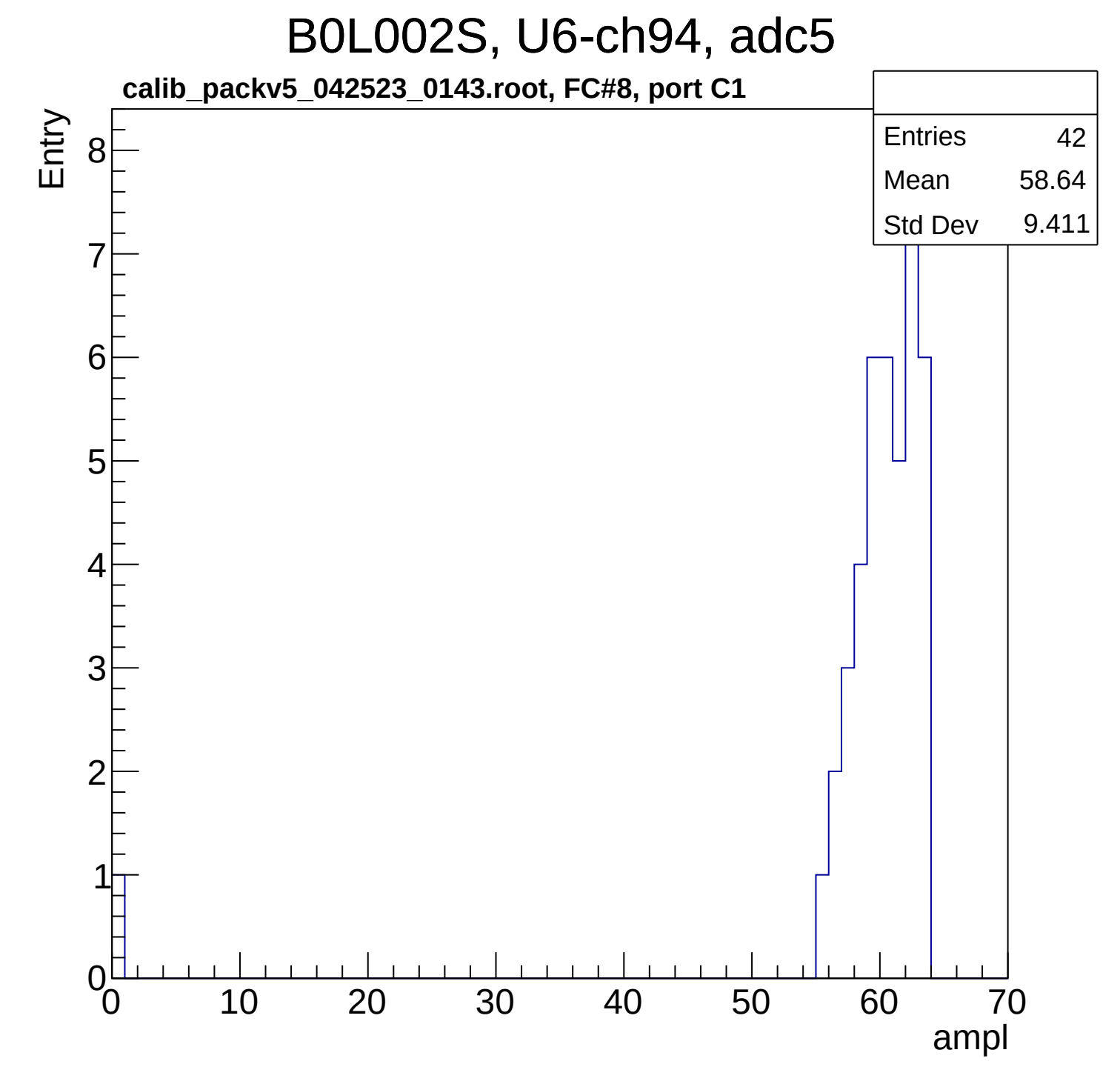

B0L002S, U6-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 28.58
Std Dev 4.94
Gaus mean : 30.1256
Gaus Width: 3.3302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

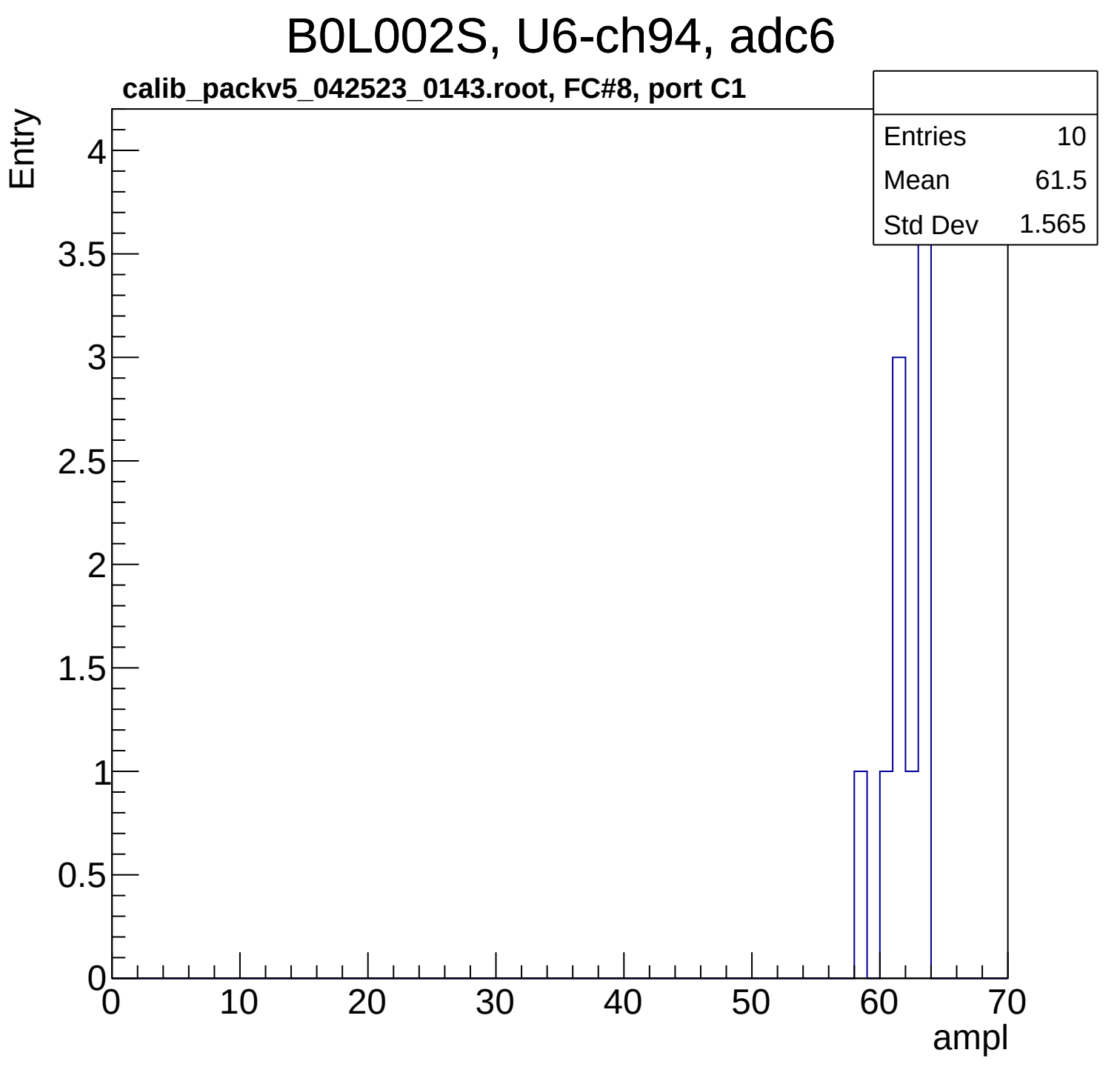

B0L002S, U6-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 35.57
Std Dev 3.375
Gaus mean : 35.7351
Gaus Width: 3.8116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

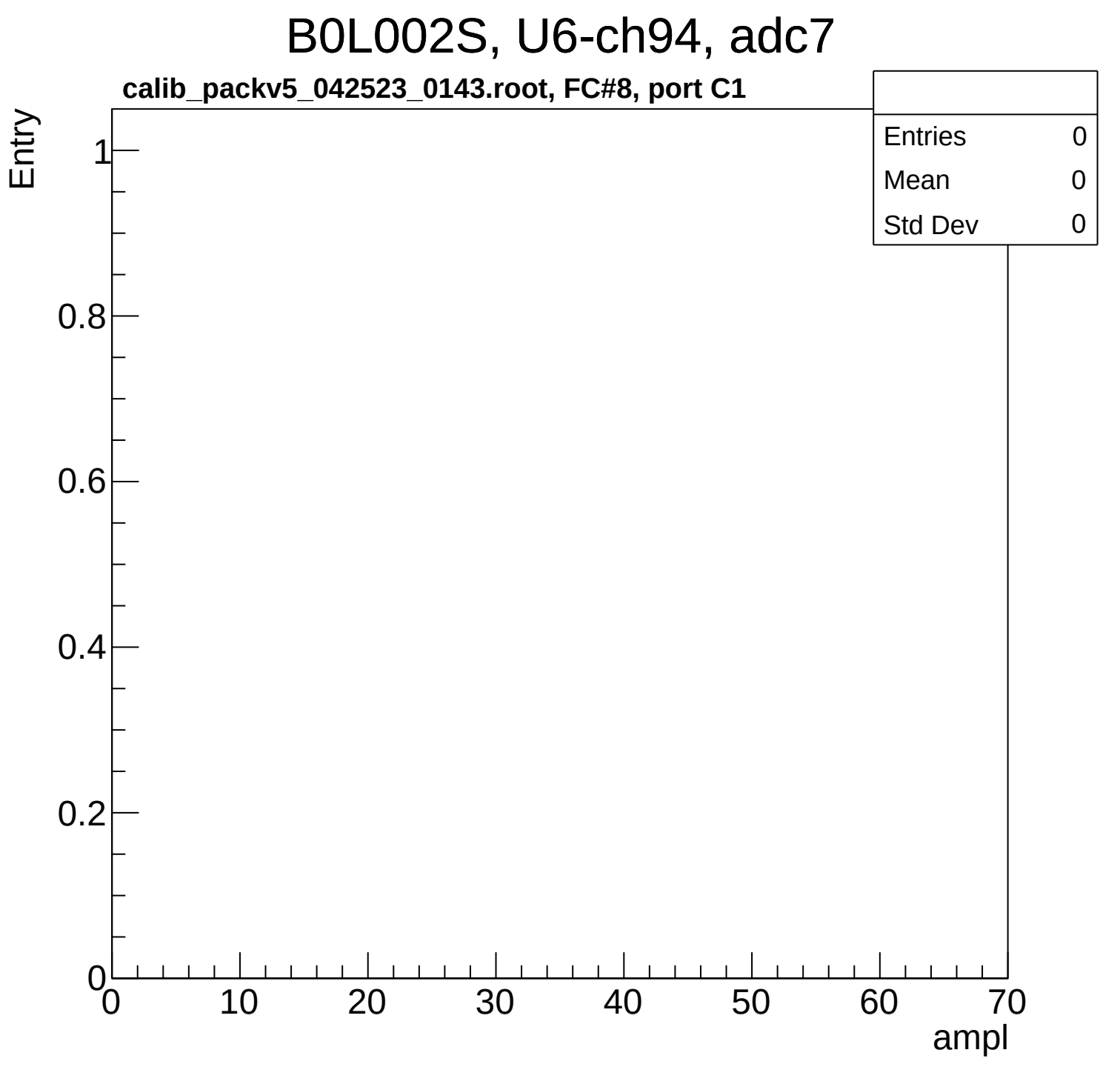

B0L002S, U6-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 42.24
Std Dev 3.555
Gaus mean : 42.5628
Gaus Width: 3.5318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

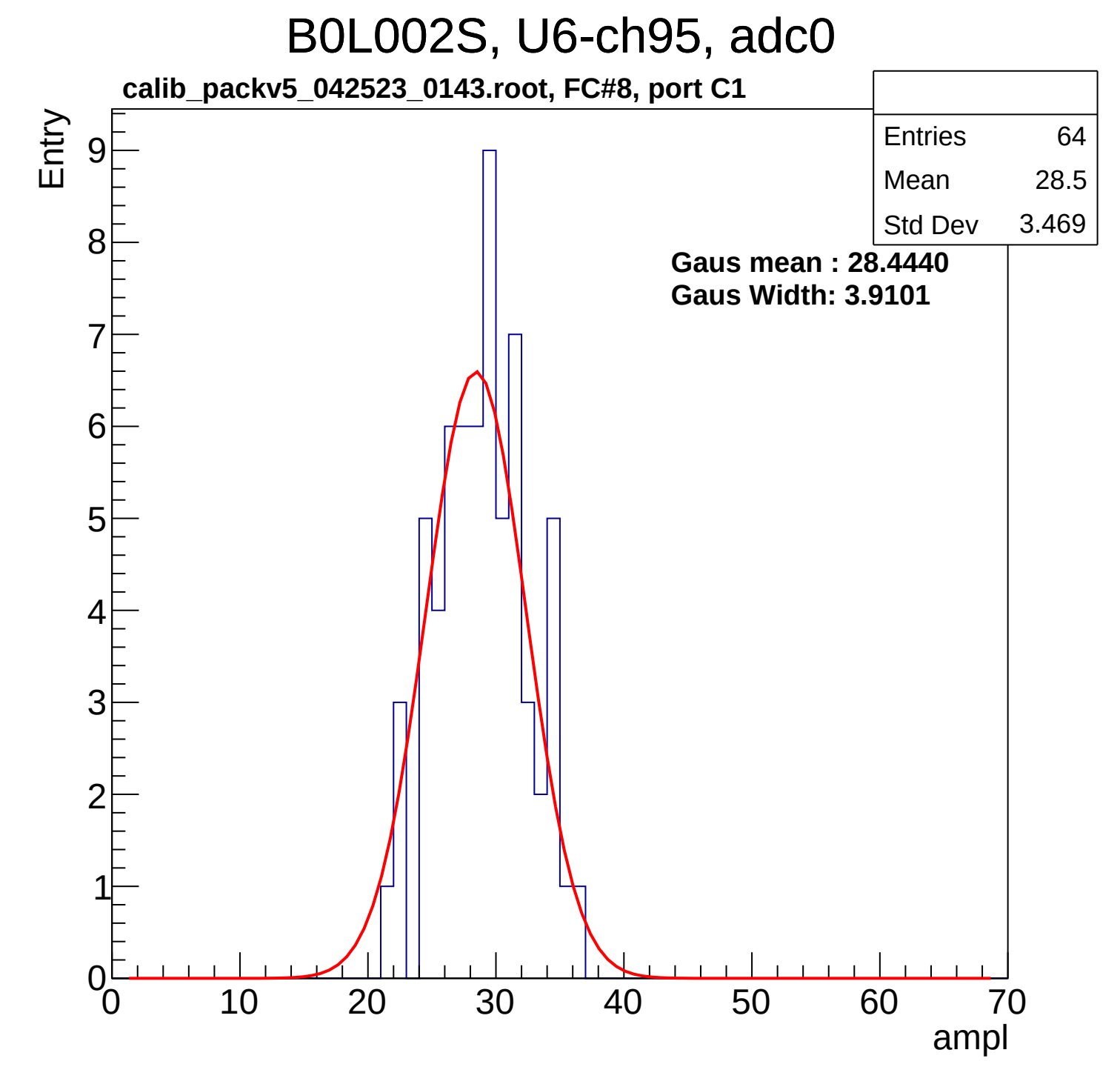

B0L002S, U6-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 48.2
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

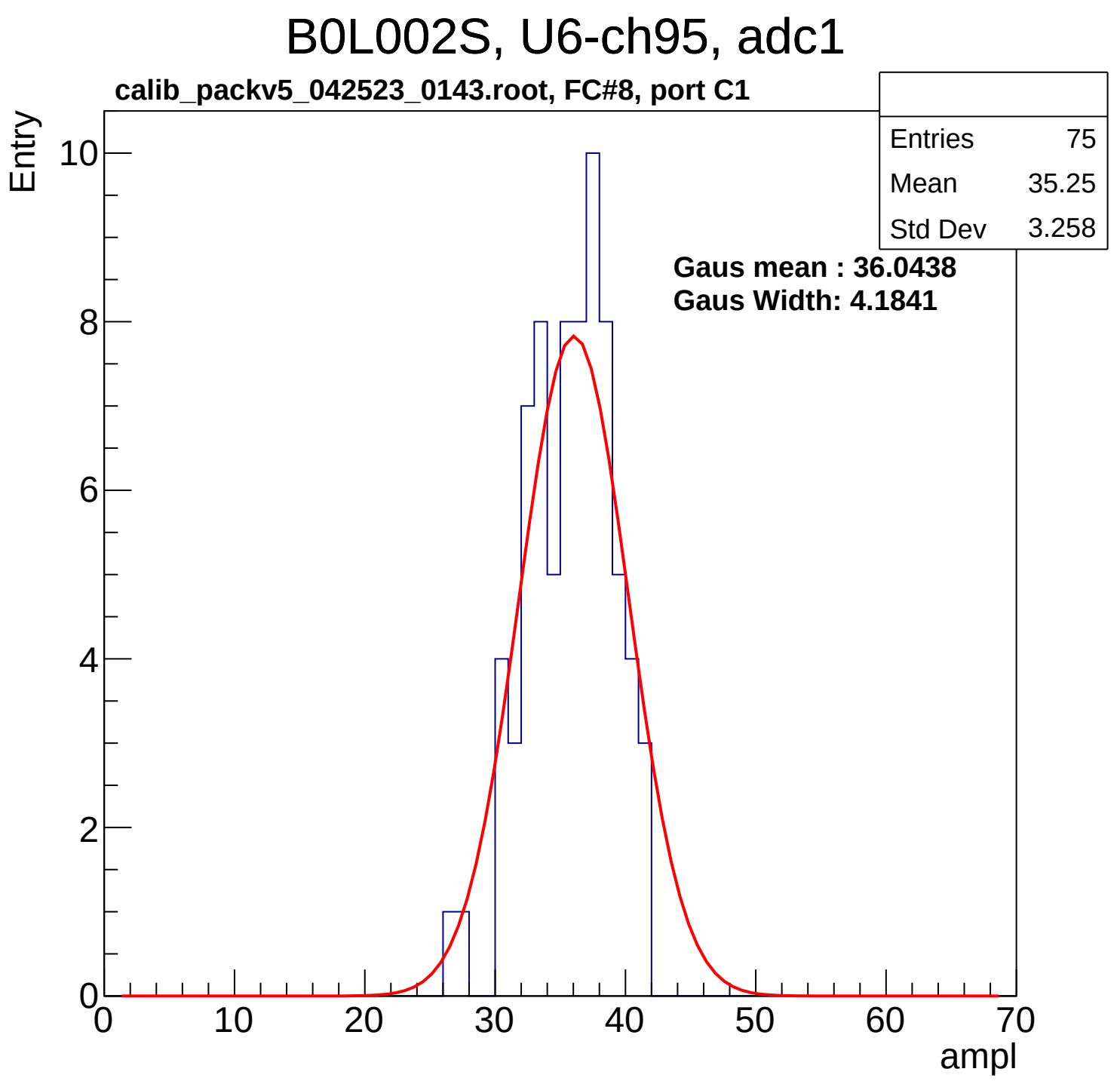

B0L002S, U6-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 54.63
Std Dev 3.249
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

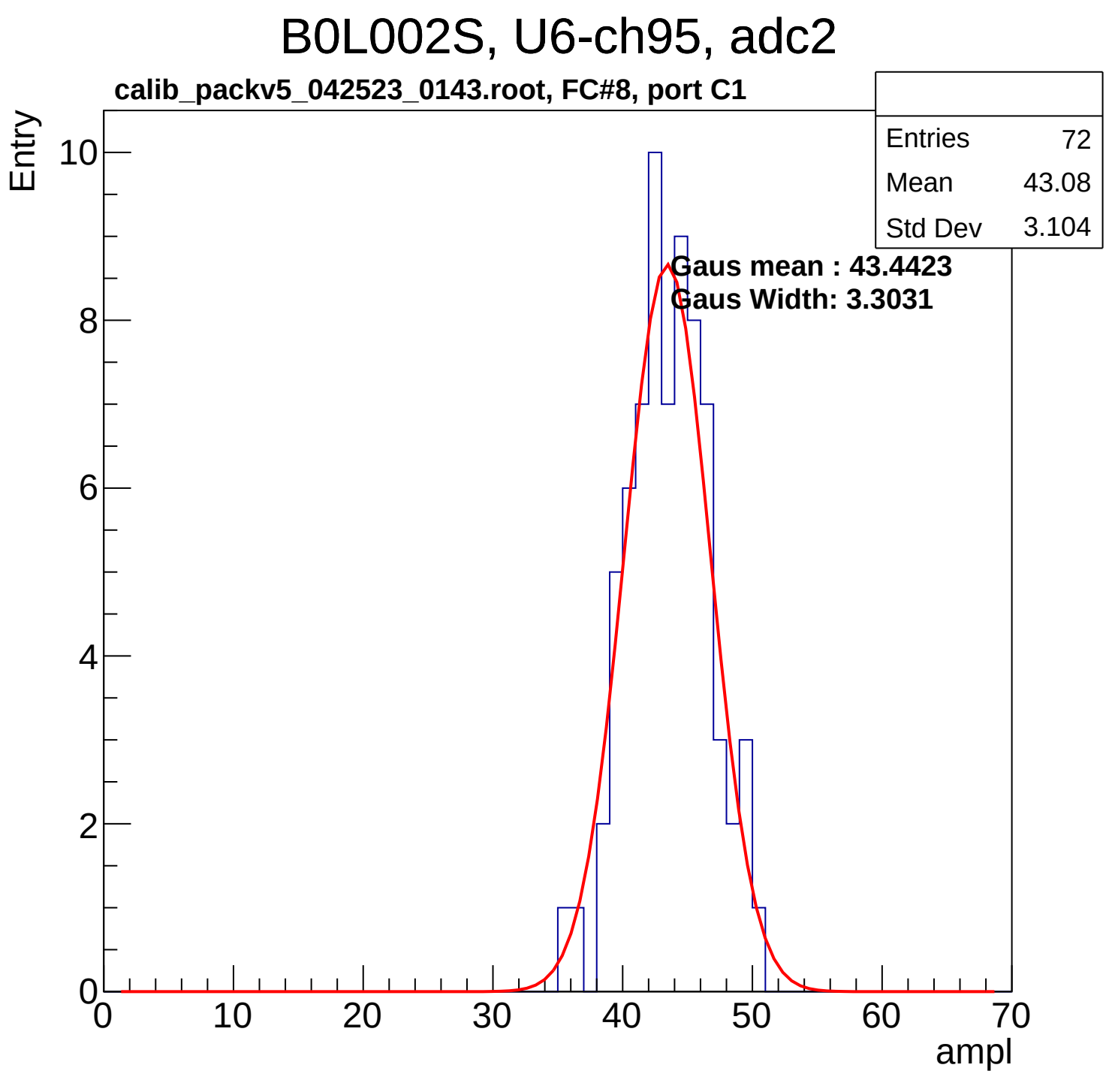

B0L002S, U6-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.64
Std Dev 9.411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

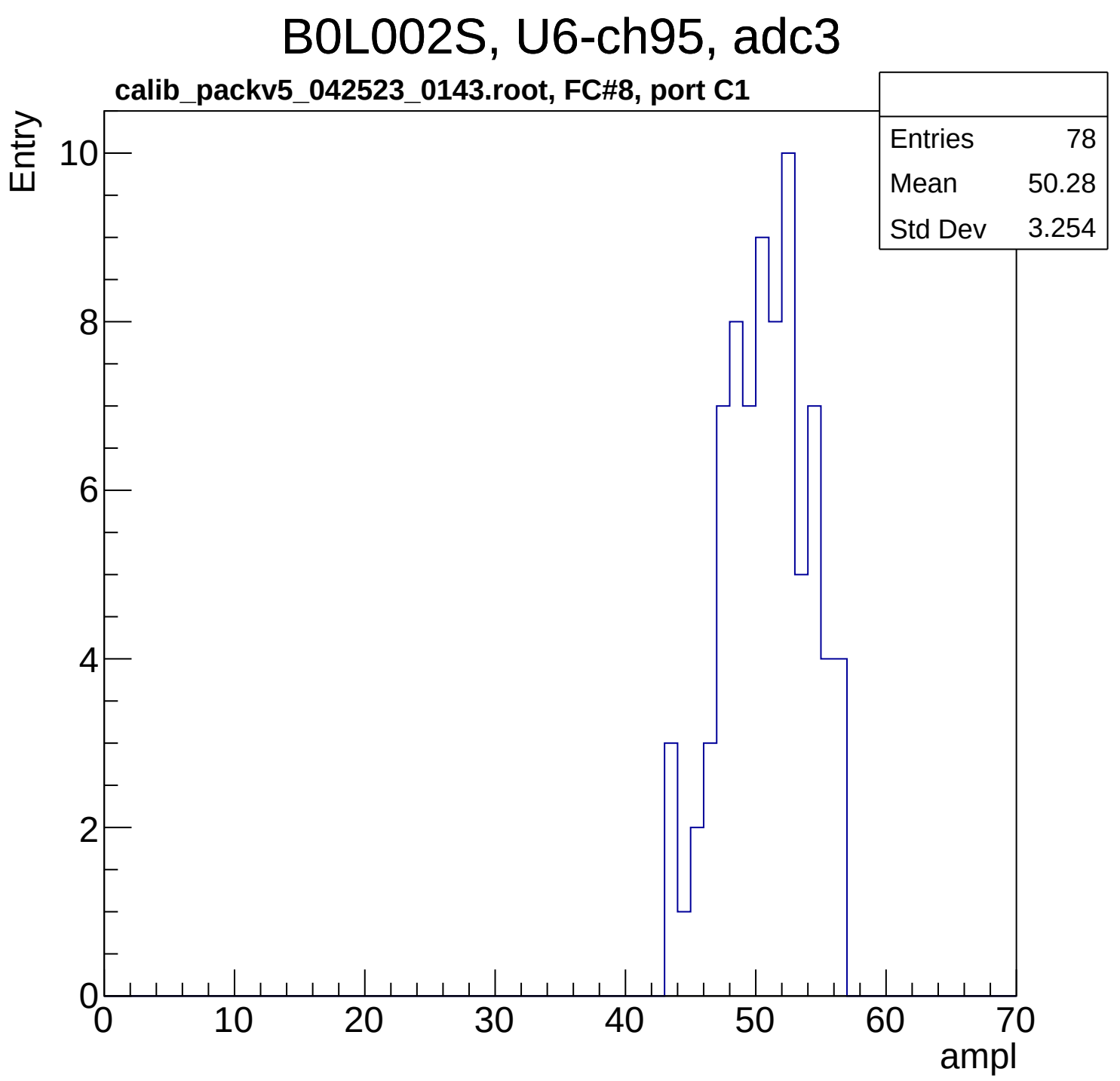

B0L002S, U6-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.5
Std Dev 1.565
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

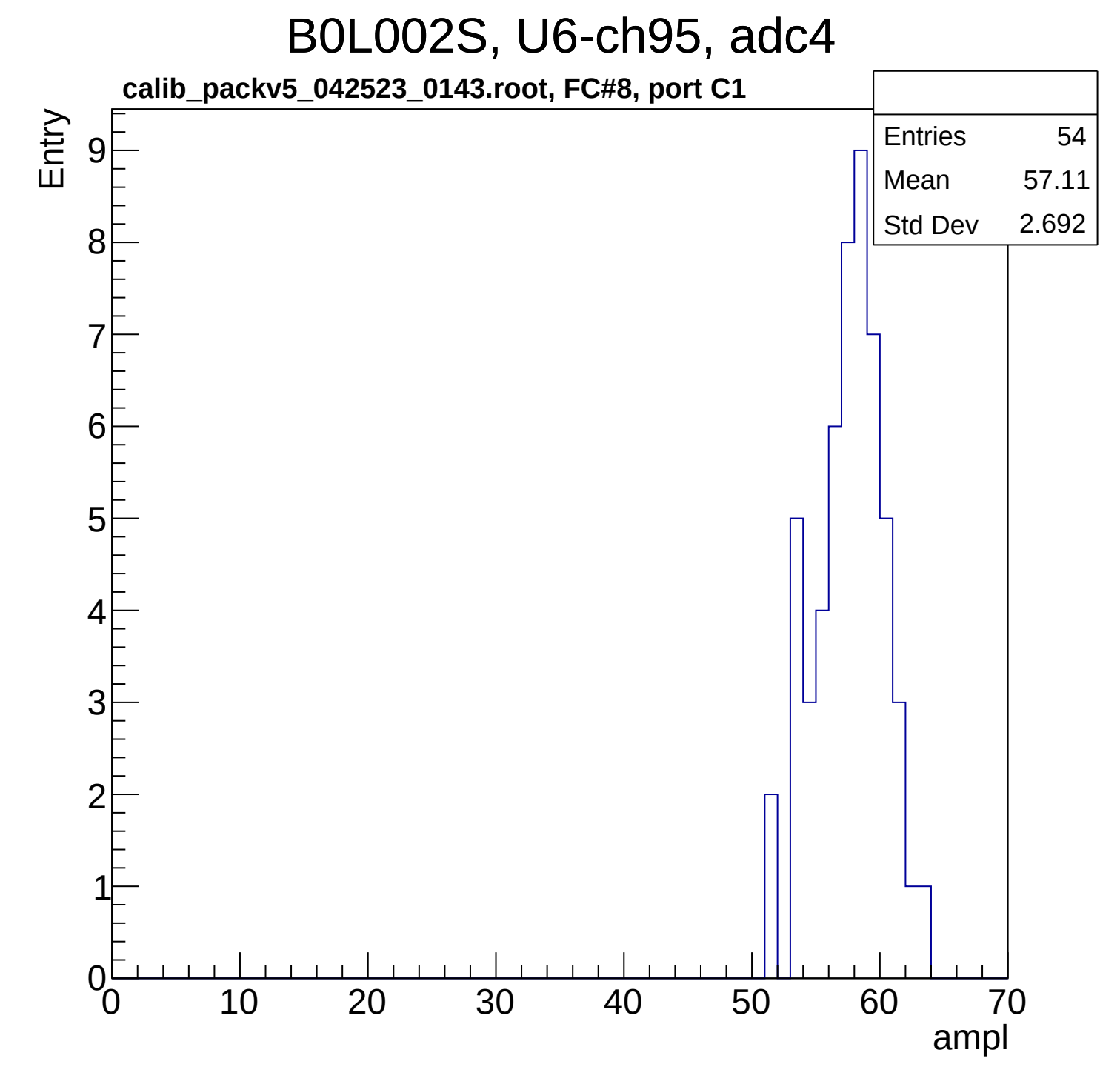

B0L002S, U6-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 28.5
Std Dev 3.469
Gaus mean : 28.4440
Gaus Width: 3.9101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 35.25
Std Dev 3.258
Gaus mean : 36.0438
Gaus Width: 4.1841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.08
Std Dev 3.104
Gaus mean : 43.4423
Gaus Width: 3.3031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 50.28
Std Dev 3.254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.11
Std Dev 2.692
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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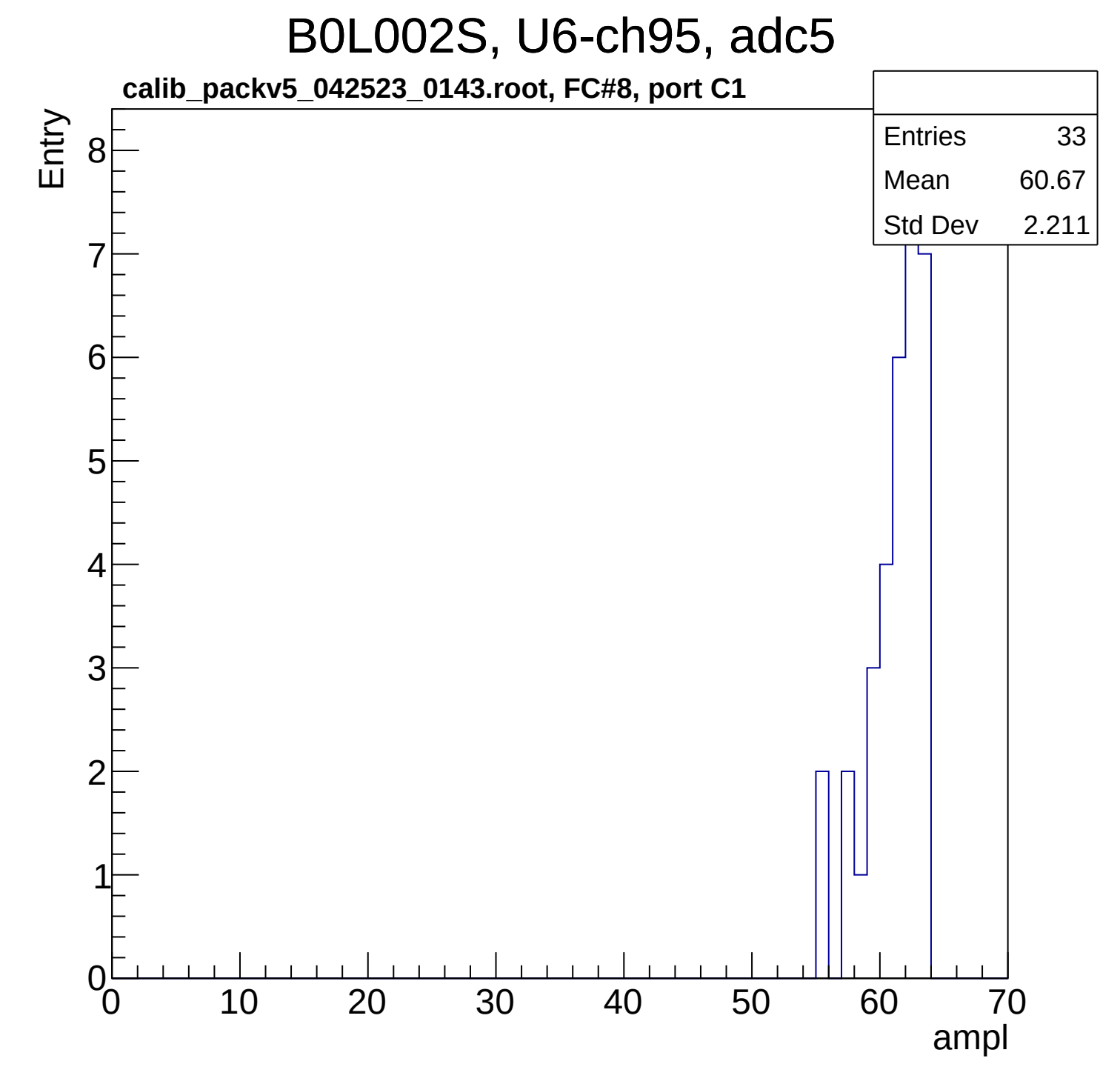
B0L002S, U6-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 60.67
Std Dev 2.211
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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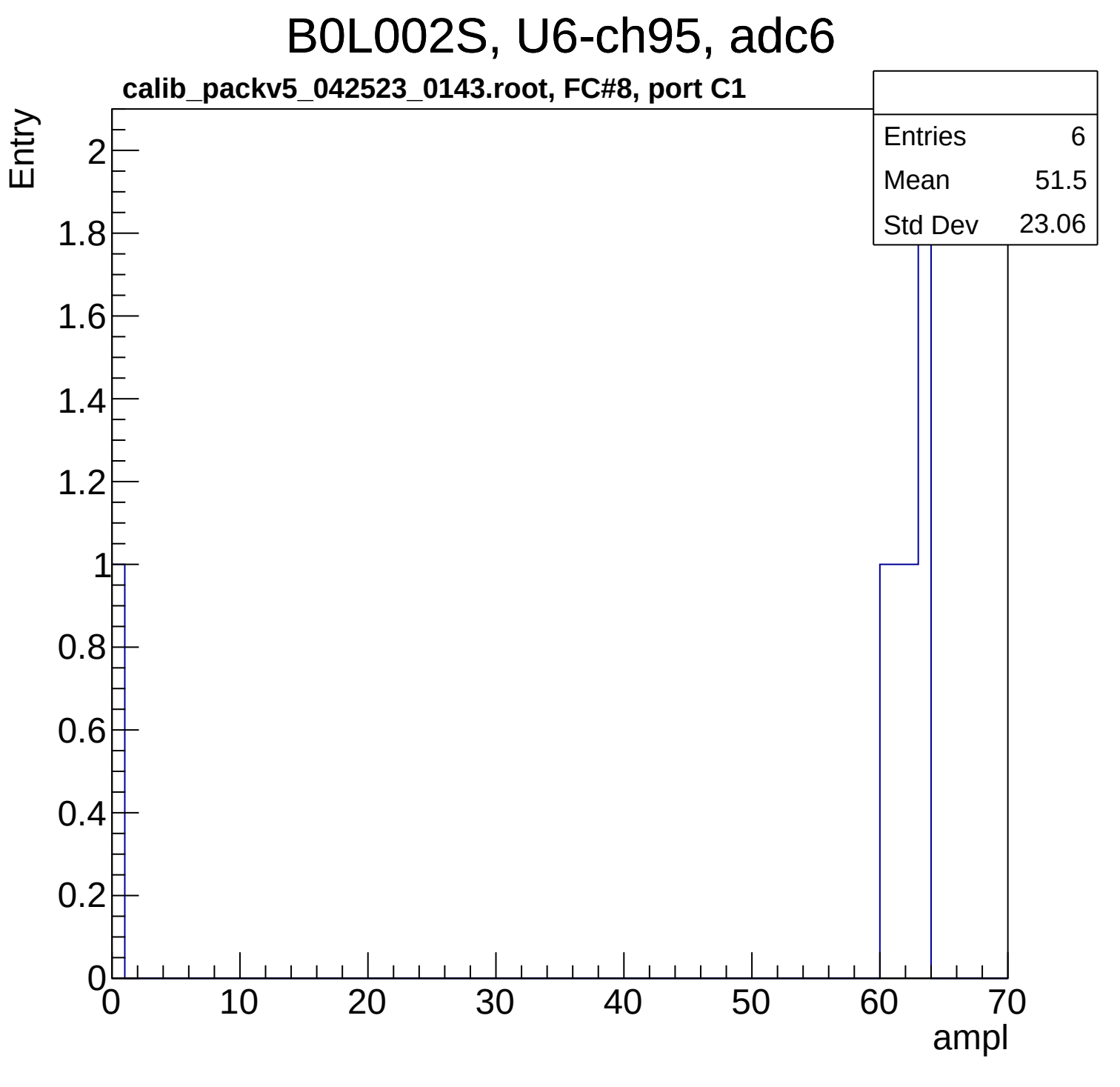
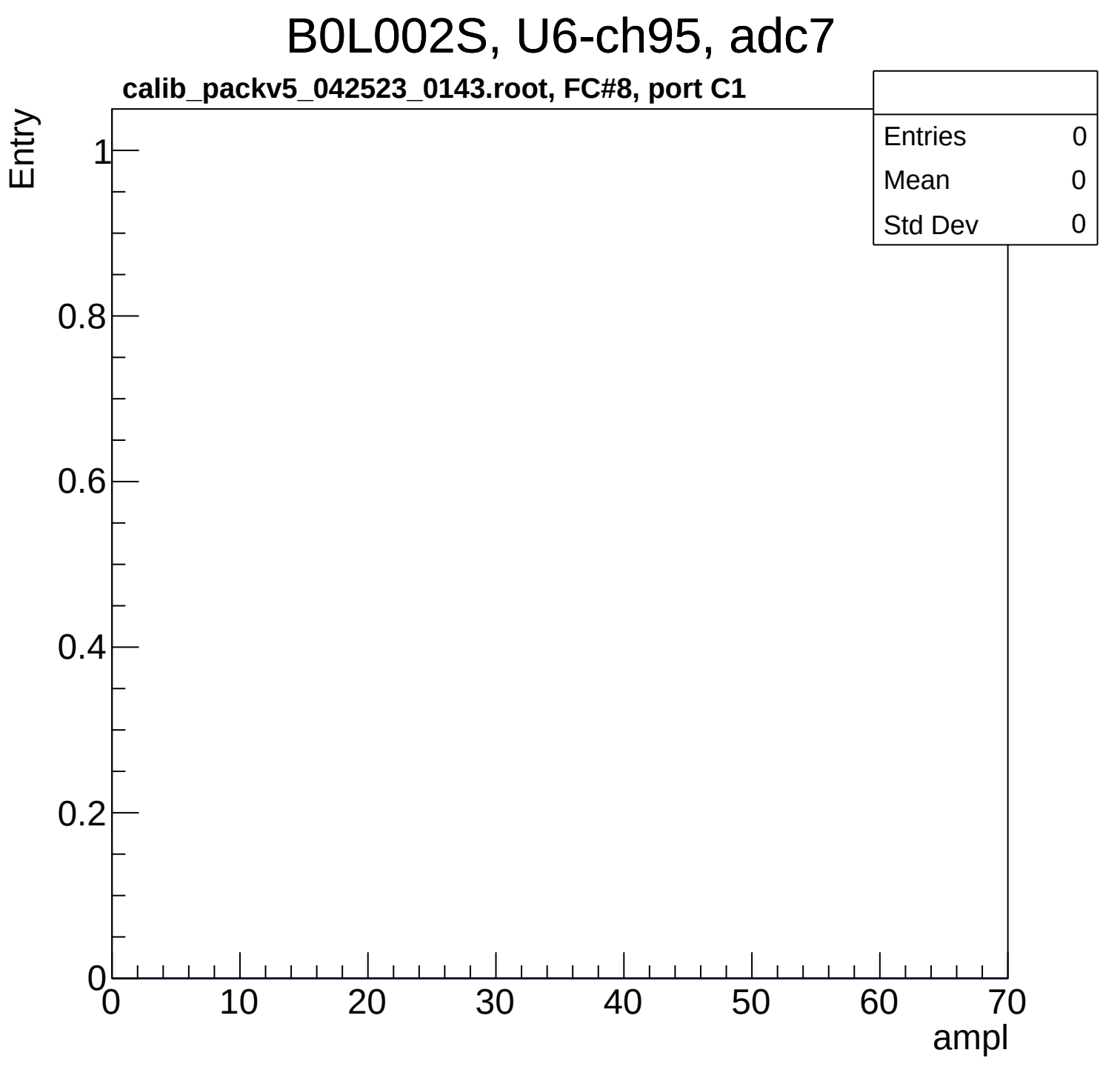
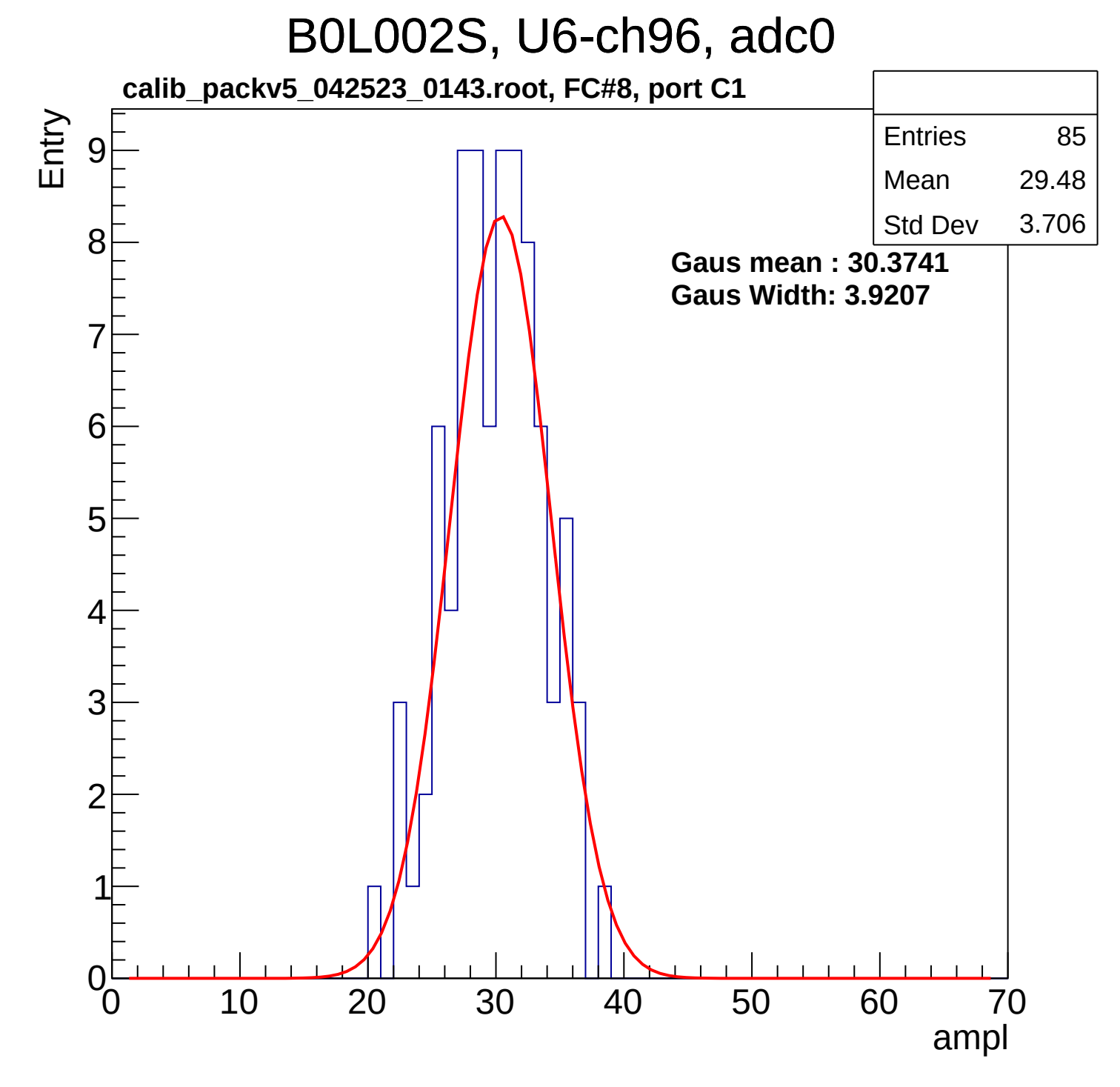

B0L002S, U6-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 29.48
Std Dev 3.706
Gaus mean : 30.3741
Gaus Width: 3.9207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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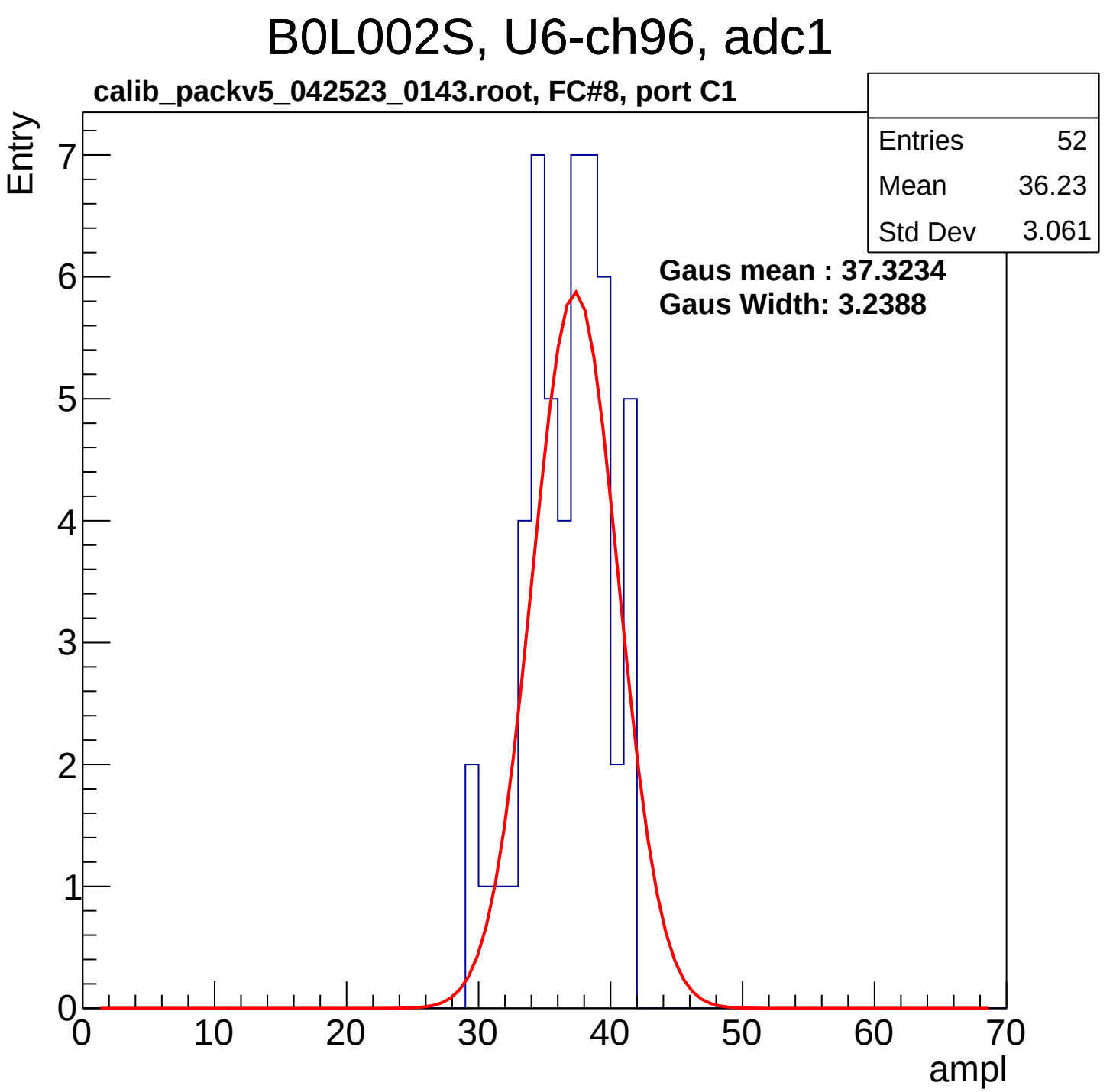
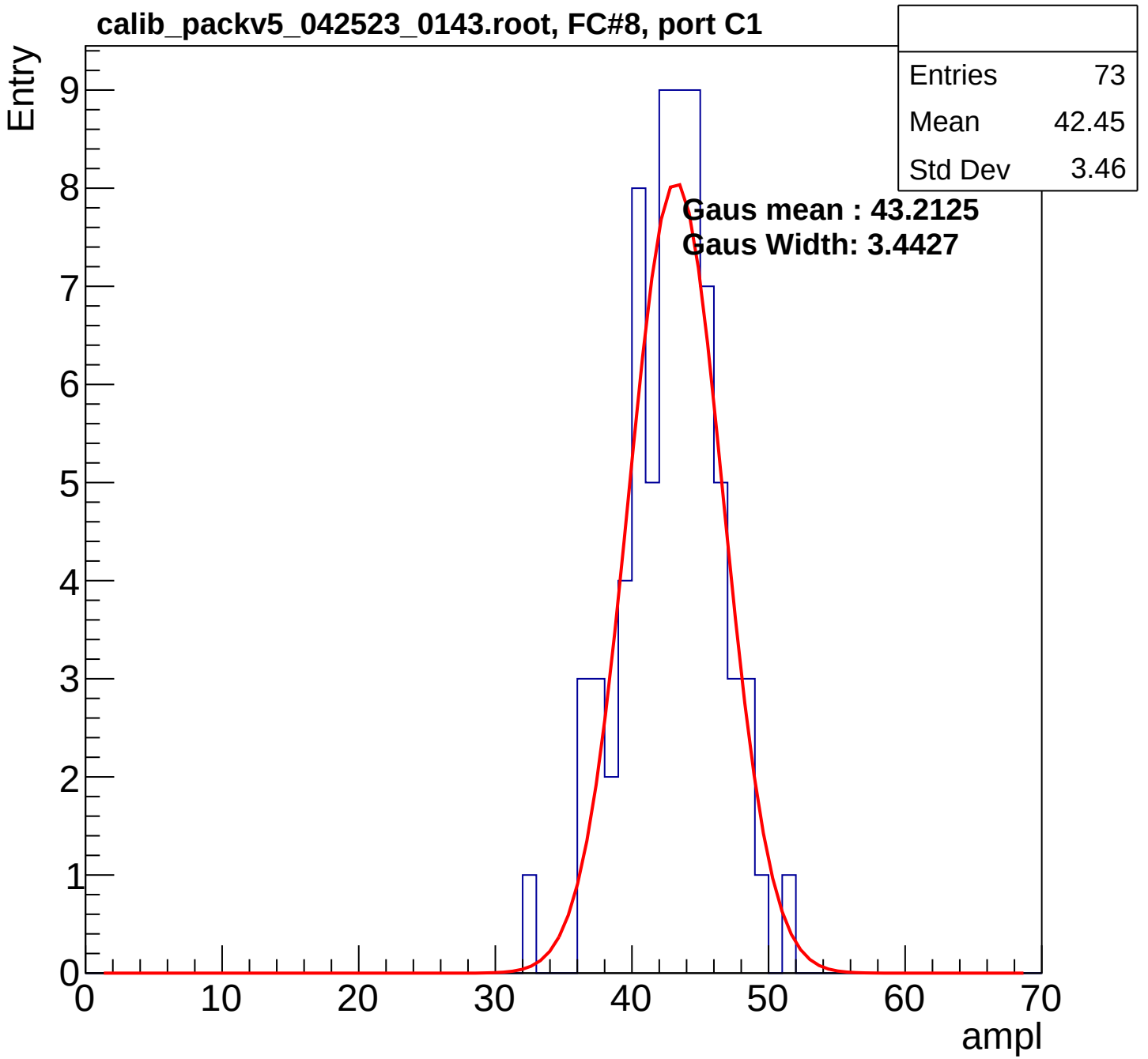

B0L002S, U6-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 36.23
Std Dev 3.061
Gaus mean : 37.3234
Gaus Width: 3.2388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 42.45
Std Dev 3.46
Gaus mean : 43.2125
Gaus Width: 3.4427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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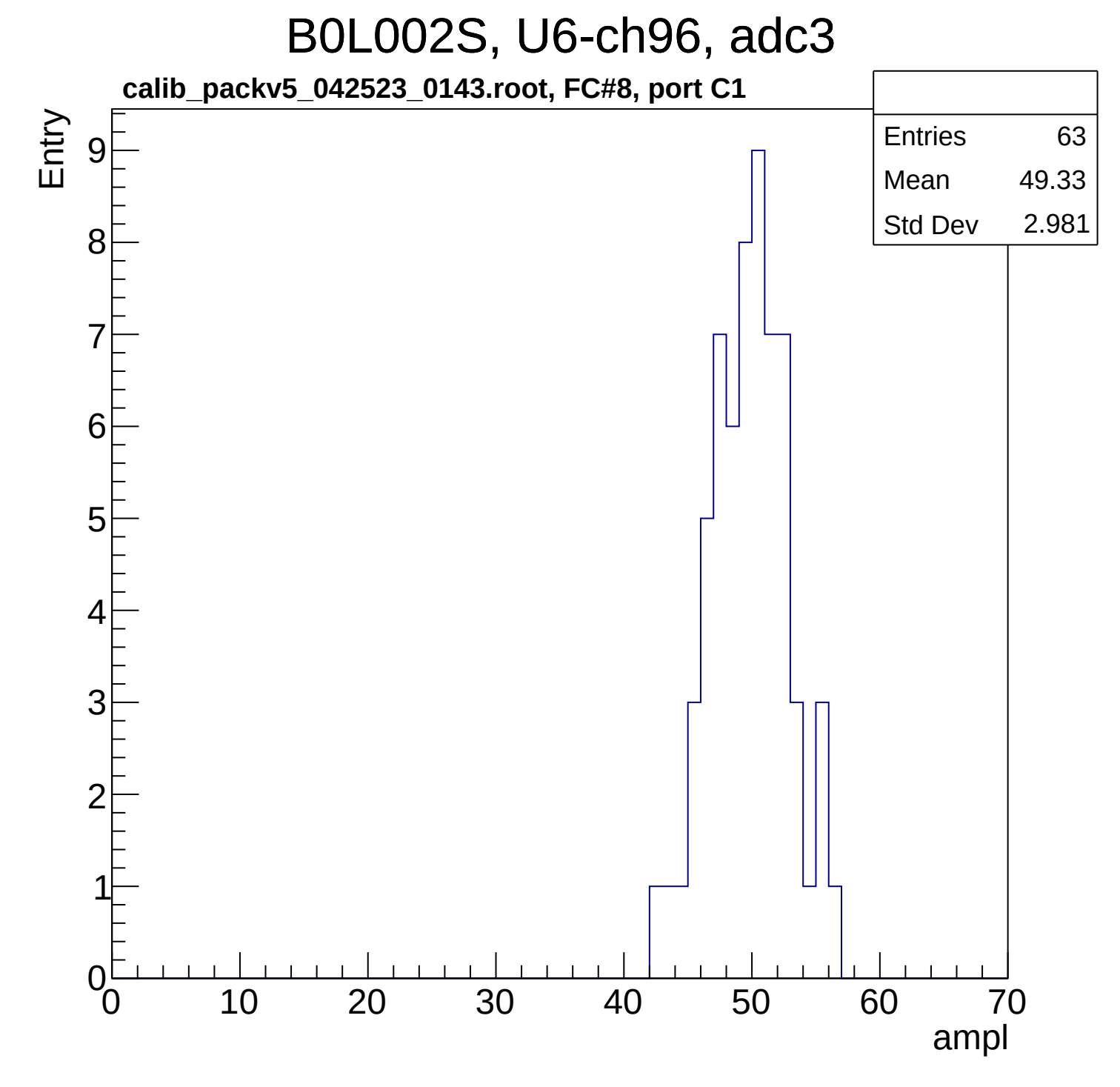
B0L002S, U6-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 49.33
Std Dev 2.981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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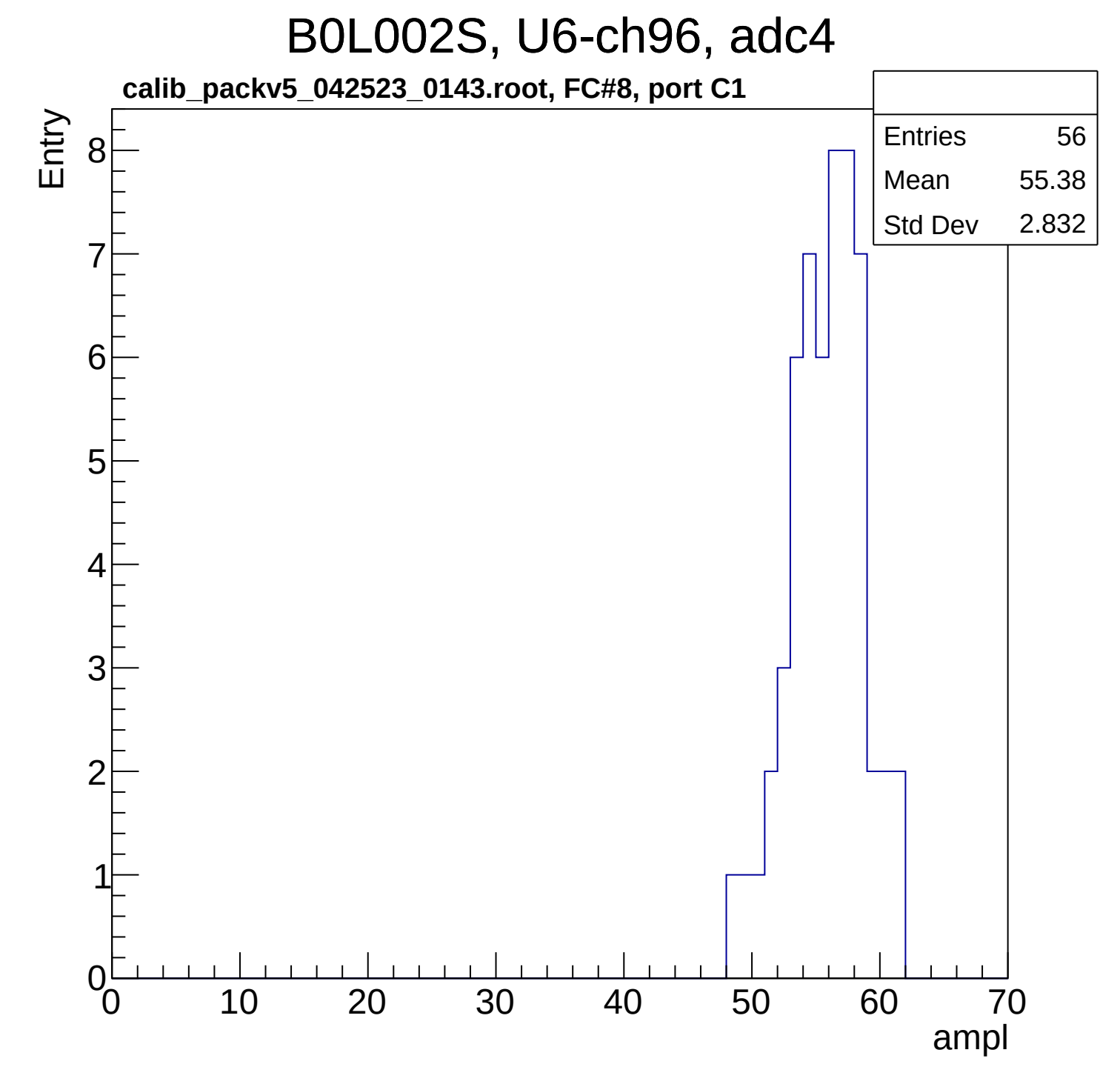
B0L002S, U6-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.38
Std Dev 2.832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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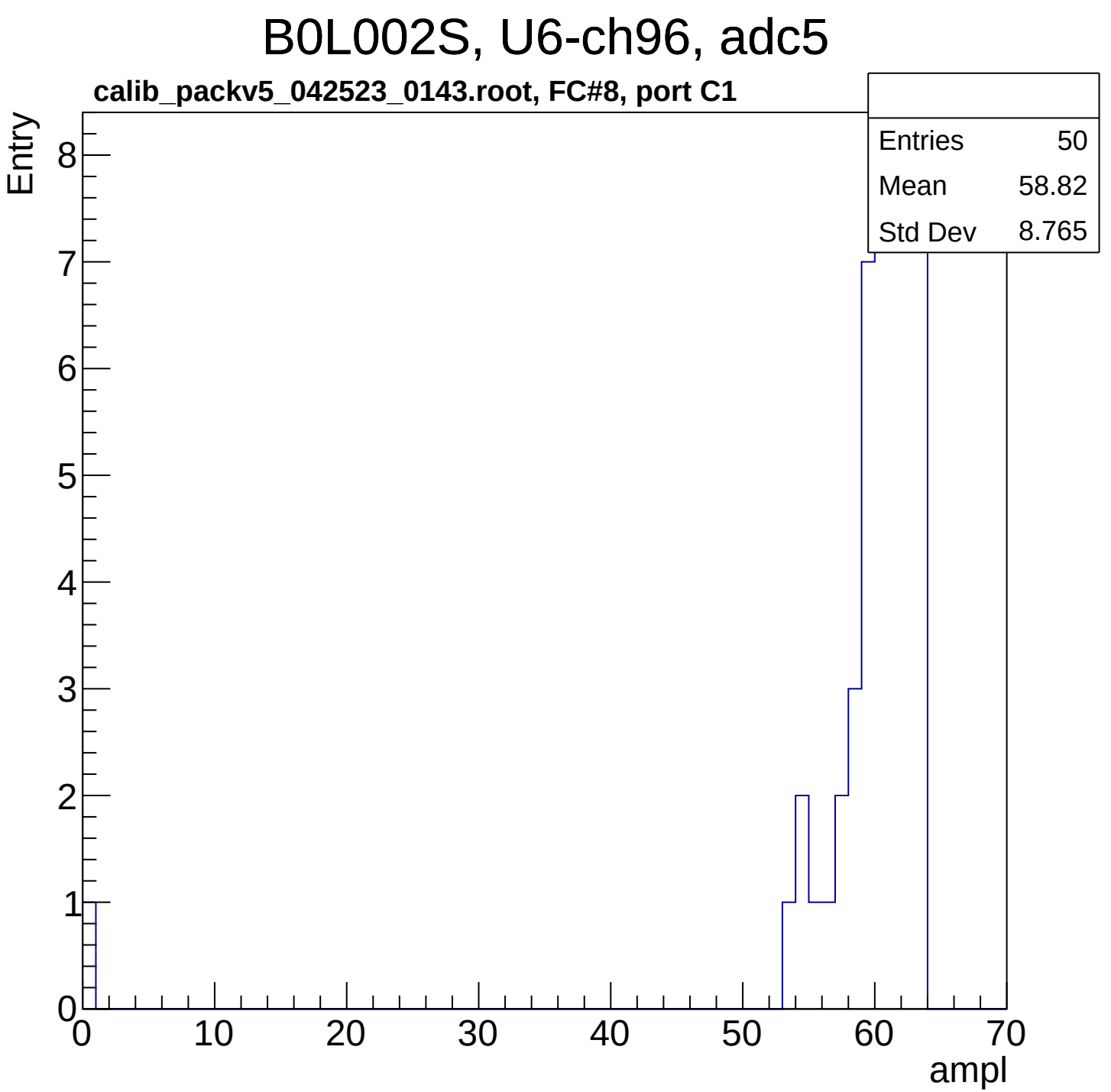
B0L002S, U6-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.82
Std Dev 8.765
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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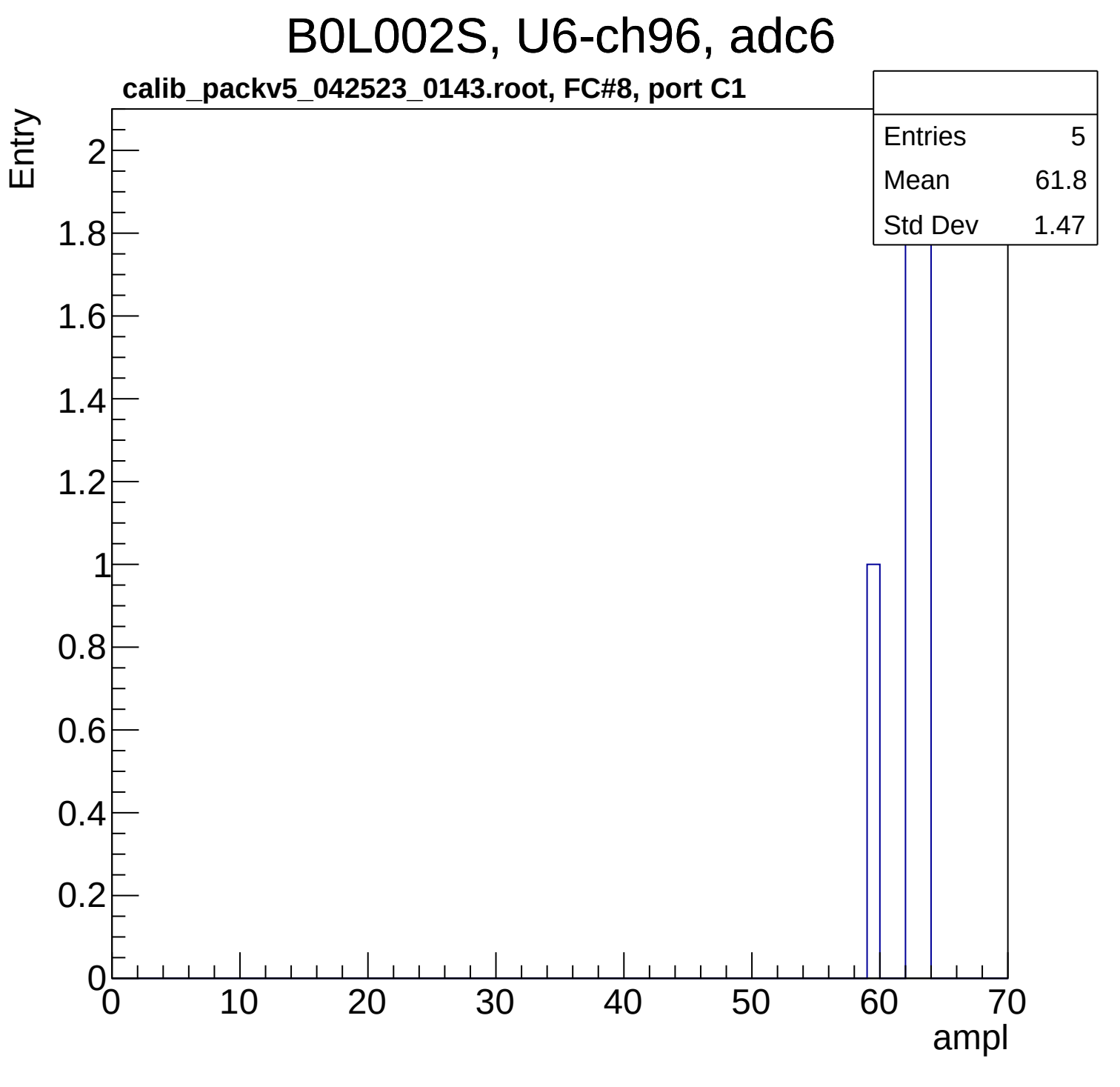
B0L002S, U6-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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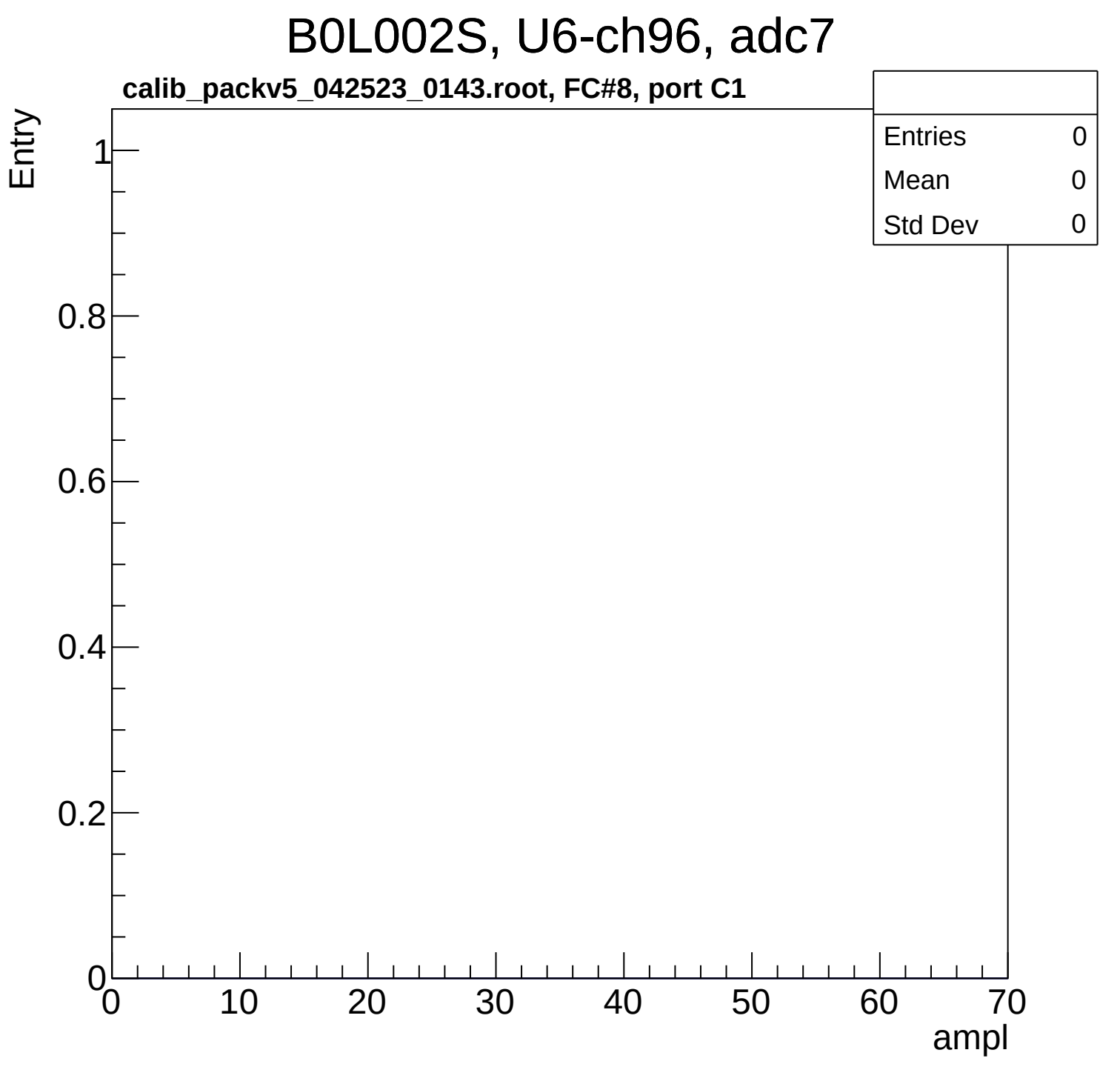
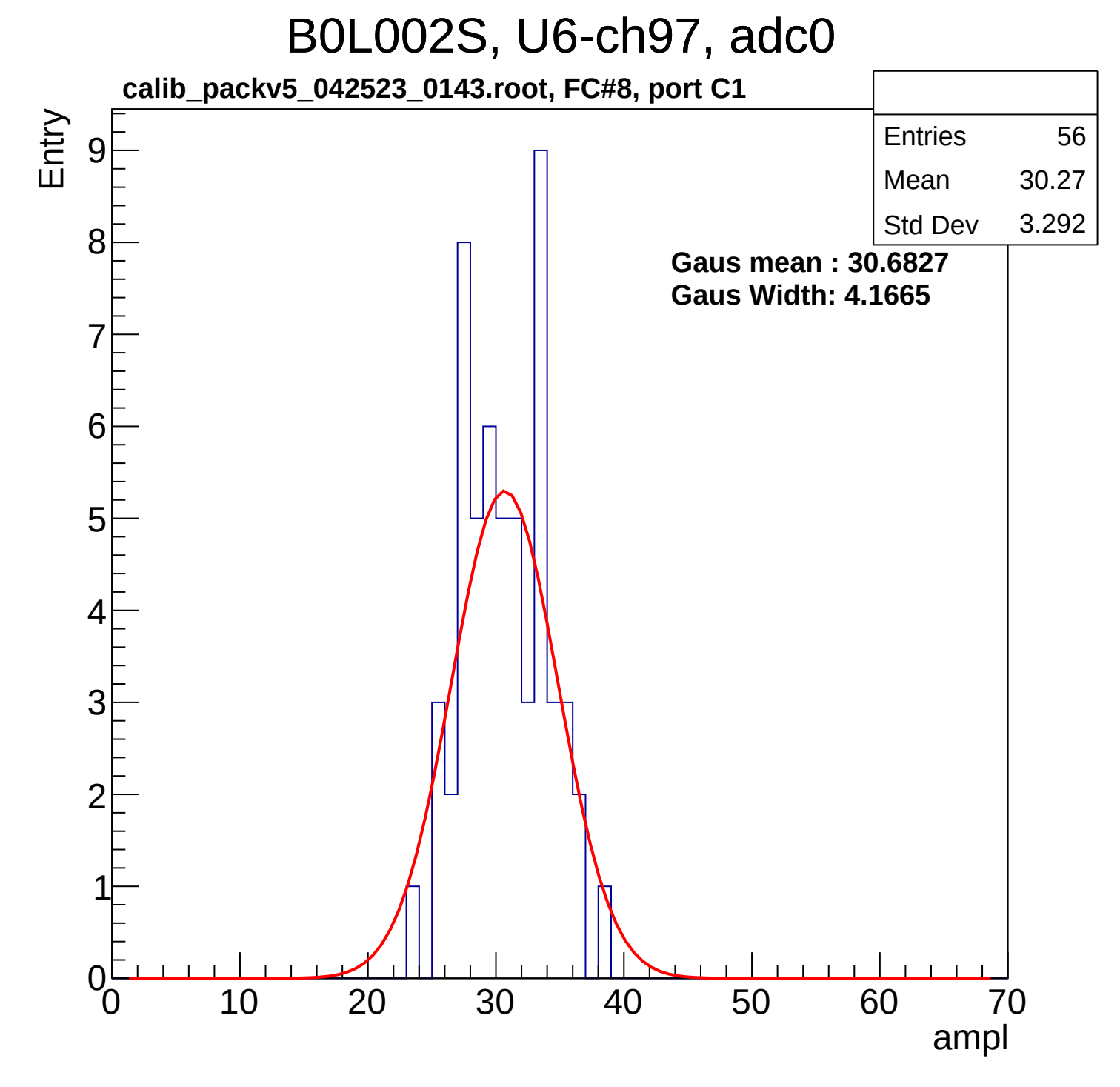

B0L002S, U6-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 30.27
Std Dev 3.292
Gaus mean : 30.6827
Gaus Width: 4.1665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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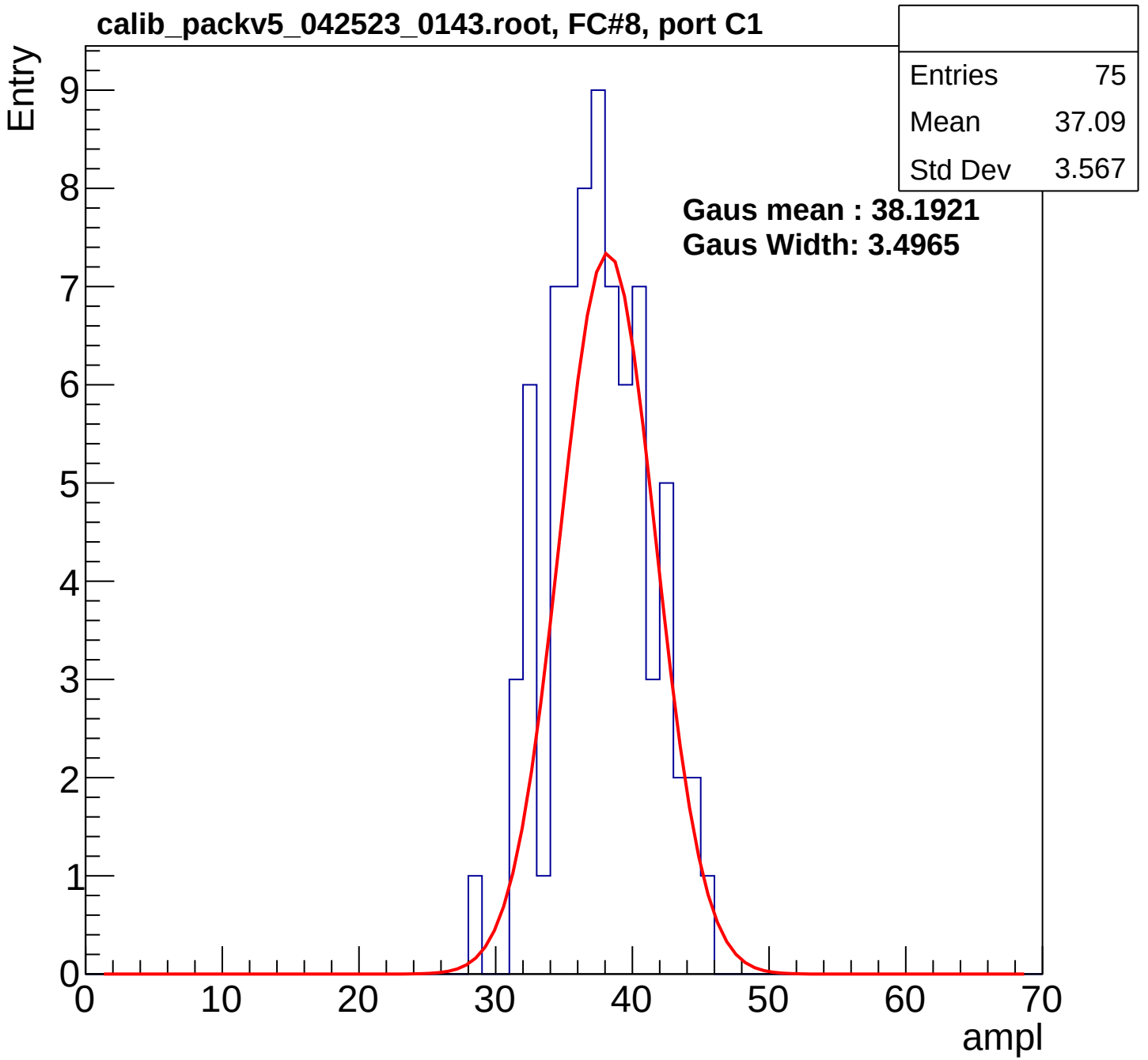
B0L002S, U6-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.09
Std Dev 3.567
Gaus mean : 38.1921
Gaus Width: 3.4965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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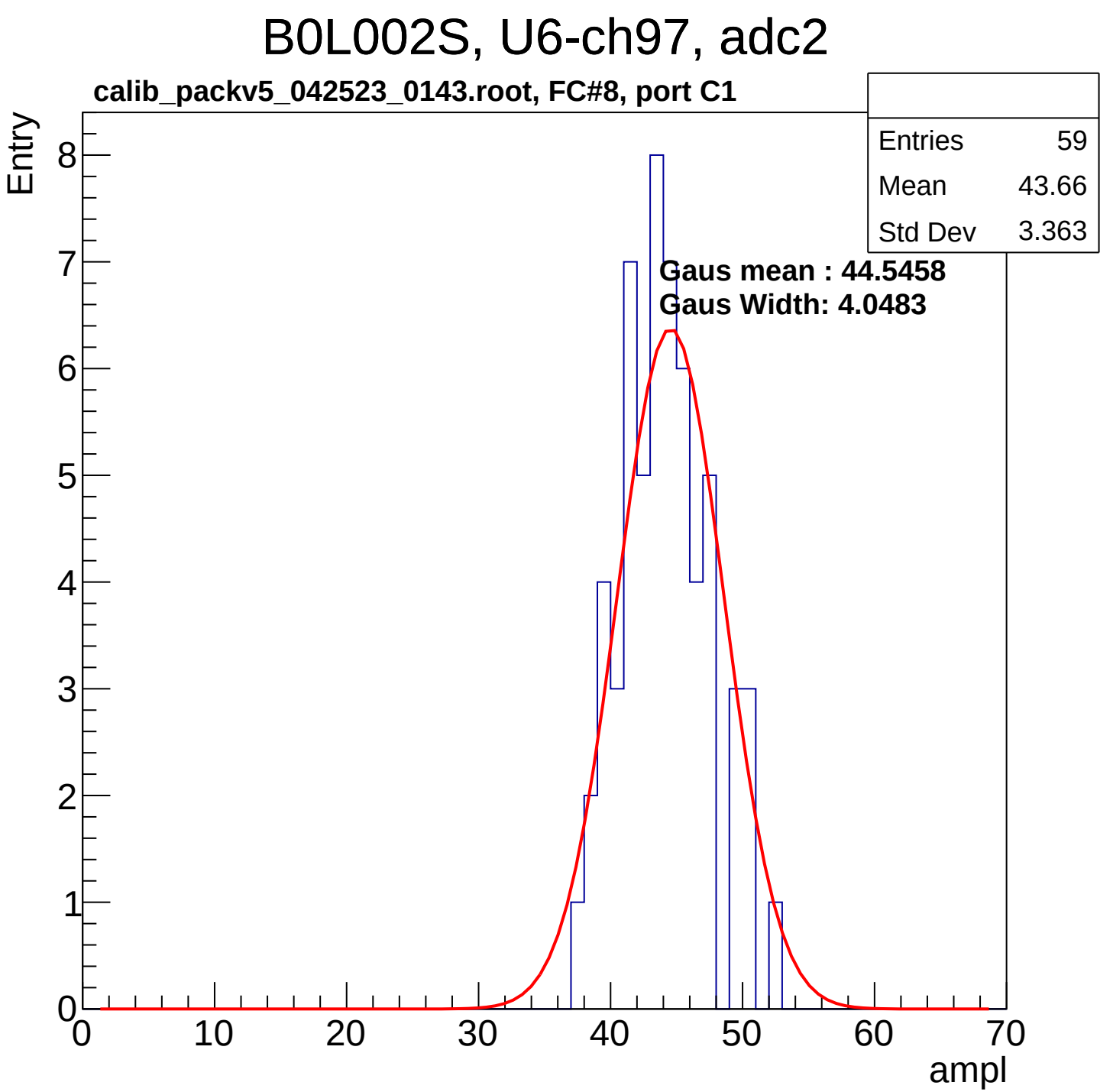
B0L002S, U6-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.66
Std Dev 3.363
Gaus mean : 44.5458
Gaus Width: 4.0483
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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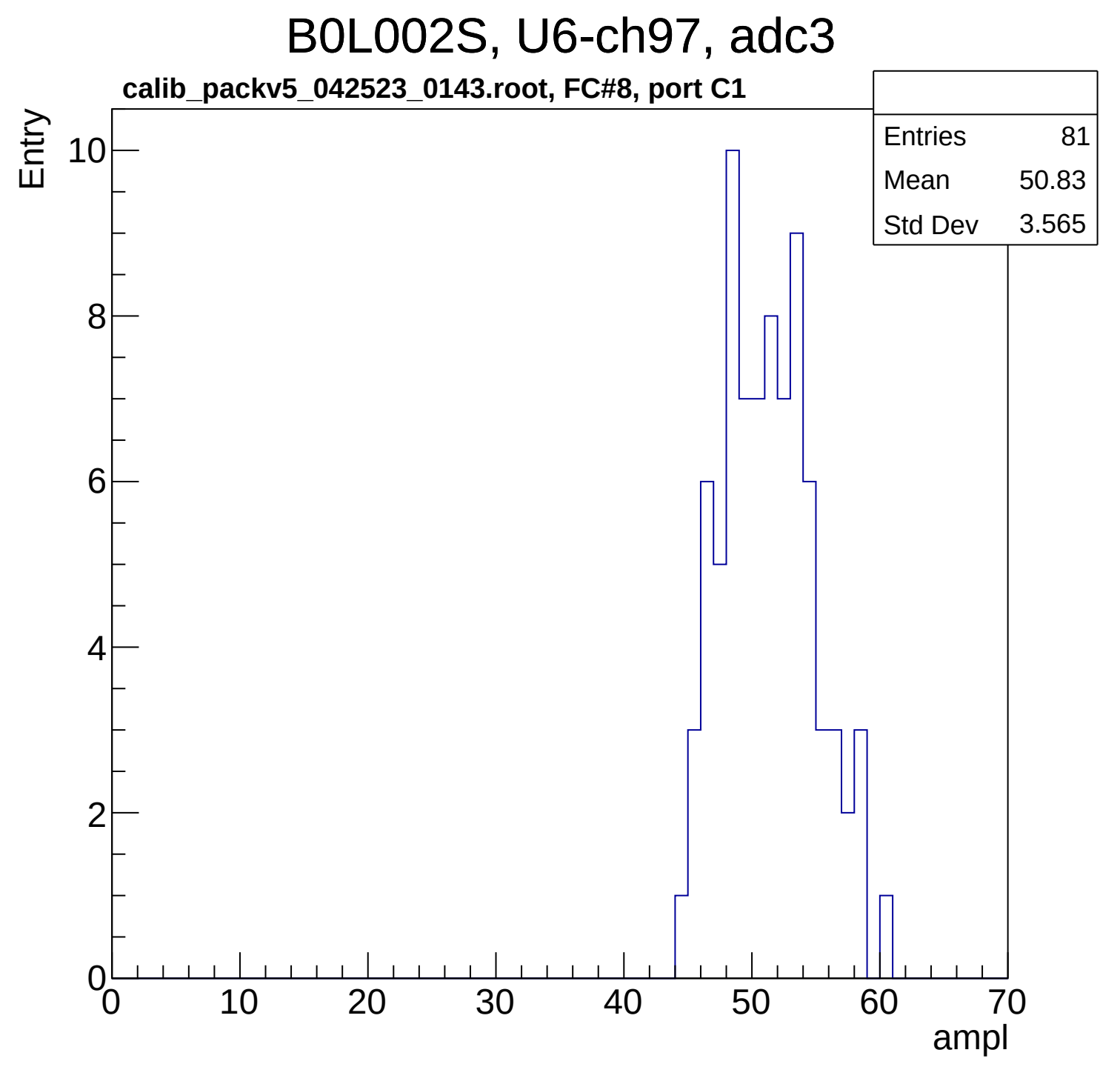
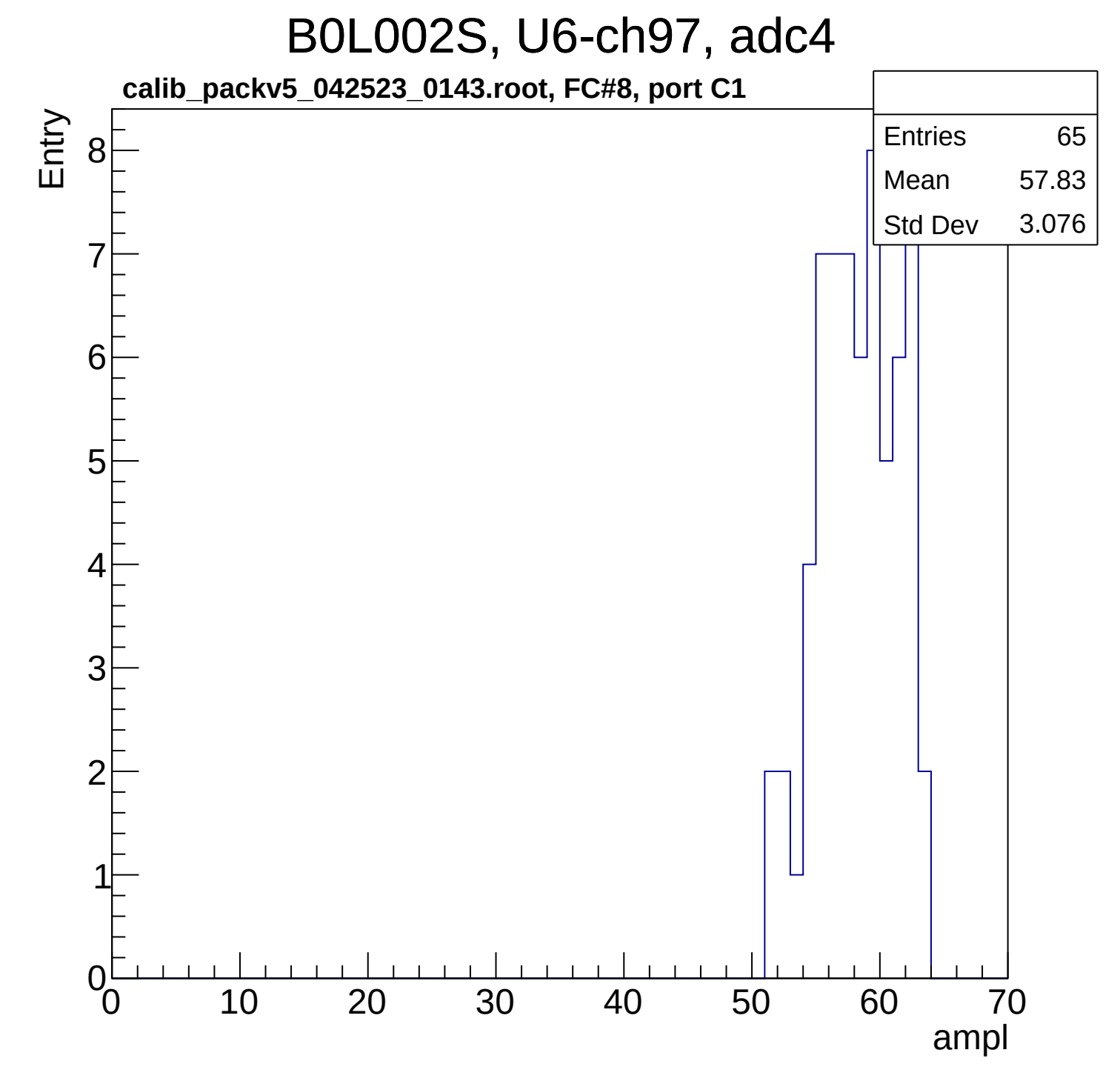
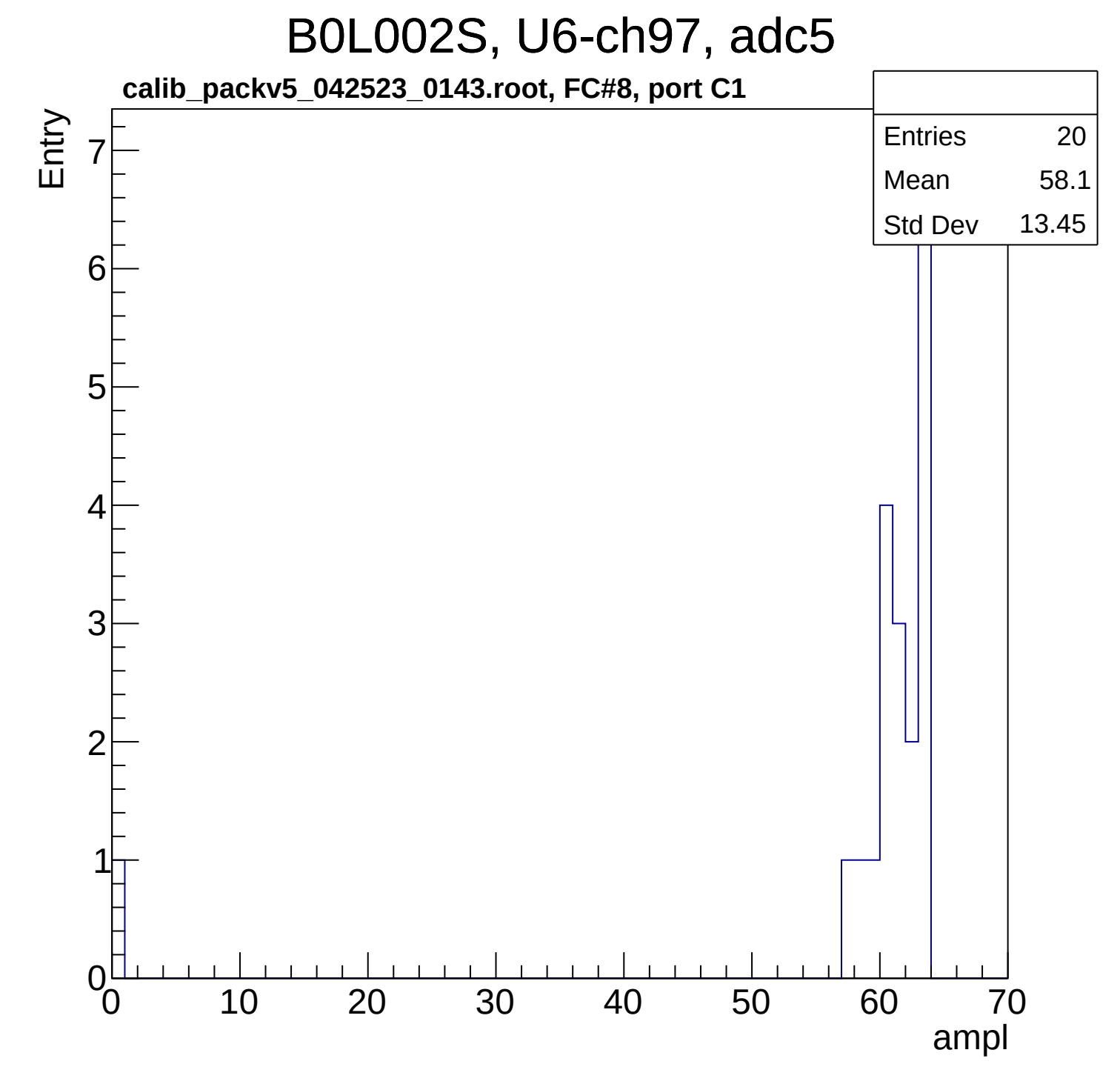

B0L002S, U6-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 50.83
Std Dev 3.565
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.83
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 58.1
Std Dev 13.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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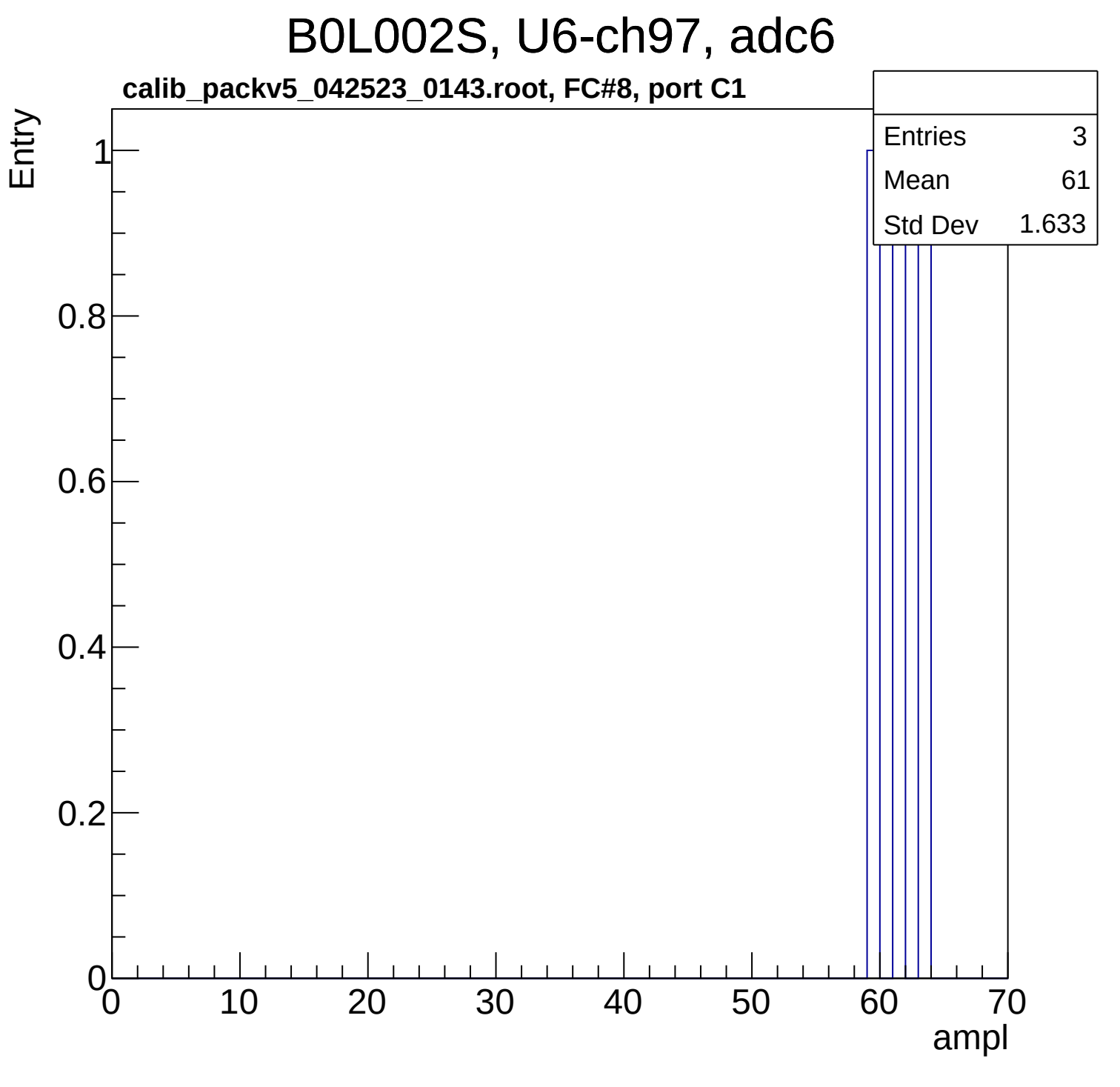
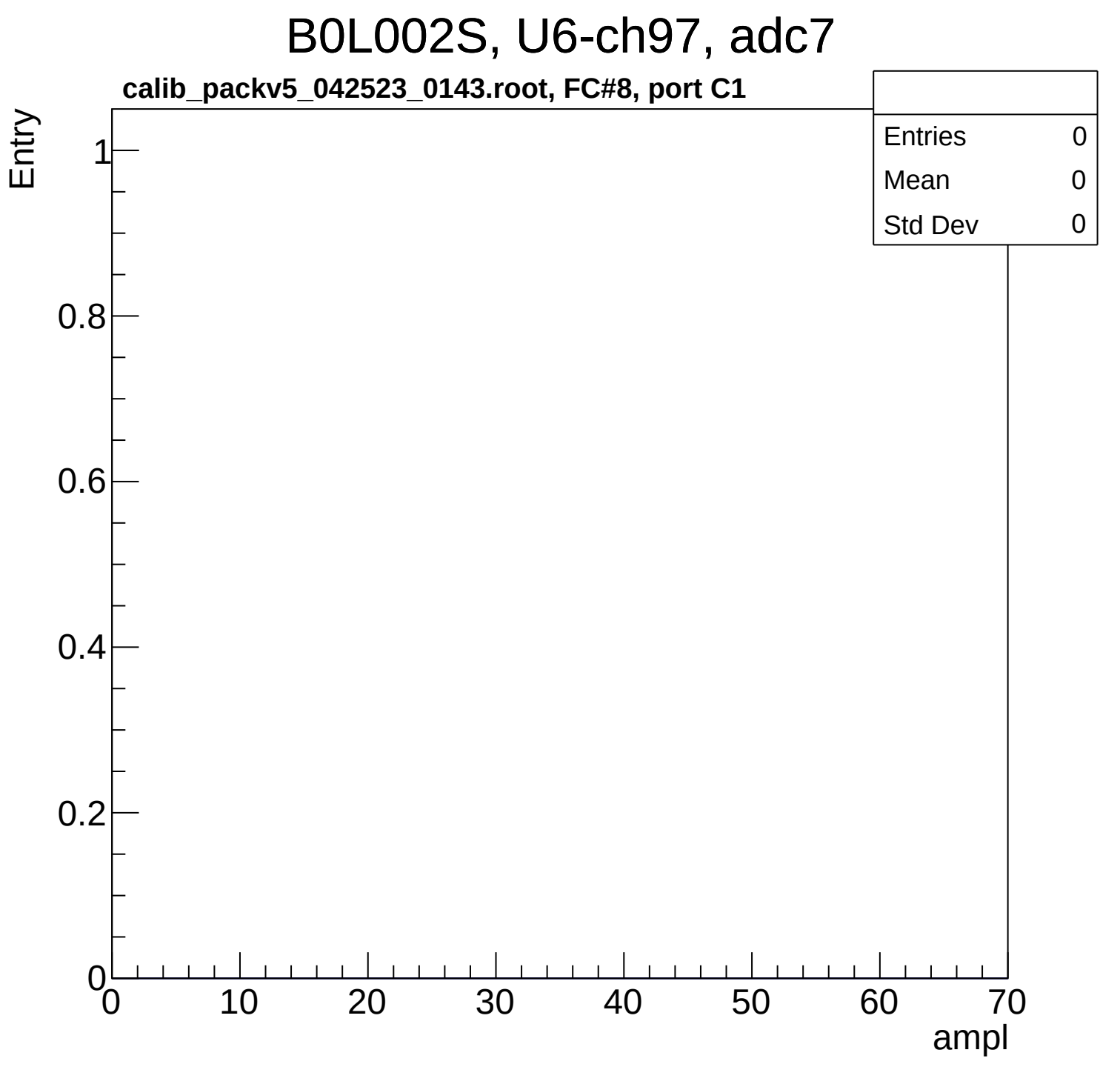

B0L002S, U6-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61
Std Dev 1.633
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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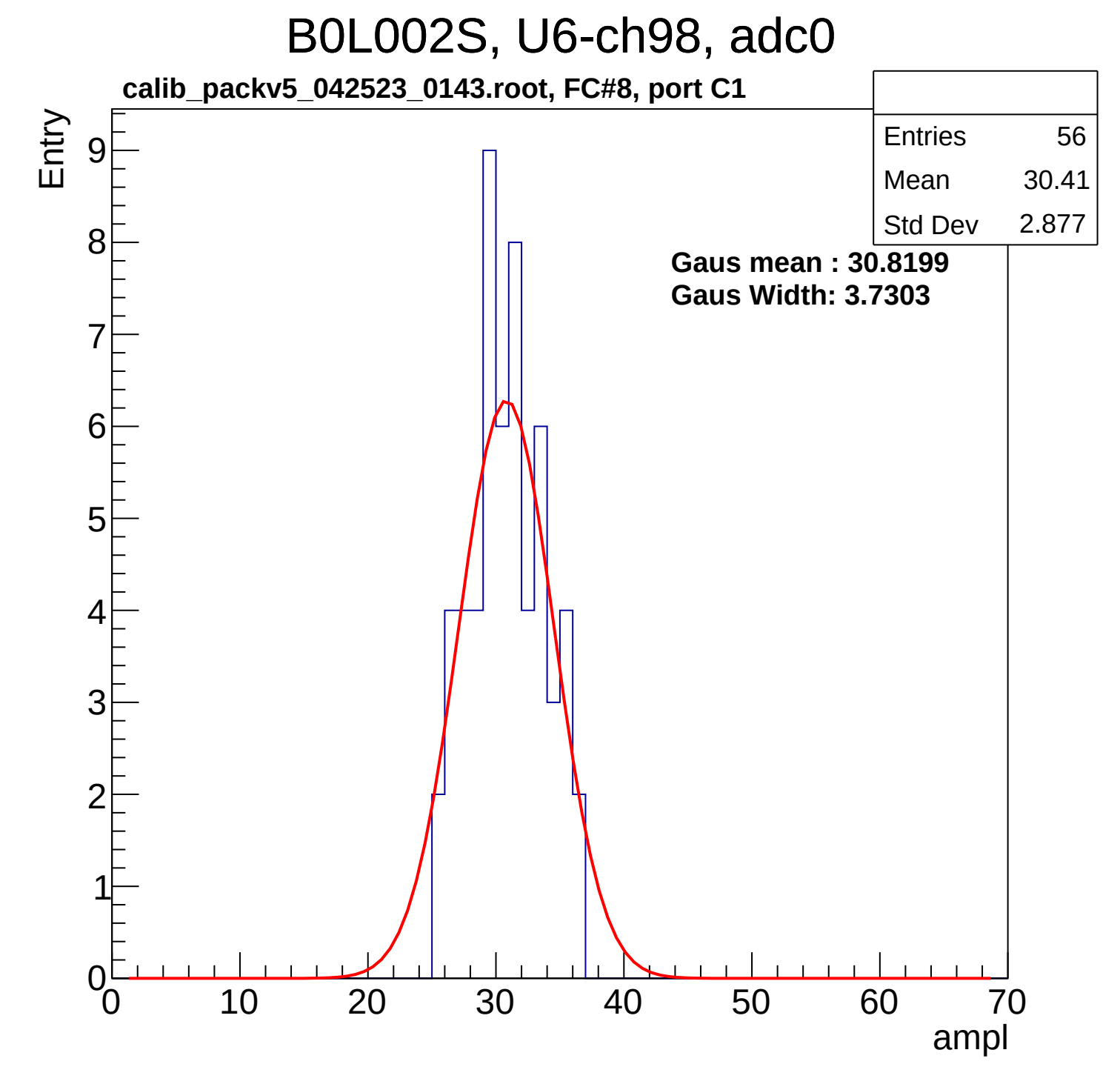
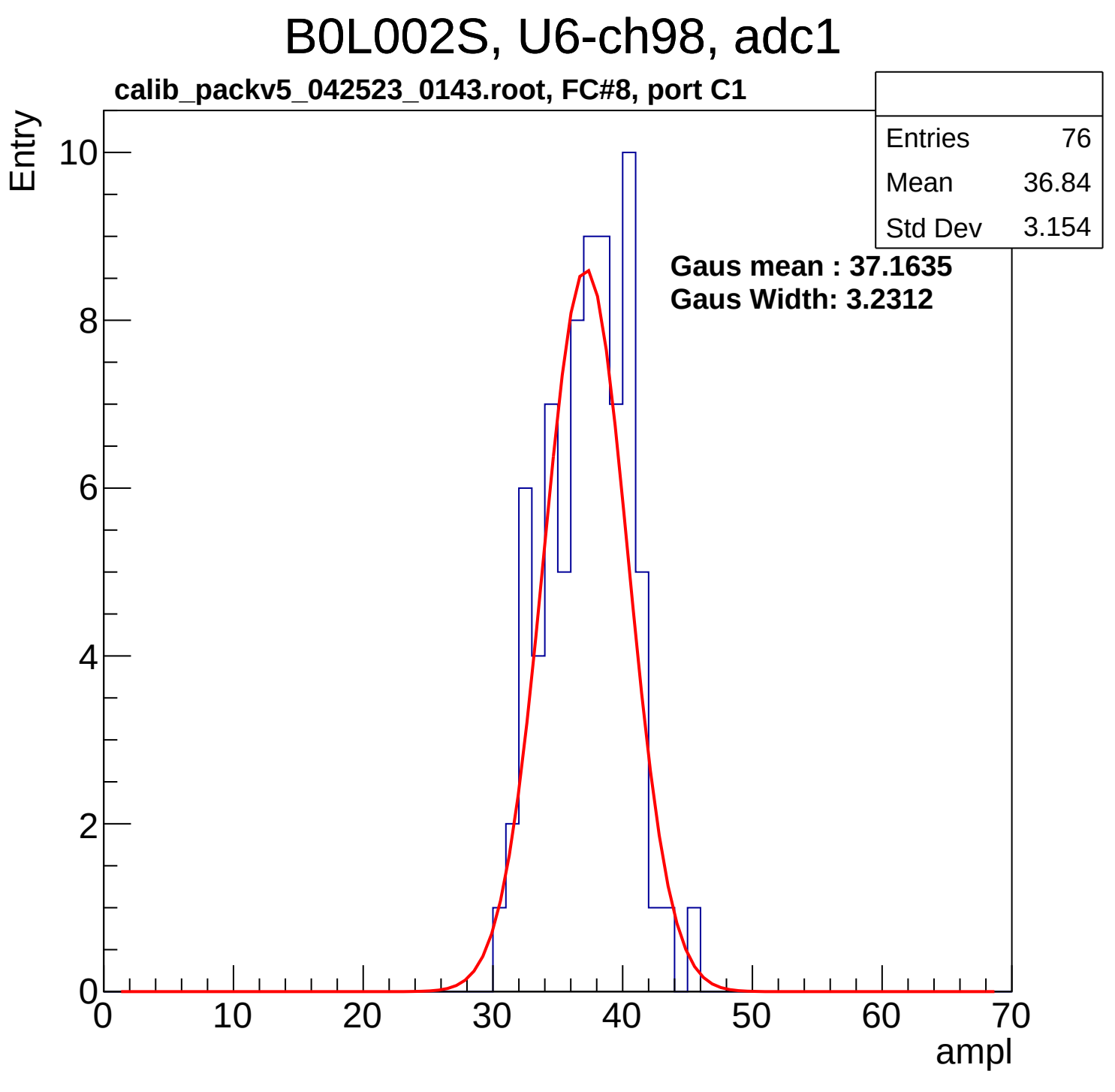
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 30.41
Std Dev 2.877
Gaus mean : 30.8199
Gaus Width: 3.7303
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.84
Std Dev 3.154
Gaus mean : 37.1635
Gaus Width: 3.2312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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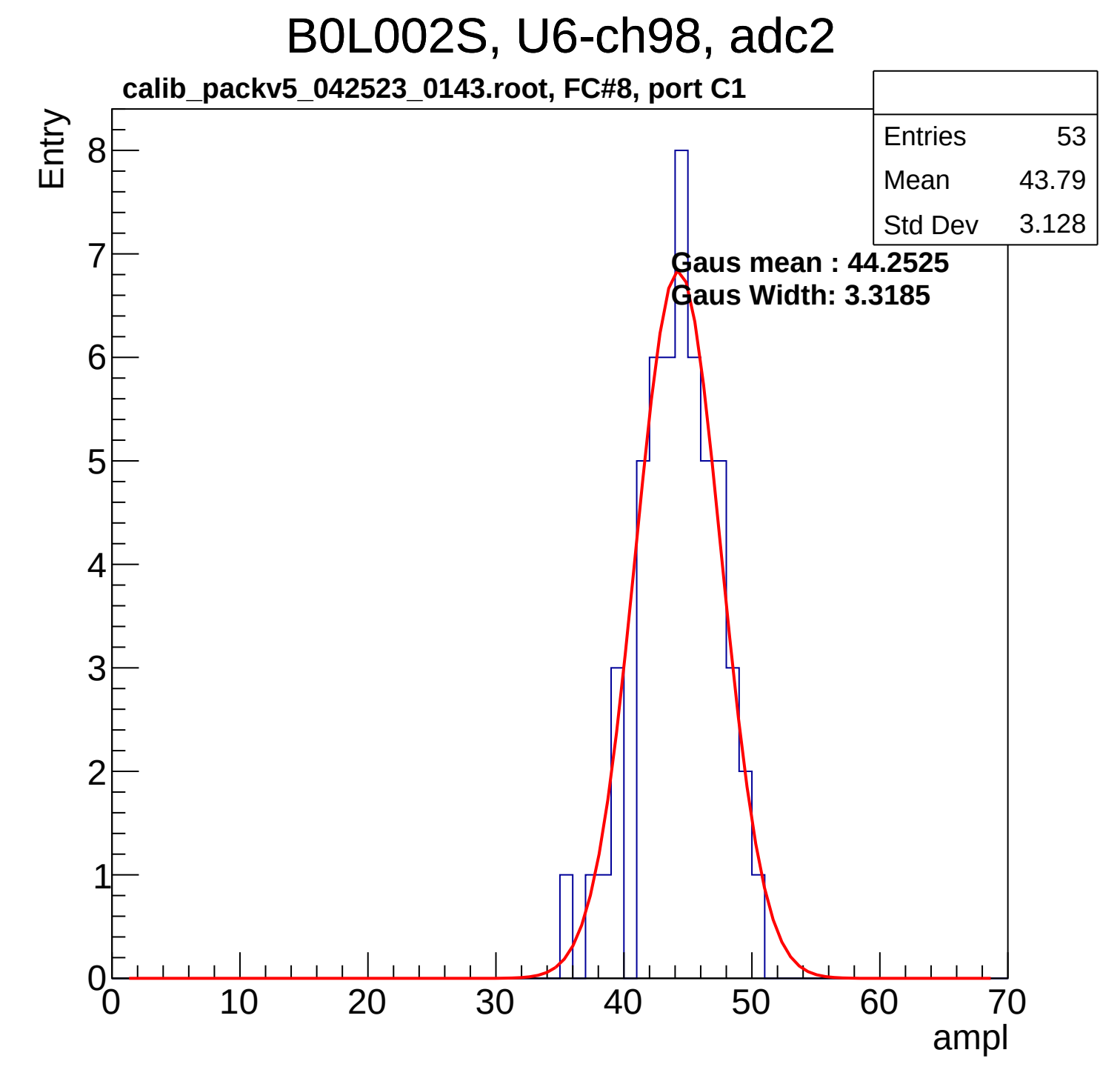
B0L002S, U6-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 43.79
Std Dev 3.128
Gaus mean : 44.2525
Gaus Width: 3.3185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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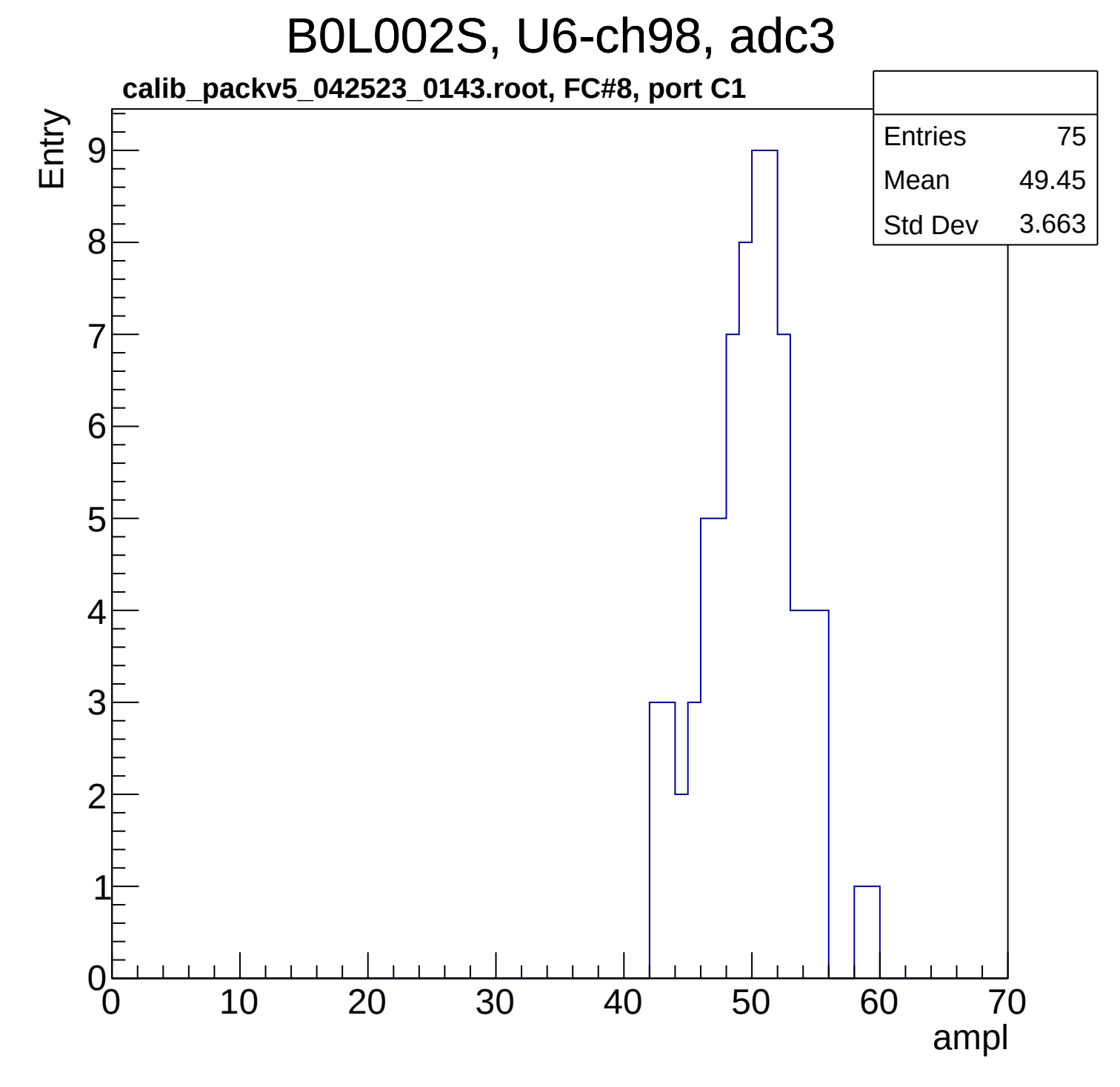
40
50
60
70


B0L002S, U6-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 49.45
Std Dev 3.663
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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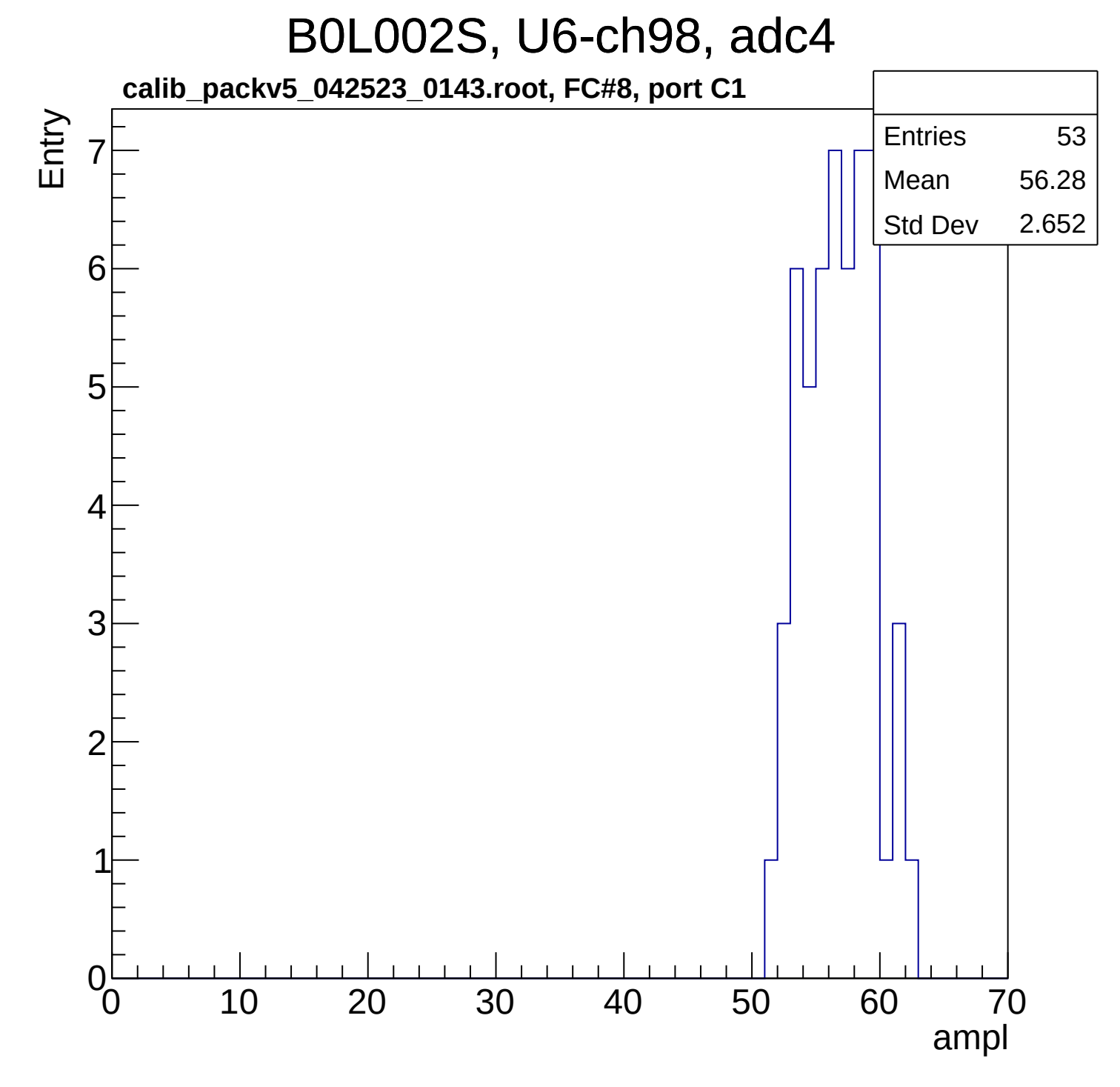
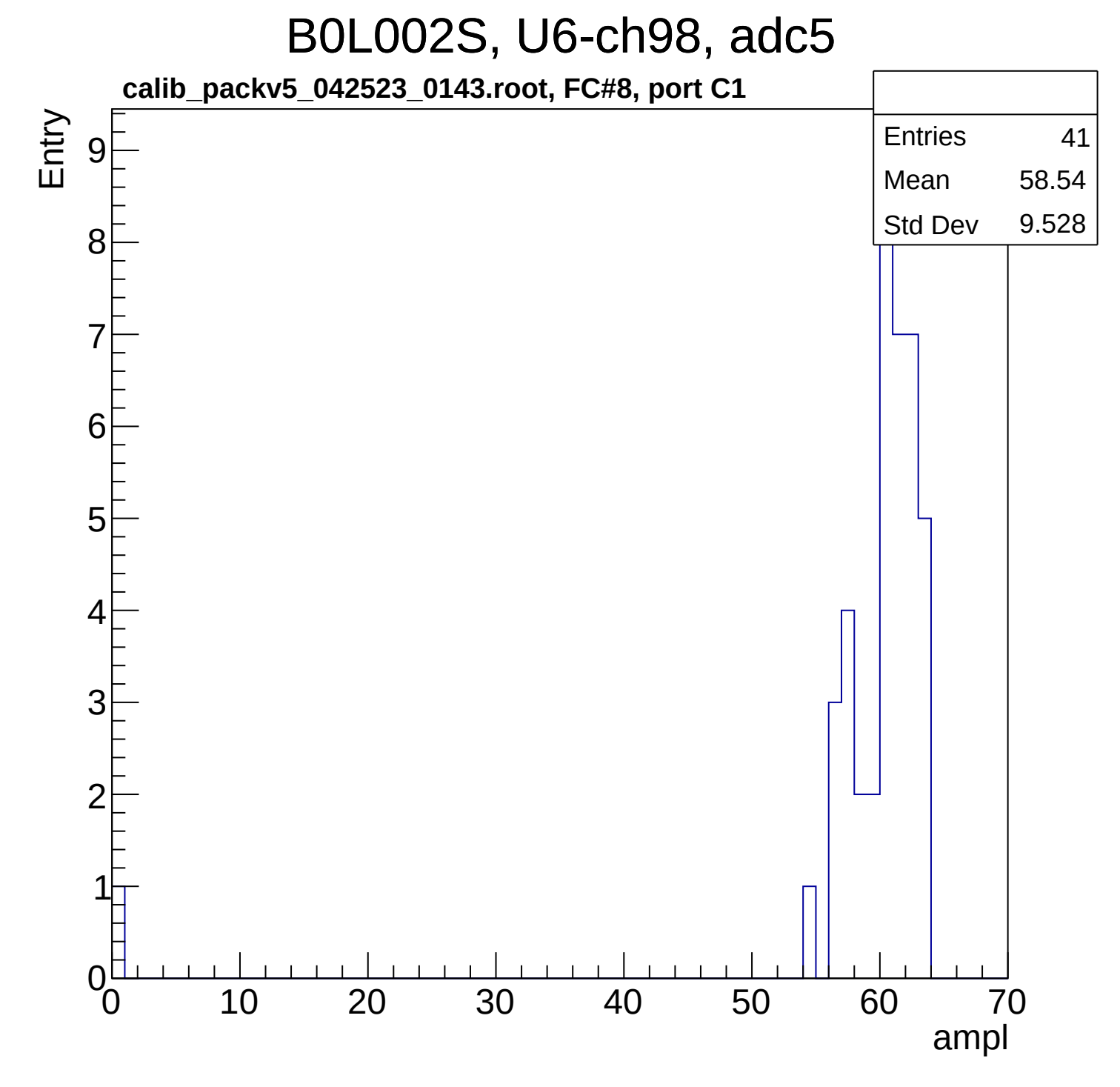

B0L002S, U6-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.28
Std Dev 2.652
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.54
Std Dev 9.528
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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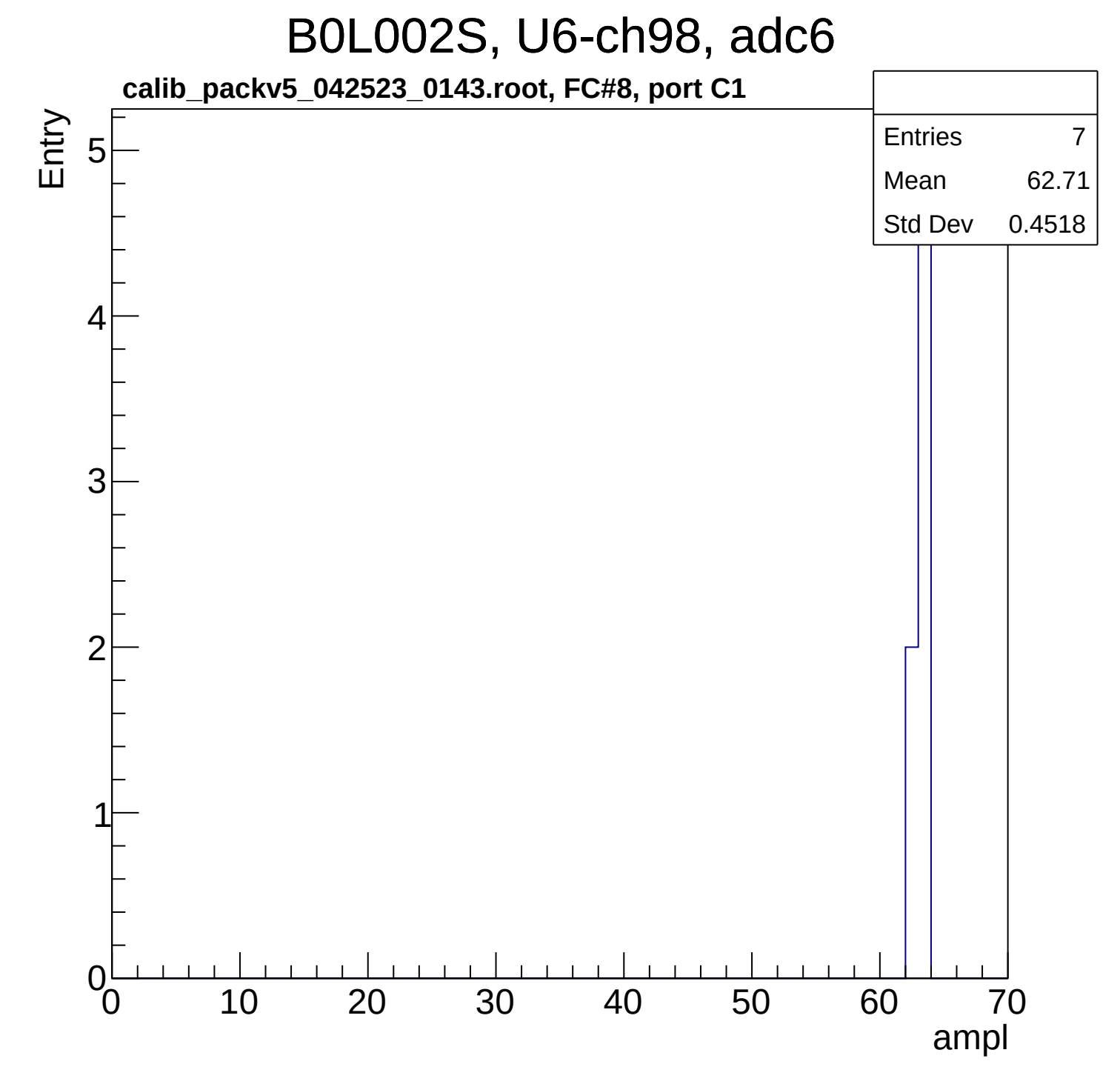
B0L002S, U6-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
ampl
0
1
2
3
4
5
0
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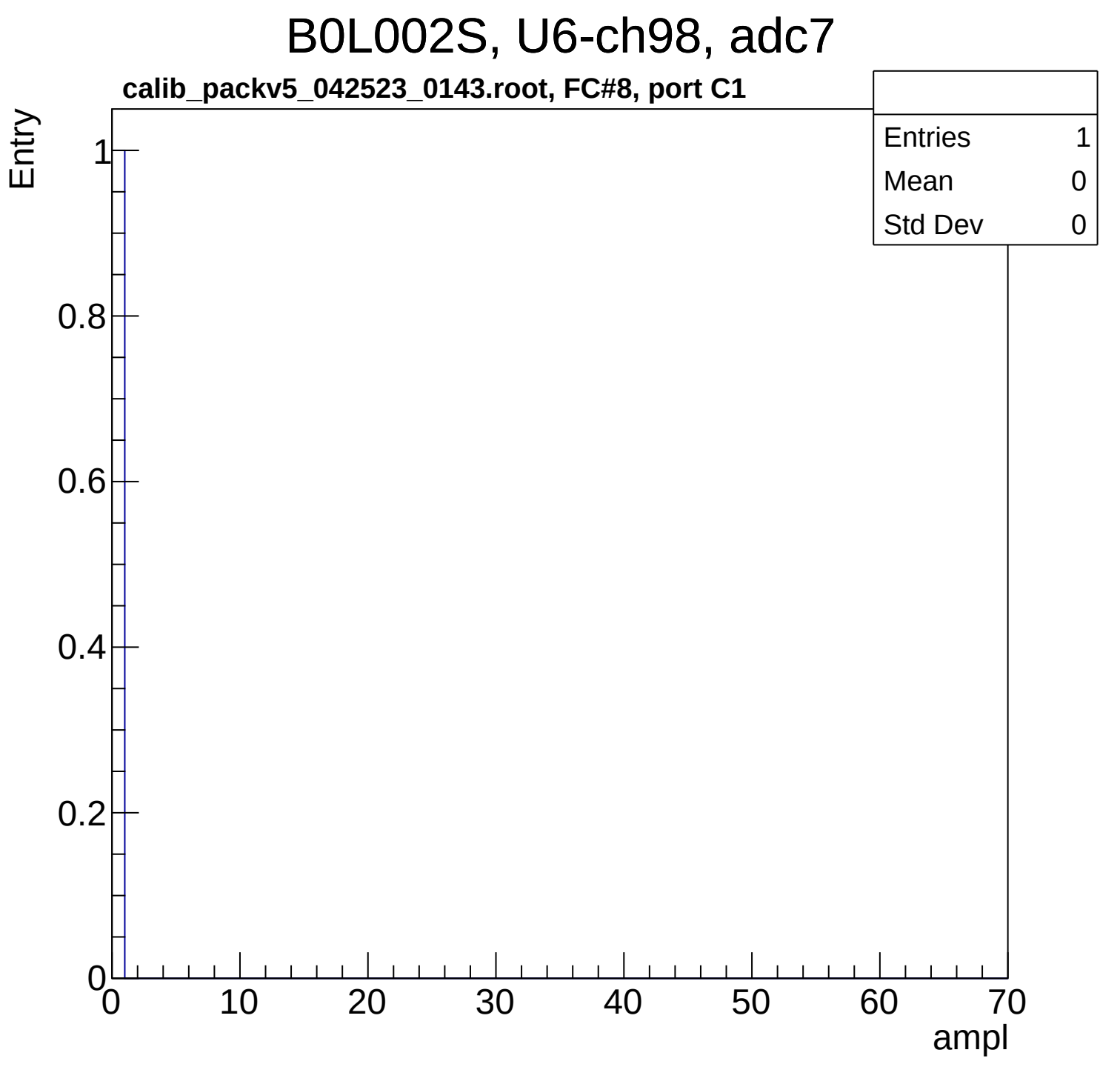
20
30
40
50
60
70


B0L002S, U6-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
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10
20
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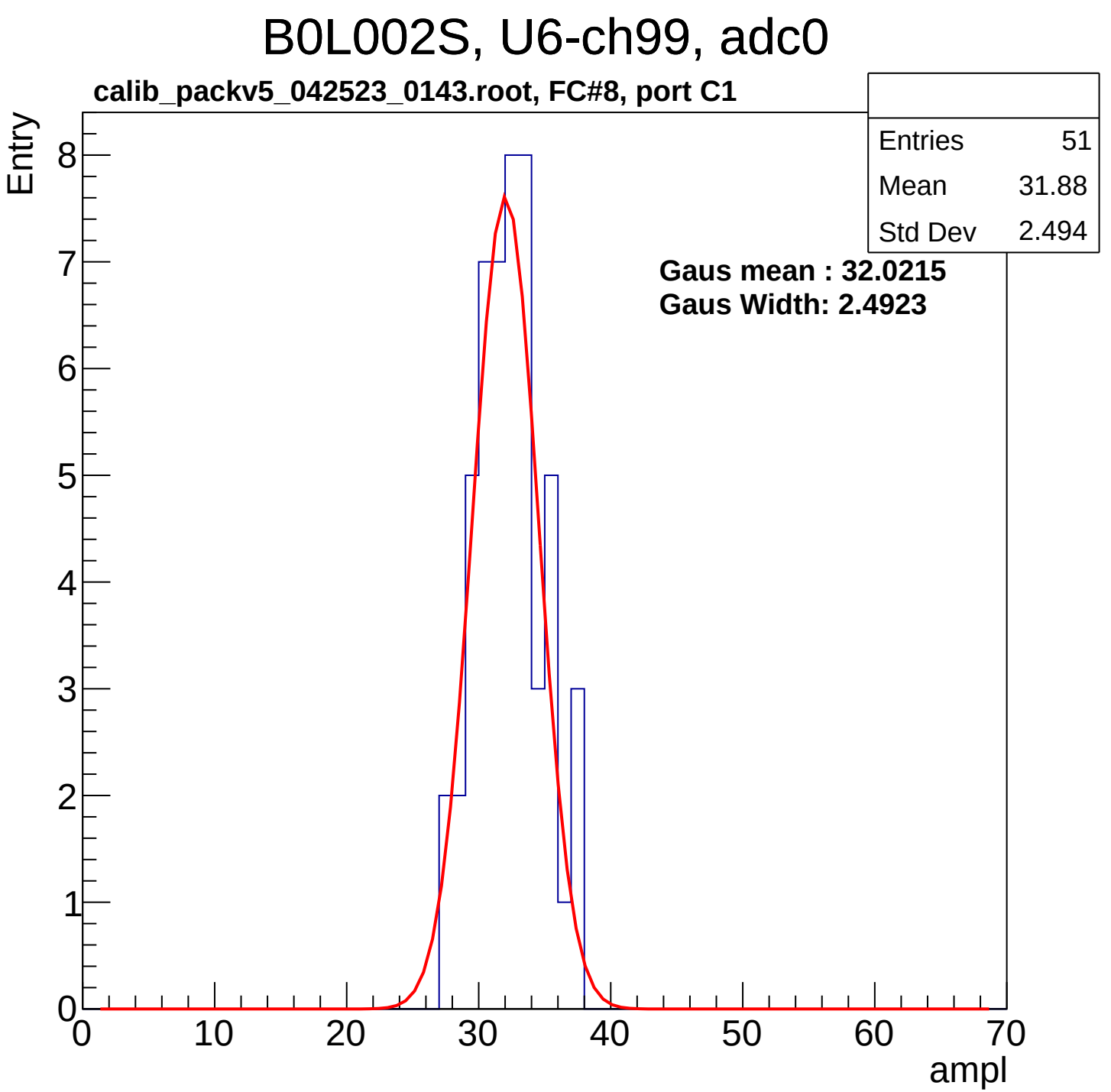
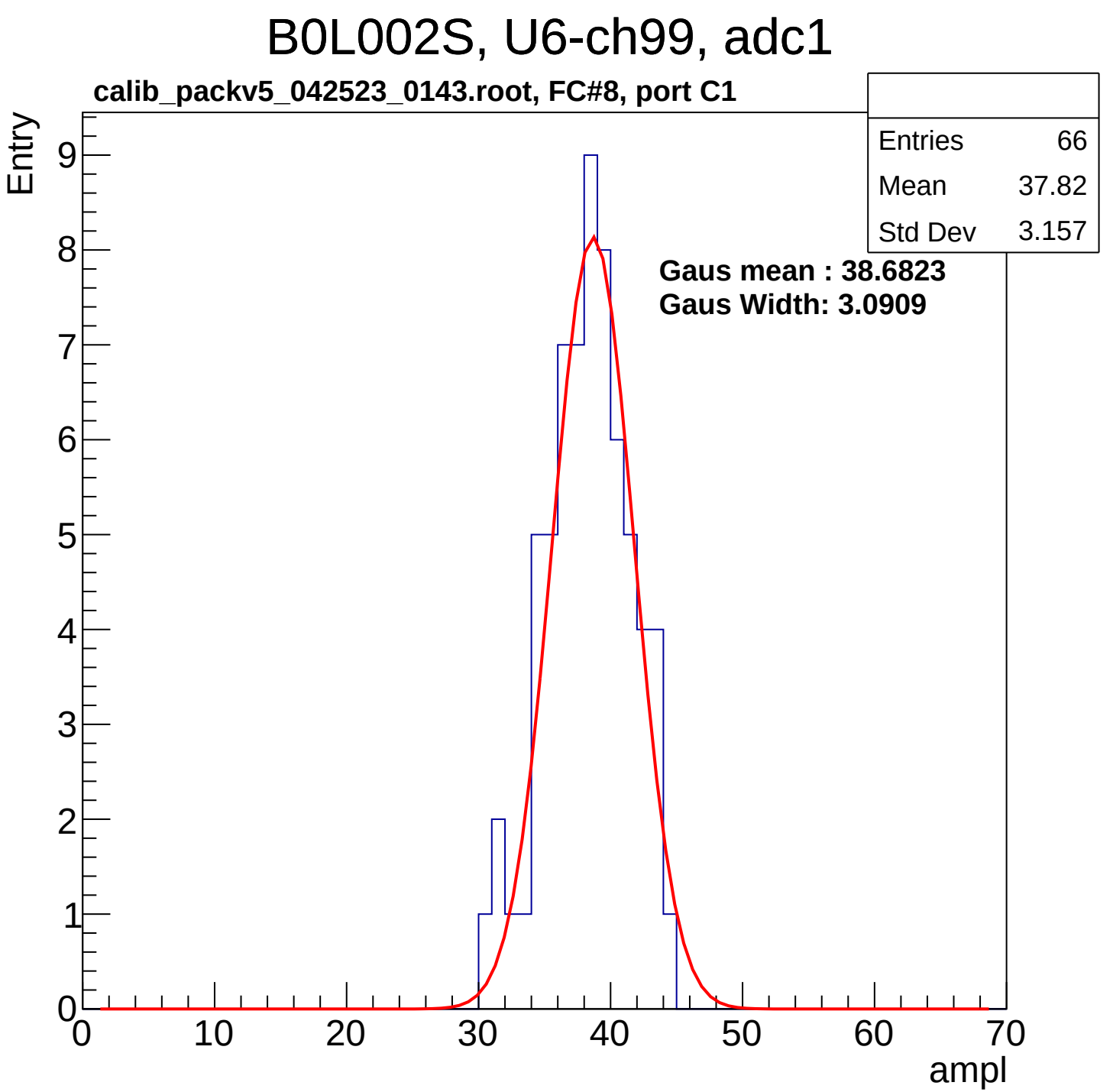
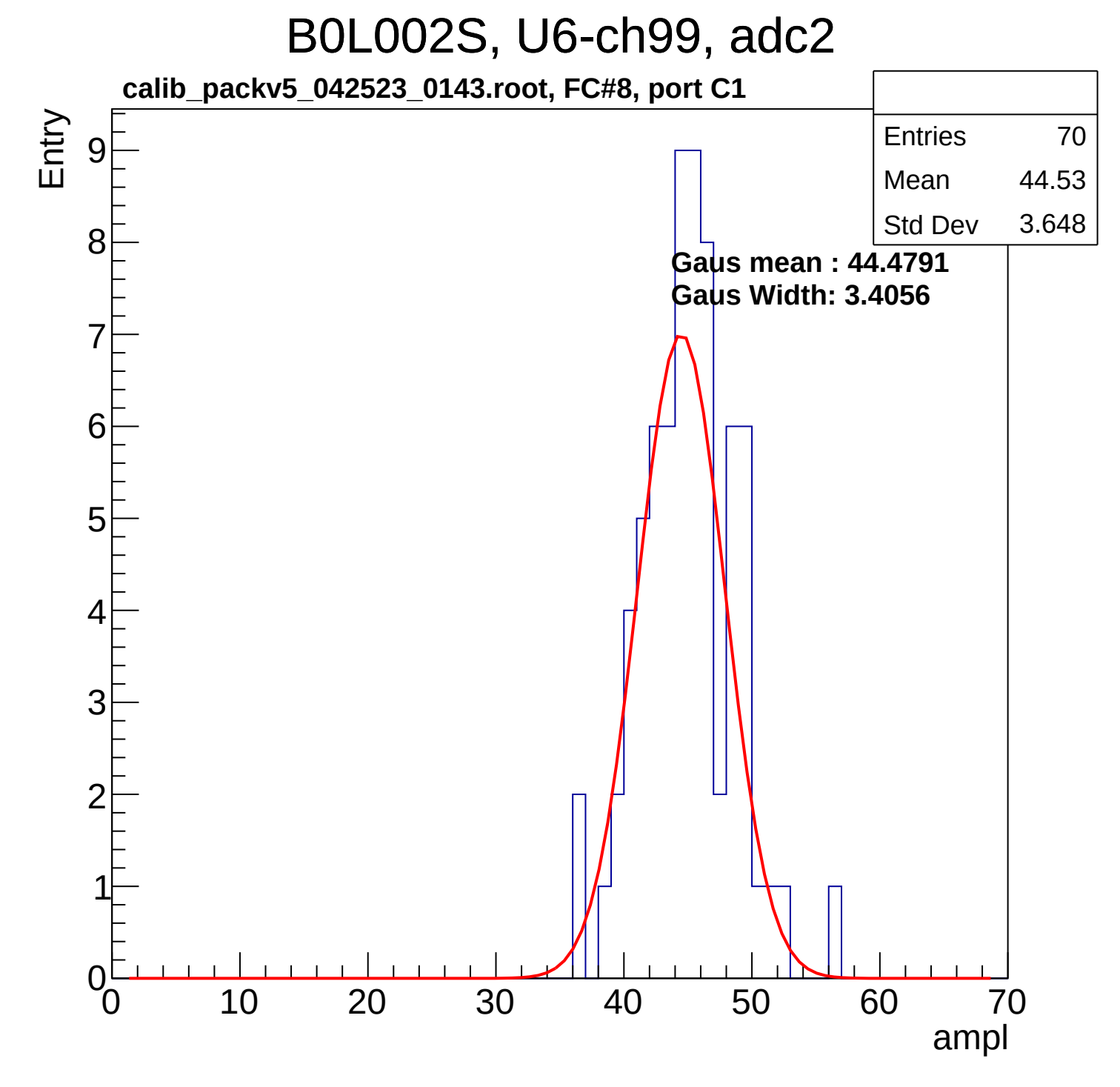
50
60
70
ampl


B0L002S, U6-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 31.88
Std Dev 2.494
Gaus mean : 32.0215
Gaus Width: 2.4923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.82
Std Dev 3.157
Gaus mean : 38.6823
Gaus Width: 3.0909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.53
Std Dev 3.648
Gaus mean : 44.4791
Gaus Width: 3.4056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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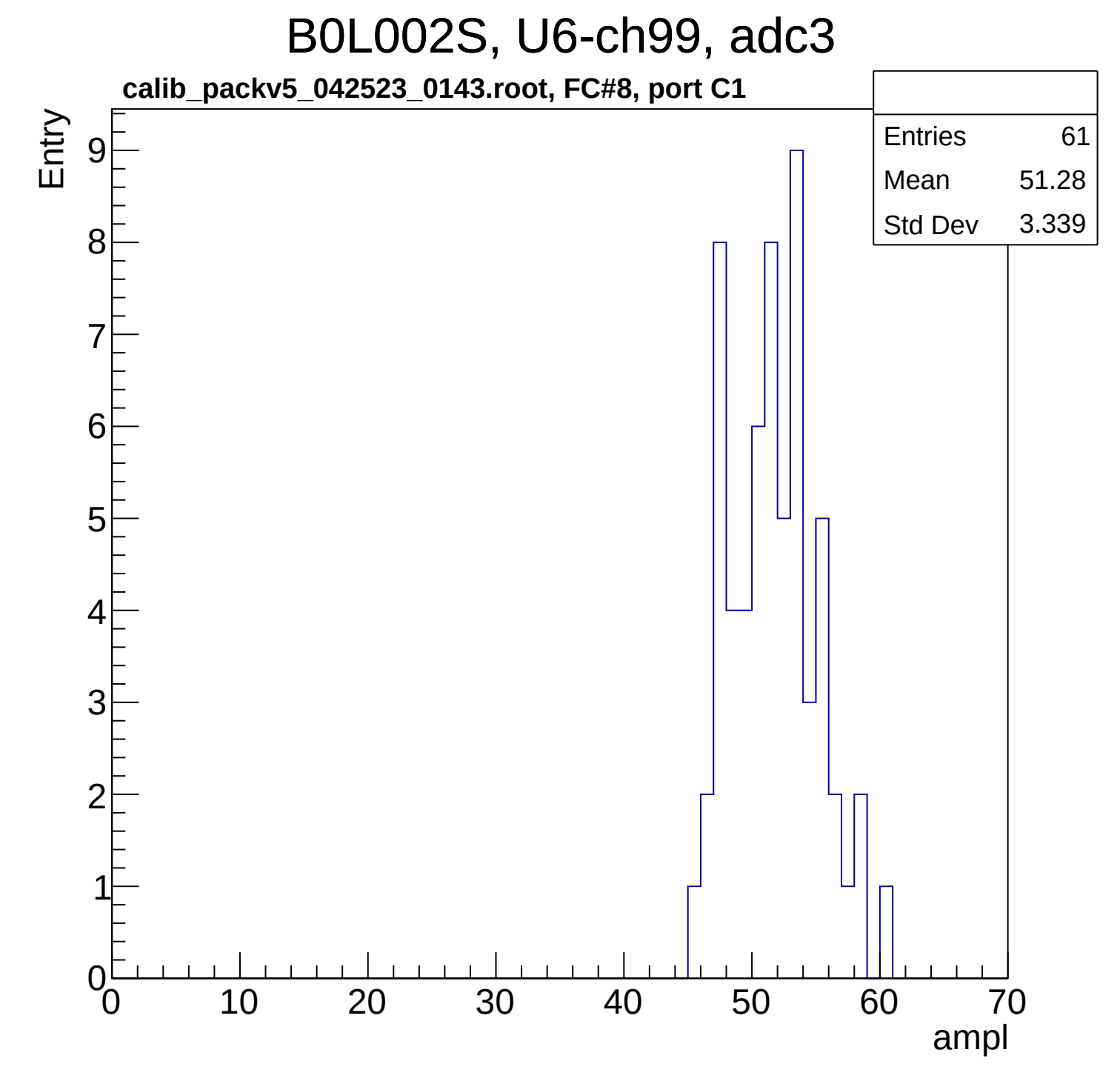
B0L002S, U6-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.28
Std Dev 3.339
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
50
60
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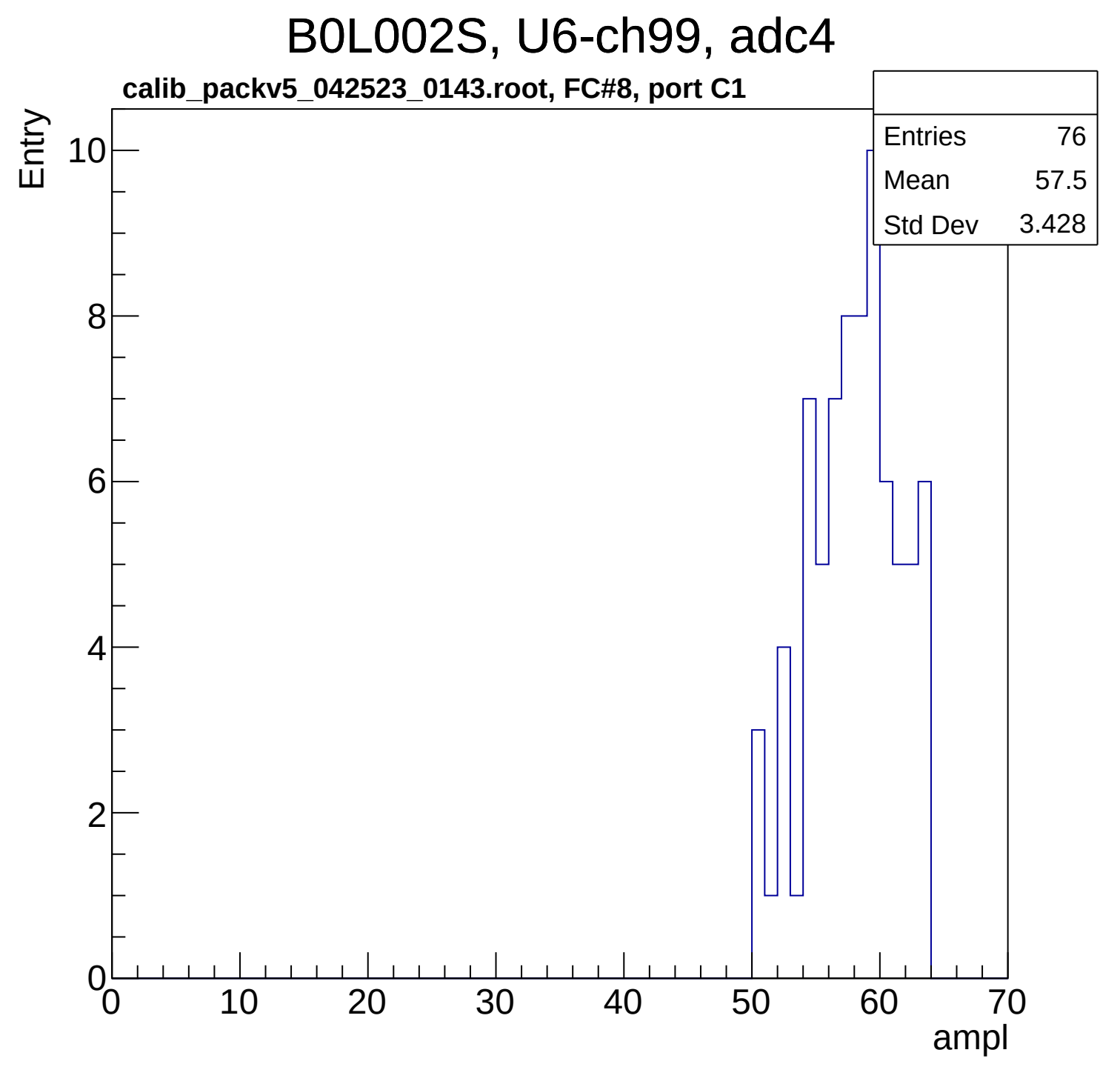
B0L002S, U6-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 57.5
Std Dev 3.428
Entry
ampl
0
2
4
6
8
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0
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20
30
40
50
60
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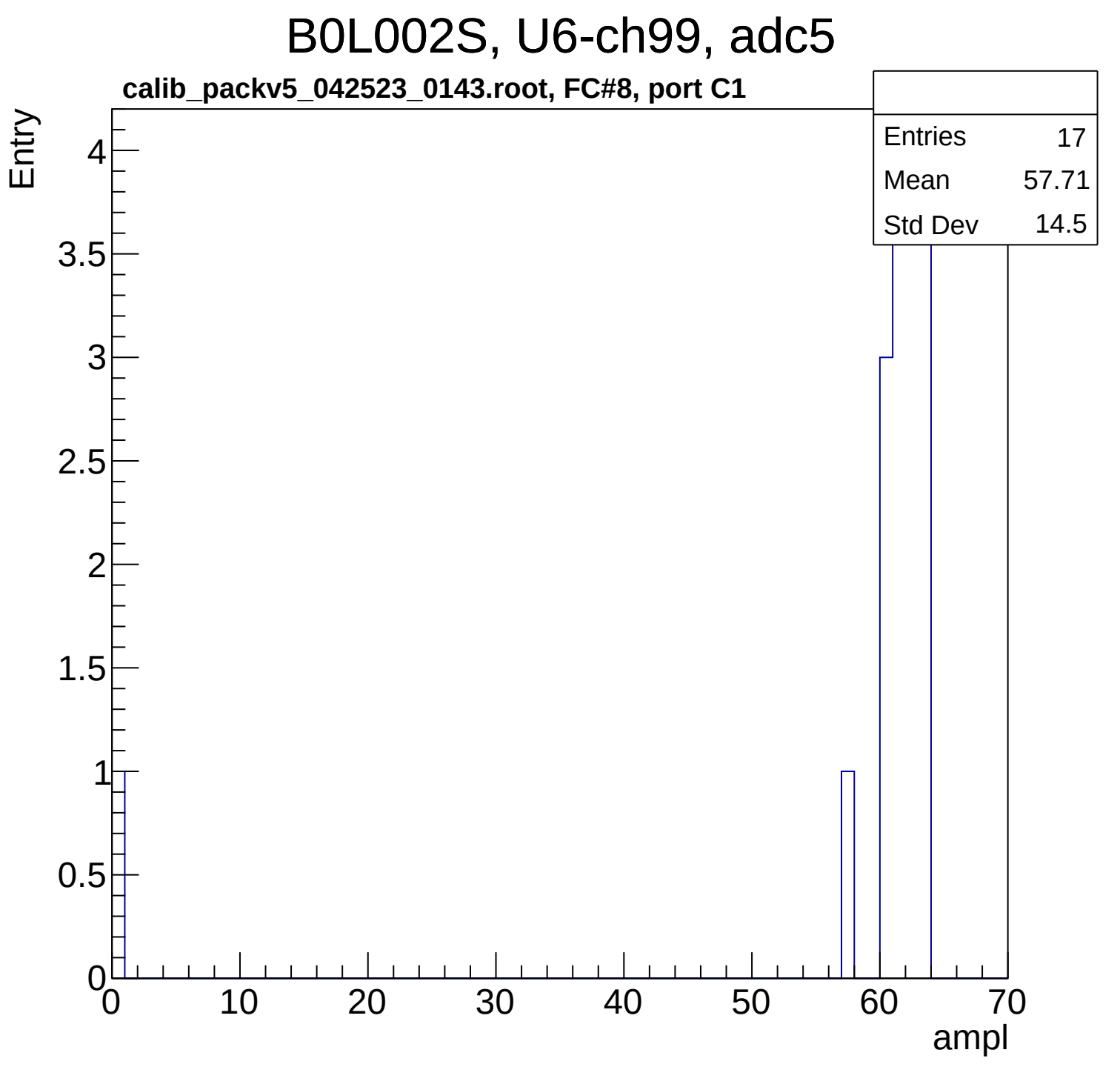
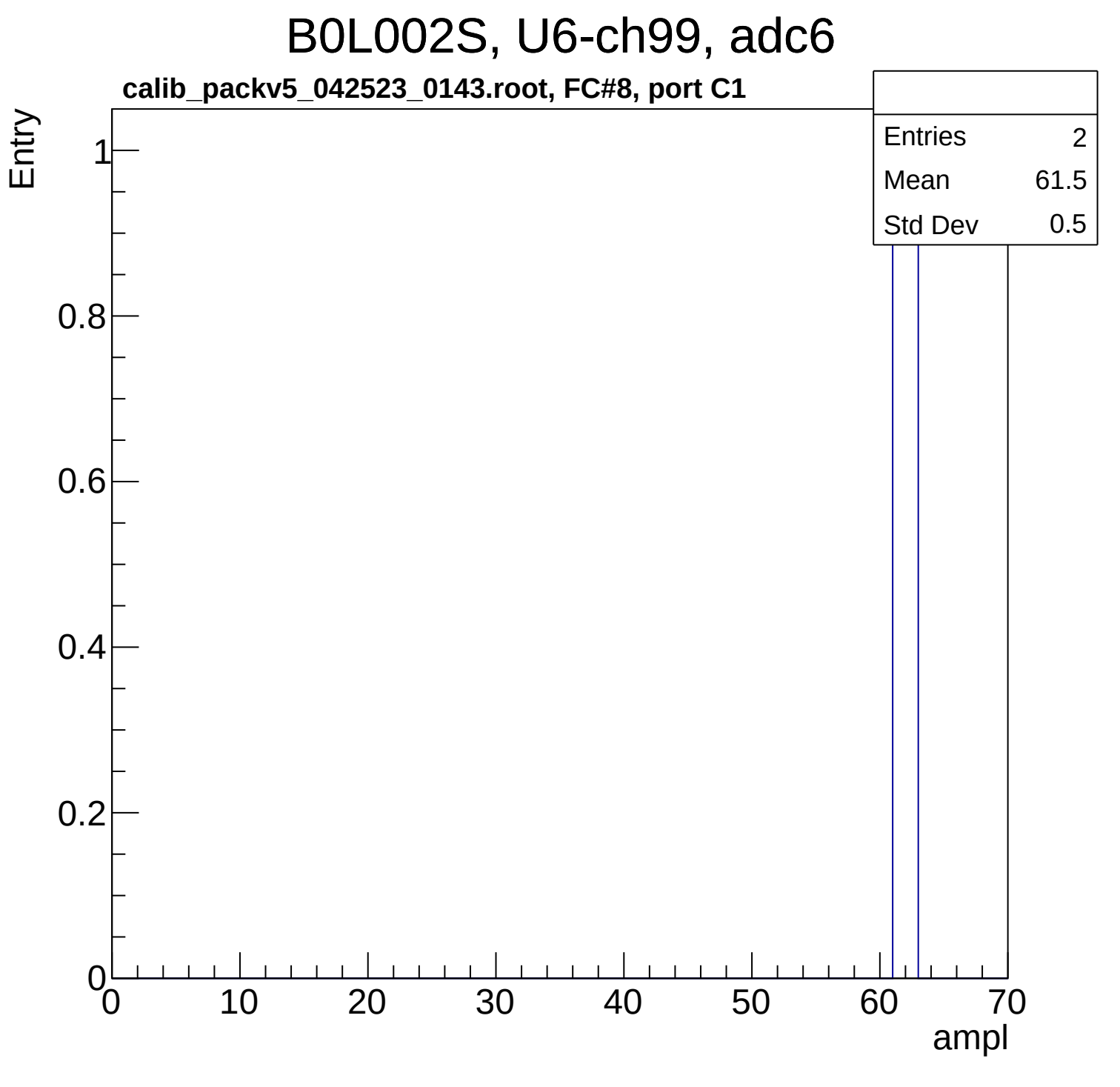
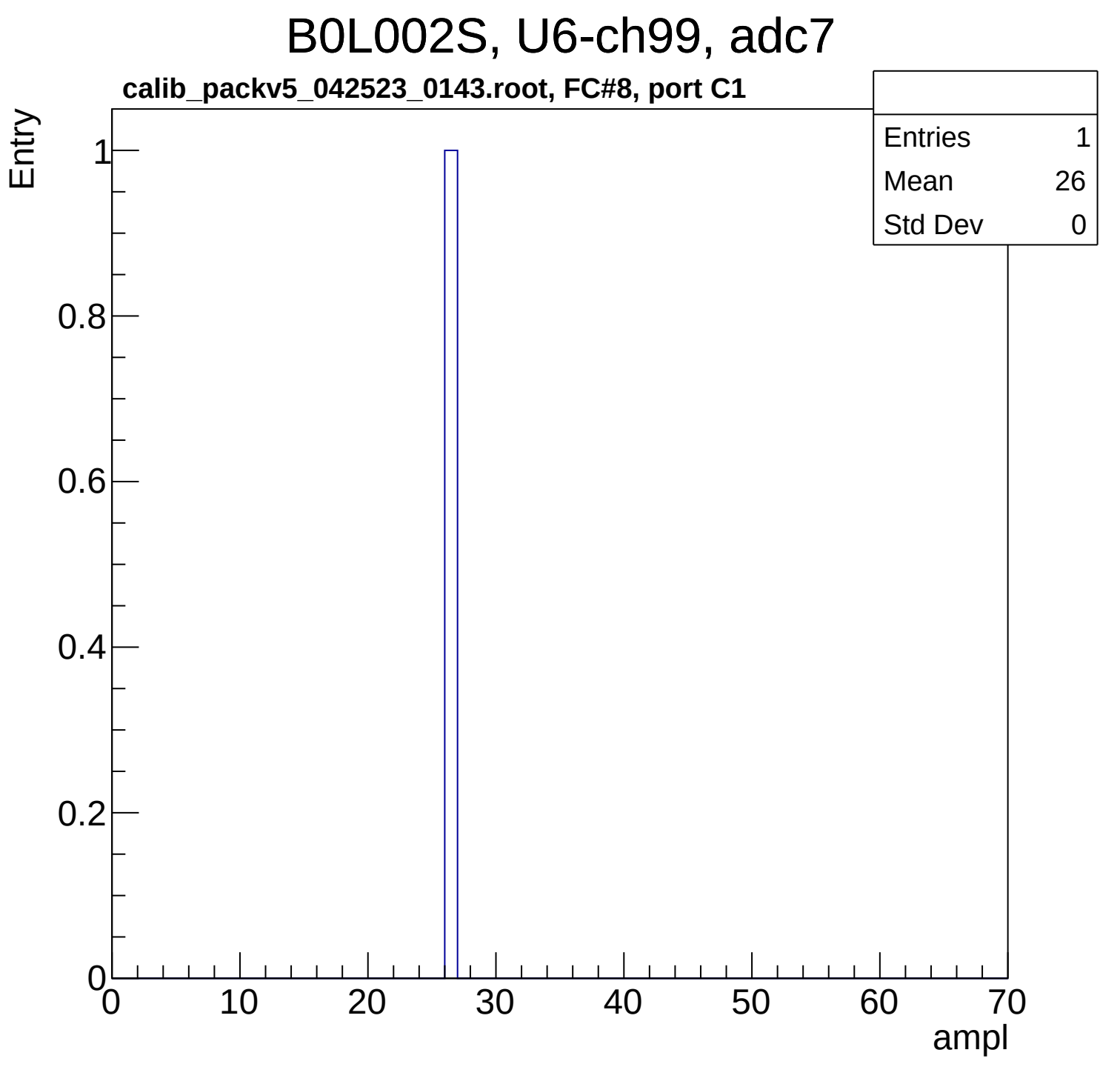

B0L002S, U6-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 57.71
Std Dev 14.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U6-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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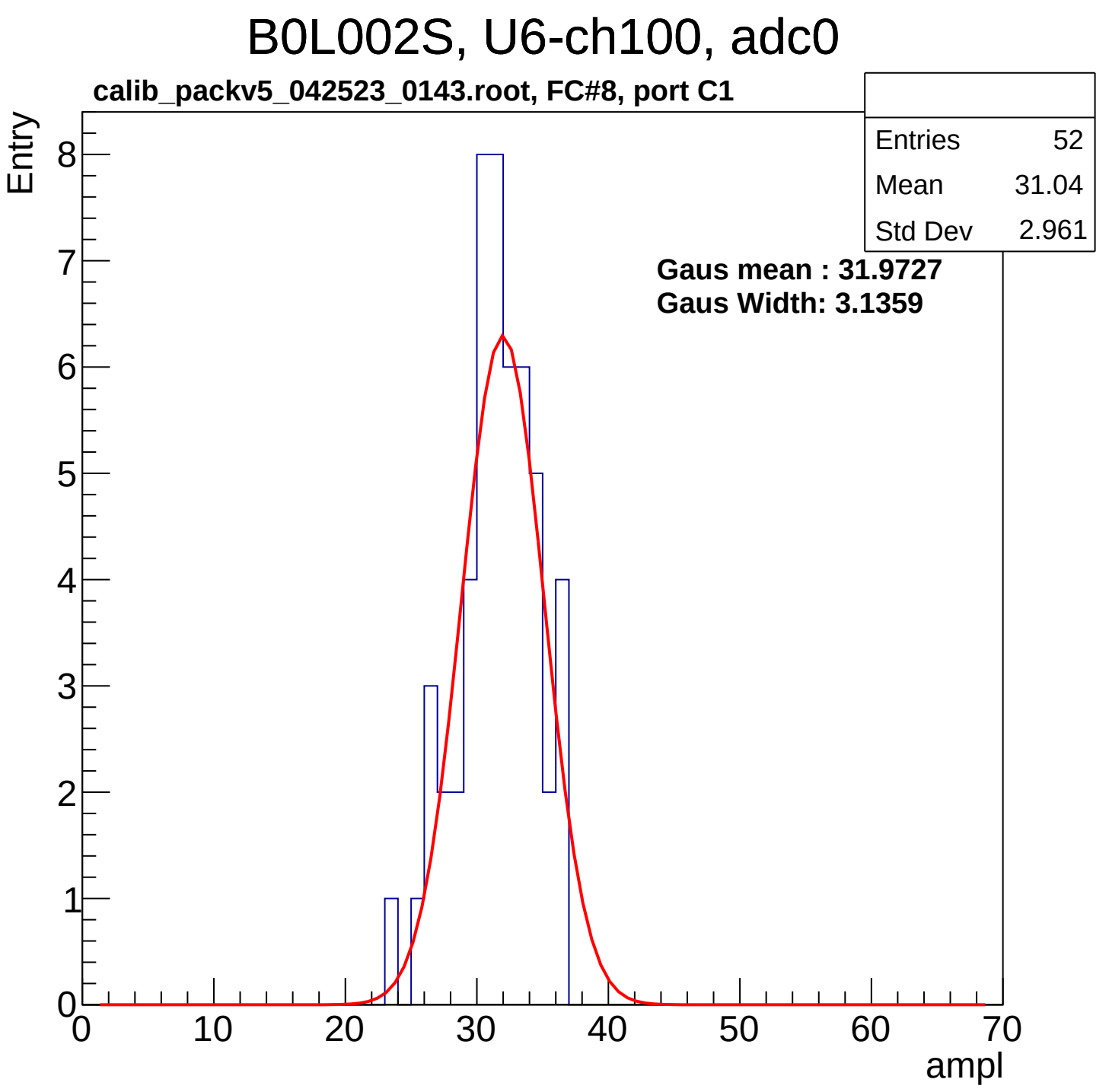
50
60
70
ampl


B0L002S, U6-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 31.04
Std Dev 2.961
Gaus mean : 31.9727
Gaus Width: 3.1359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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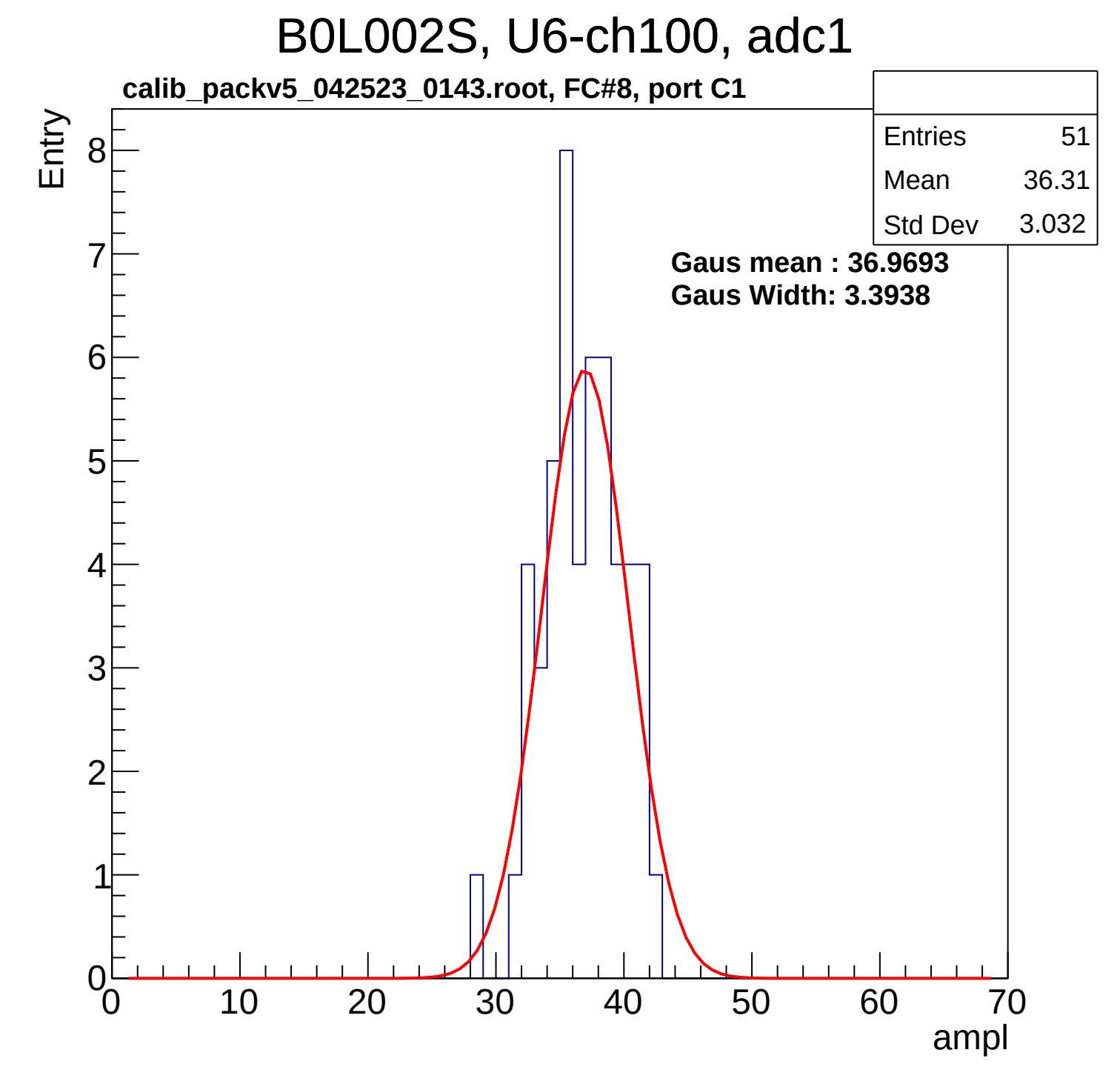
B0L002S, U6-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 36.31
Std Dev 3.032
Gaus mean : 36.9693
Gaus Width: 3.3938
Entry
ampl
0
1
2
3
4
5
6
7
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0
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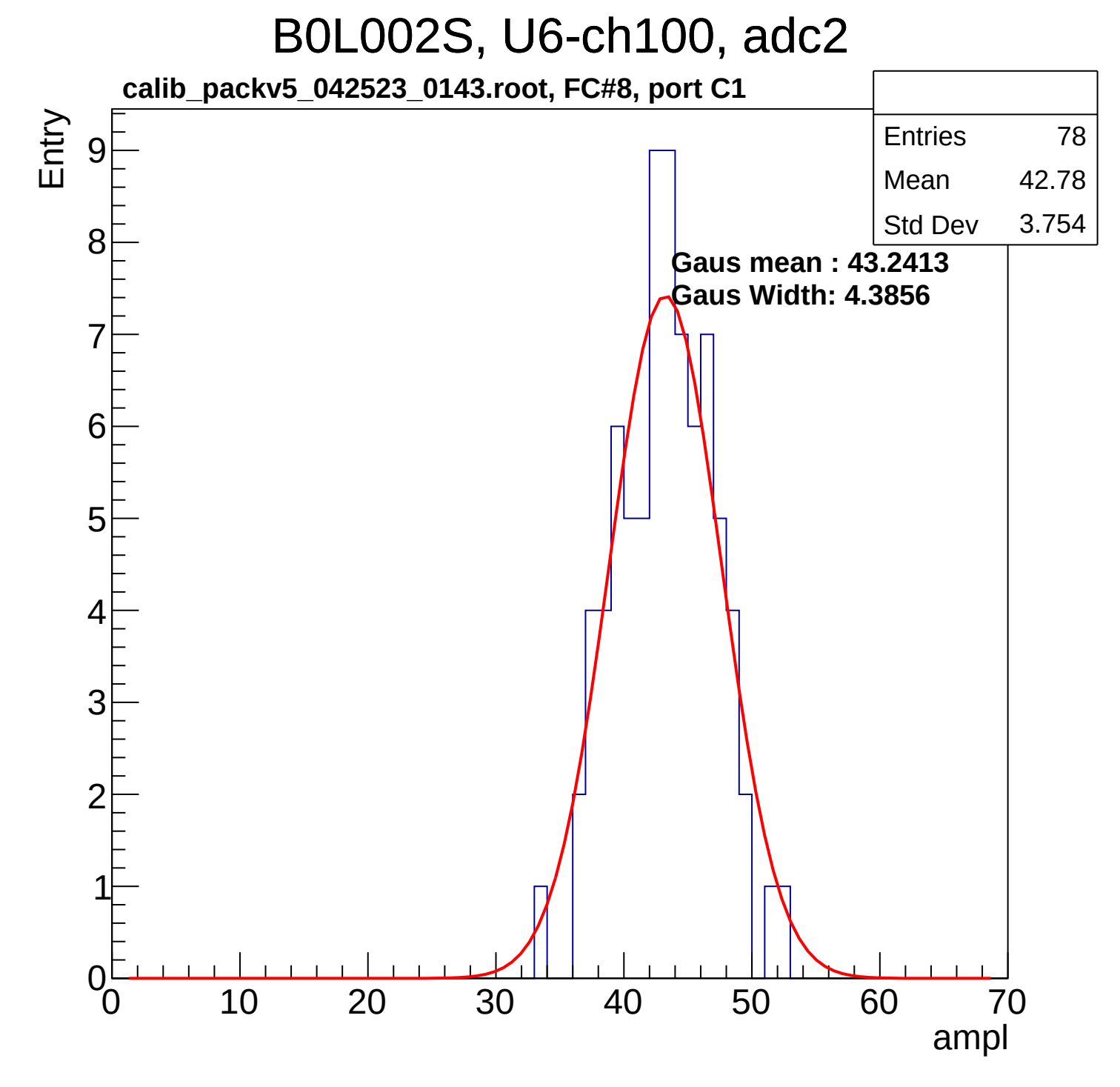
B0L002S, U6-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 42.78
Std Dev 3.754
Gaus mean : 43.2413
Gaus Width: 4.3856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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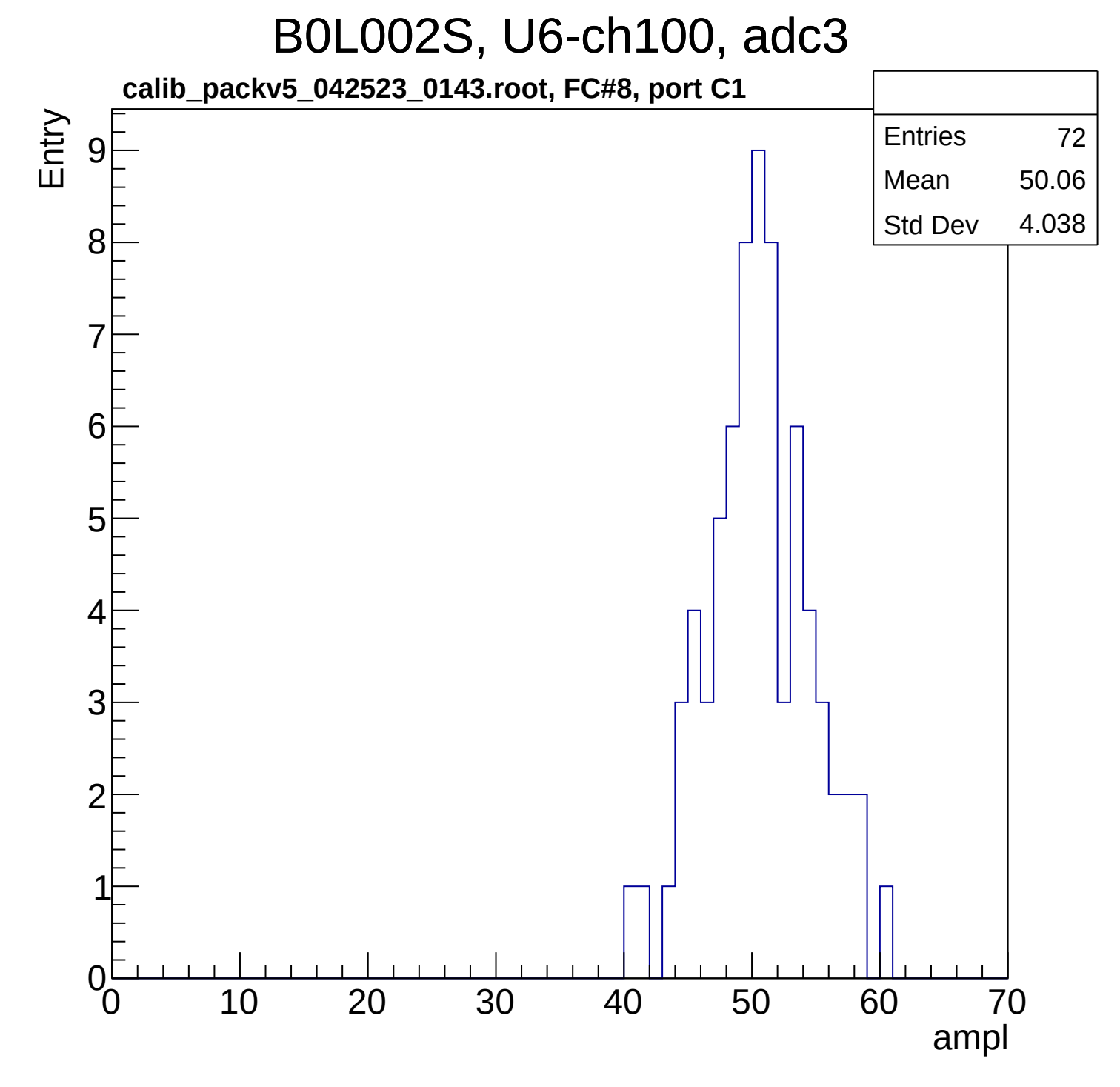
B0L002S, U6-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.06
Std Dev 4.038
Entry
ampl
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1
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4
5
6
7
8
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0
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40
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60
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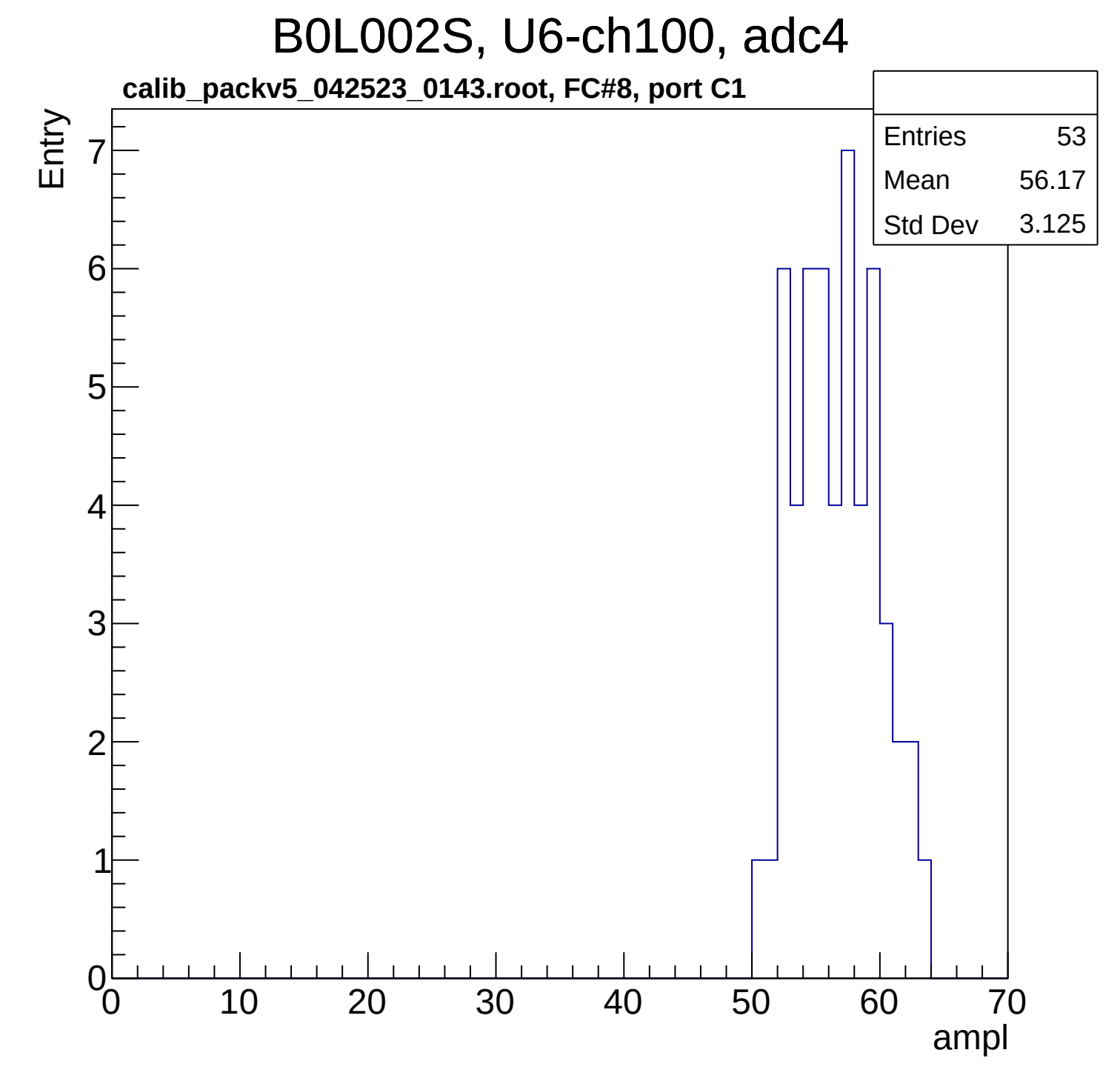
B0L002S, U6-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.17
Std Dev 3.125
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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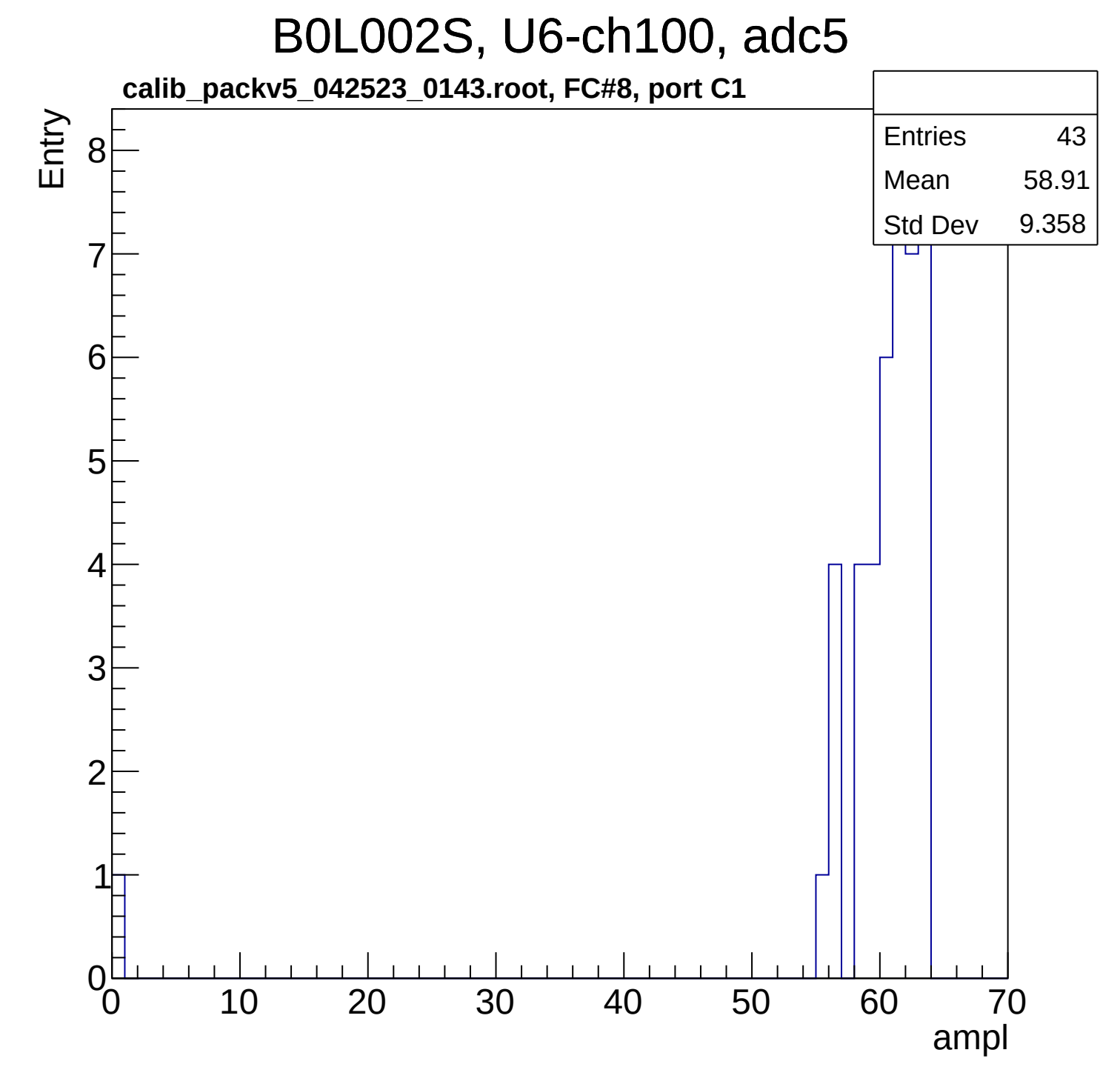
B0L002S, U6-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 58.91
Std Dev 9.358
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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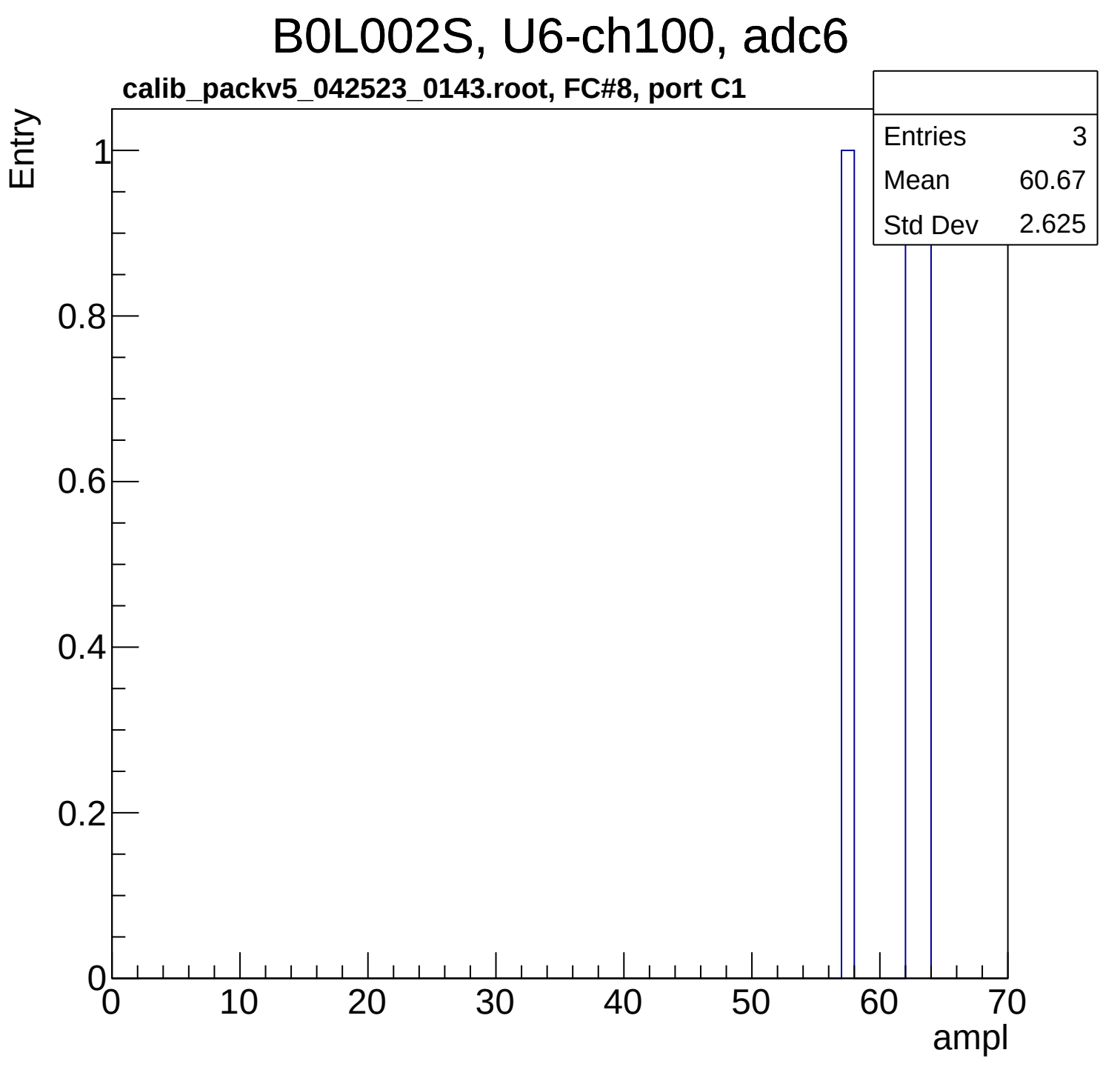
B0L002S, U6-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 60.67
Std Dev 2.625
Entry
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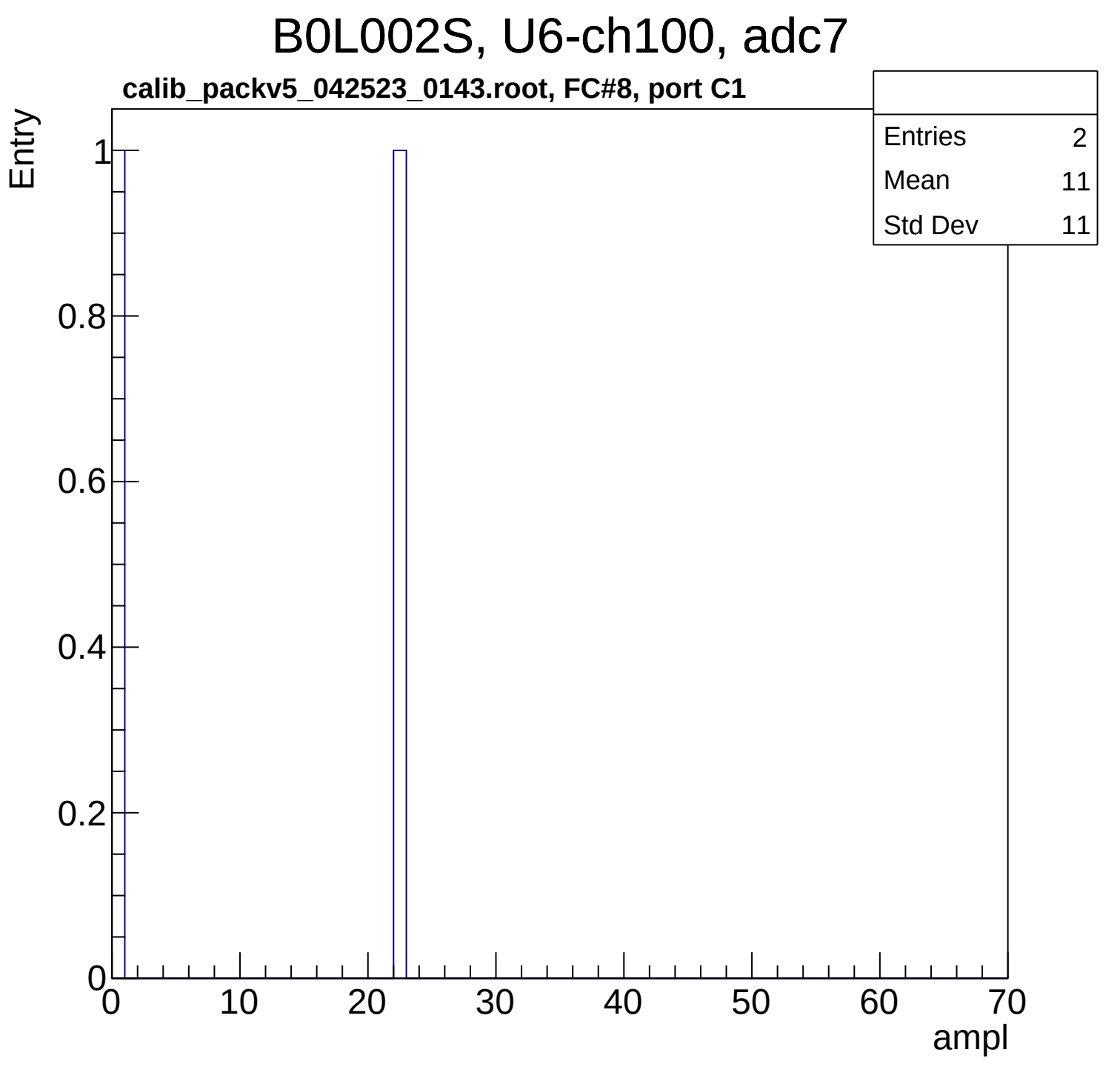
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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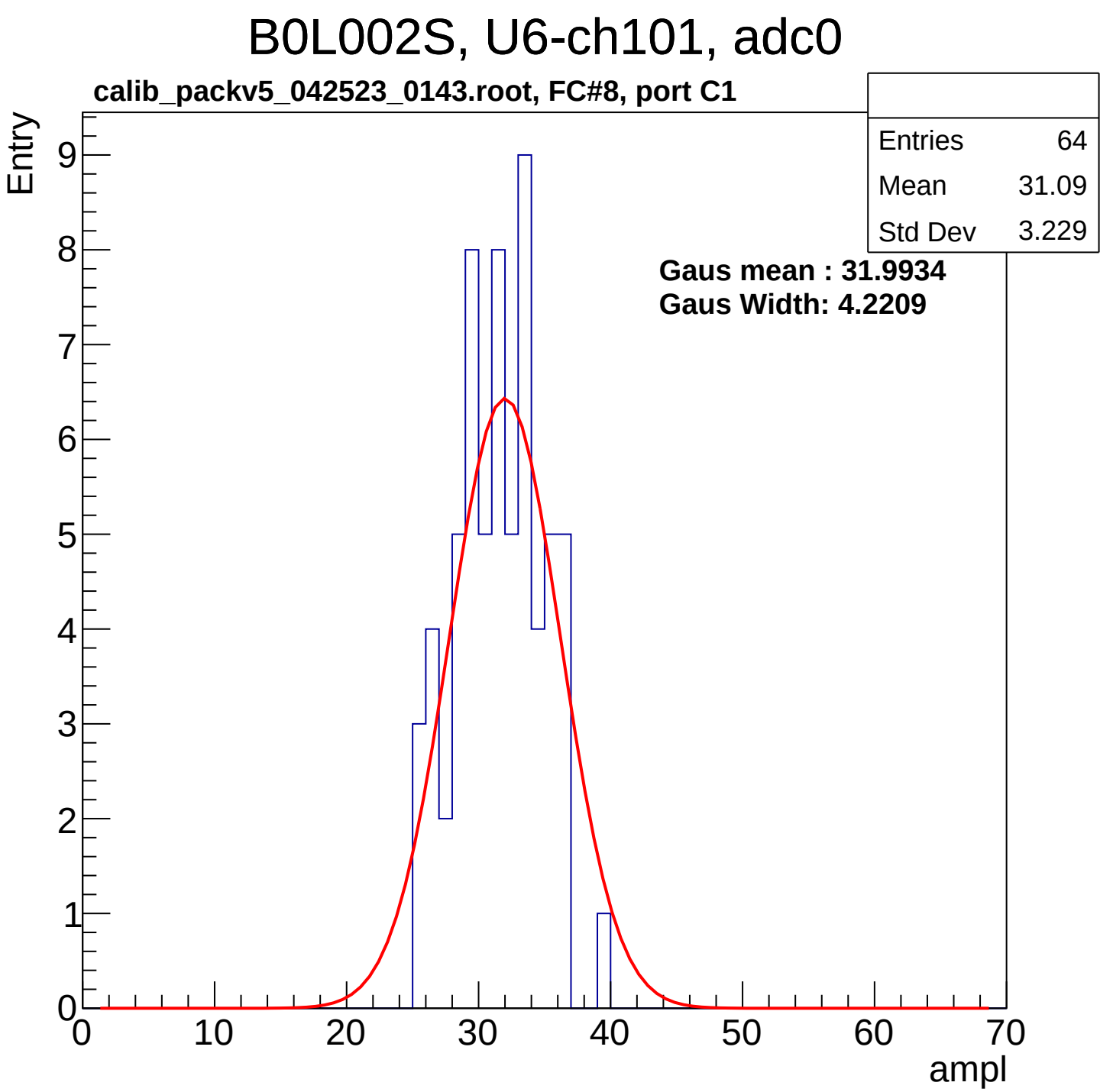
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.09
Std Dev 3.229
Gaus mean : 31.9934
Gaus Width: 4.2209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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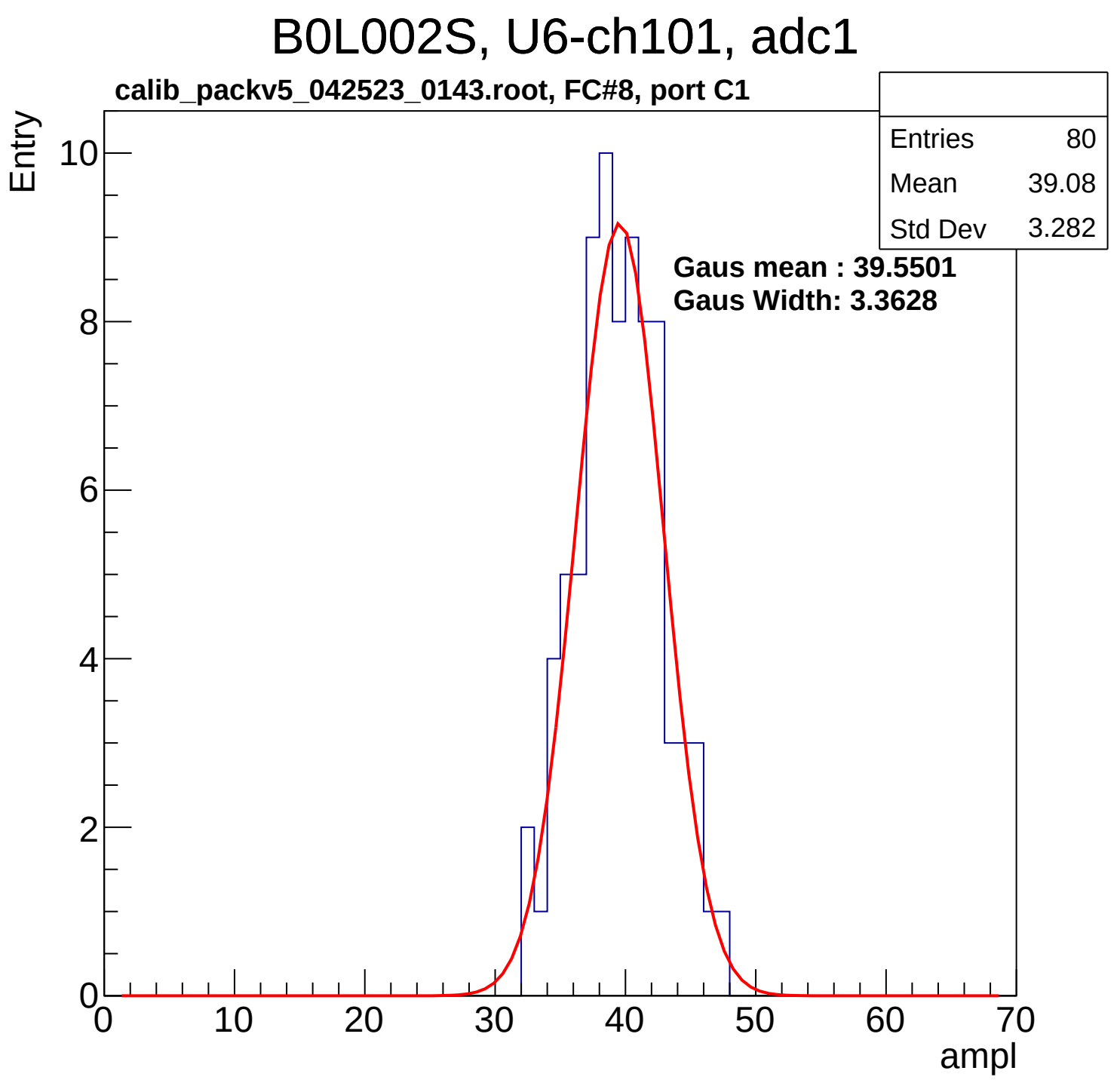
B0L002S, U6-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 39.08
Std Dev 3.282
Gaus mean : 39.5501
Gaus Width: 3.3628
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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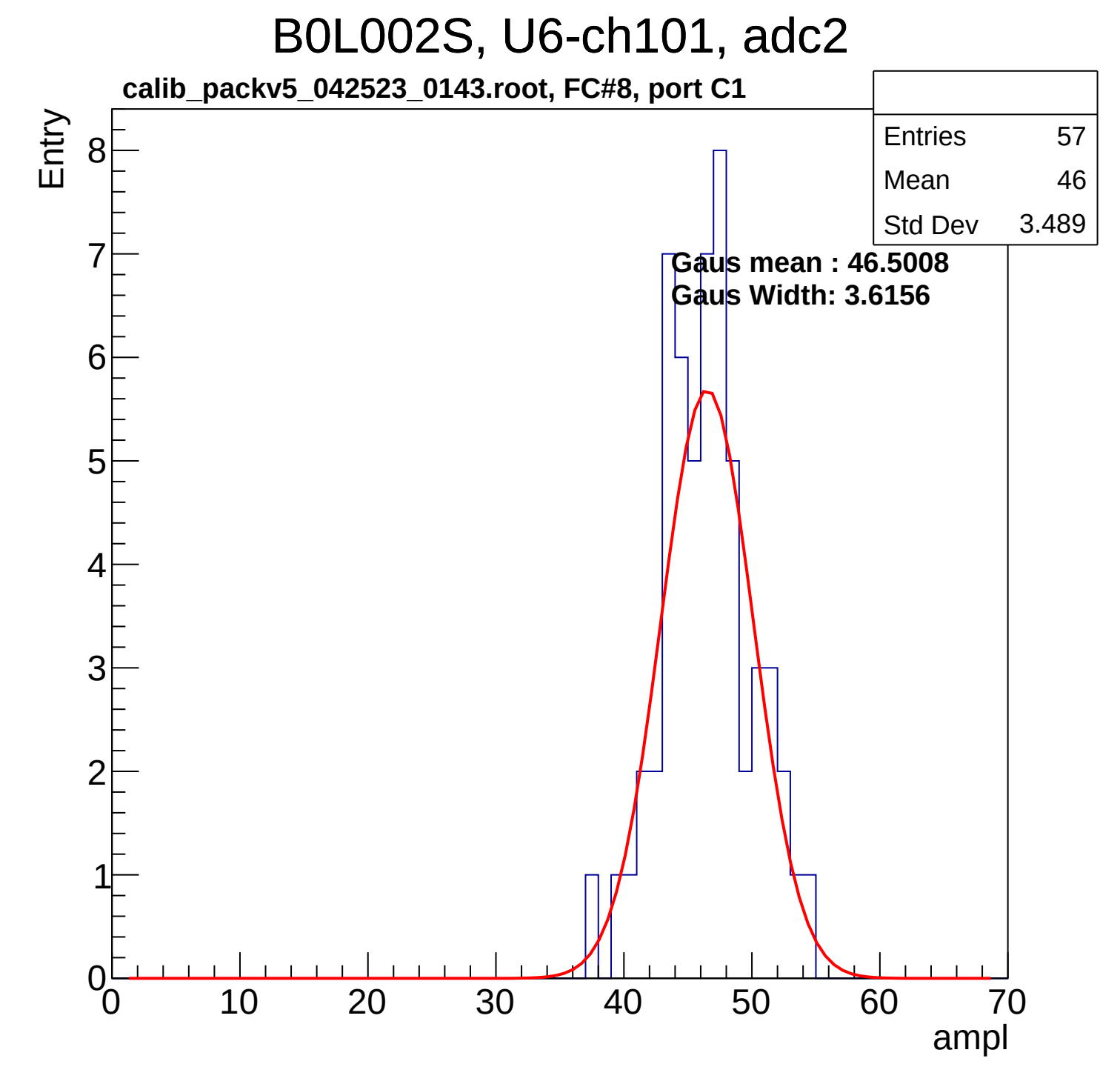
B0L002S, U6-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 46
Std Dev 3.489
Gaus mean : 46.5008
Gaus Width: 3.6156
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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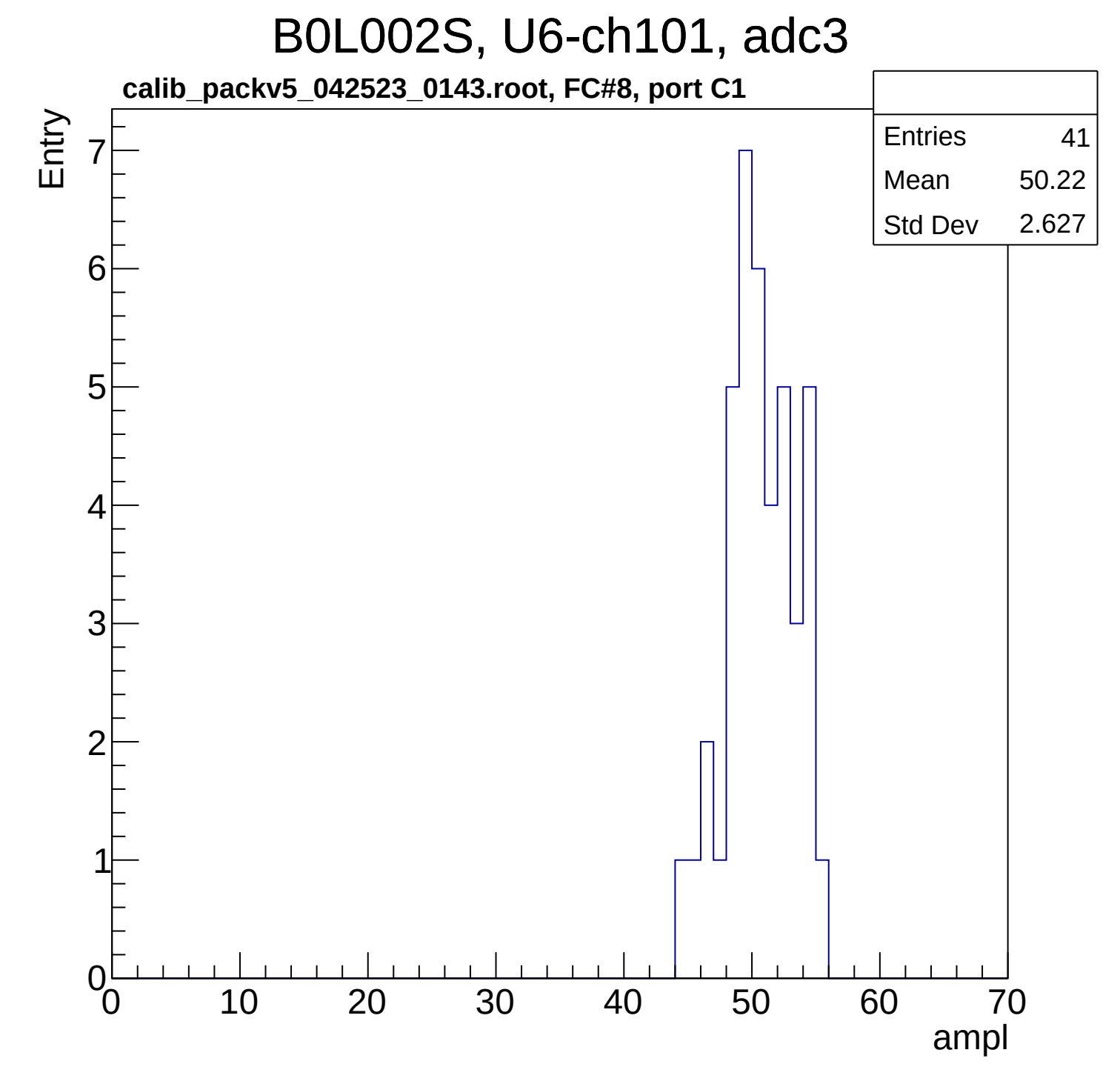
B0L002S, U6-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 50.22
Std Dev 2.627
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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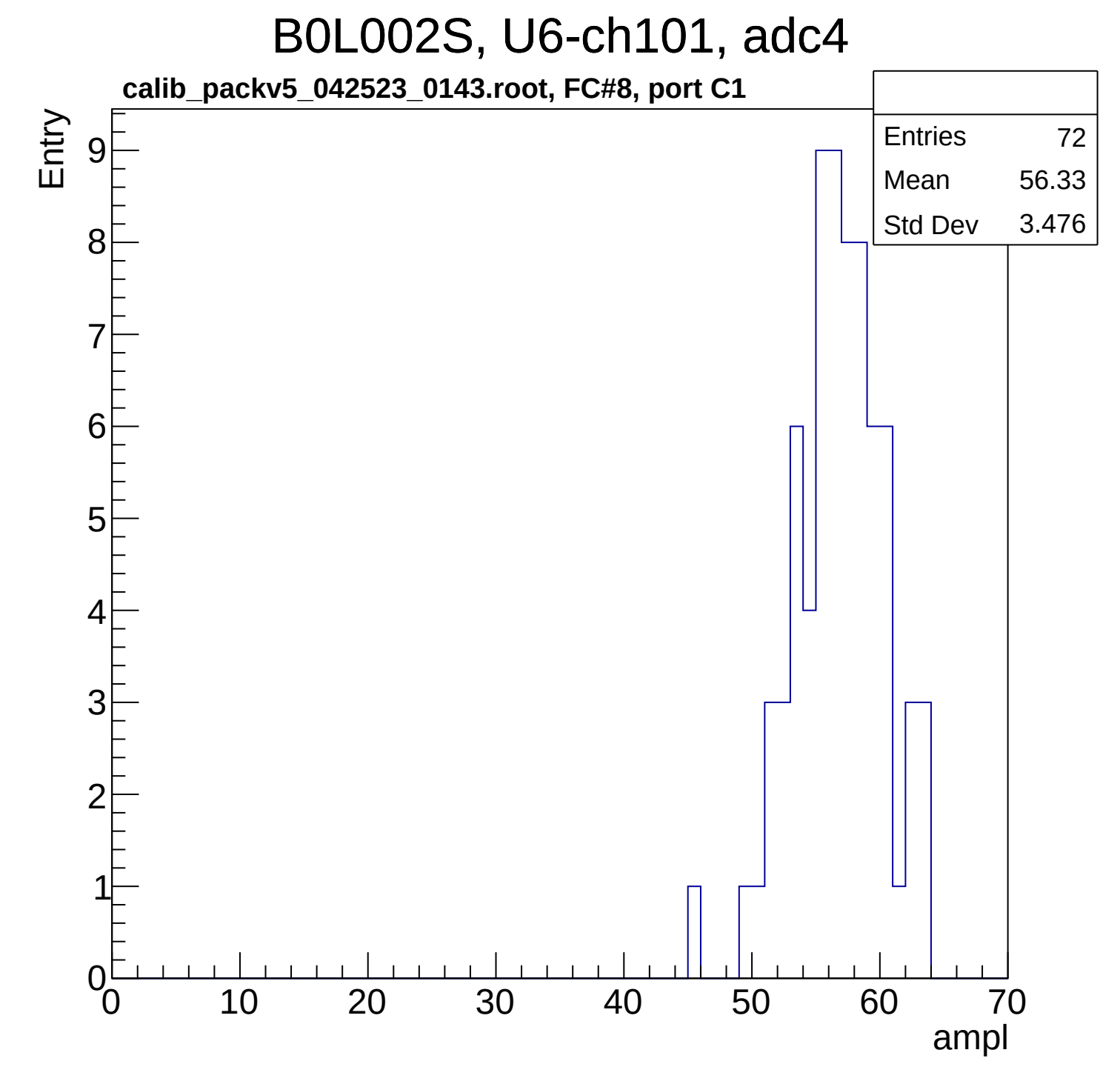
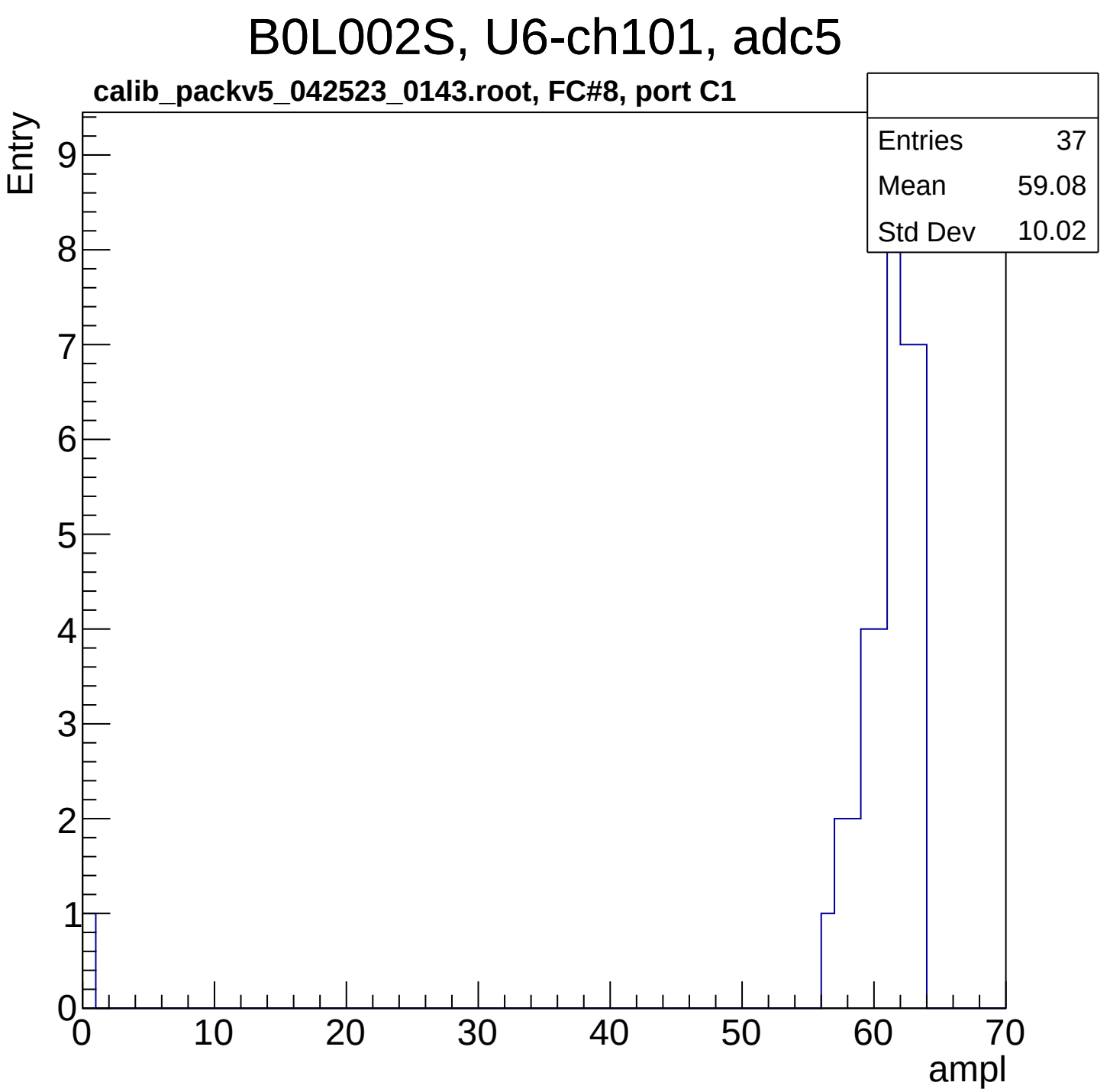

B0L002S, U6-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 56.33
Std Dev 3.476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.08
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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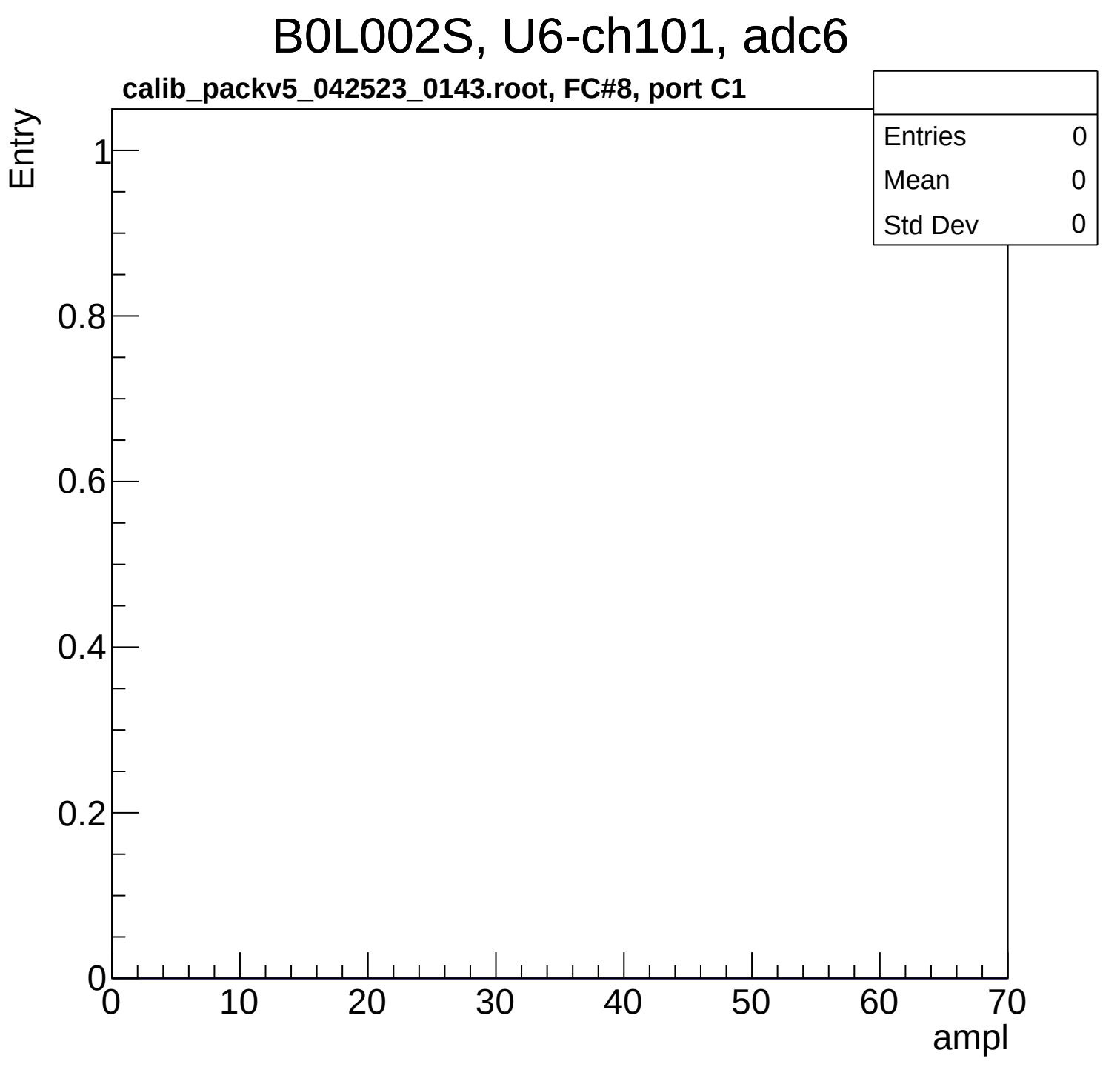
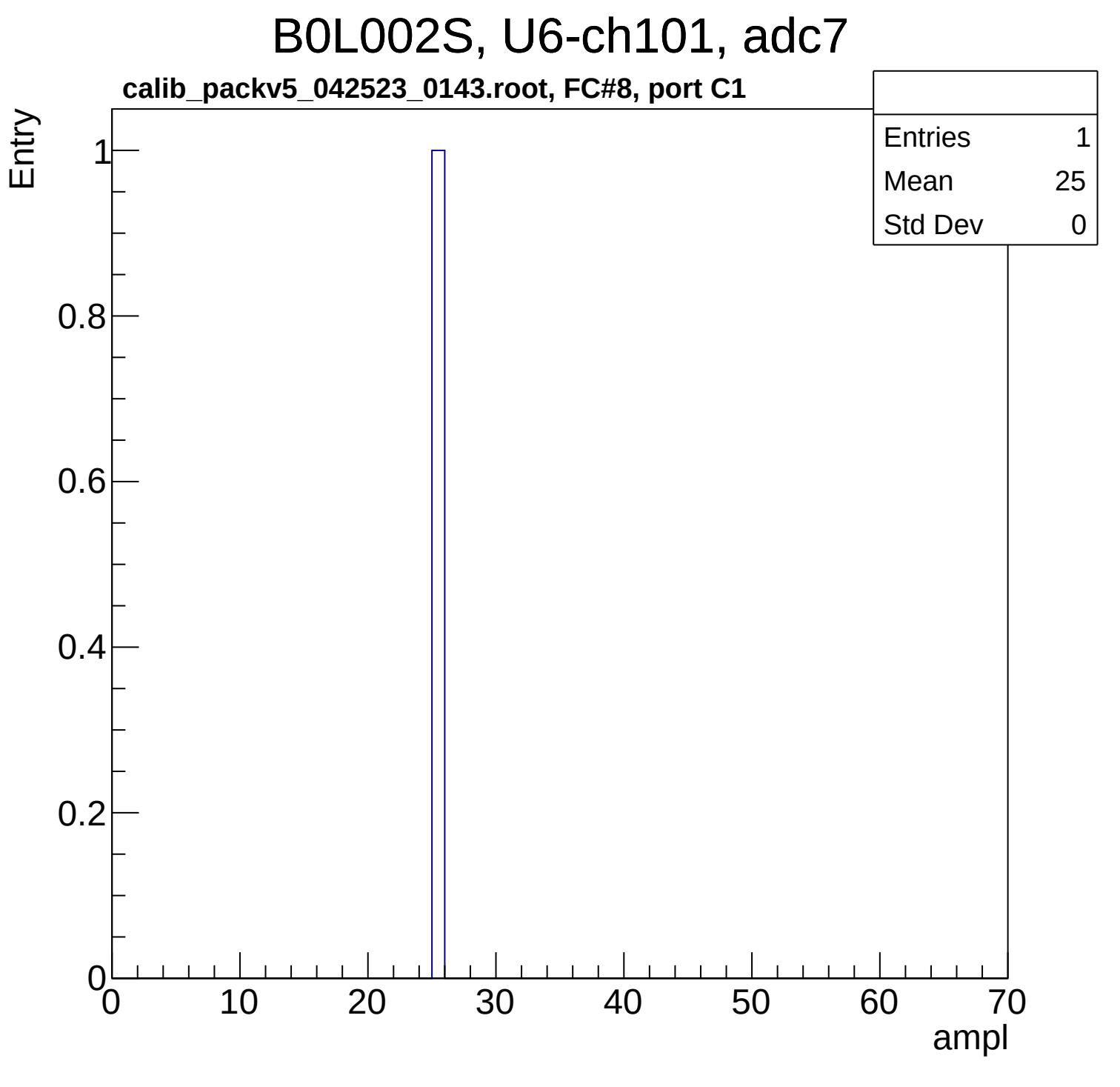
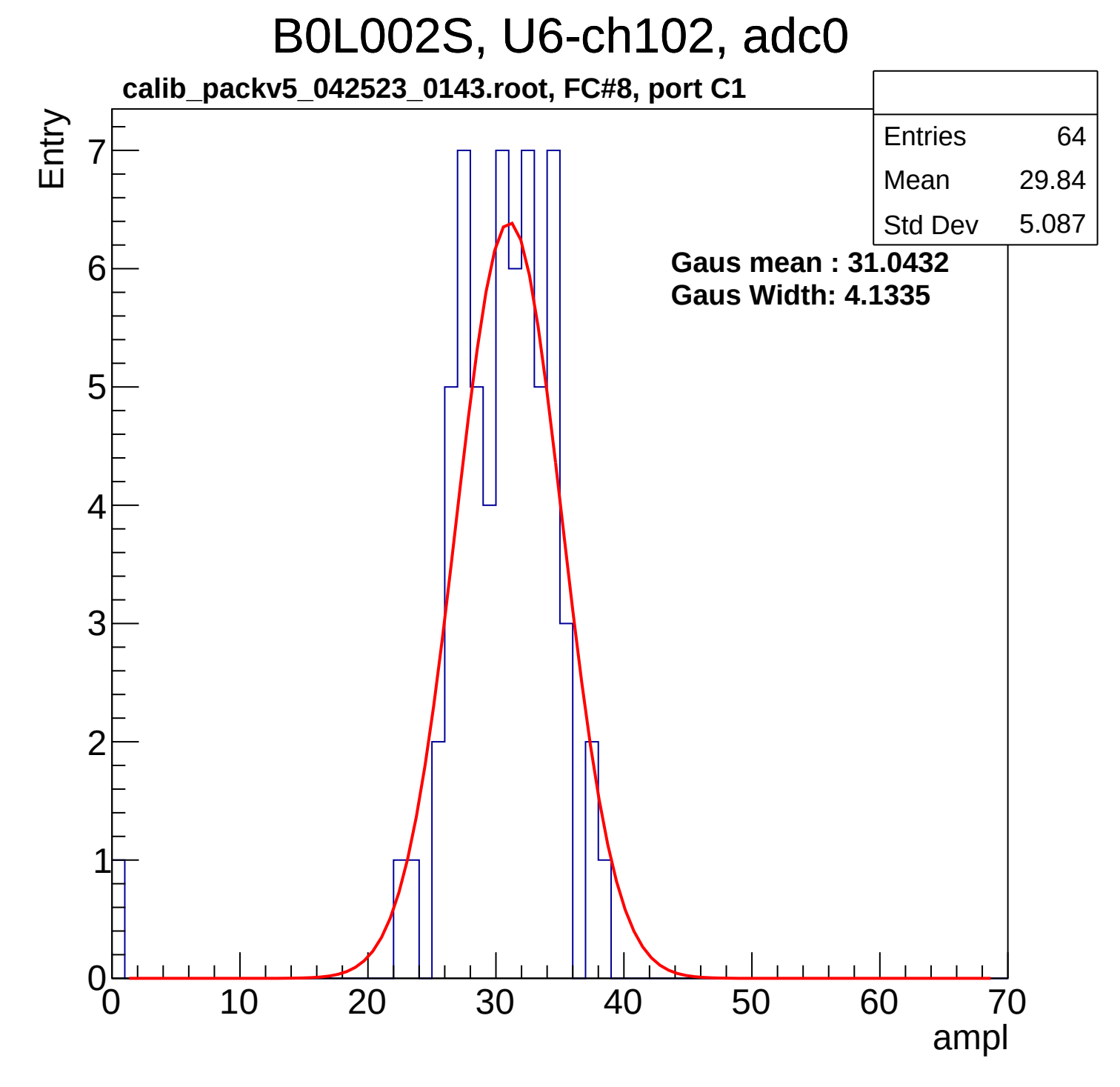
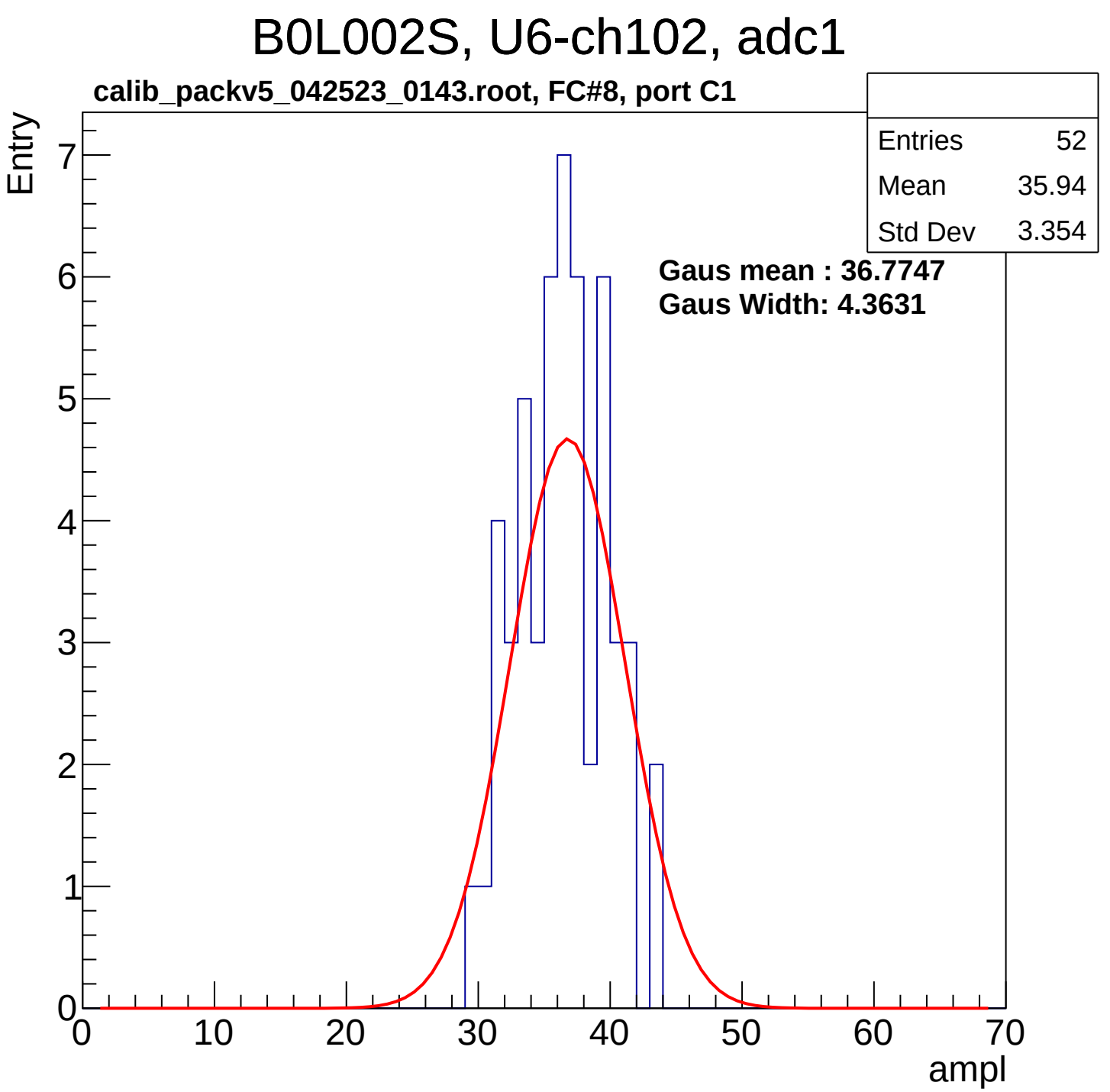
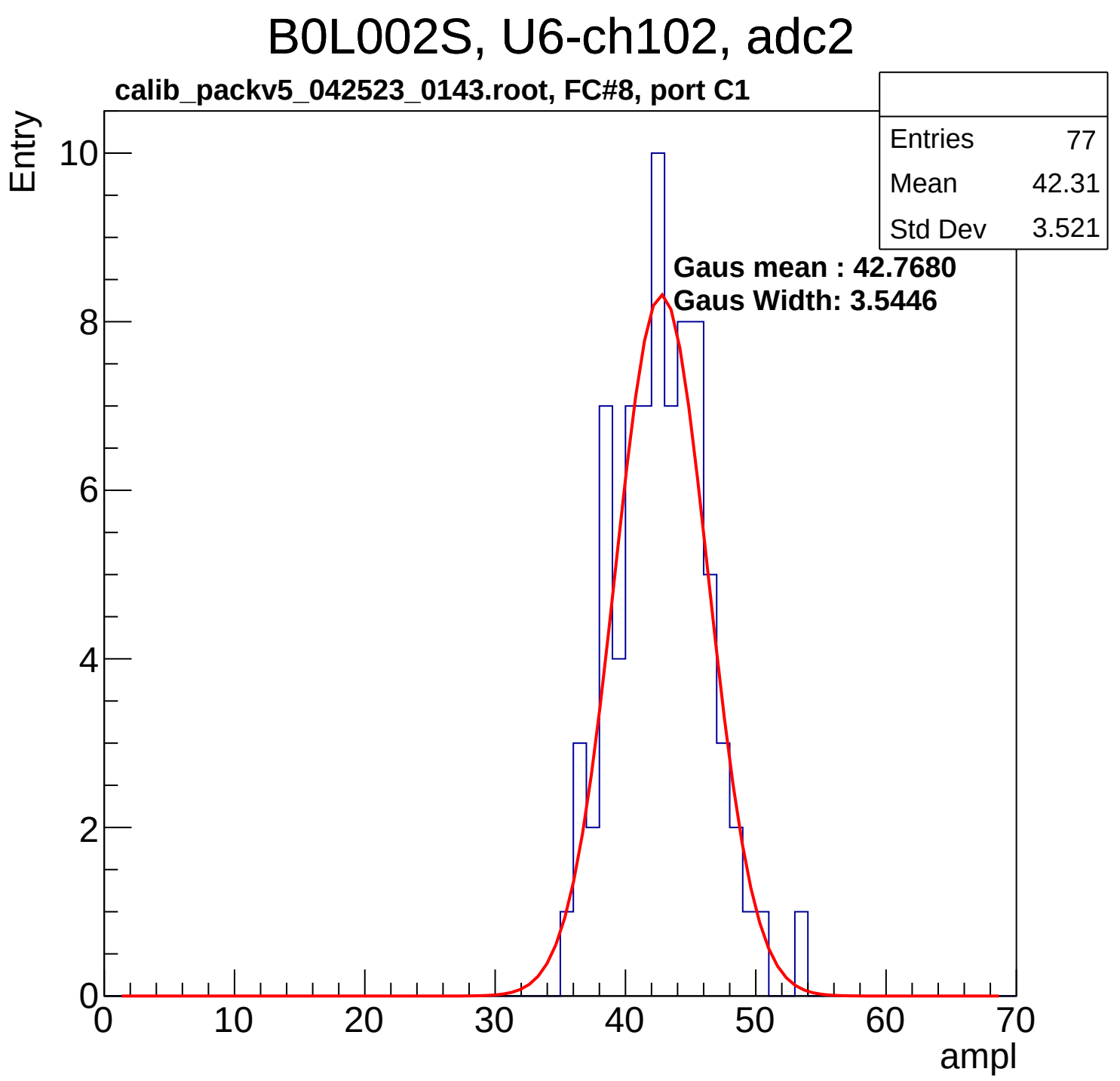
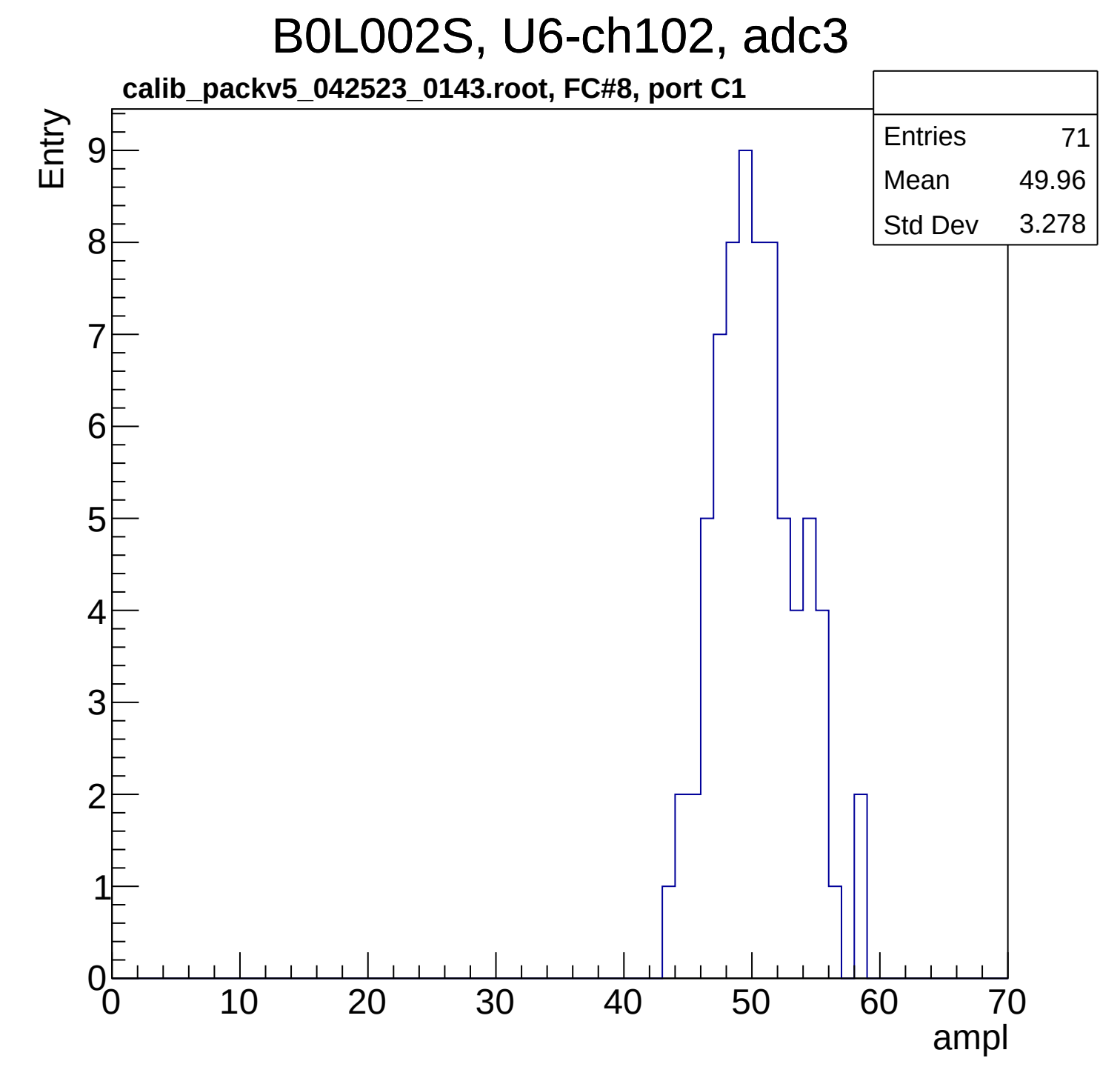

B0L002S, U6-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.84
Std Dev 5.087
Gaus mean : 31.0432
Gaus Width: 4.1335
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 35.94
Std Dev 3.354
Gaus mean : 36.7747
Gaus Width: 4.3631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 42.31
Std Dev 3.521
Gaus mean : 42.7680
Gaus Width: 3.5446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 49.96
Std Dev 3.278
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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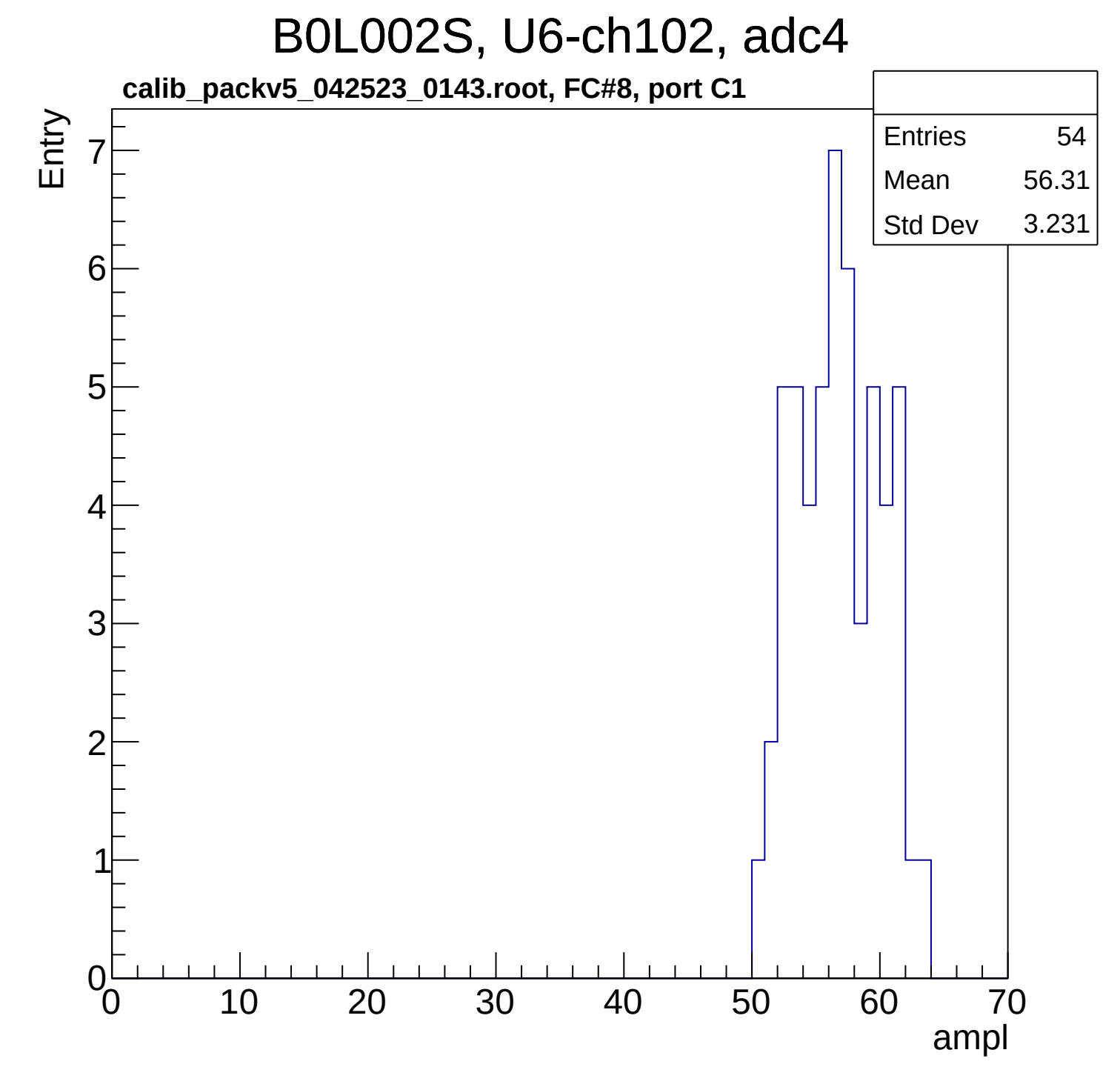
B0L002S, U6-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.31
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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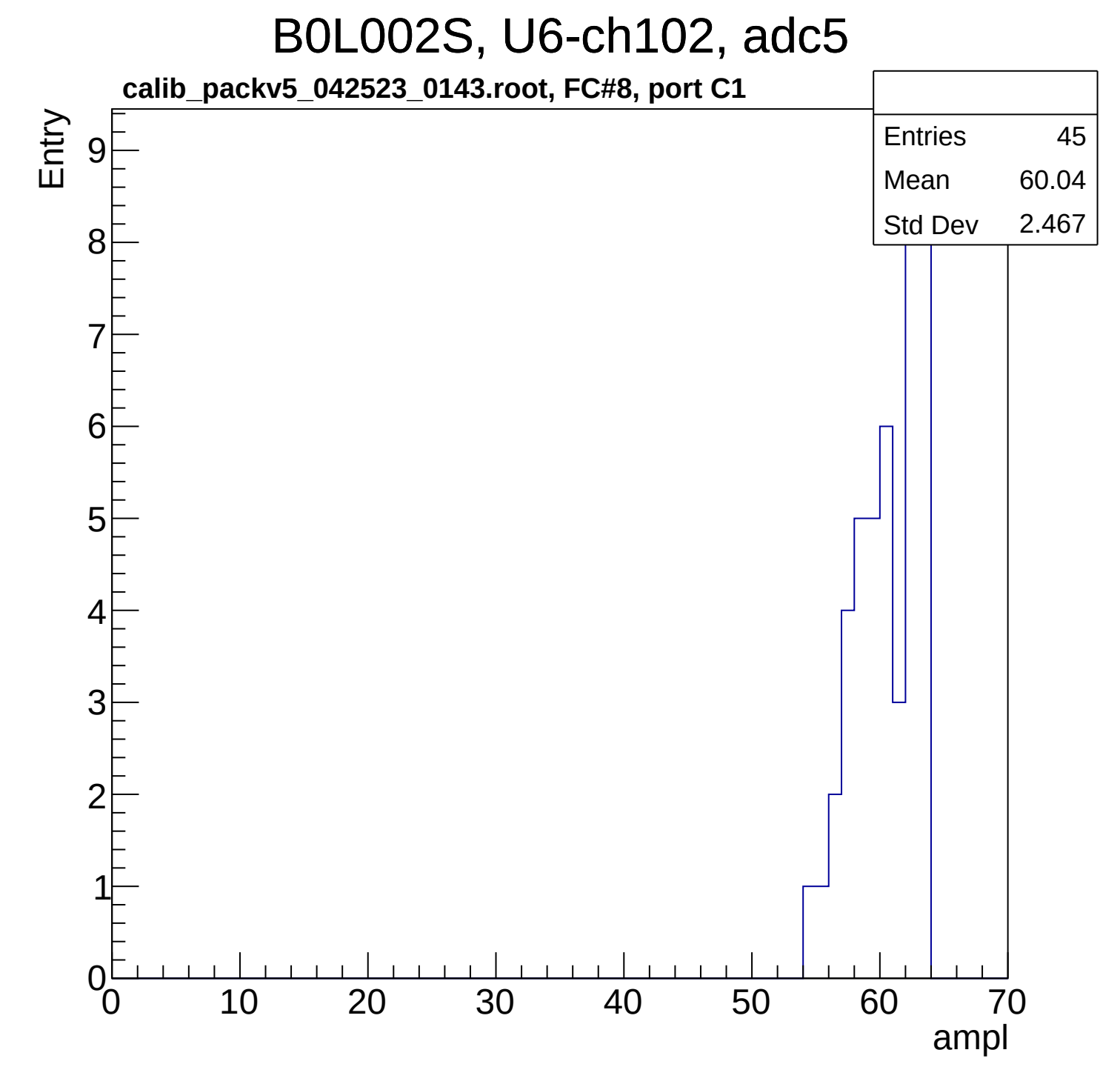
B0L002S, U6-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 60.04
Std Dev 2.467
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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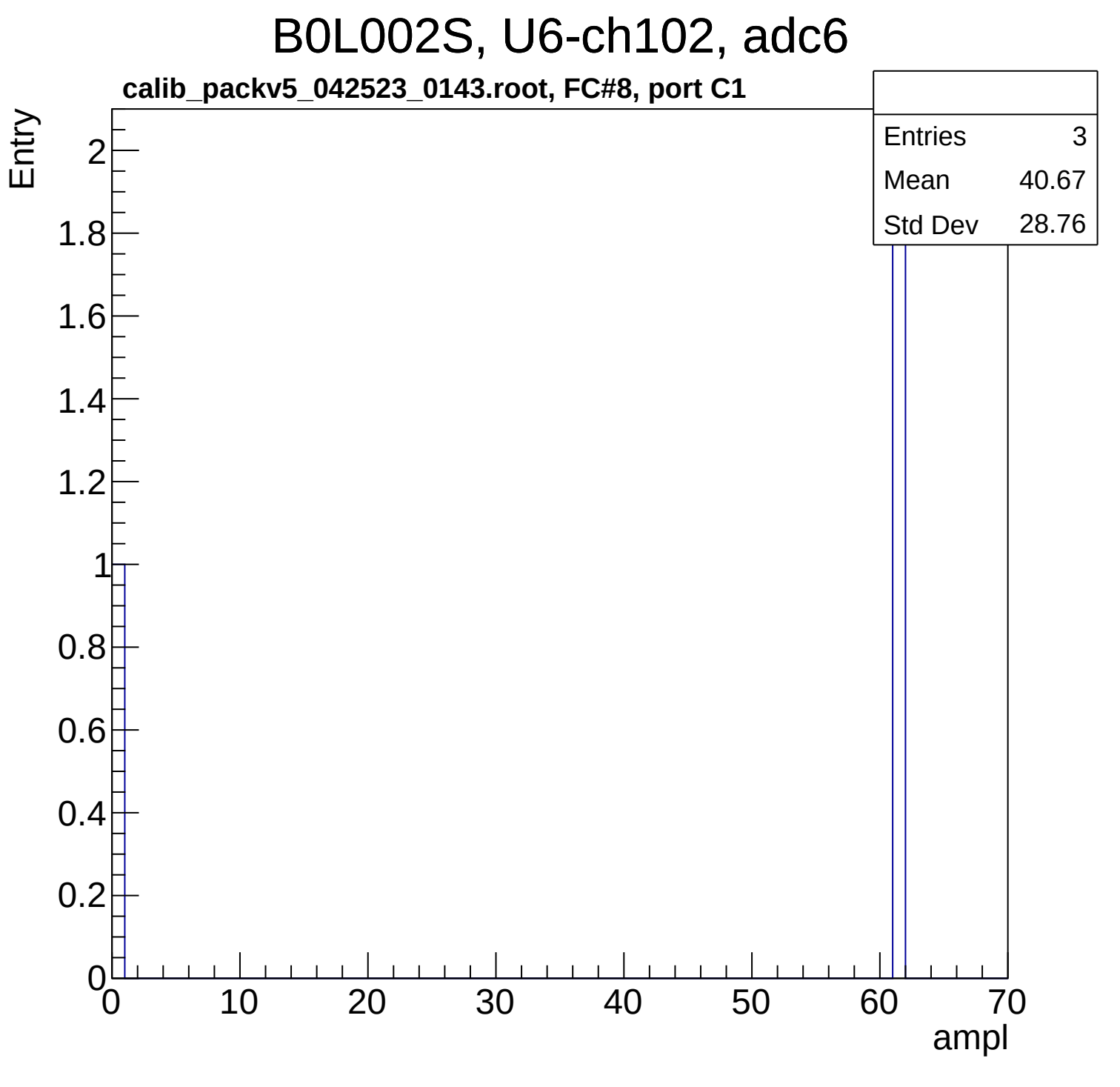
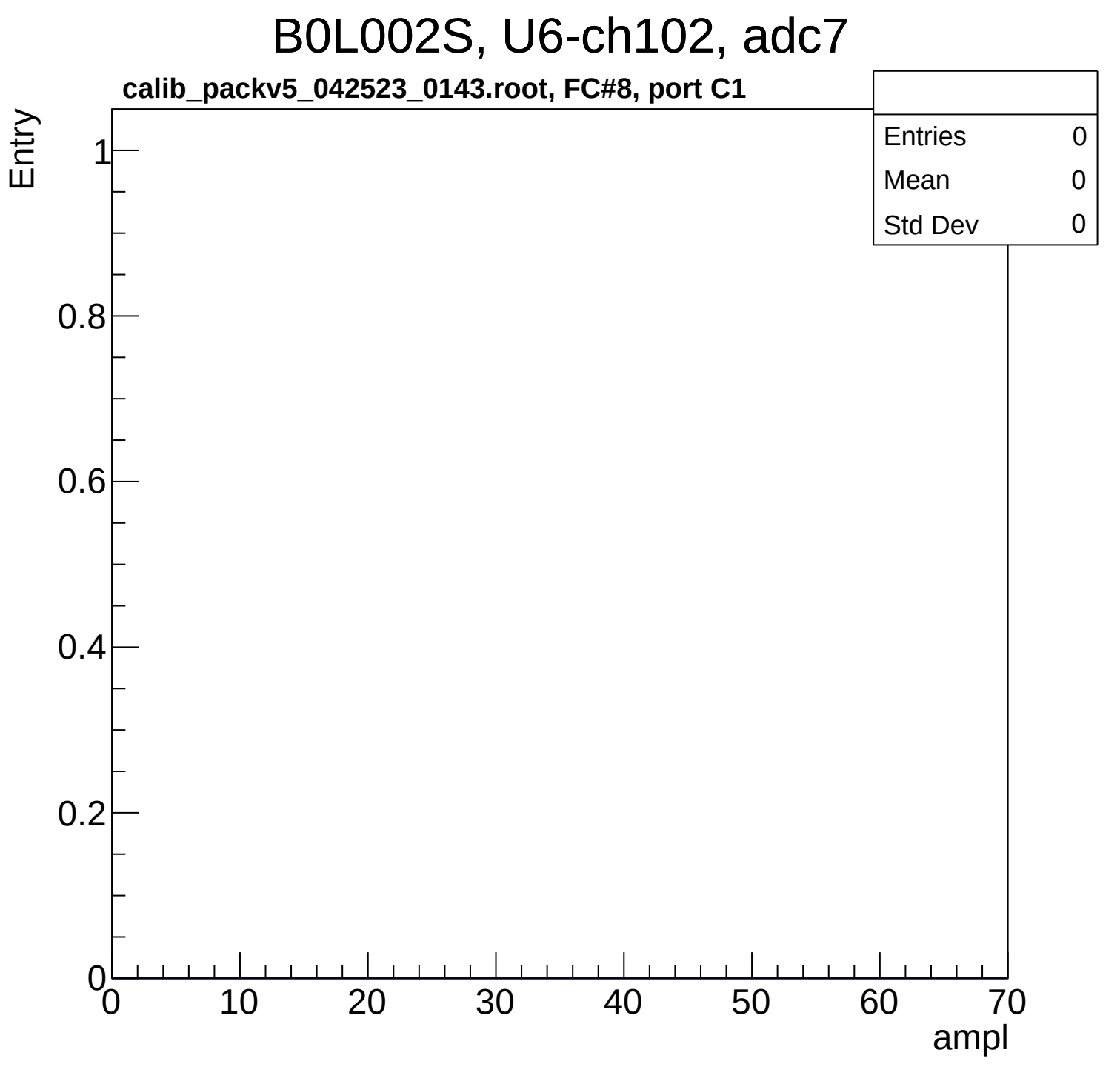
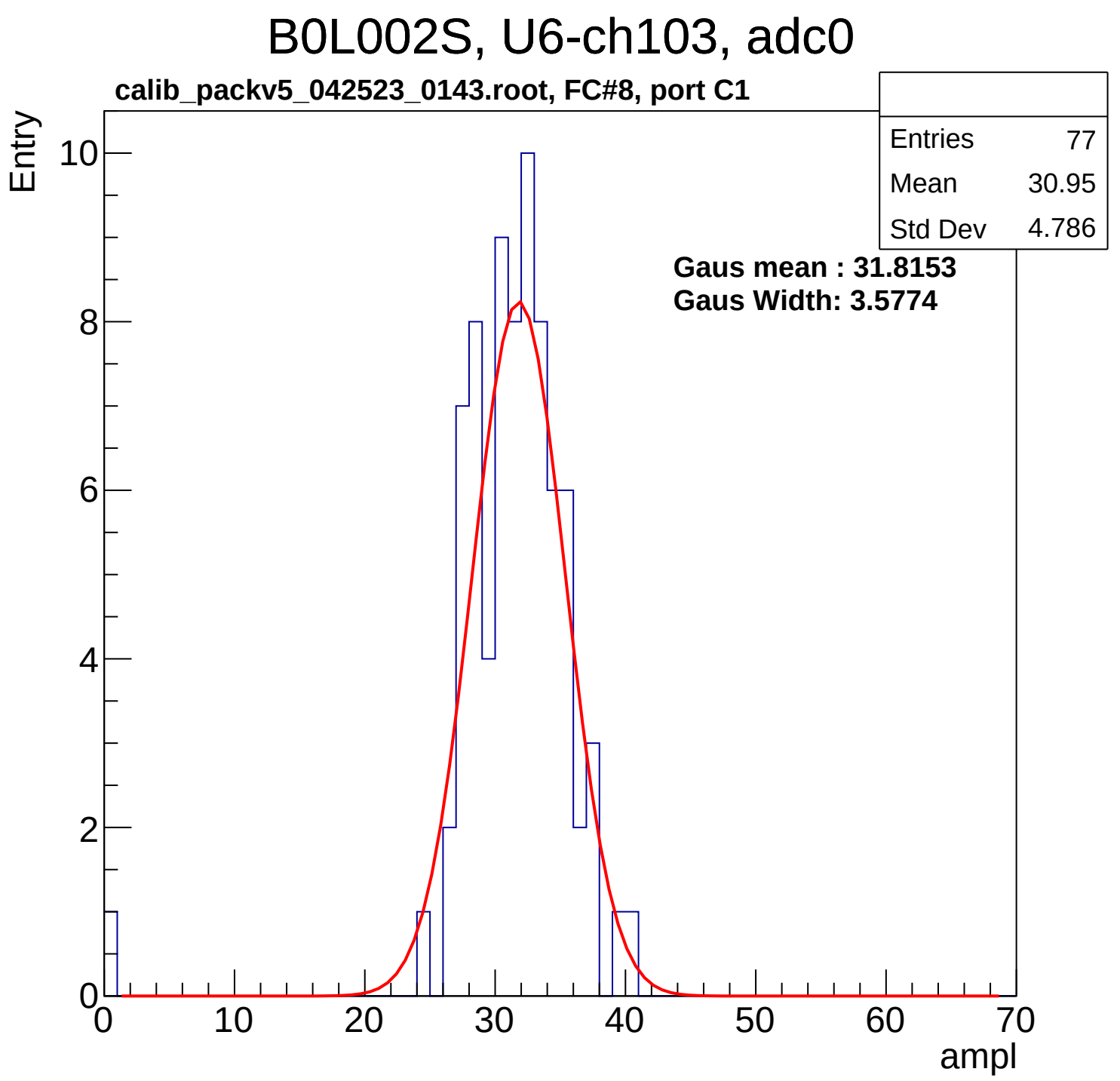
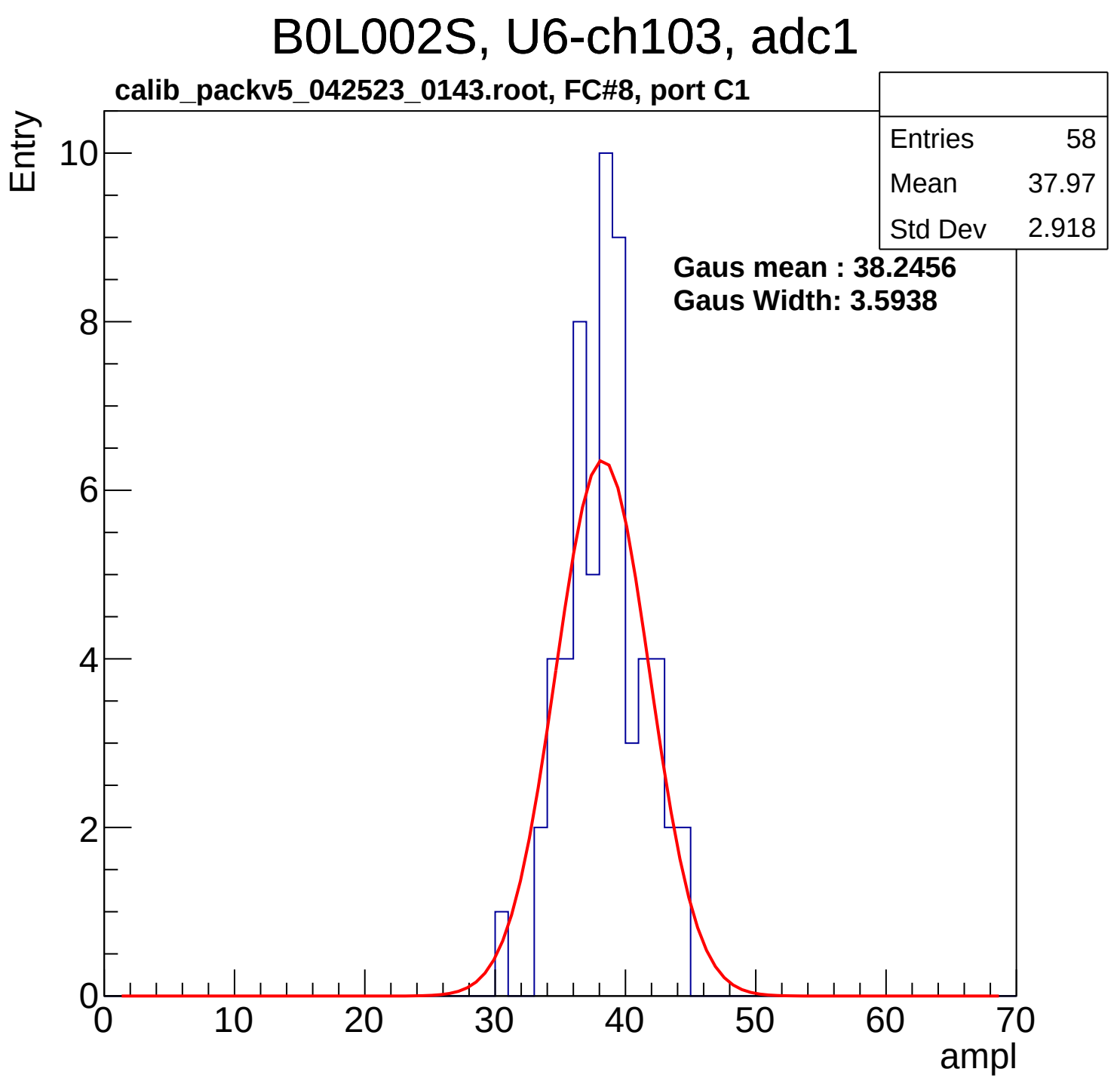
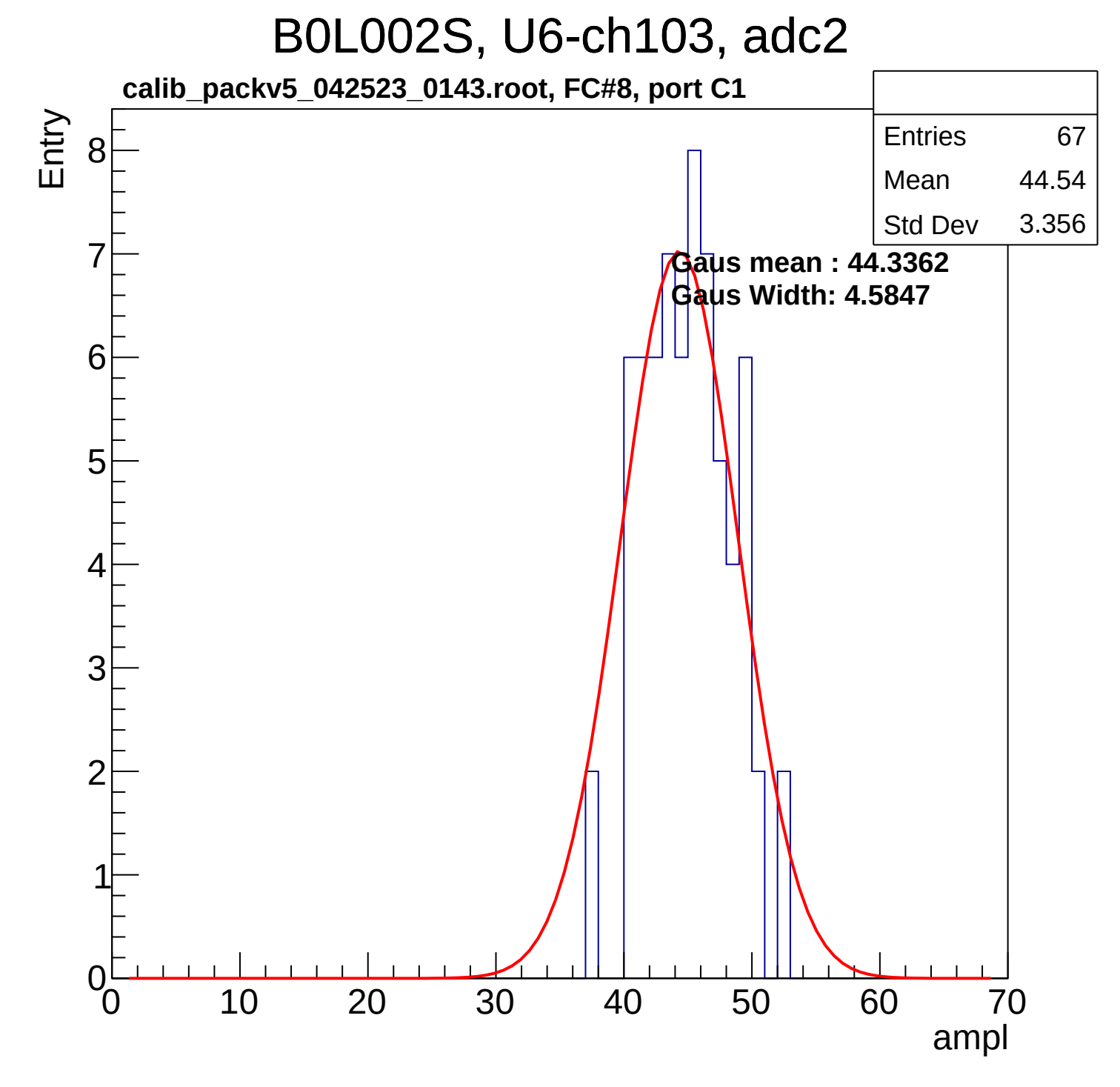

B0L002S, U6-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 40.67
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.95
Std Dev 4.786
Gaus mean : 31.8153
Gaus Width: 3.5774
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.97
Std Dev 2.918
Gaus mean : 38.2456
Gaus Width: 3.5938
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.54
Std Dev 3.356
Gaus mean : 44.3362
Gaus Width: 4.5847
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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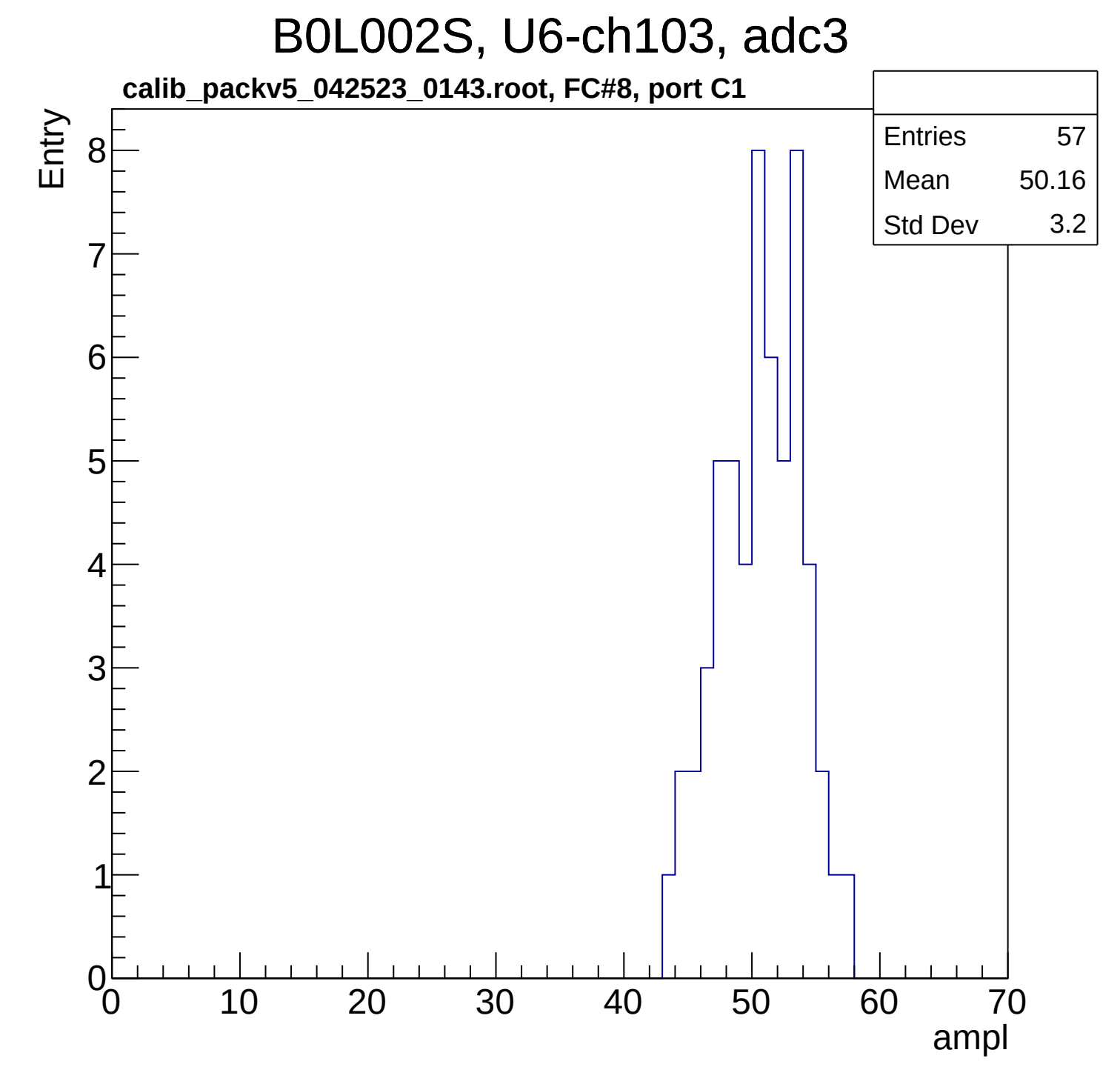
B0L002S, U6-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 50.16
Std Dev 3.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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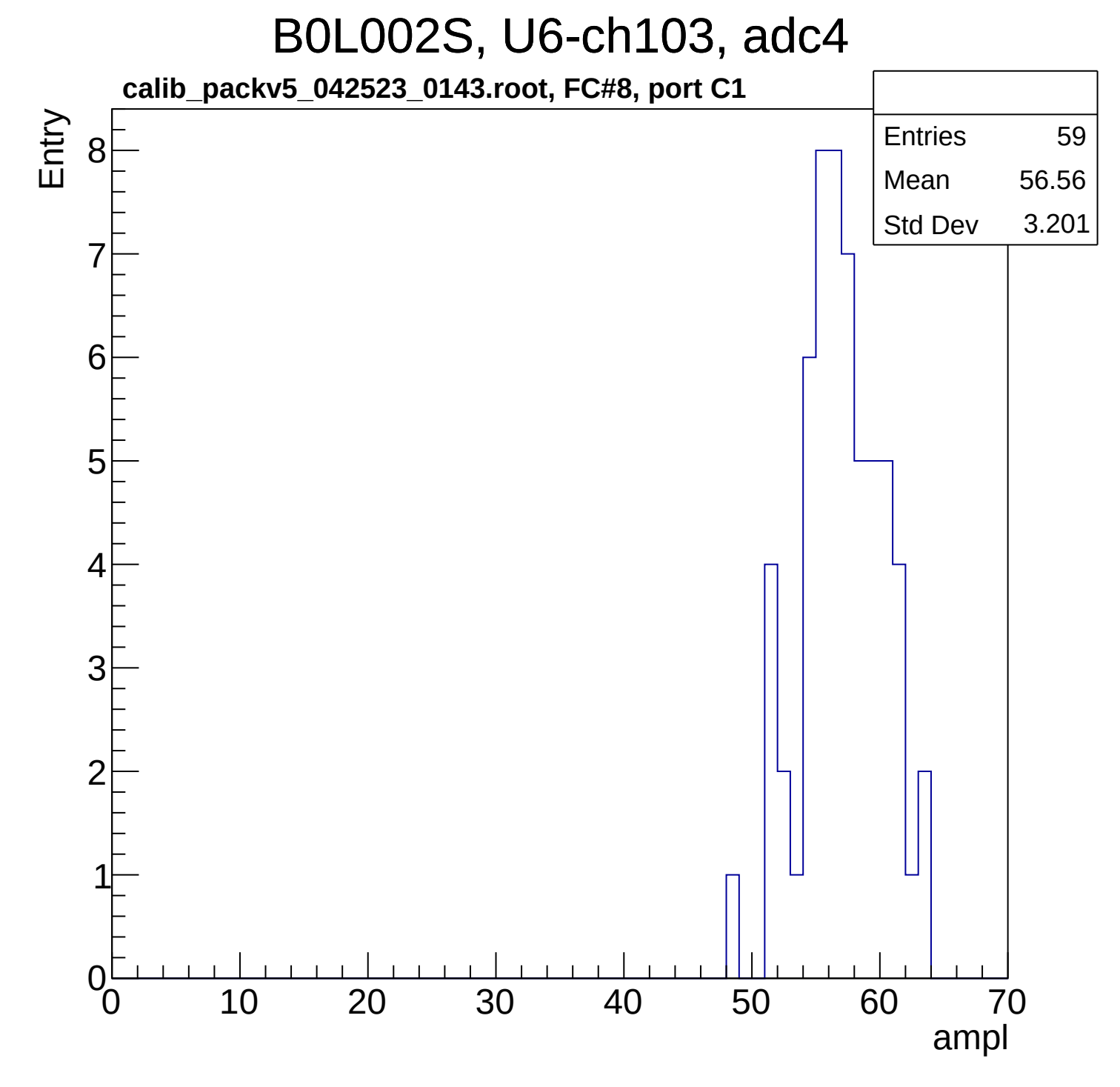
B0L002S, U6-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.56
Std Dev 3.201
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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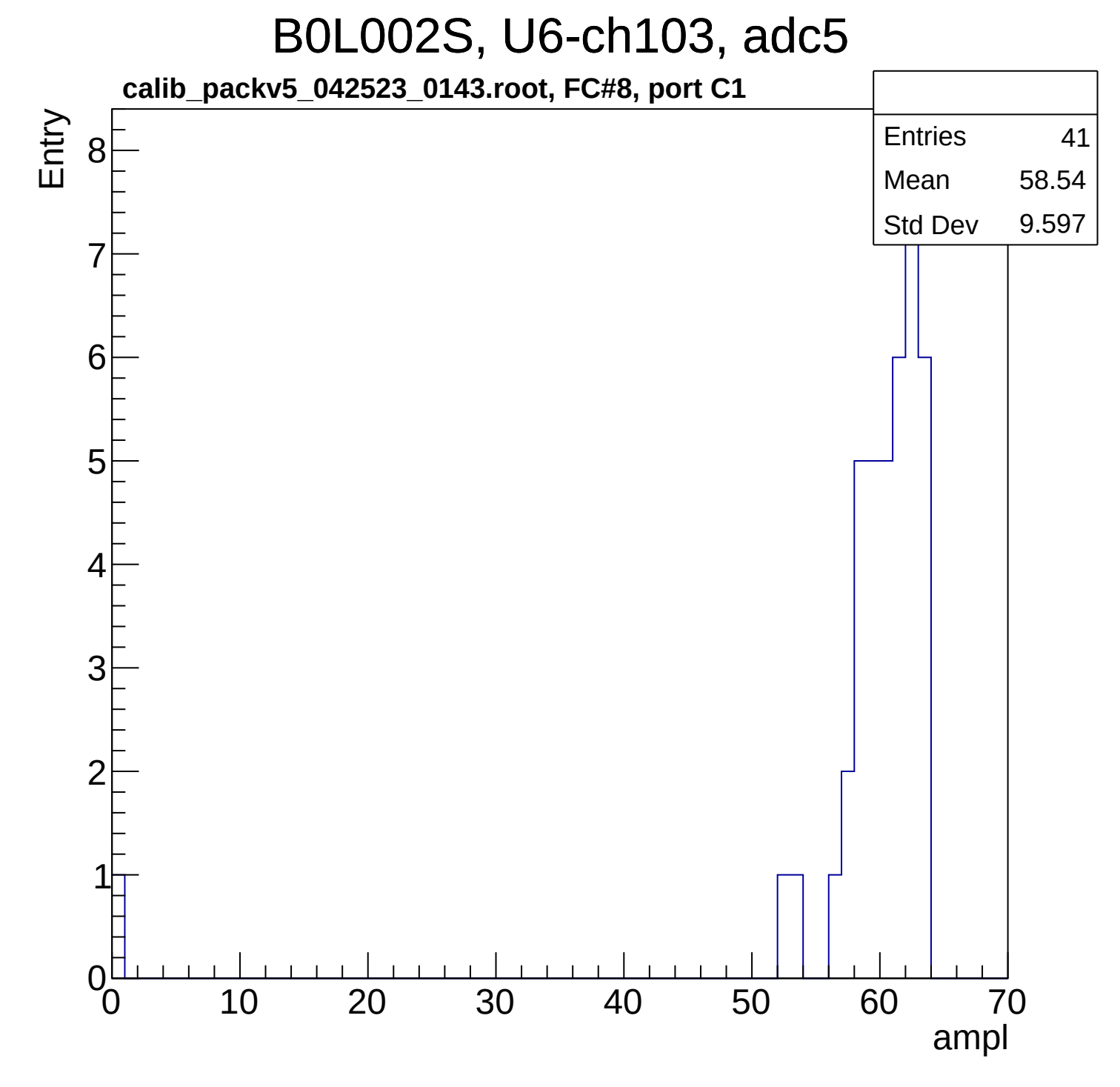
B0L002S, U6-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.54
Std Dev 9.597
Entry
ampl
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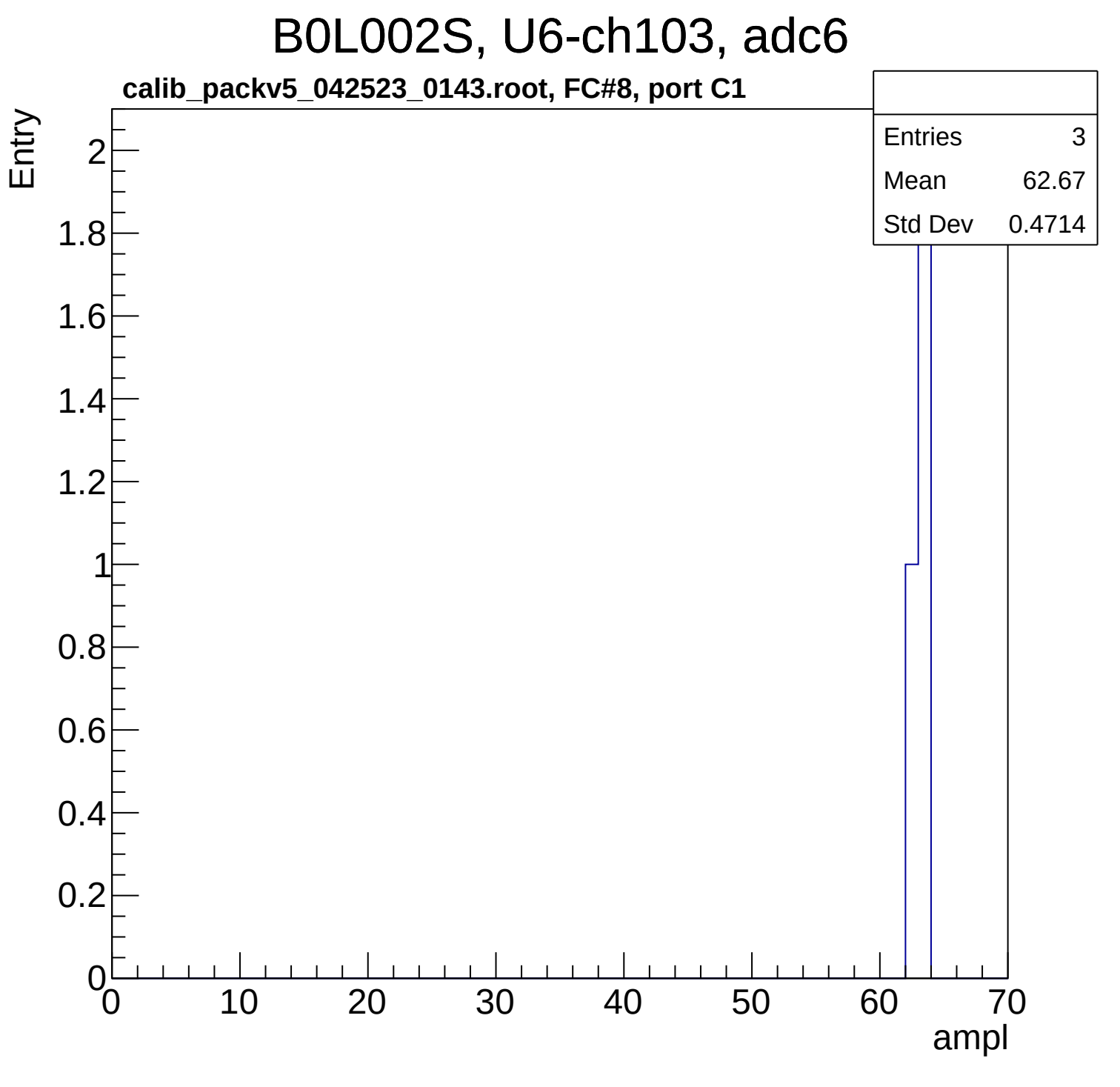
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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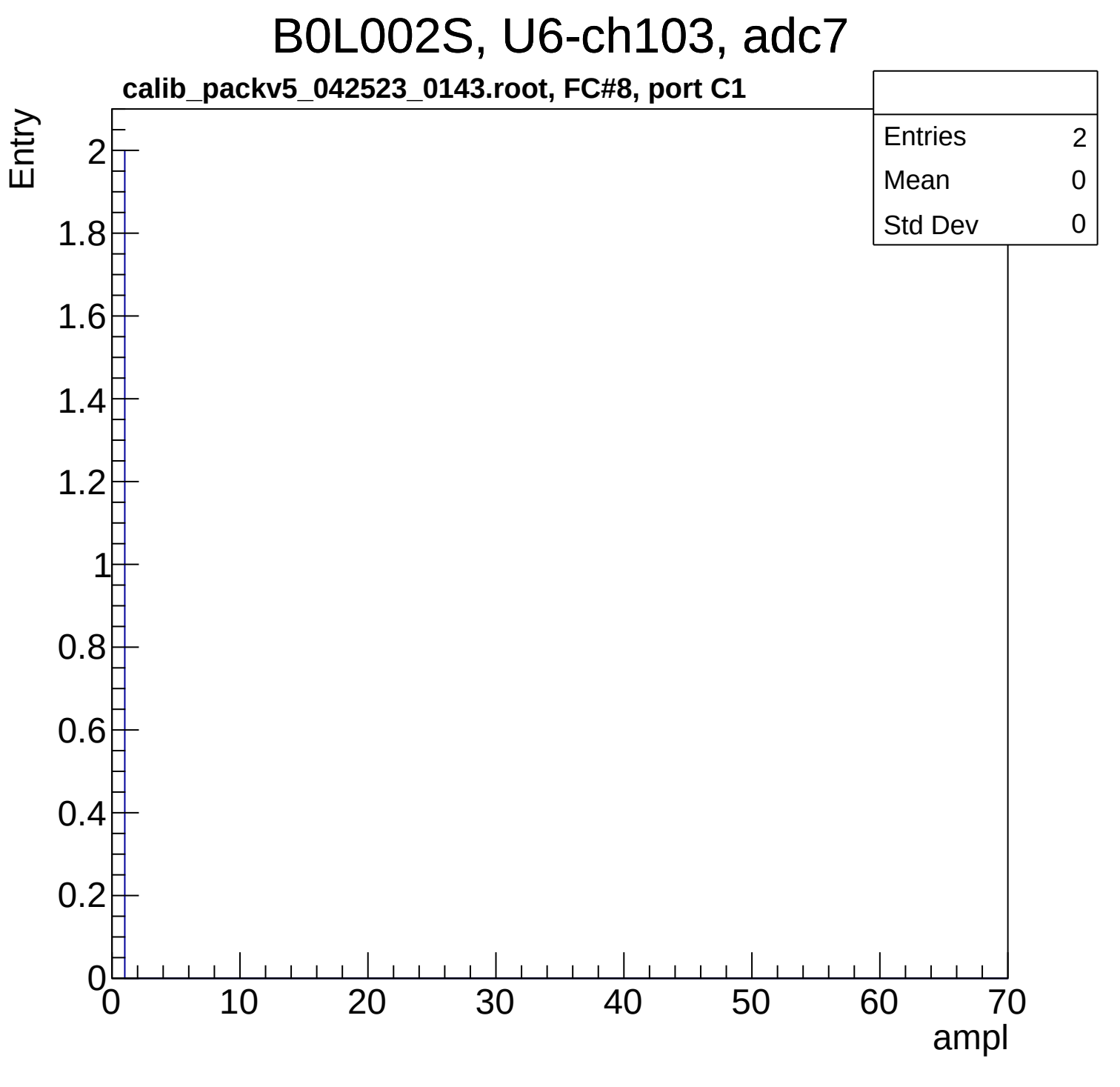
B0L002S, U6-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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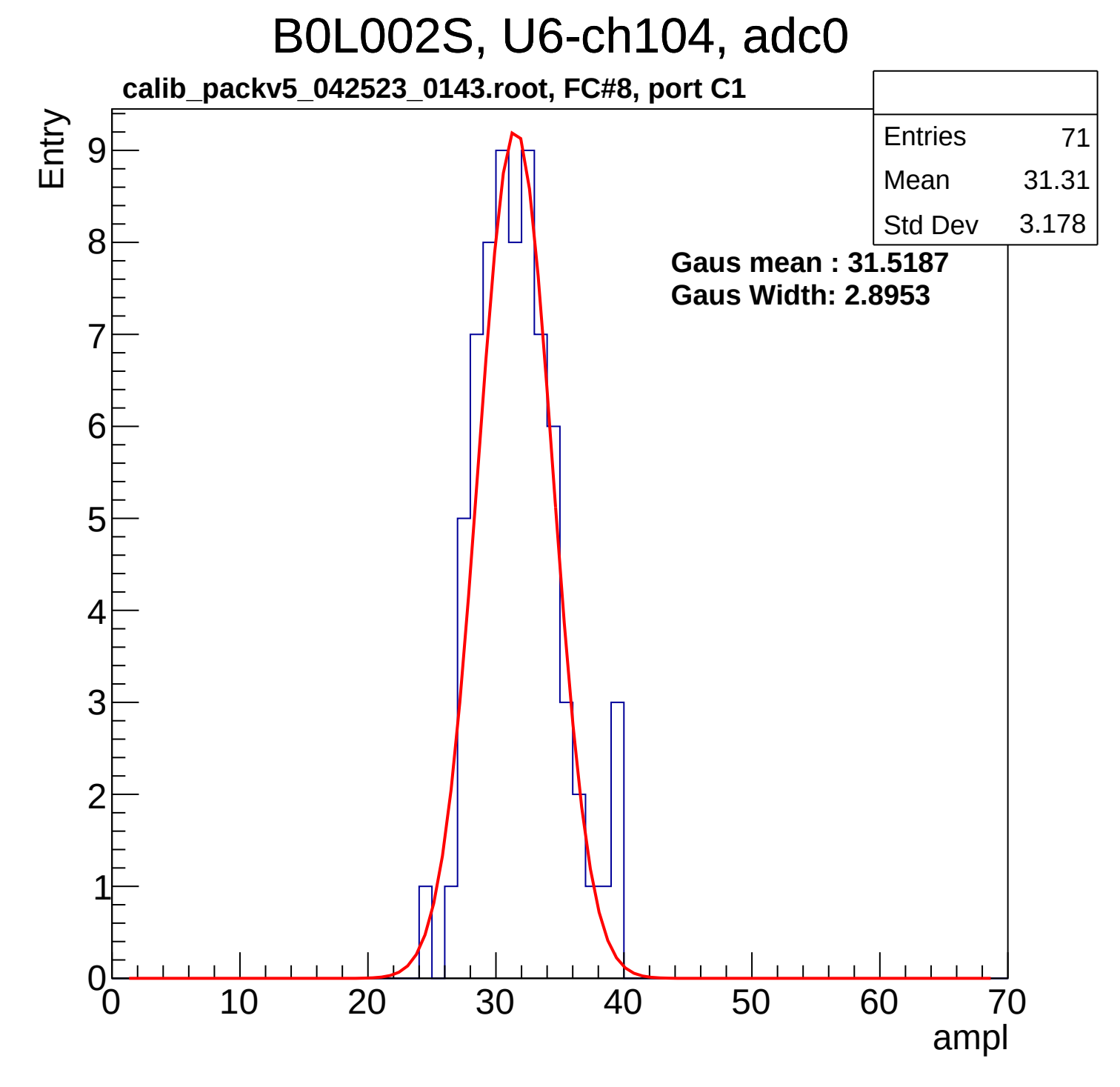
B0L002S, U6-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.31
Std Dev 3.178
Gaus mean : 31.5187
Gaus Width: 2.8953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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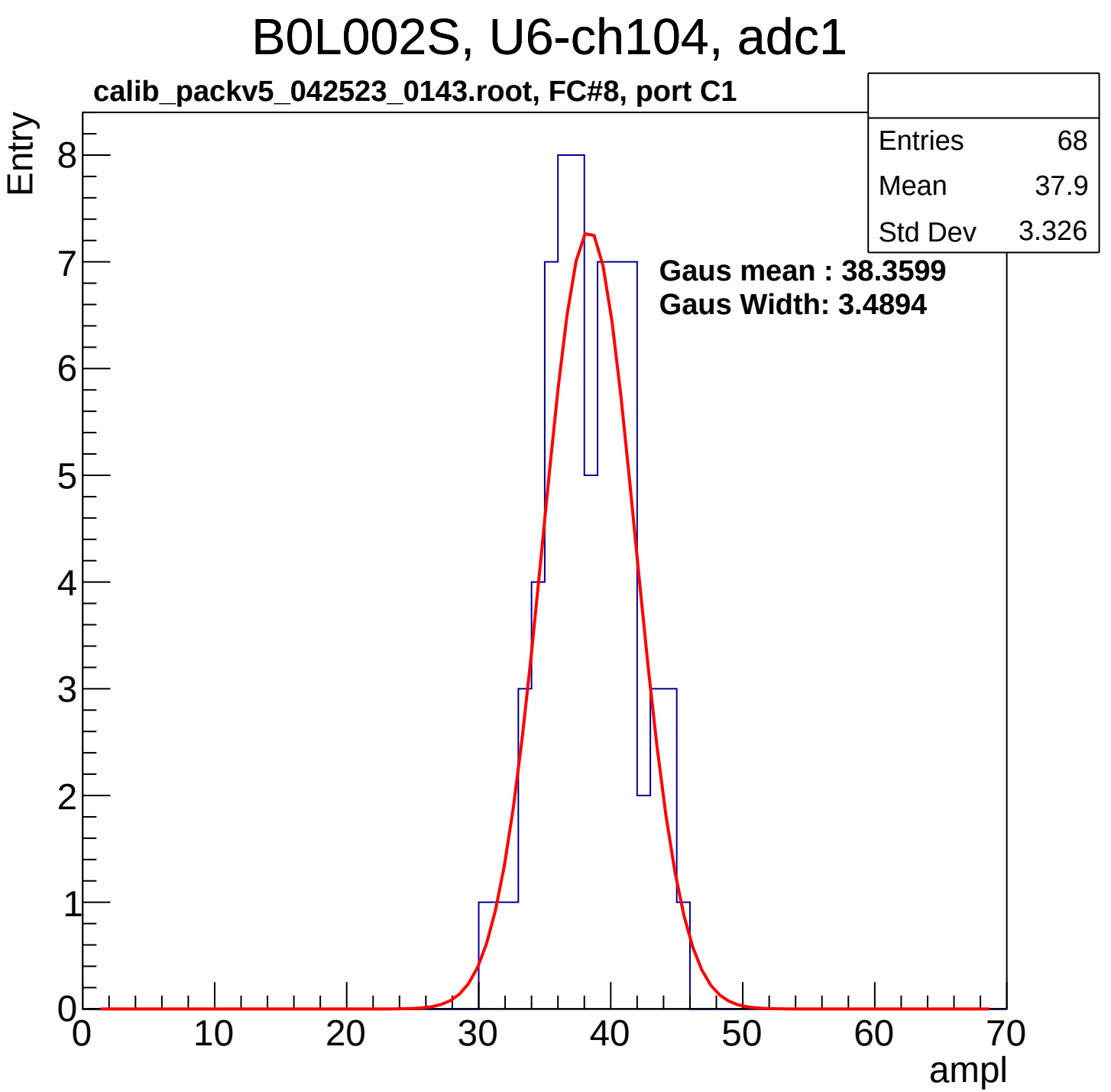
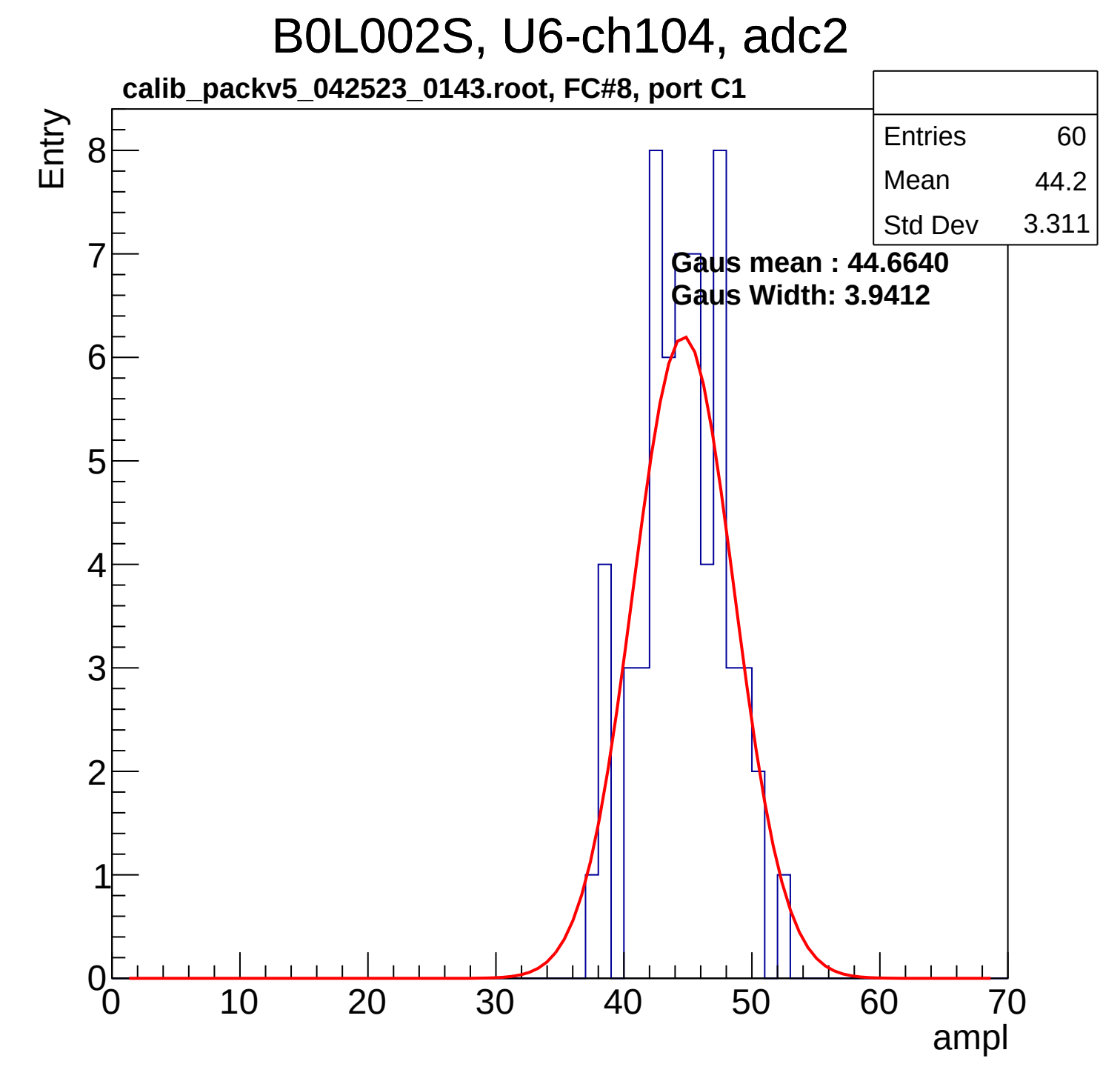
60
70


B0L002S, U6-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.9
Std Dev 3.326
Gaus mean : 38.3599
Gaus Width: 3.4894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.2
Std Dev 3.311
Gaus mean : 44.6640
Gaus Width: 3.9412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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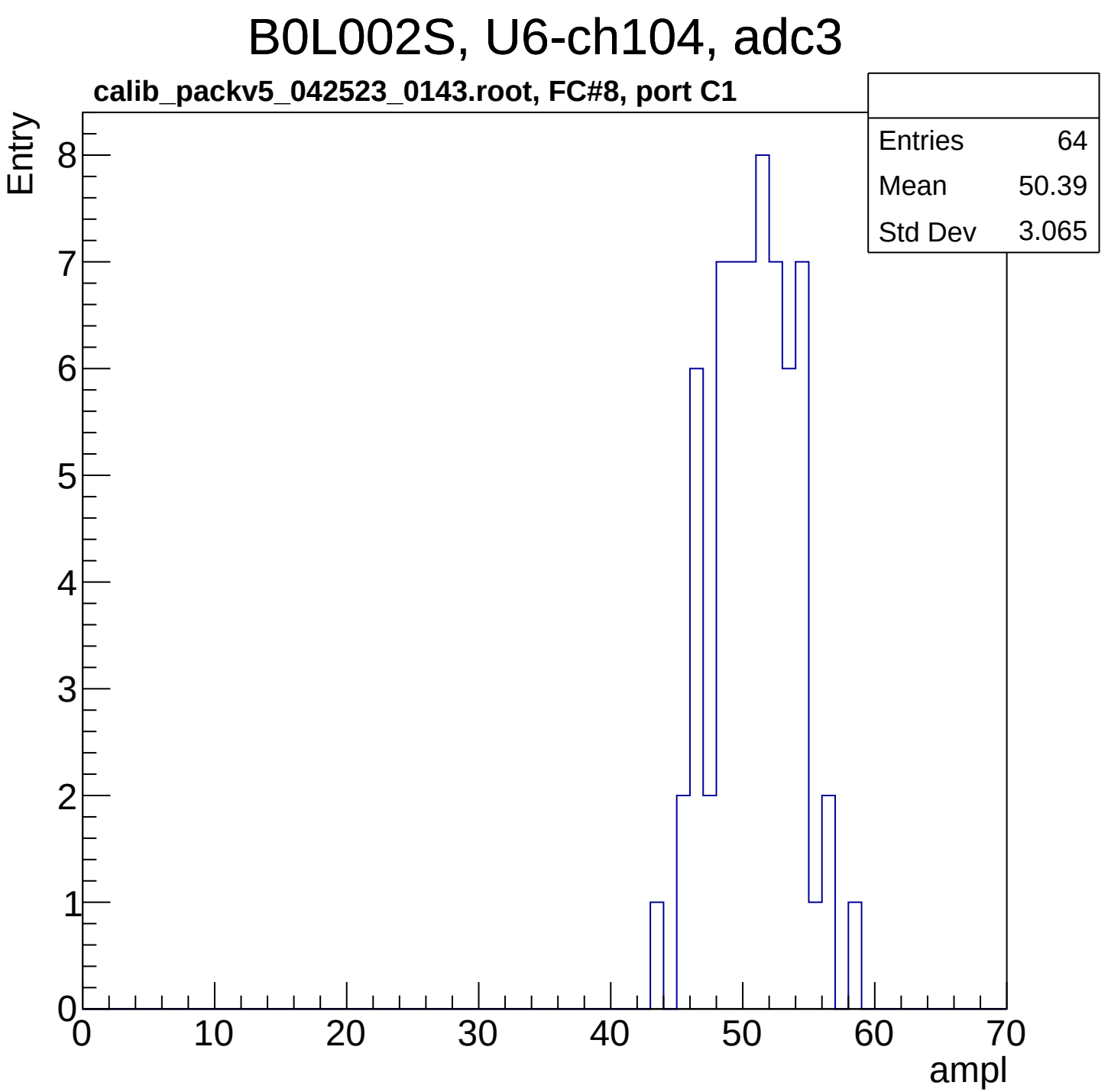
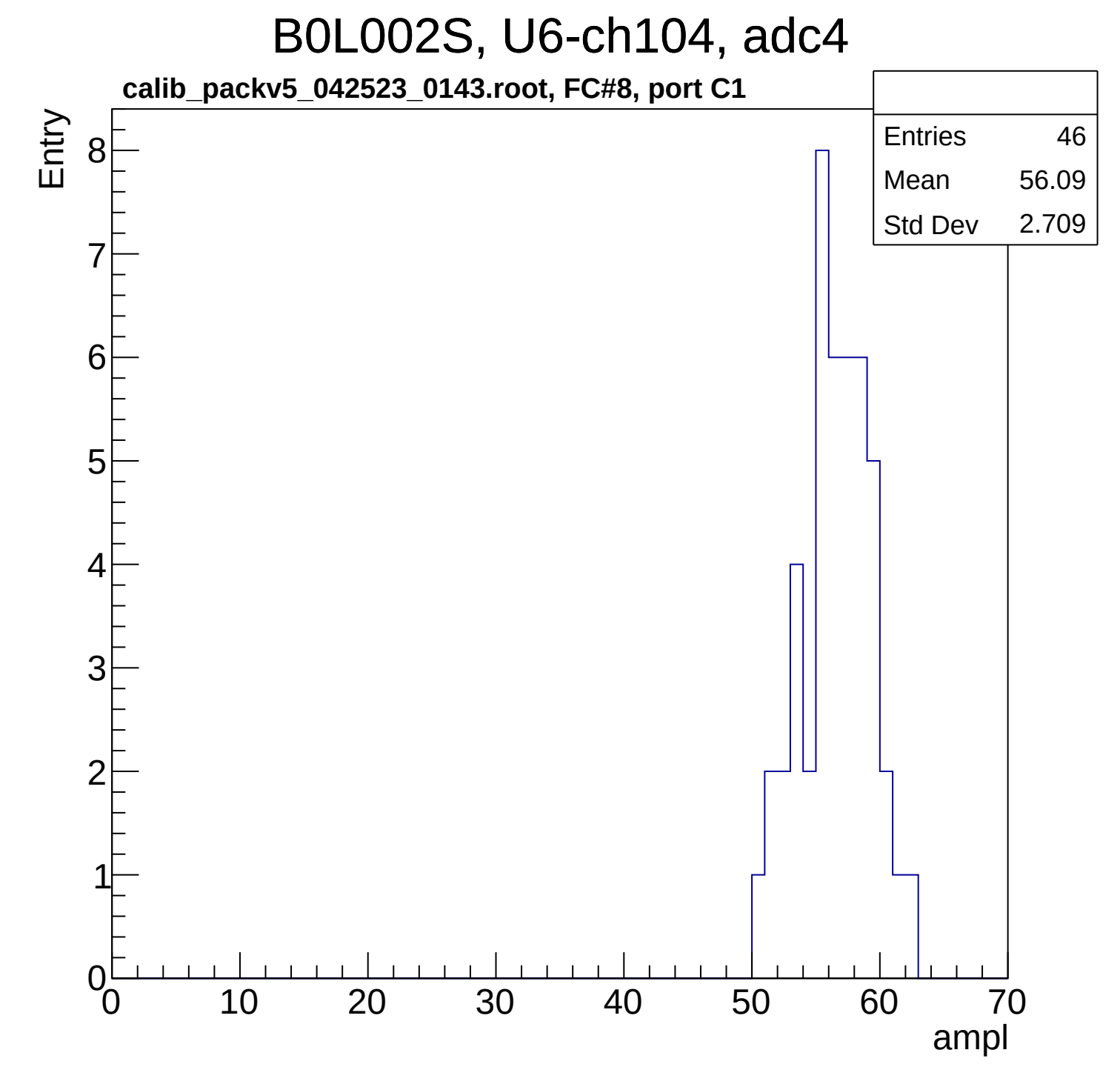
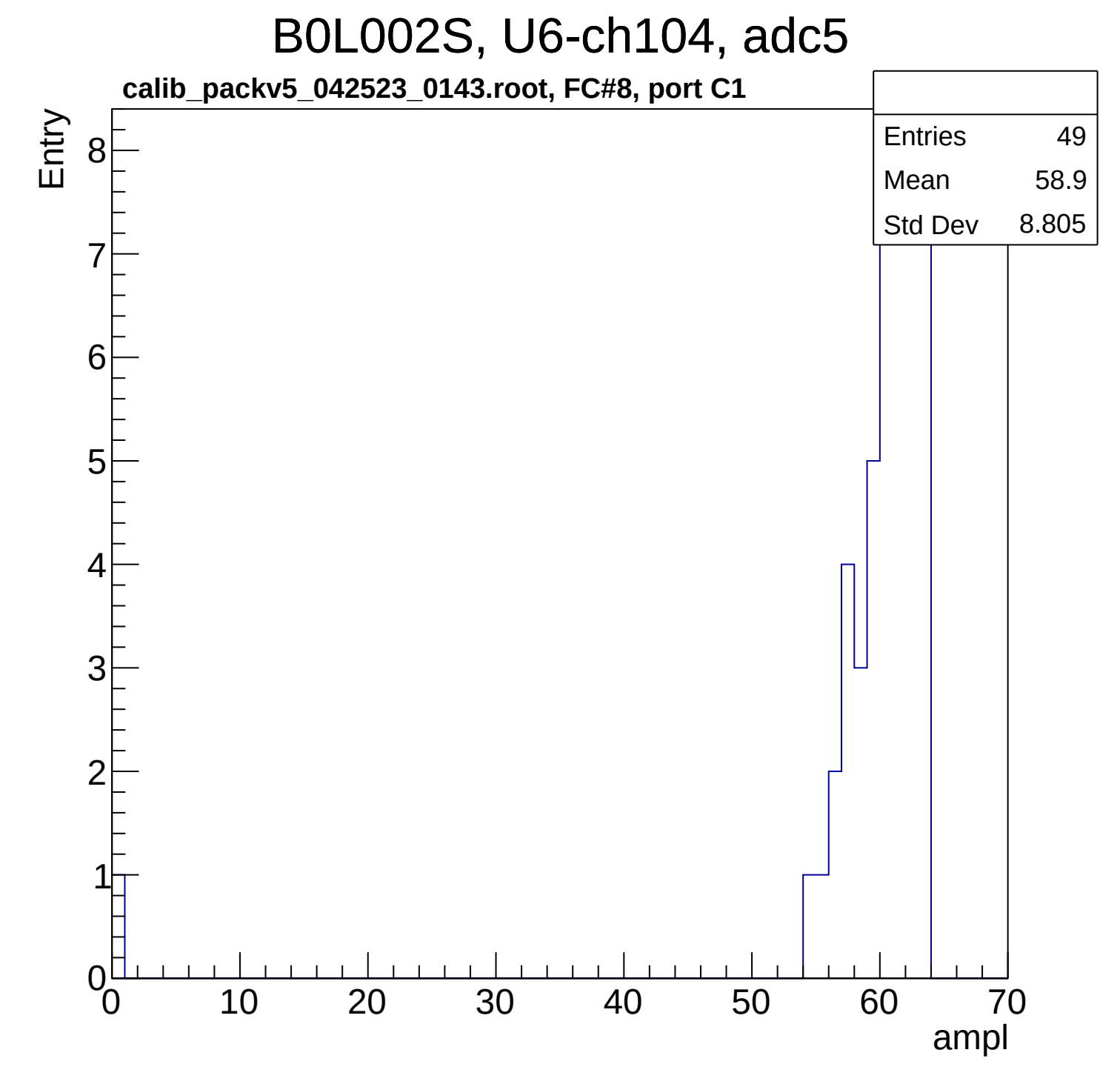
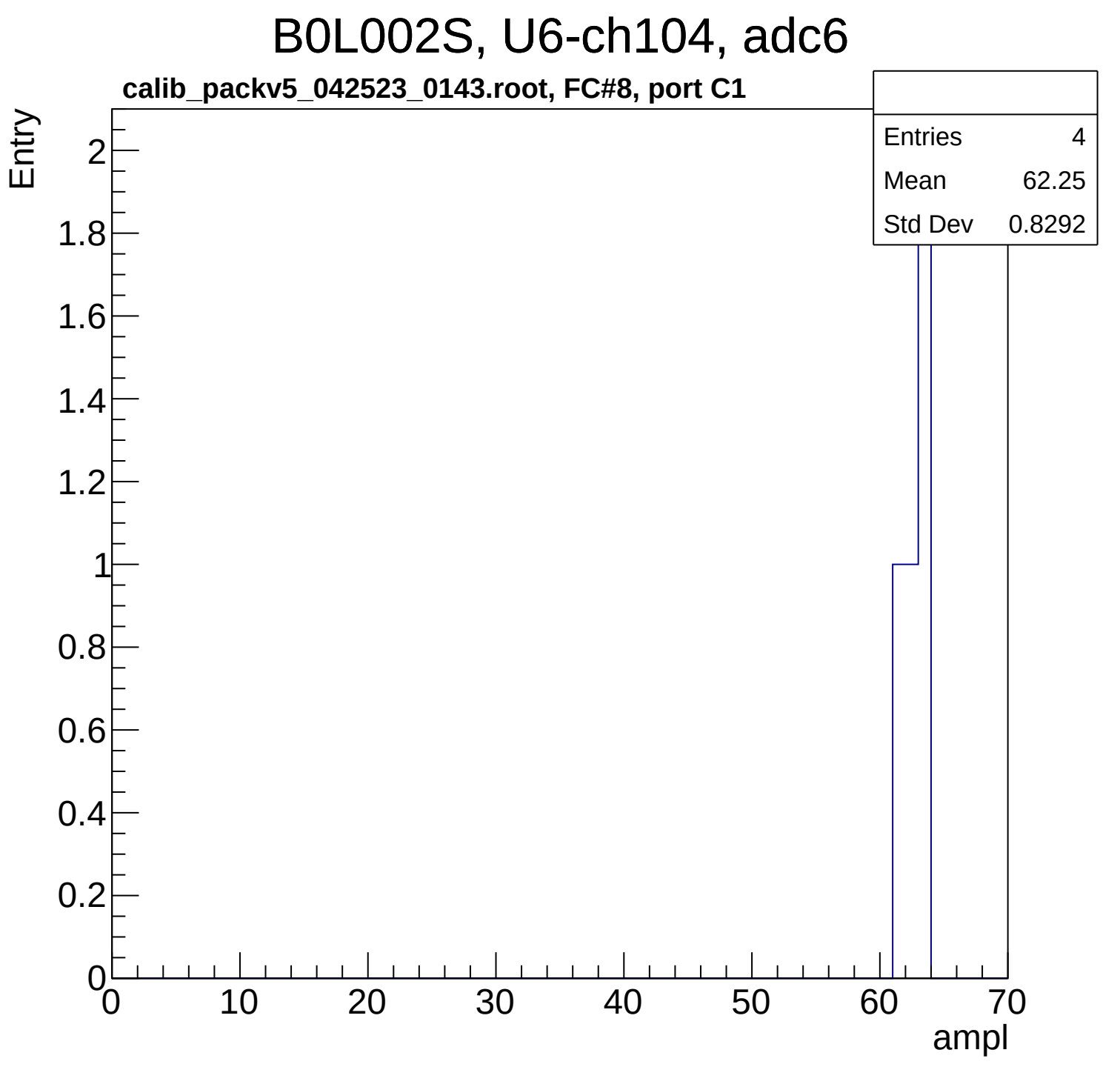
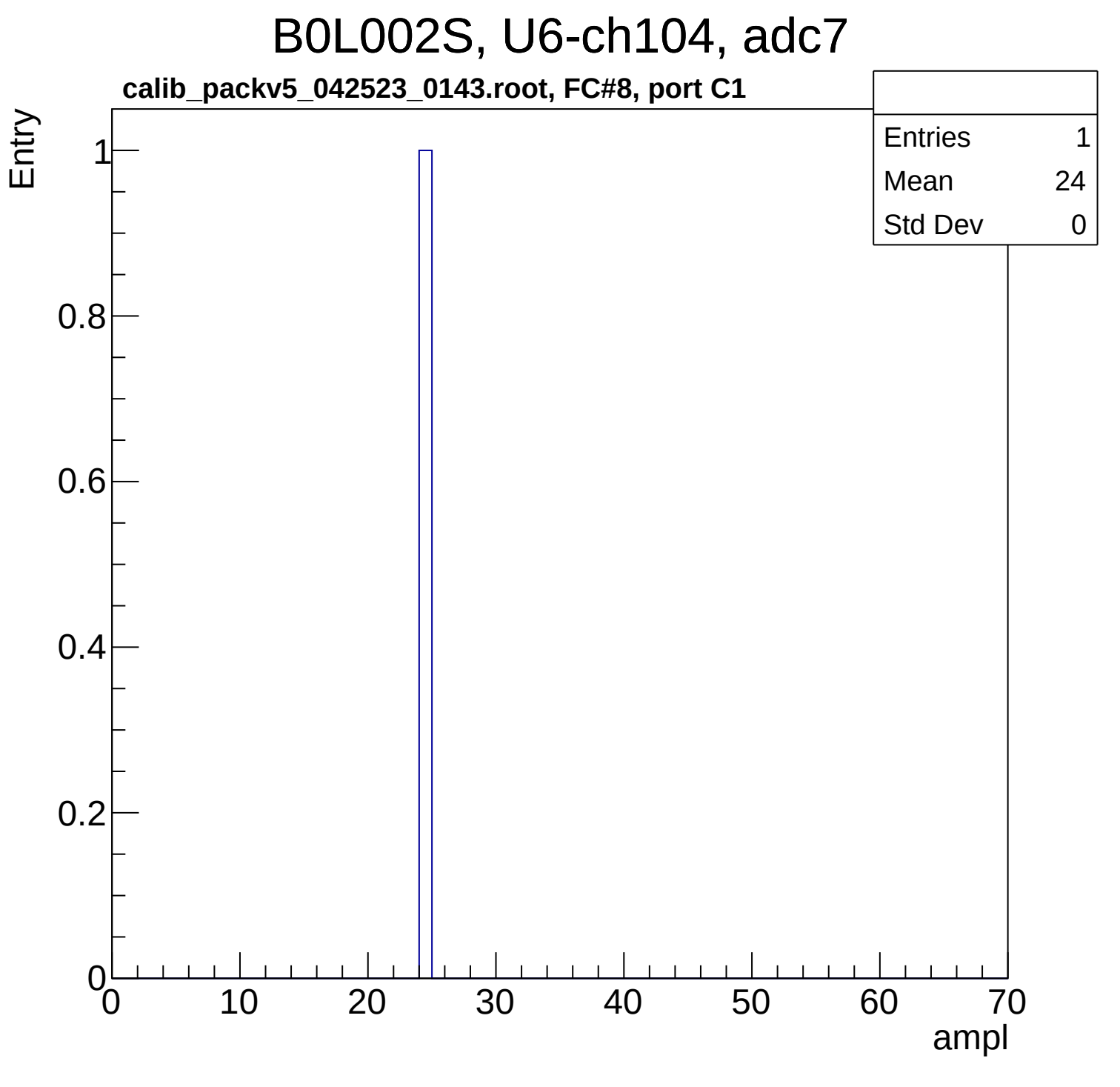

B0L002S, U6-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.39
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 56.09
Std Dev 2.709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 58.9
Std Dev 8.805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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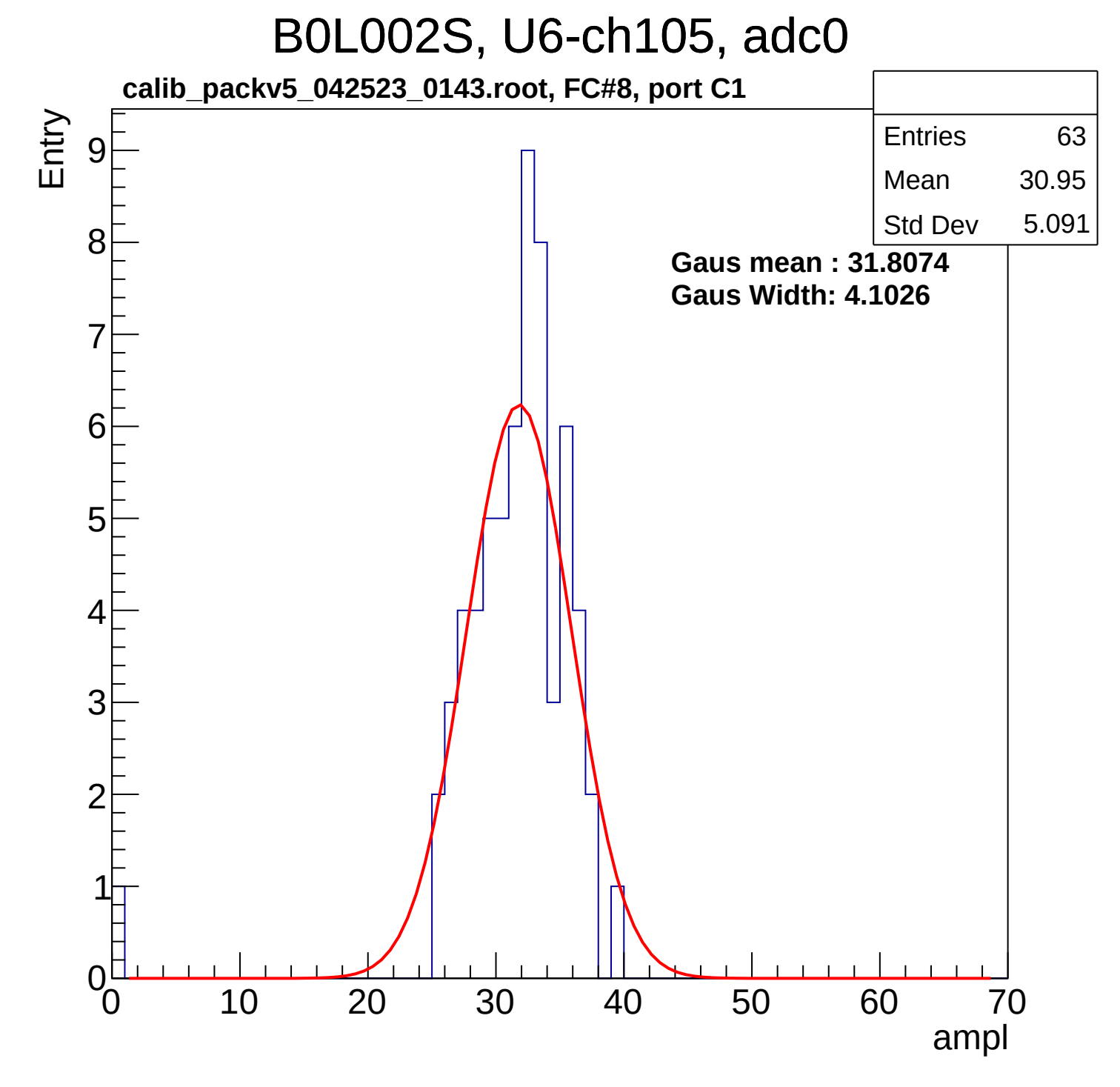
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.95
Std Dev 5.091
Gaus mean : 31.8074
Gaus Width: 4.1026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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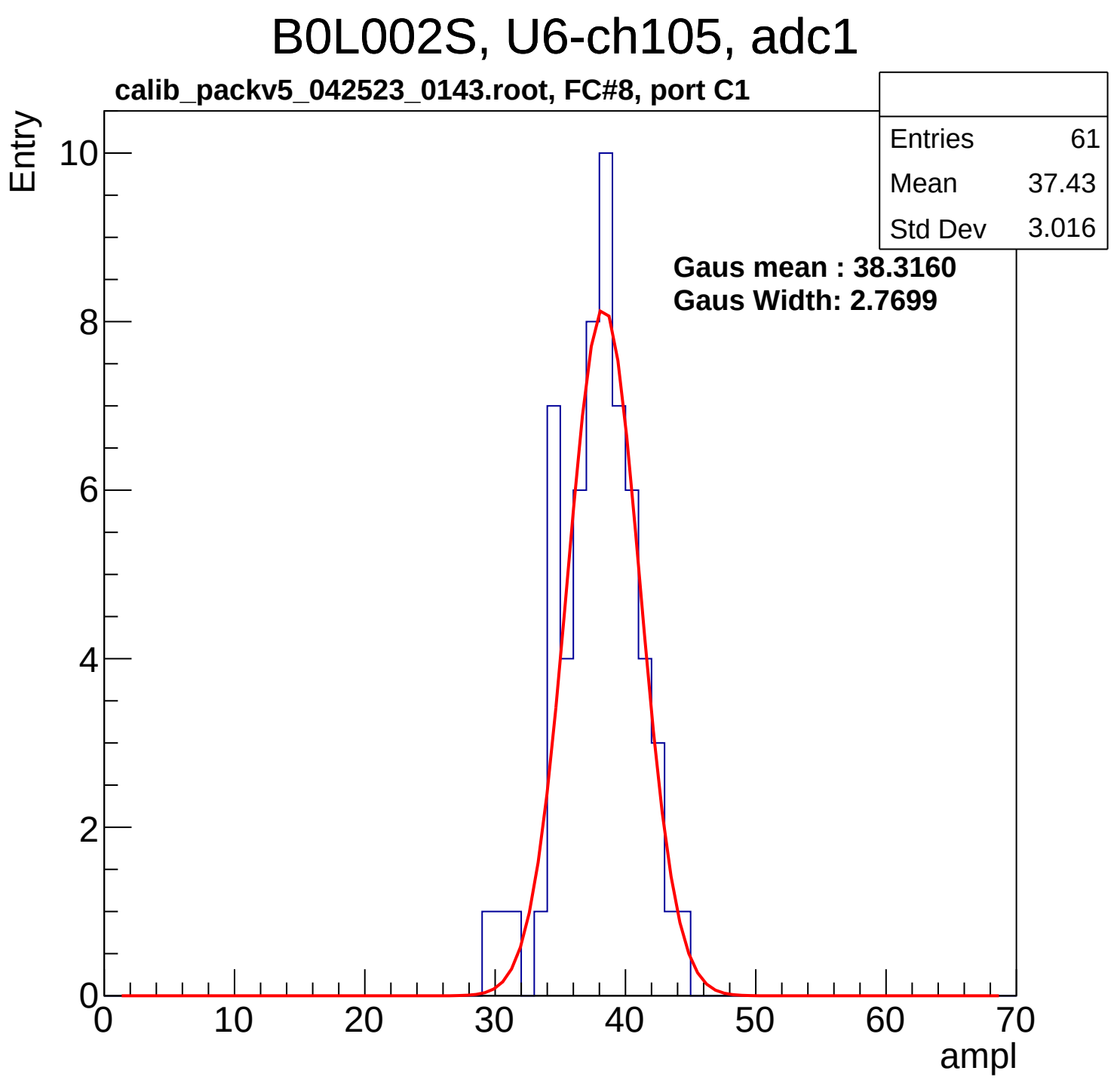
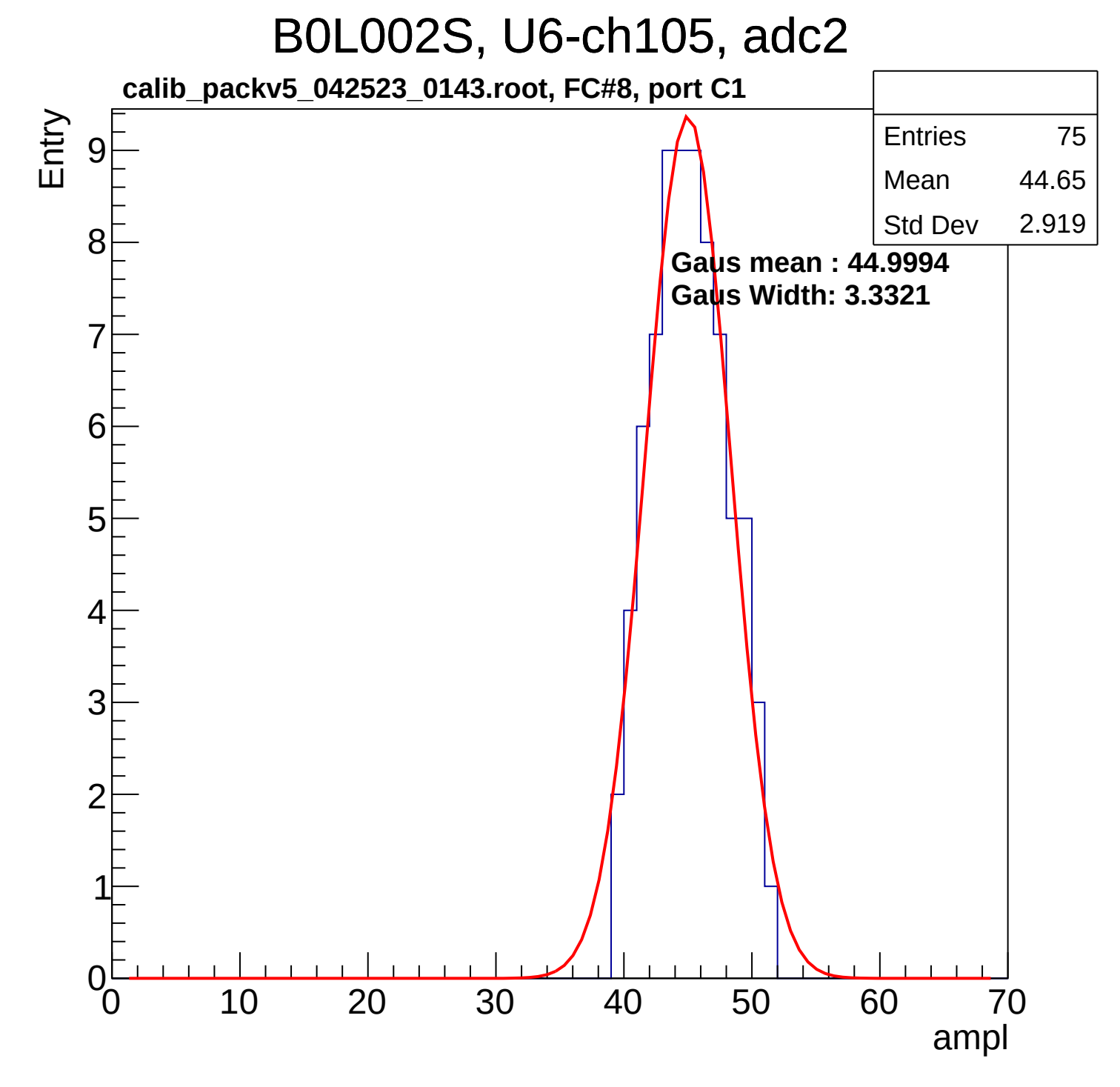
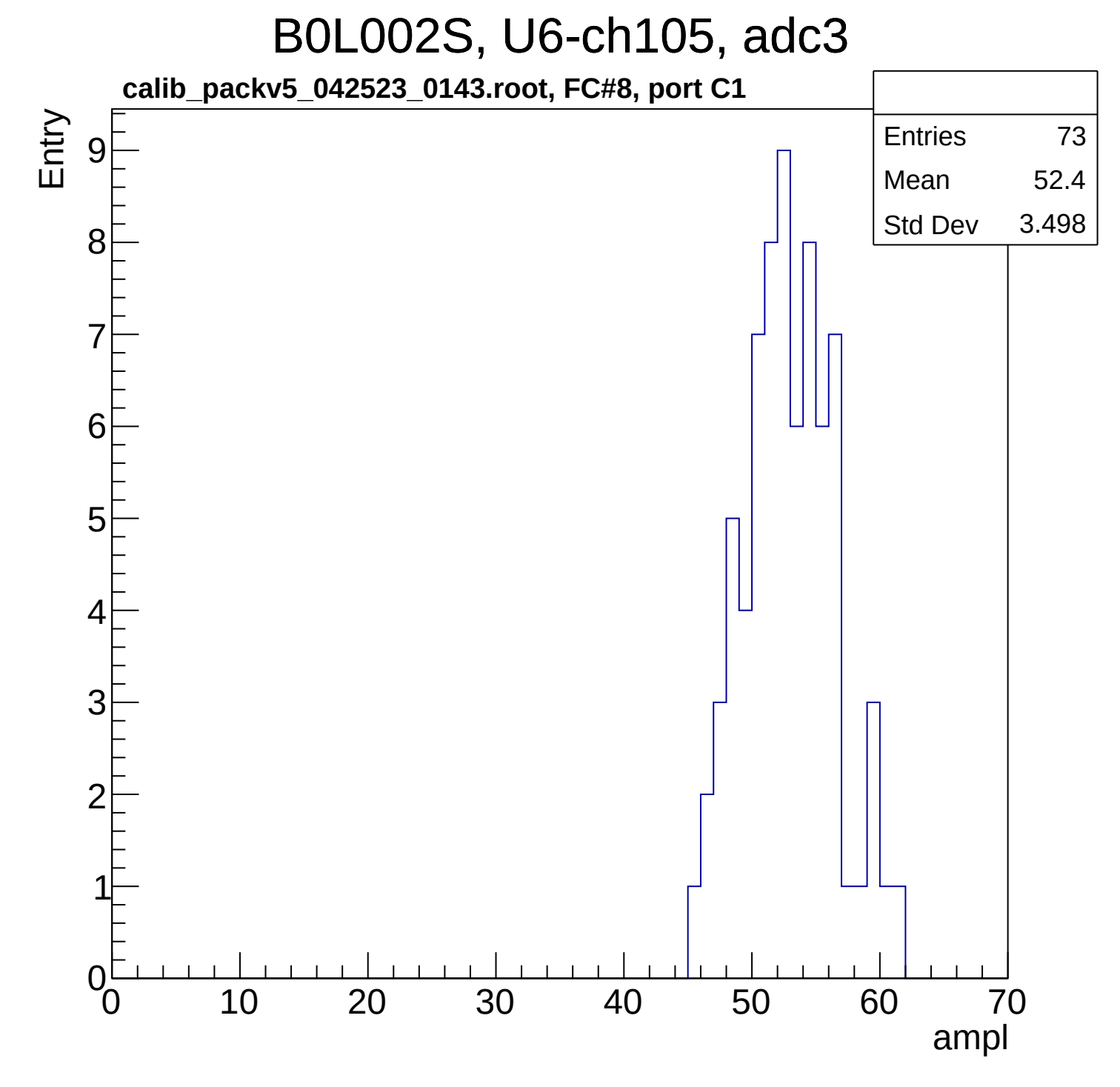
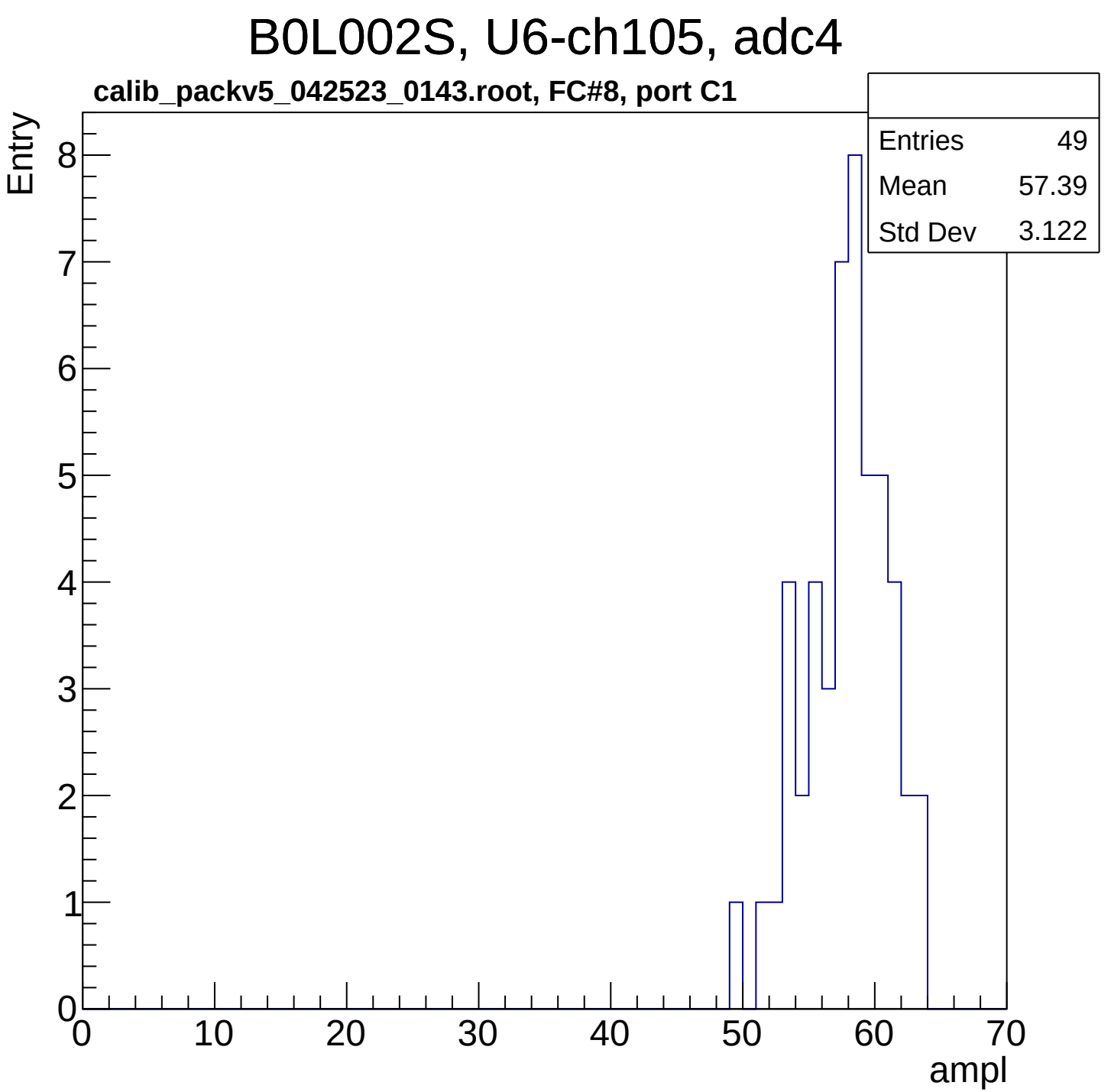

B0L002S, U6-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.43
Std Dev 3.016
Gaus mean : 38.3160
Gaus Width: 2.7699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.65
Std Dev 2.919
Gaus mean : 44.9994
Gaus Width: 3.3321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 52.4
Std Dev 3.498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.39
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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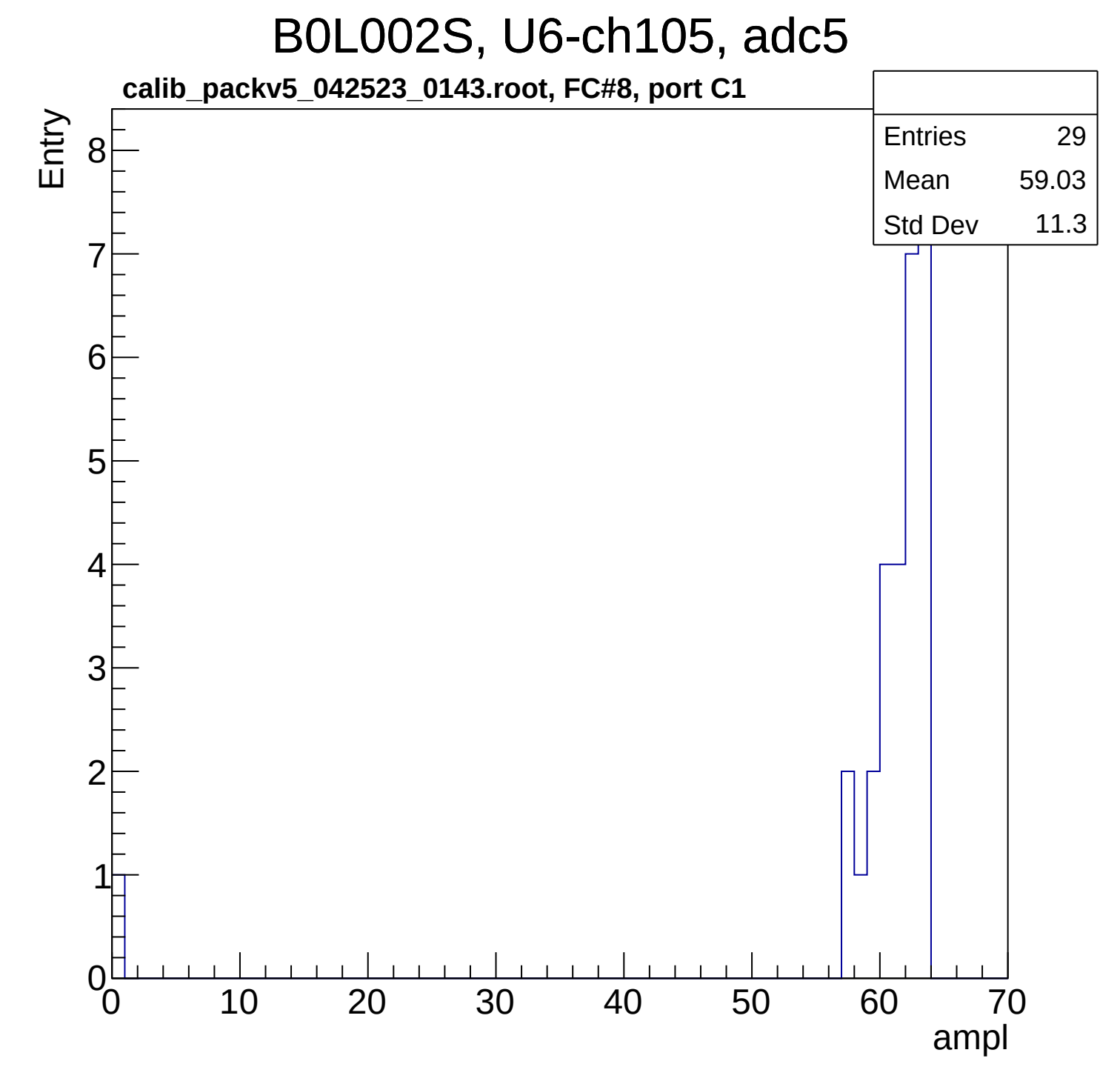
B0L002S, U6-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.03
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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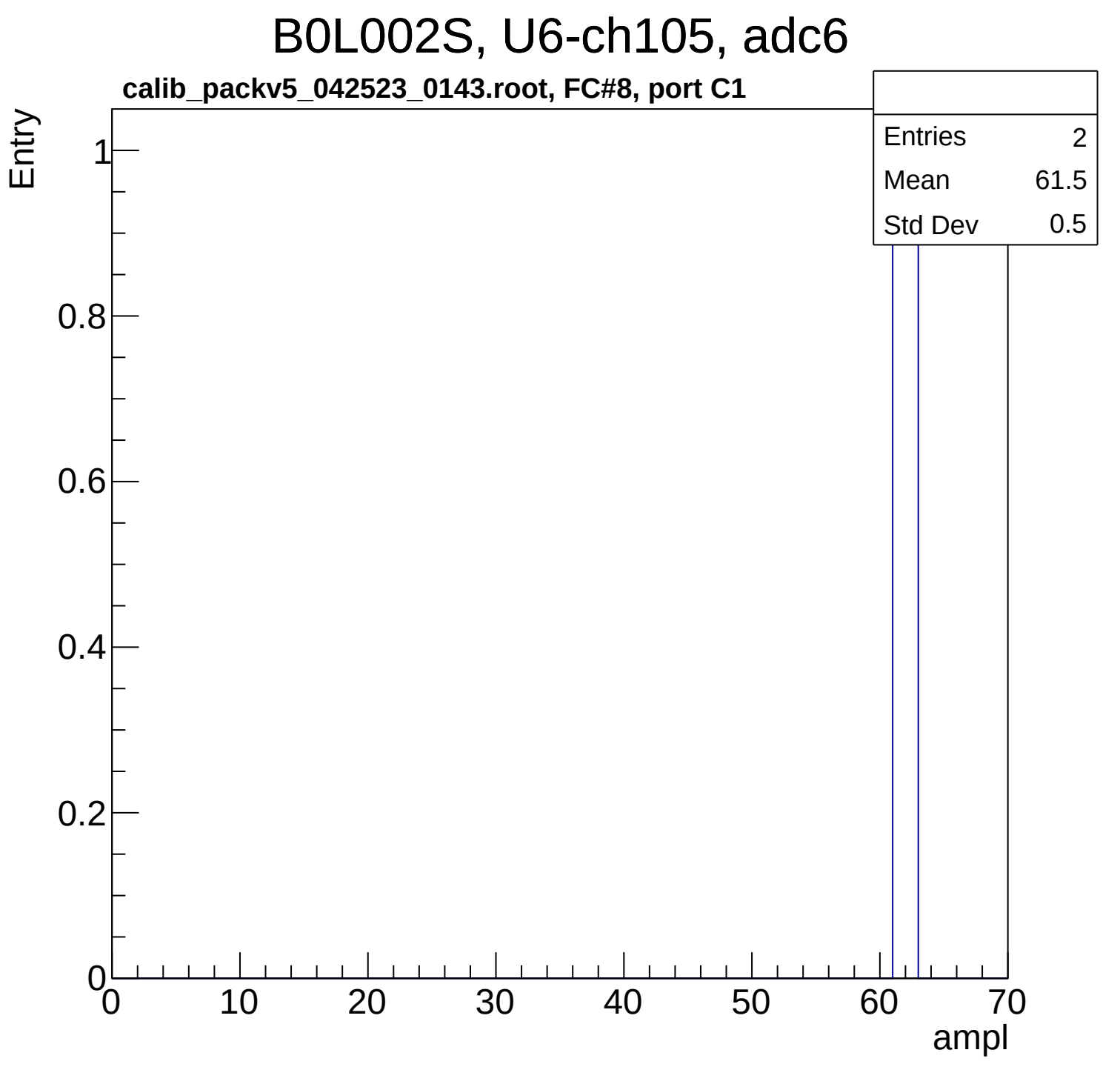
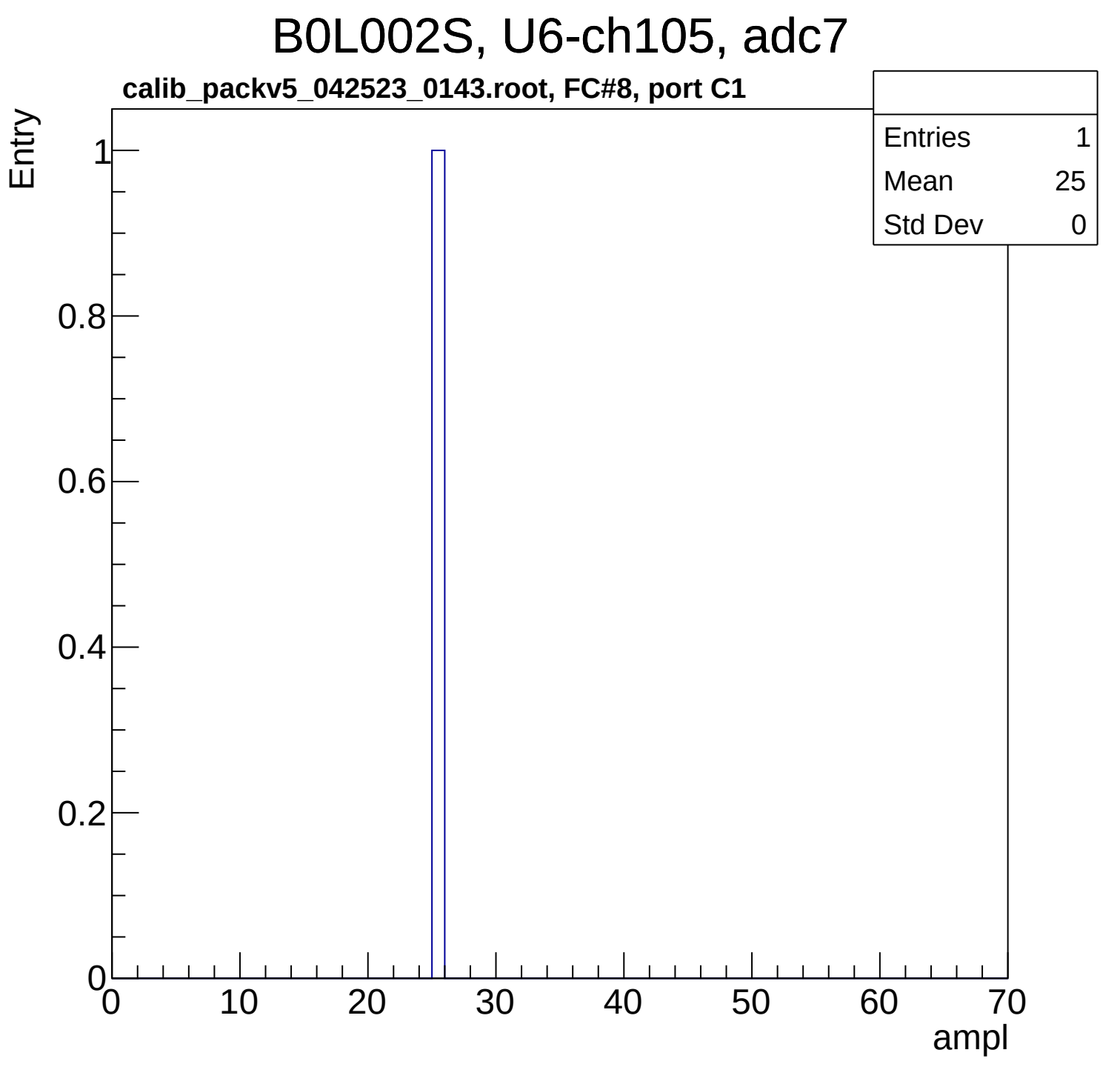
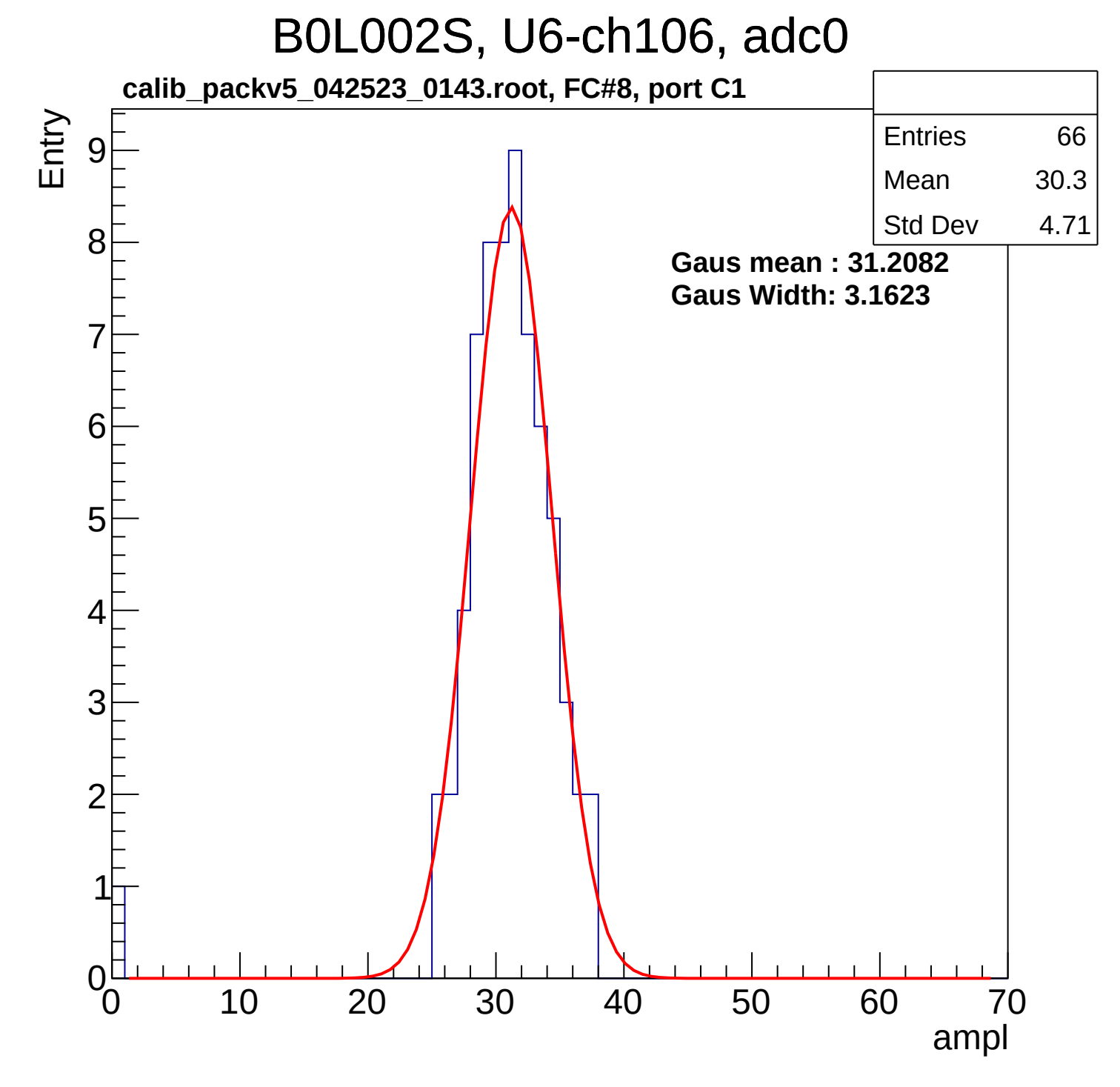
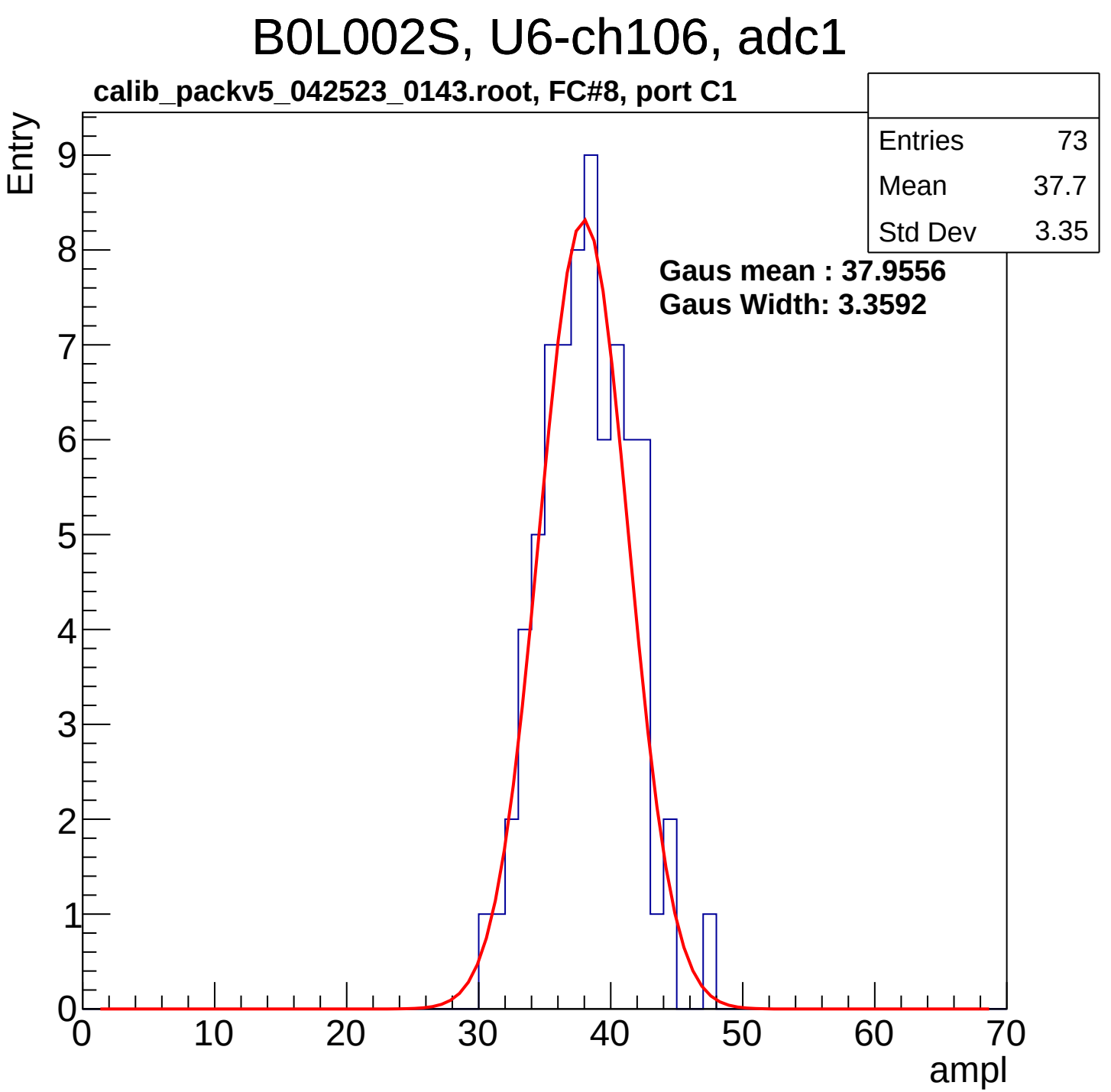
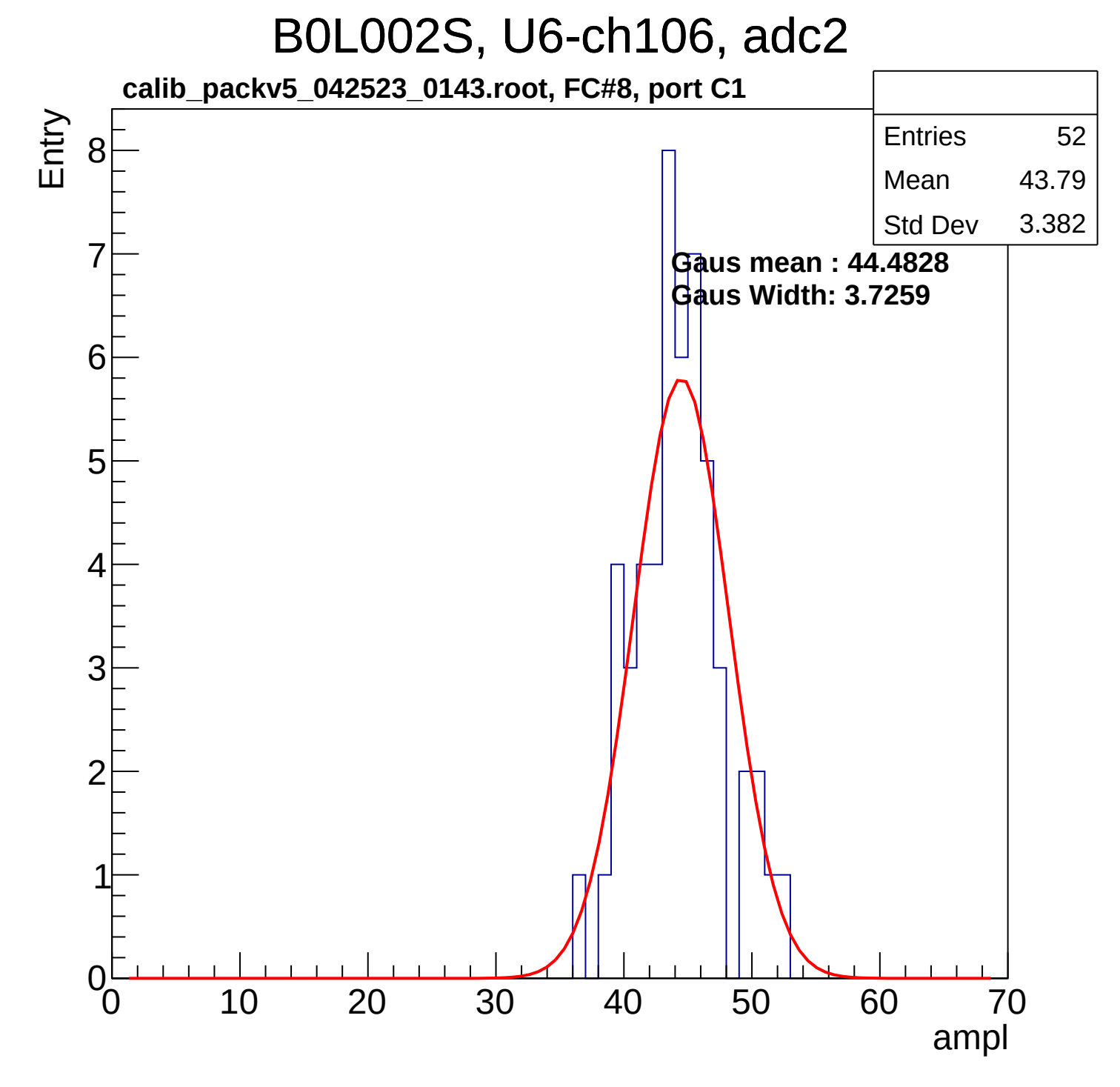

B0L002S, U6-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.3
Std Dev 4.71
Gaus mean : 31.2082
Gaus Width: 3.1623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.7
Std Dev 3.35
Gaus mean : 37.9556
Gaus Width: 3.3592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 43.79
Std Dev 3.382
Gaus mean : 44.4828
Gaus Width: 3.7259
Entry
ampl
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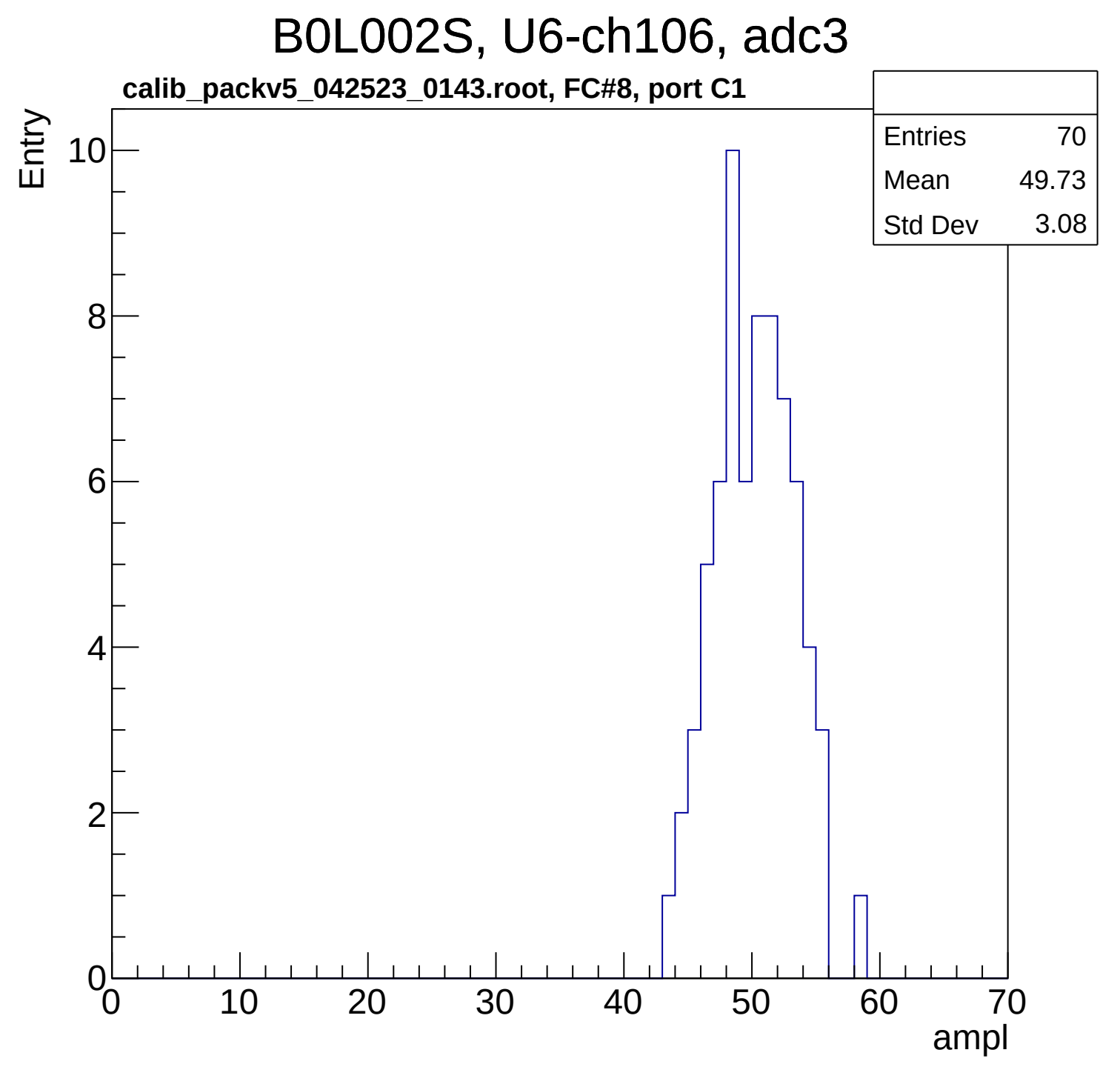
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 49.73
Std Dev 3.08
Entry
ampl
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2
4
6
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10
0
10
20
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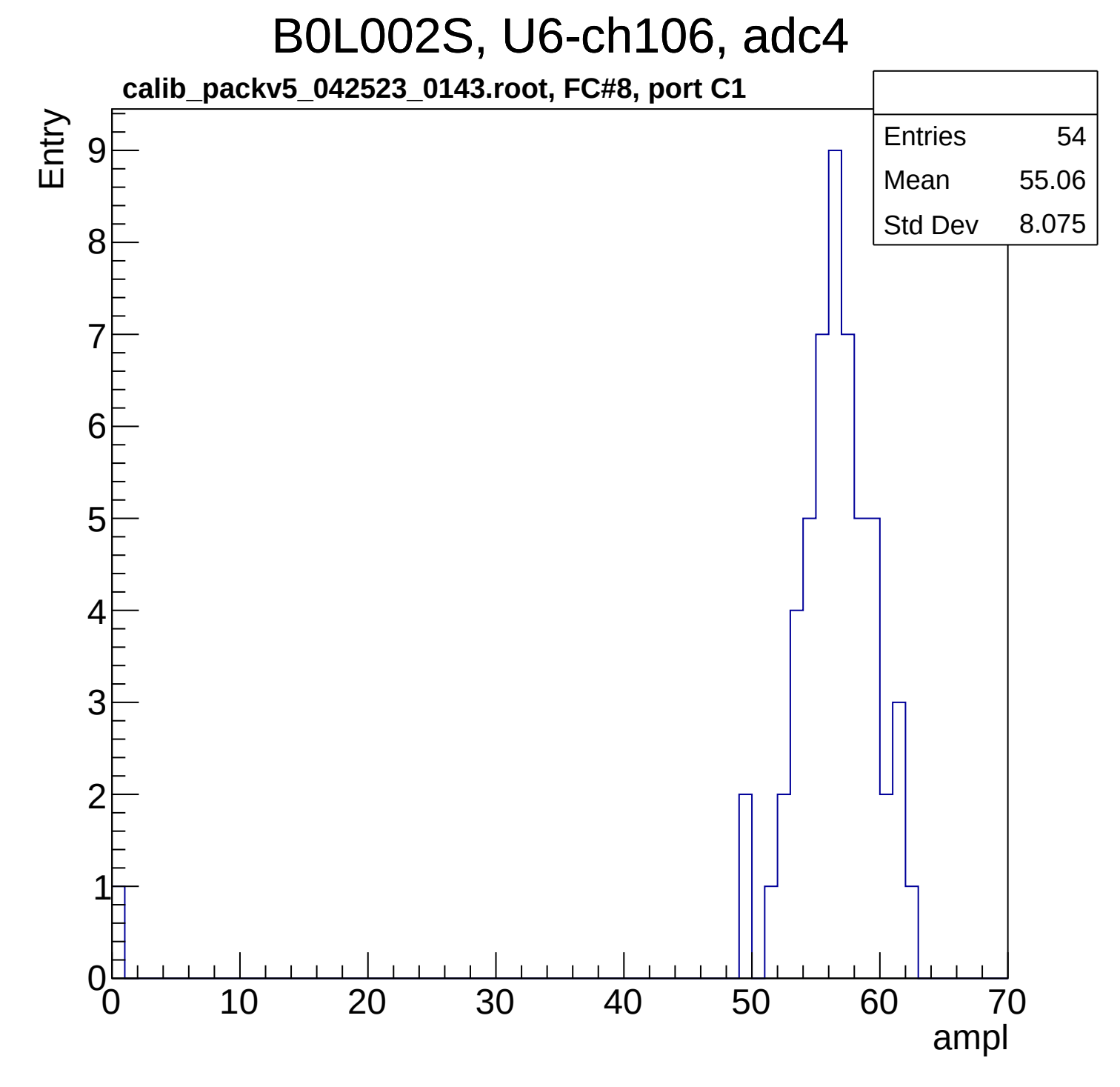
40
50
60
70


B0L002S, U6-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 55.06
Std Dev 8.075
Entry
ampl
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2
3
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7
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0
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60
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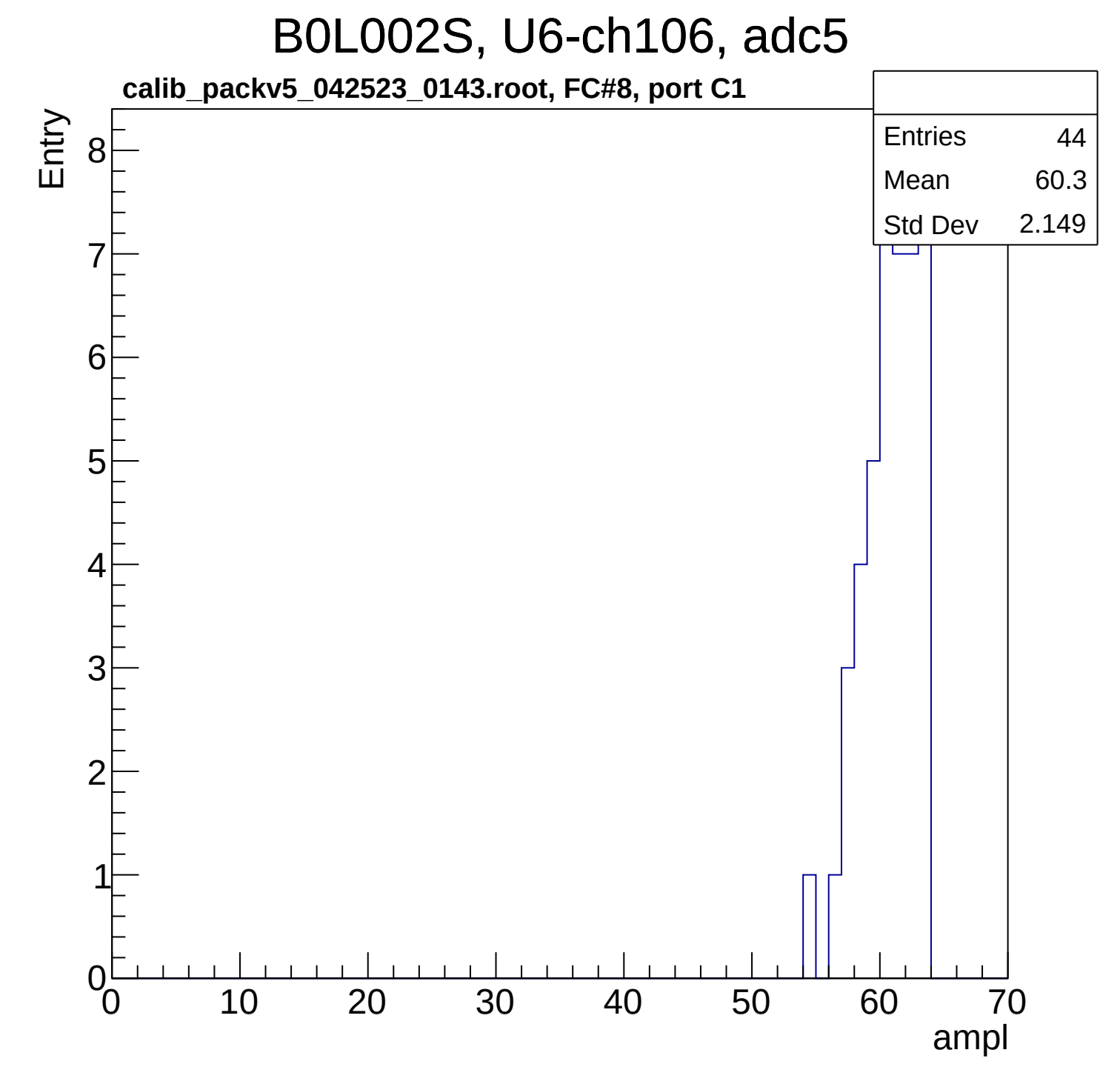
B0L002S, U6-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 60.3
Std Dev 2.149
Entry
ampl
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2
3
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0
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60
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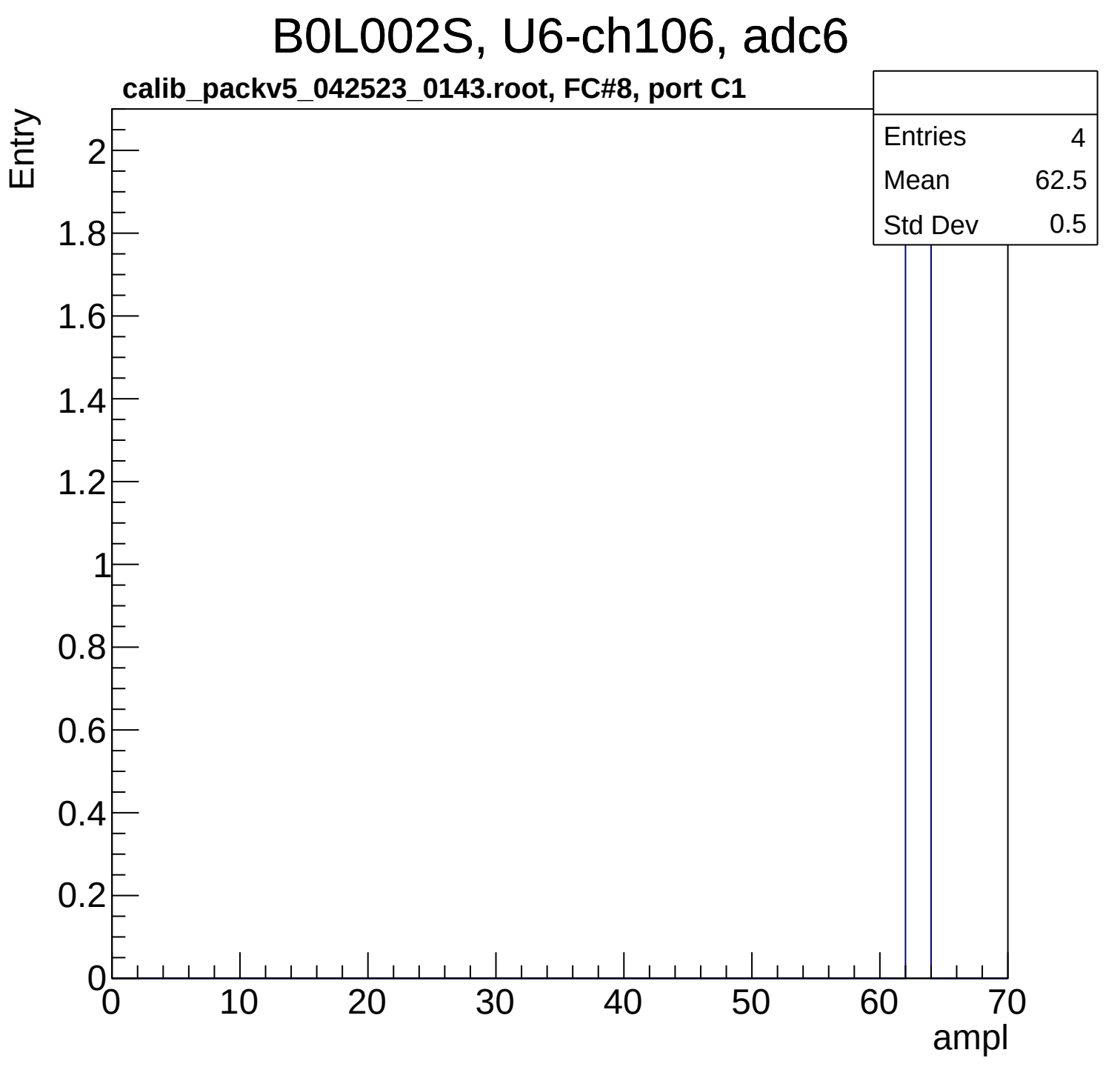
B0L002S, U6-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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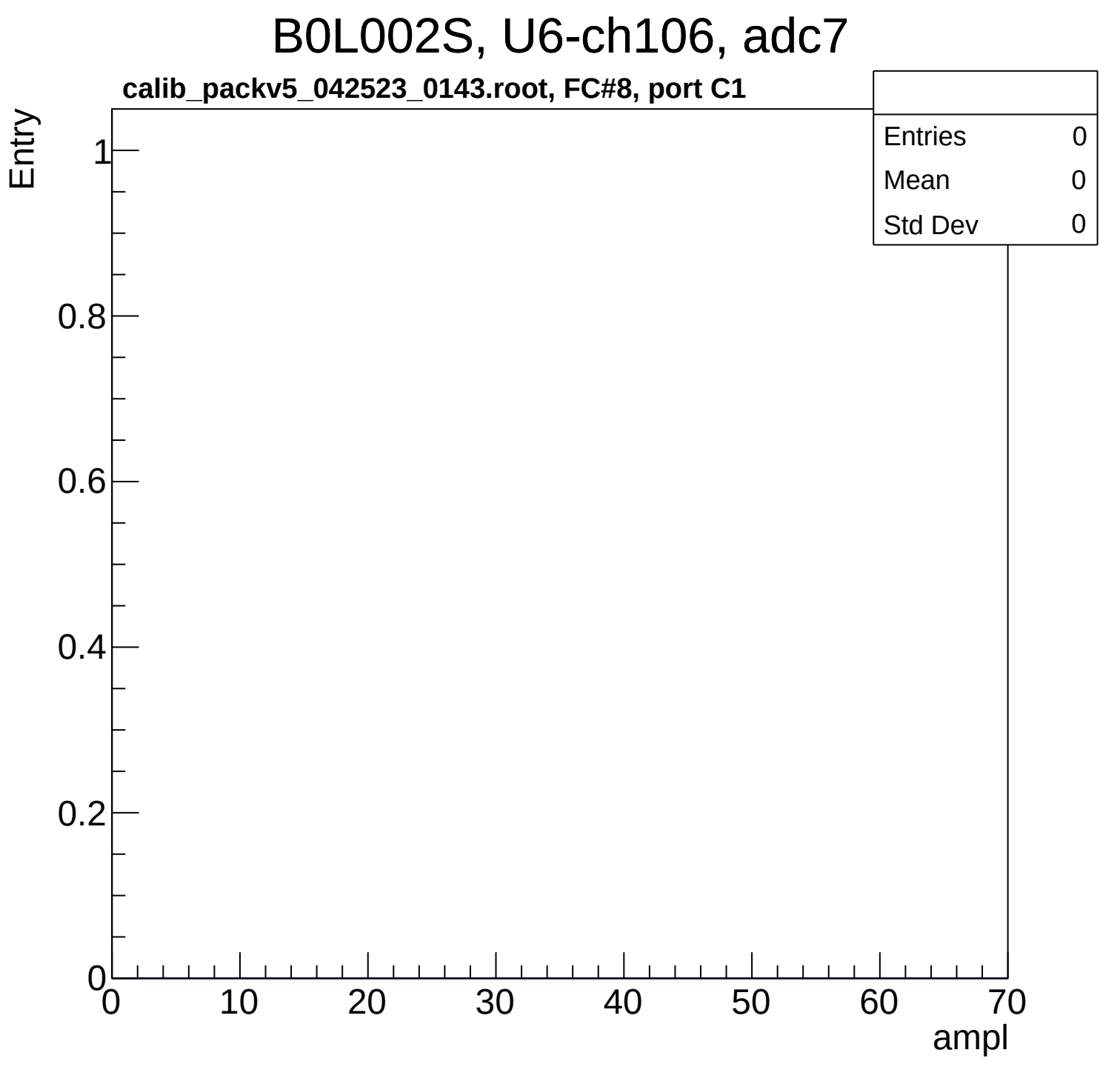
B0L002S, U6-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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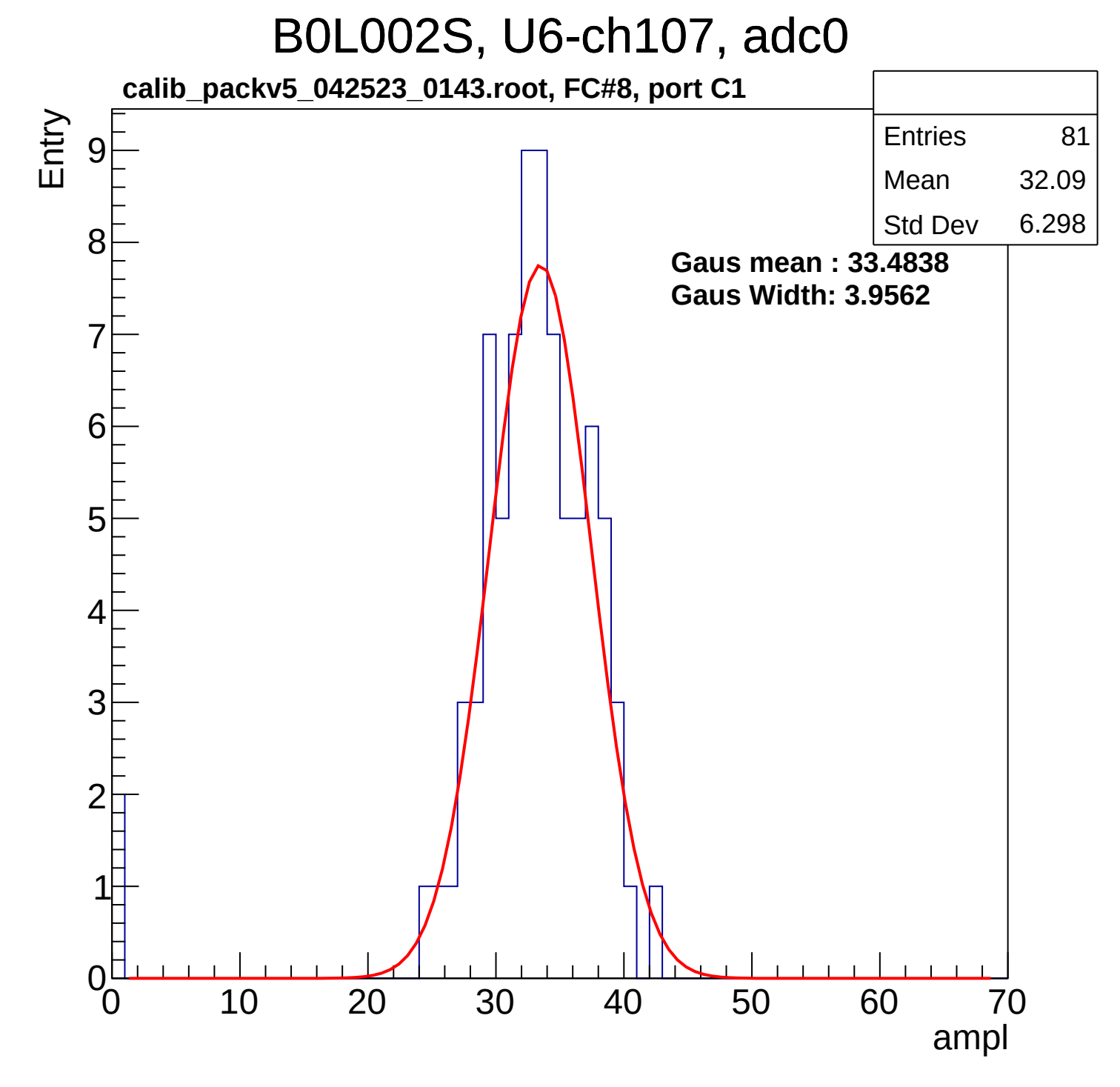
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 32.09
Std Dev 6.298
Gaus mean : 33.4838
Gaus Width: 3.9562
Entry
ampl
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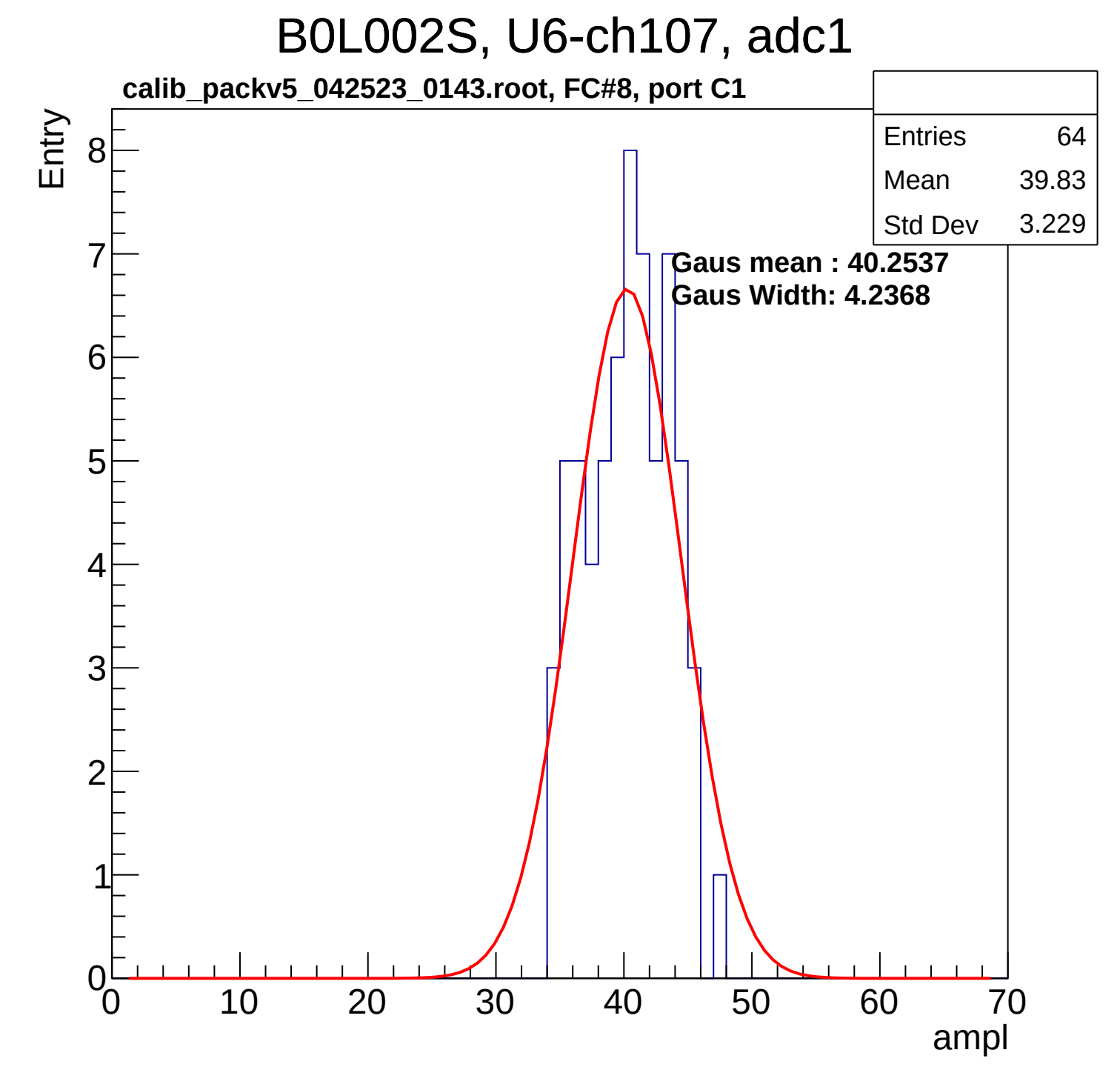
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 39.83
Std Dev 3.229
Gaus mean : 40.2537
Gaus Width: 4.2368
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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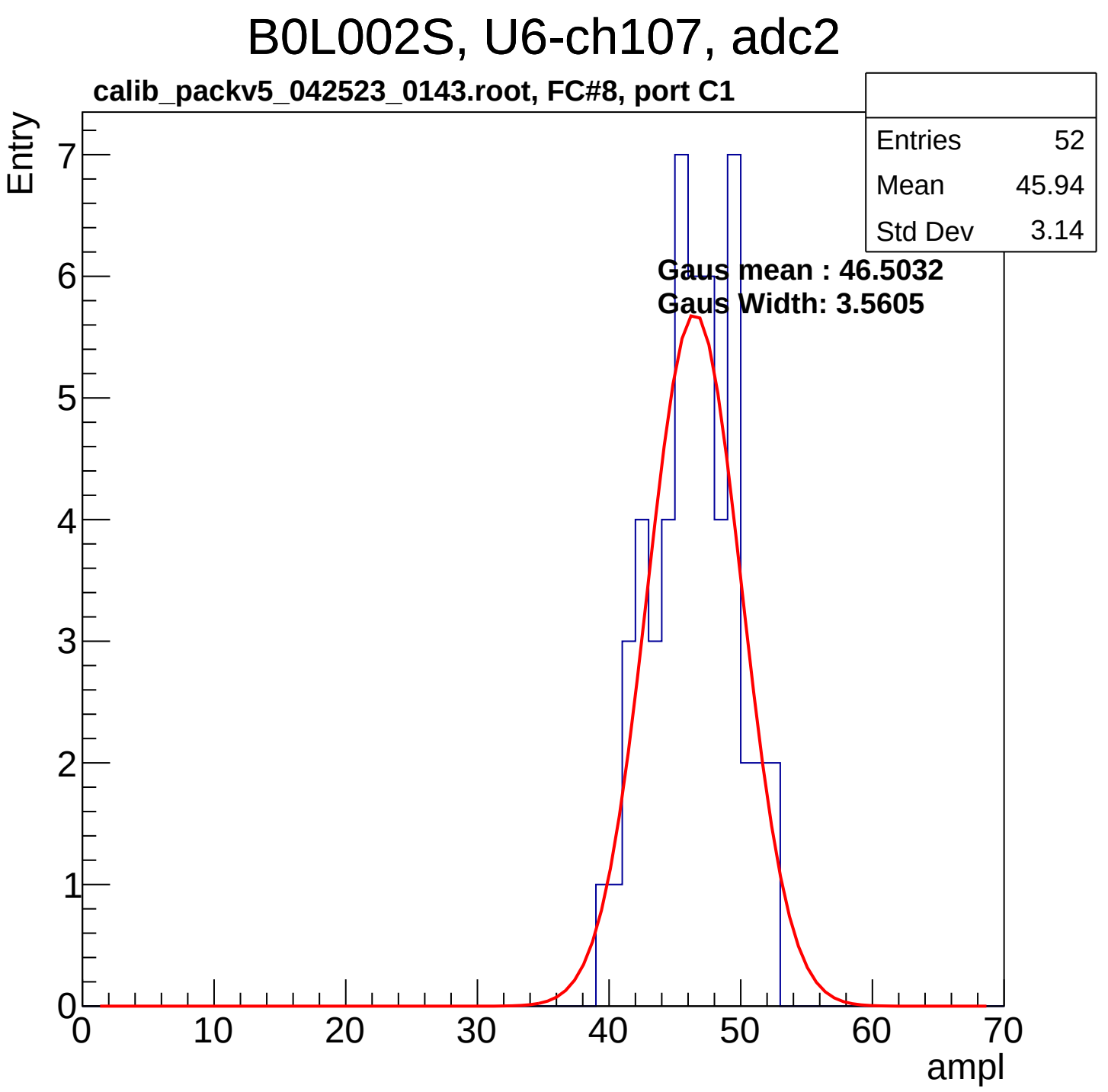
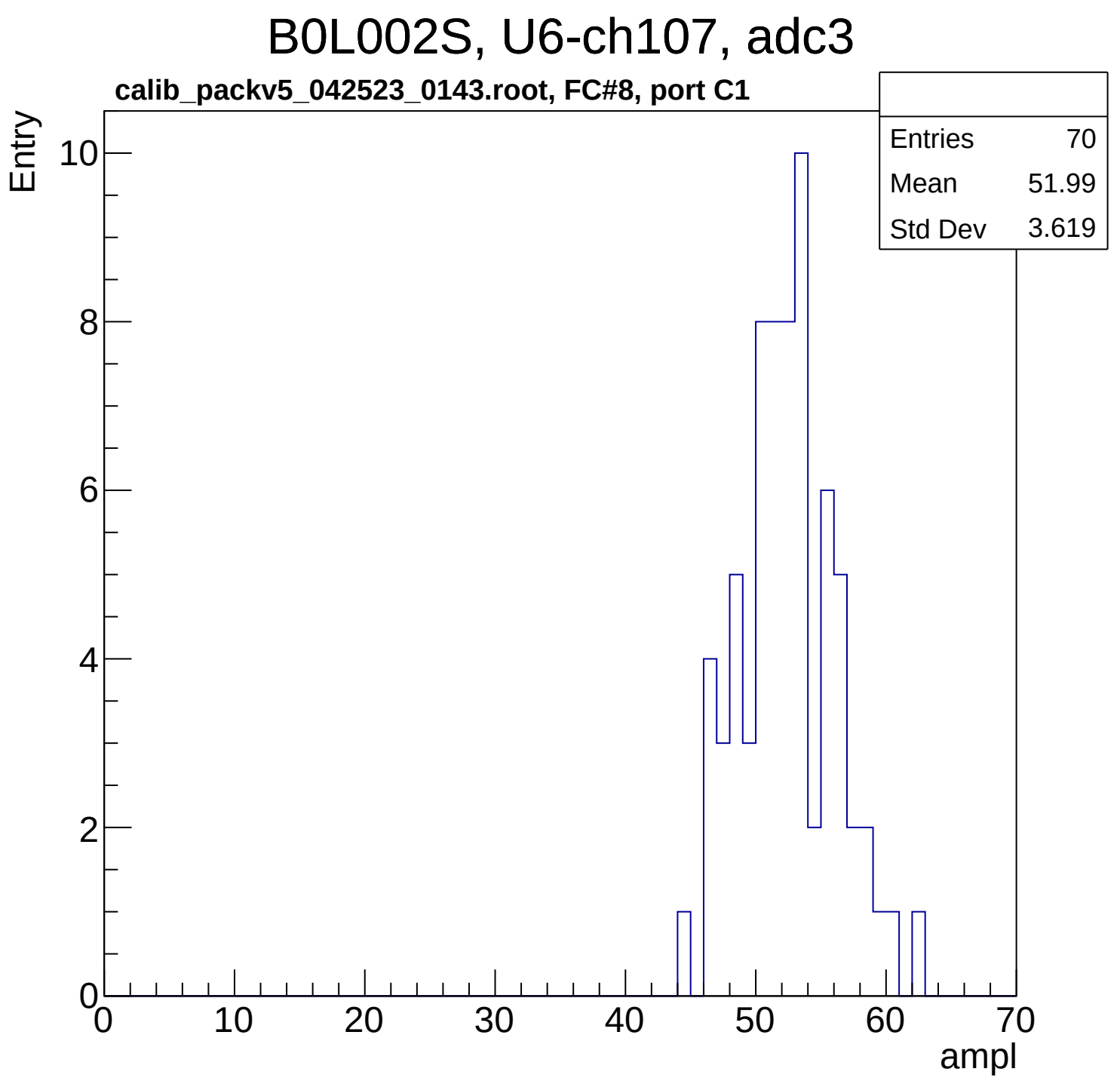
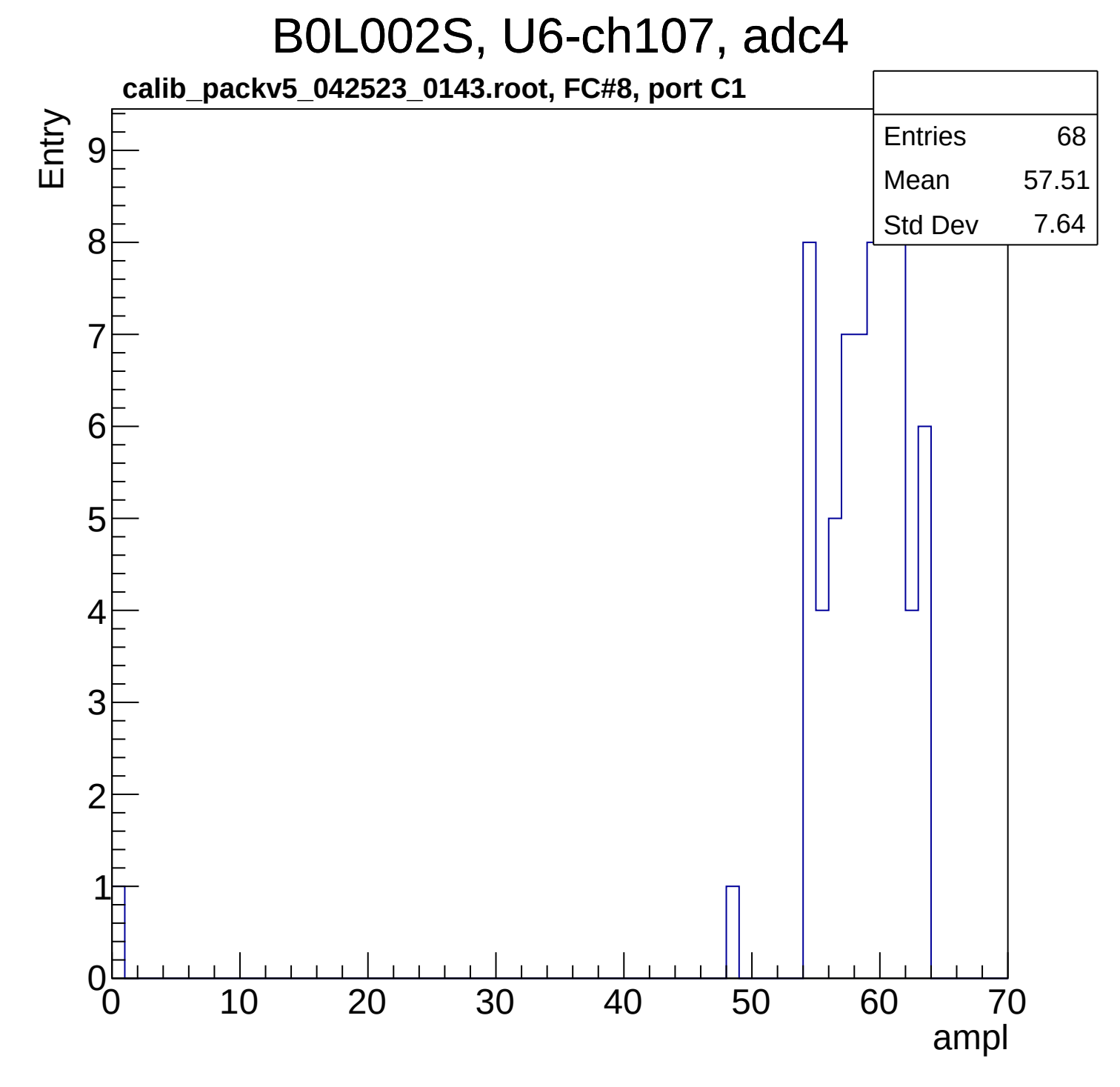

B0L002S, U6-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 45.94
Std Dev 3.14
Gaus mean : 46.5032
Gaus Width: 3.5605
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.99
Std Dev 3.619
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 57.51
Std Dev 7.64
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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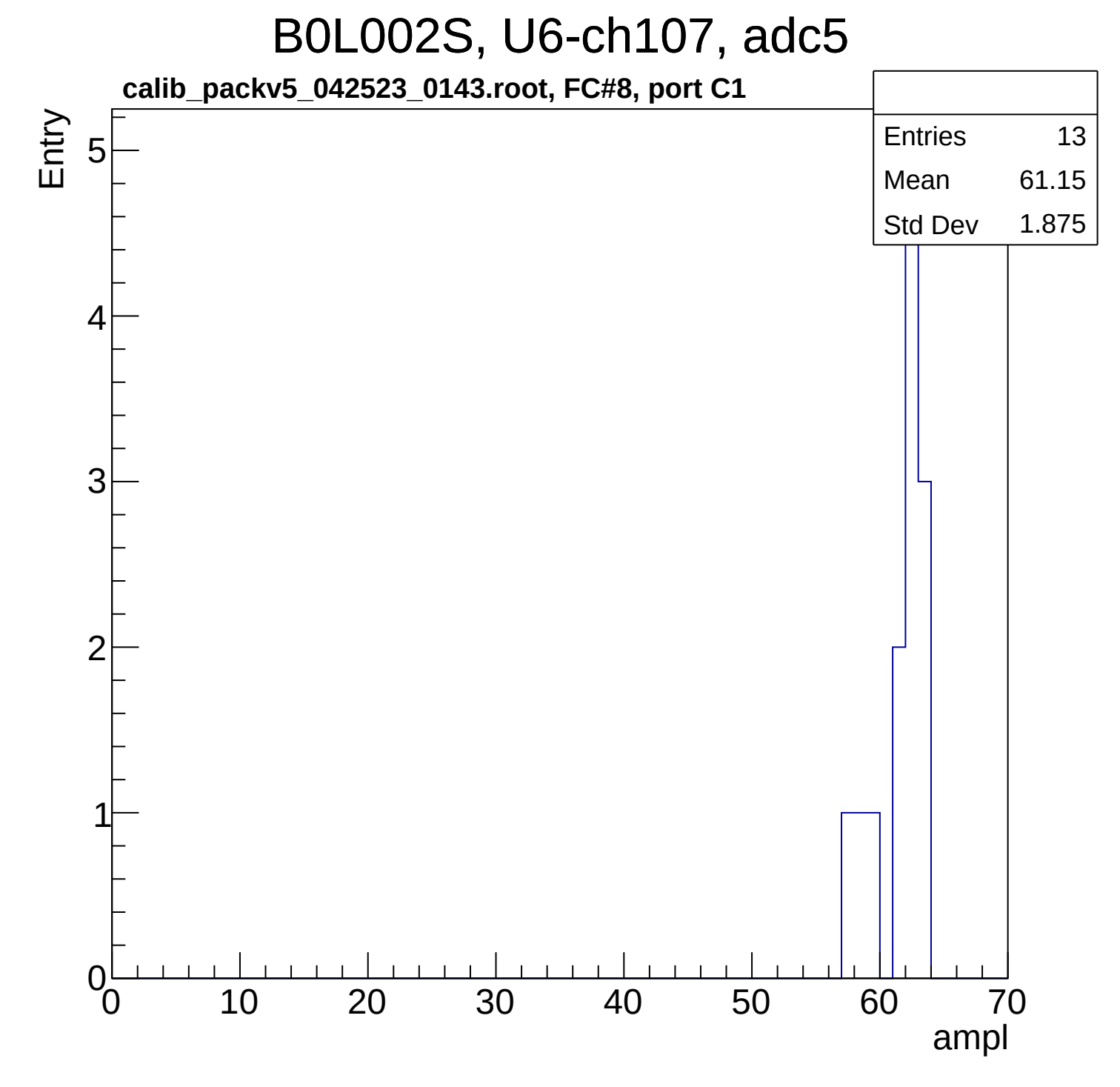
40
50
60
70


B0L002S, U6-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 61.15
Std Dev 1.875
Entry
ampl
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2
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60
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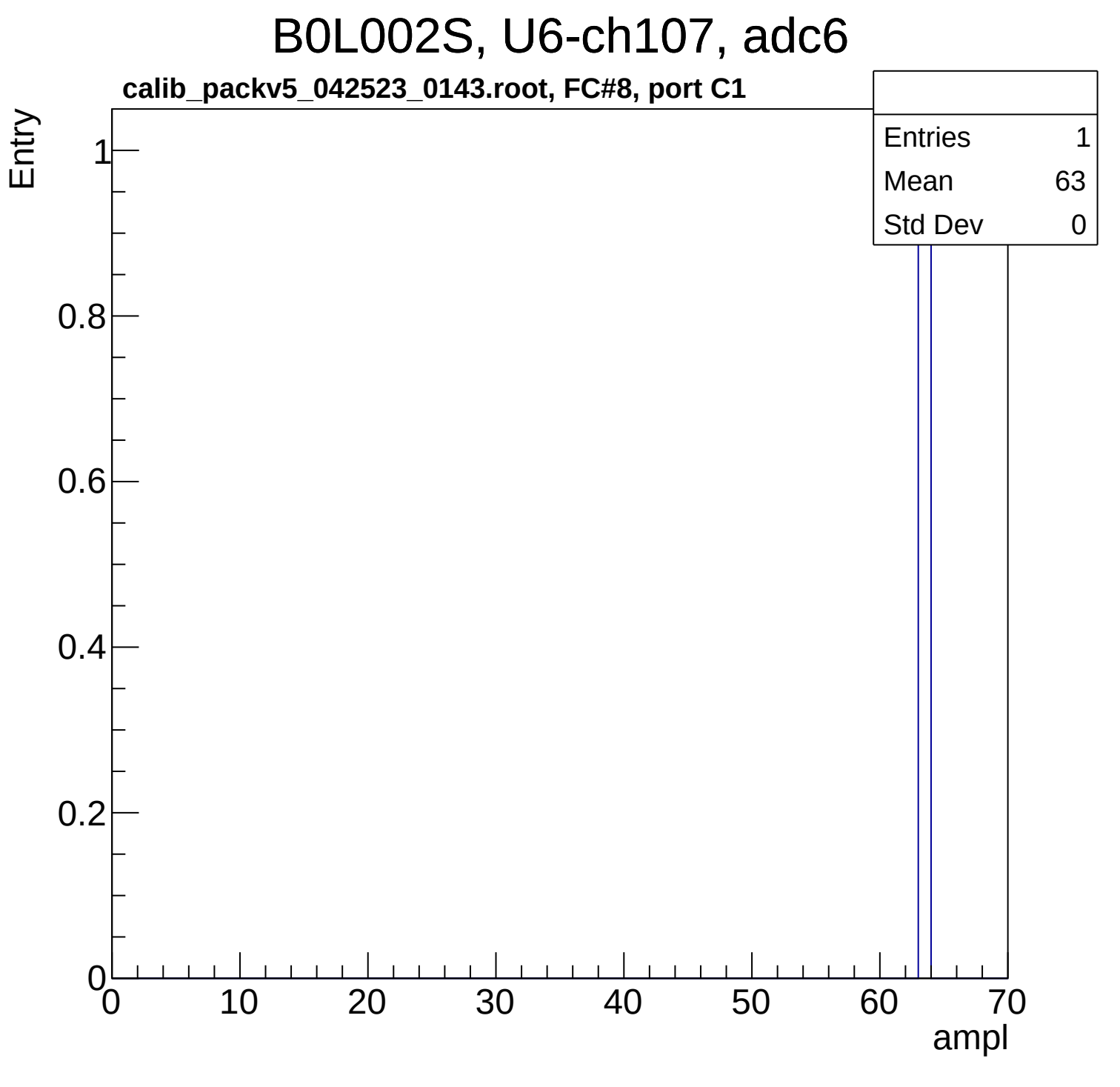
B0L002S, U6-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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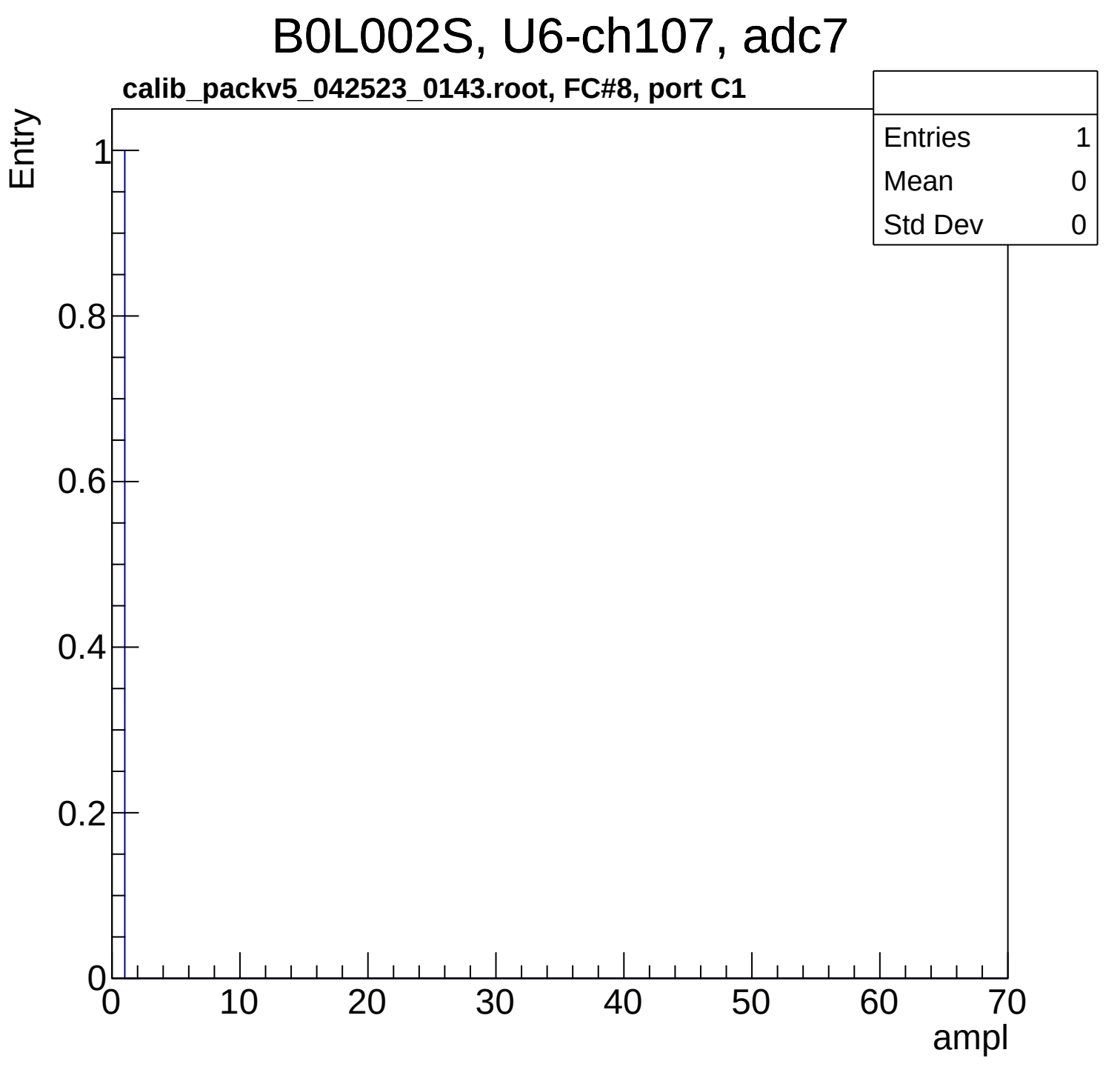
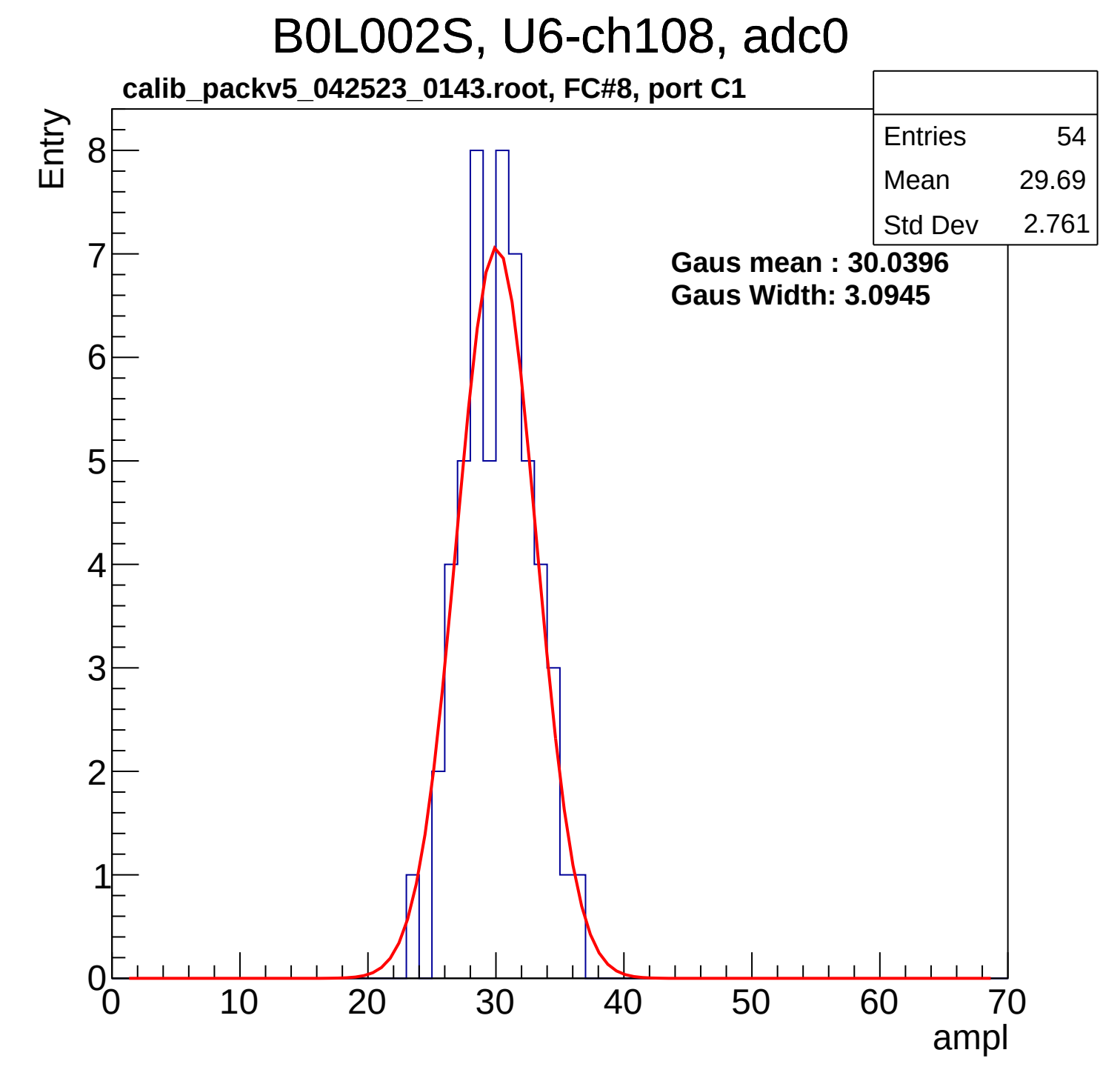
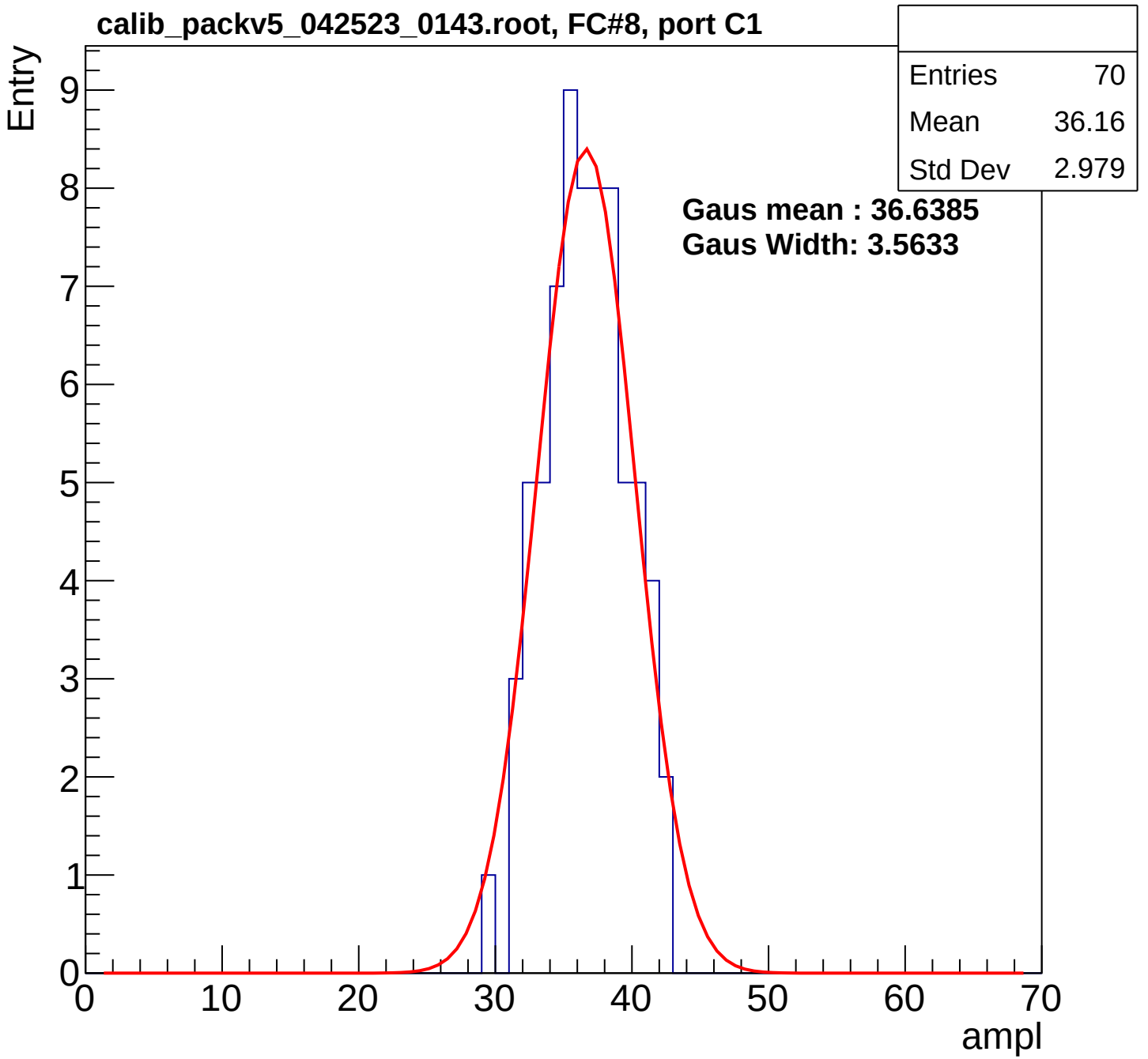
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 29.69
Std Dev 2.761
Gaus mean : 30.0396
Gaus Width: 3.0945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.16
Std Dev 2.979
Gaus mean : 36.6385
Gaus Width: 3.5633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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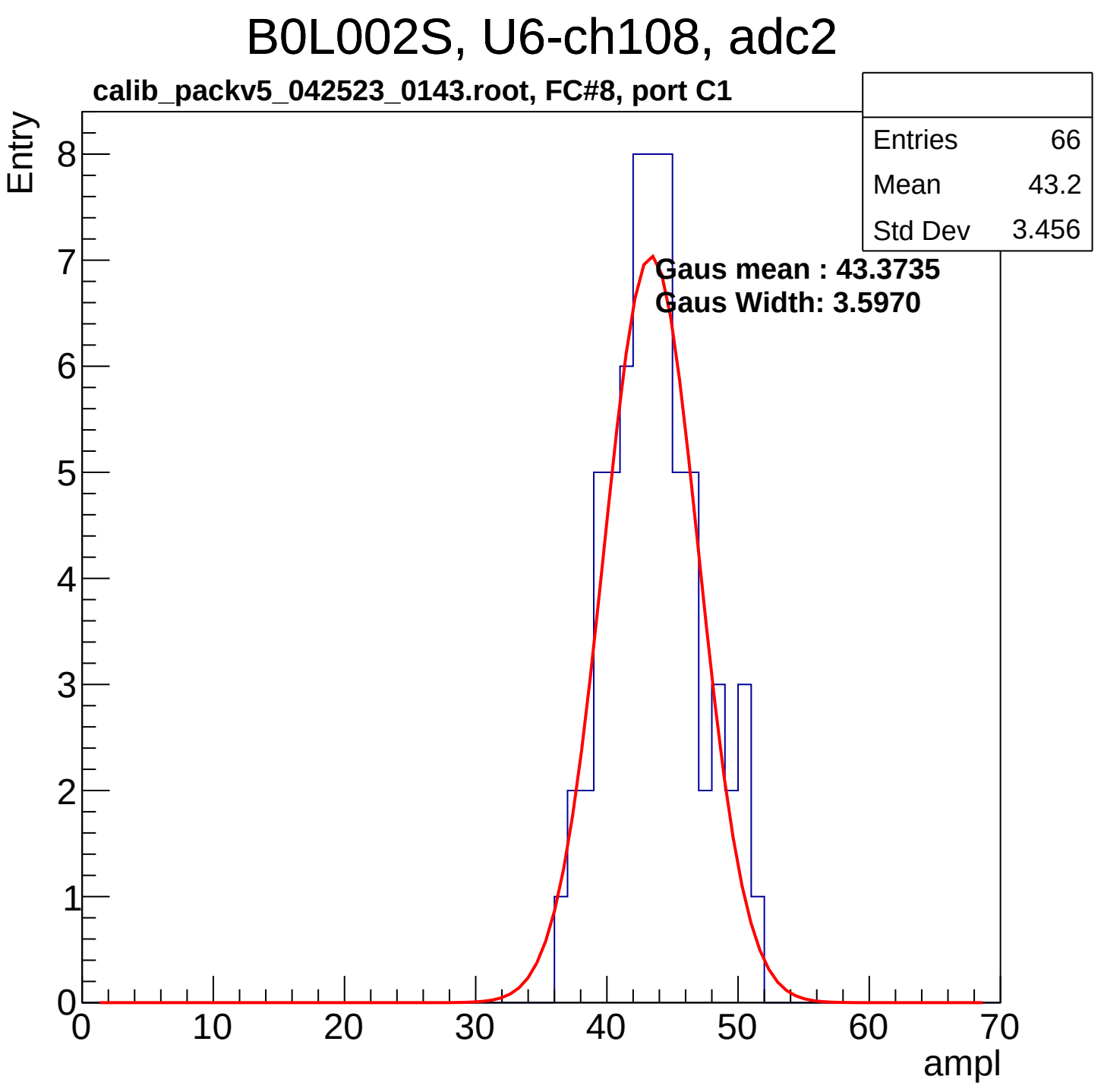
B0L002S, U6-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.2
Std Dev 3.456
Gaus mean : 43.3735
Gaus Width: 3.5970
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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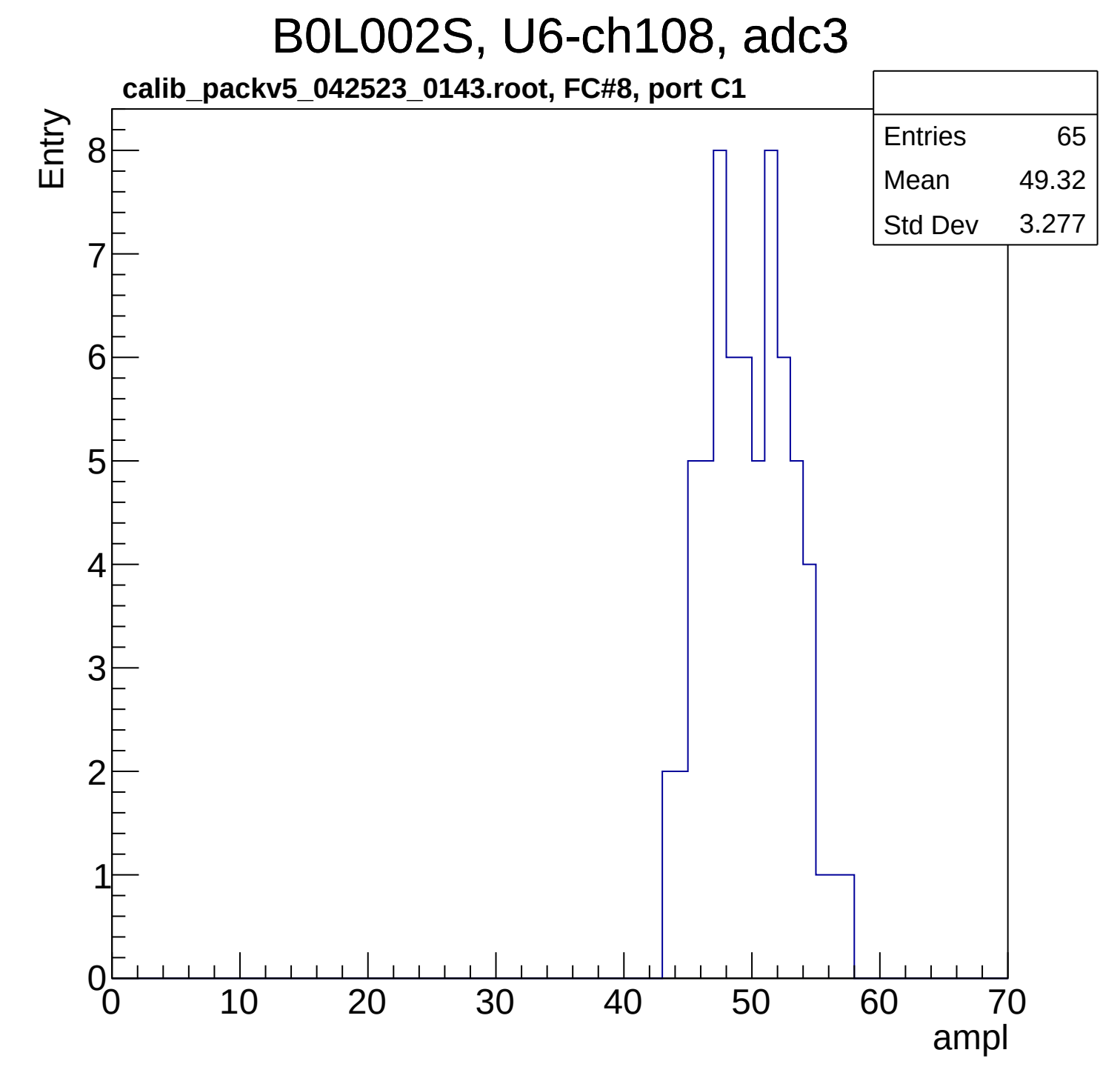
B0L002S, U6-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.32
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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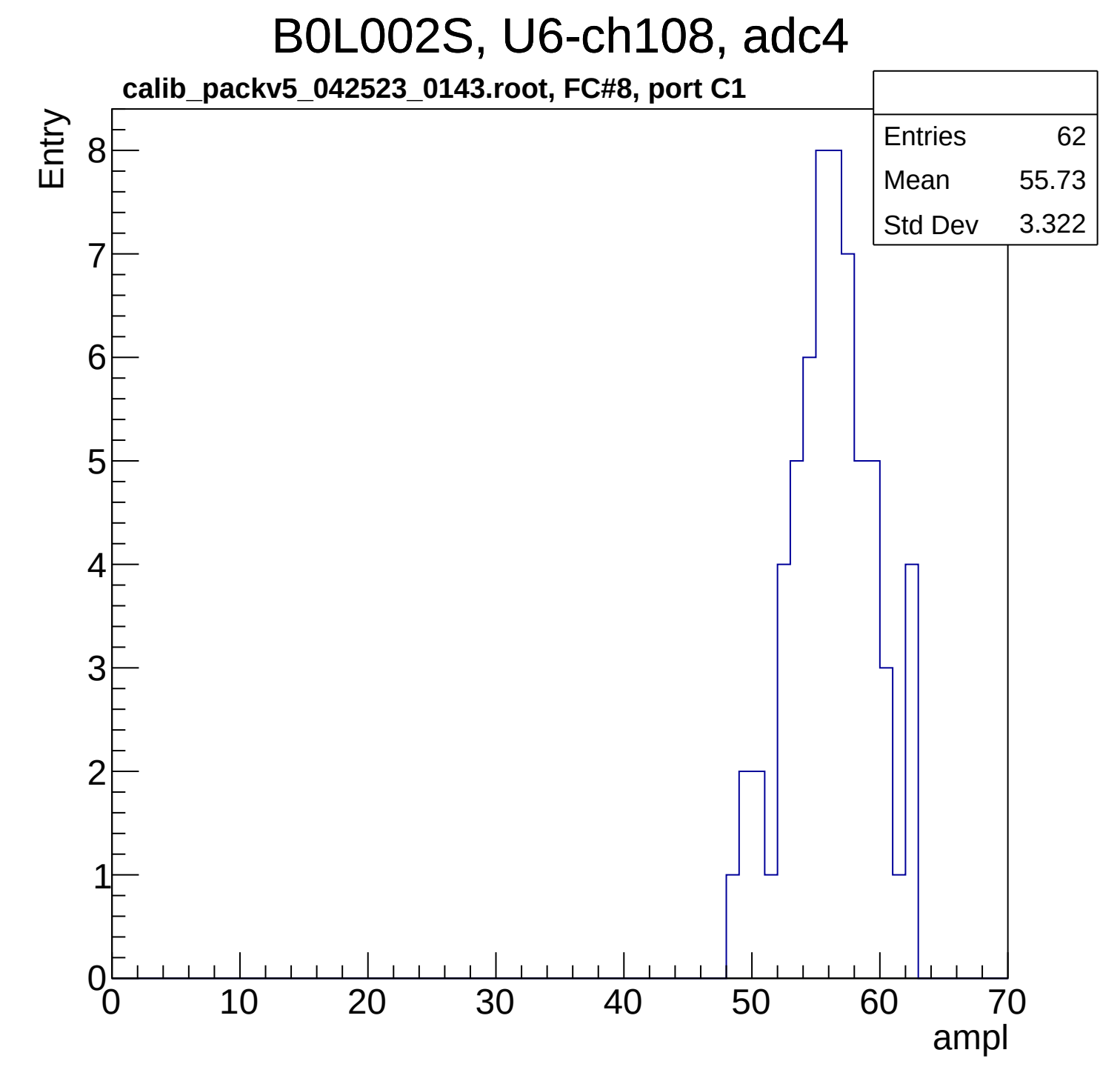
B0L002S, U6-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 55.73
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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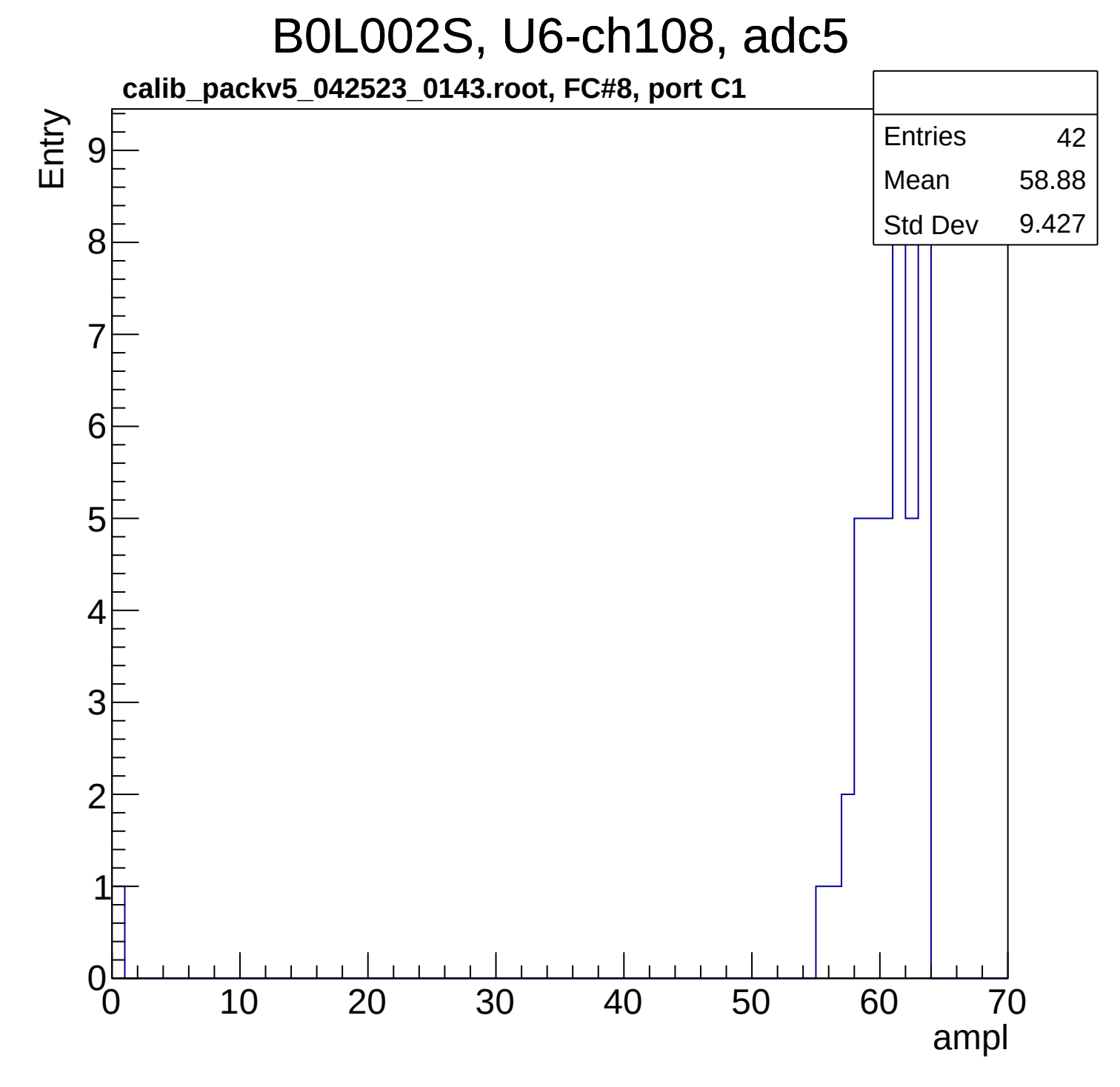
B0L002S, U6-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.88
Std Dev 9.427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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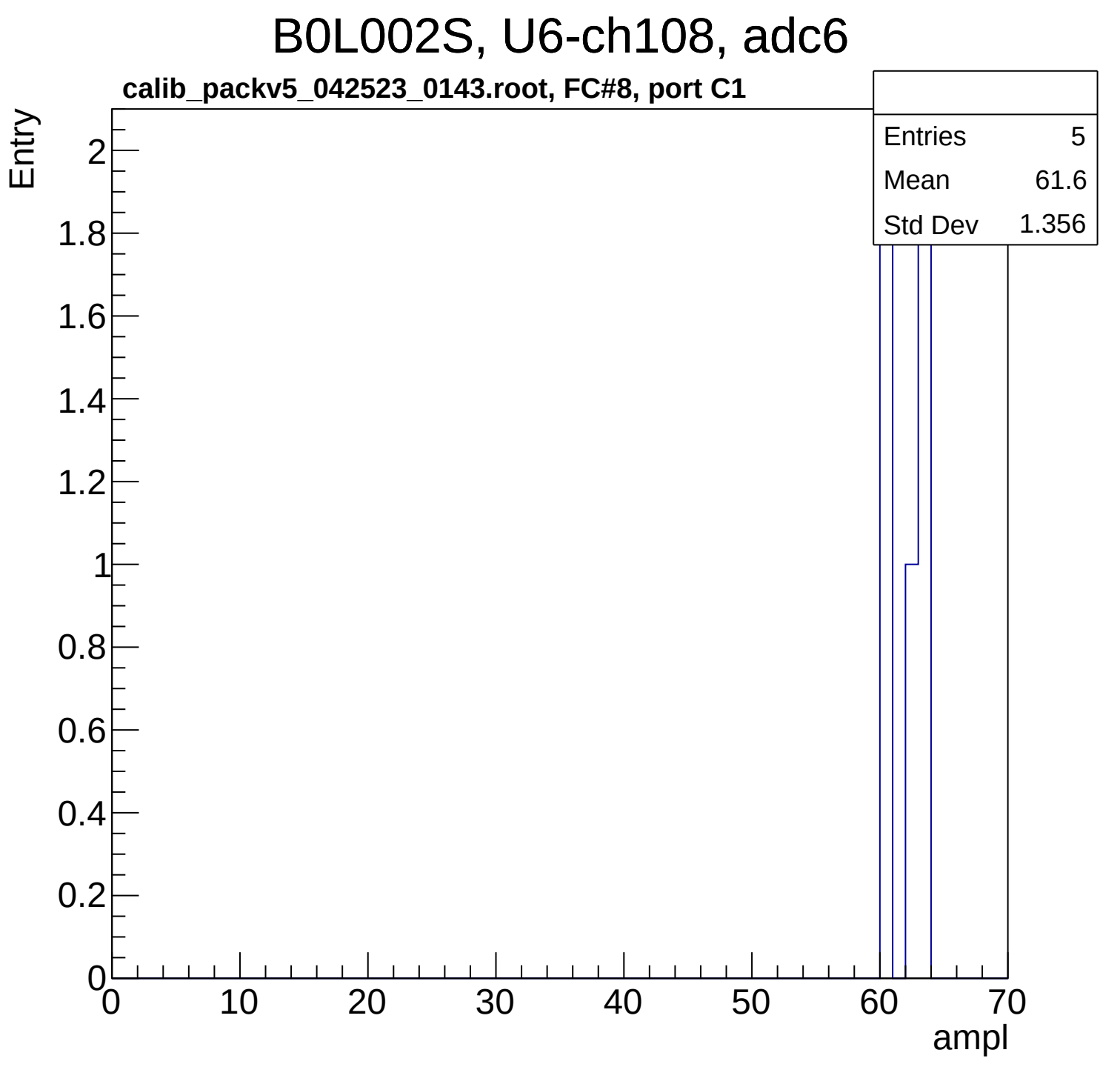
B0L002S, U6-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.6
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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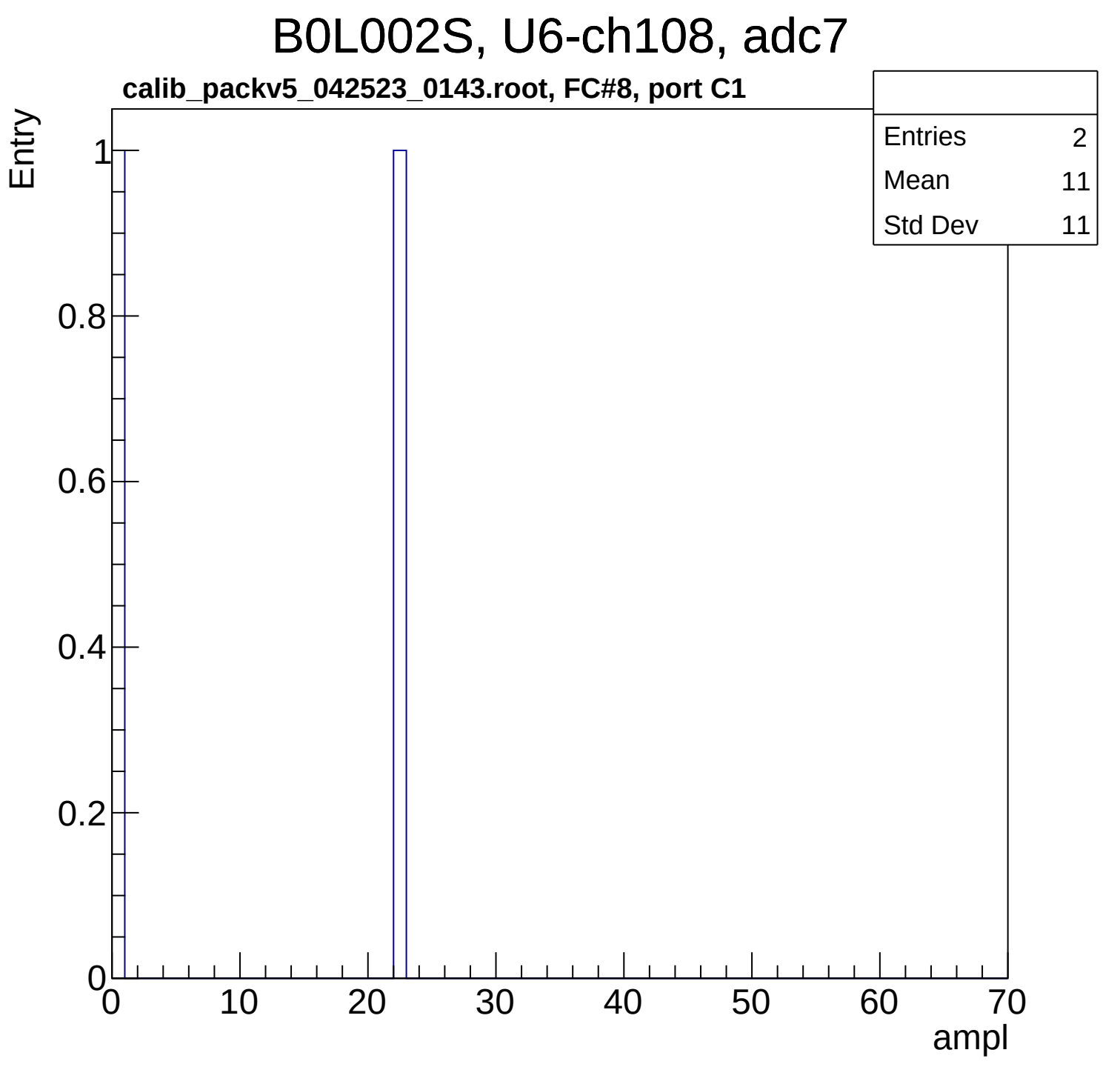
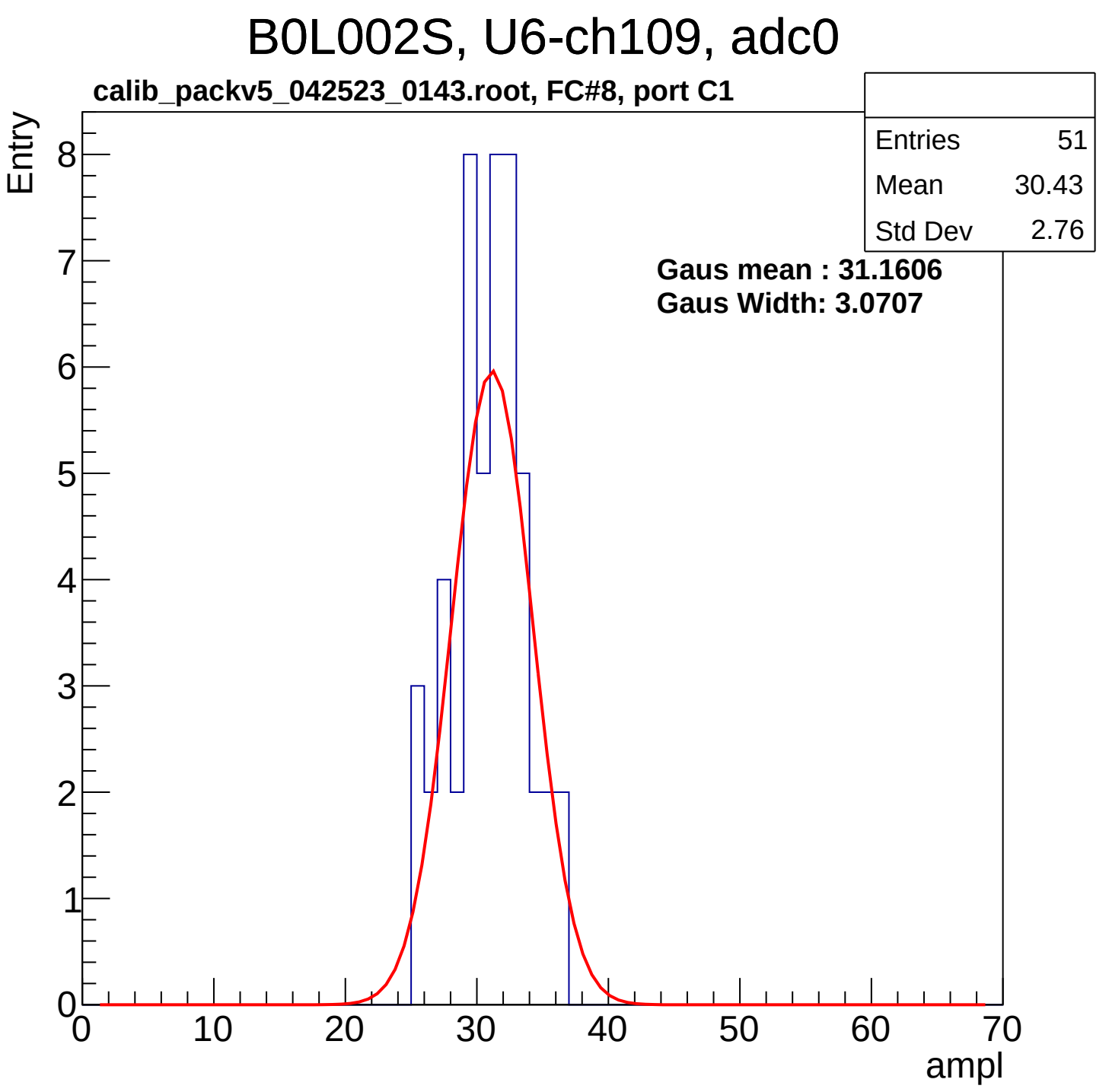
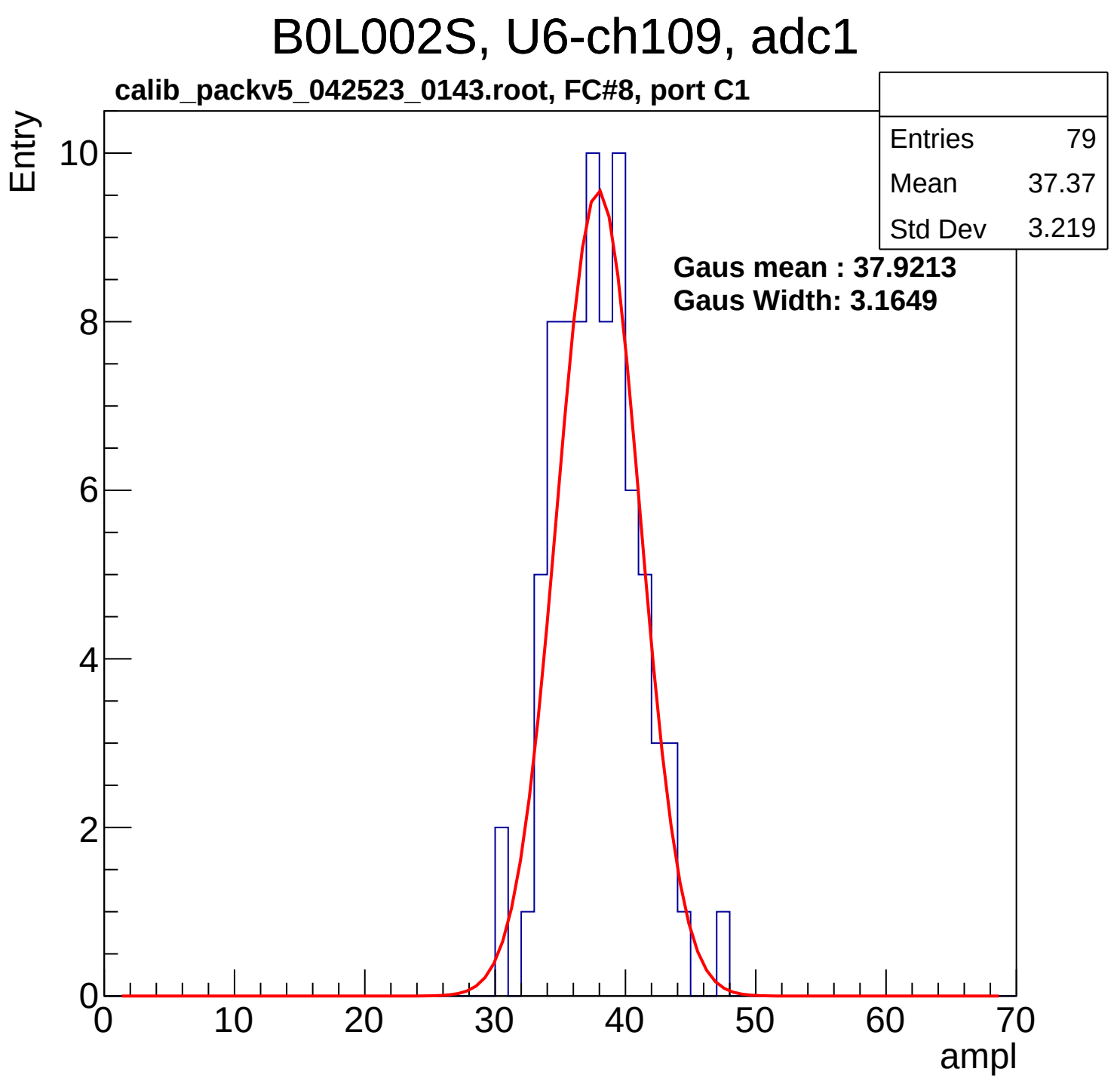
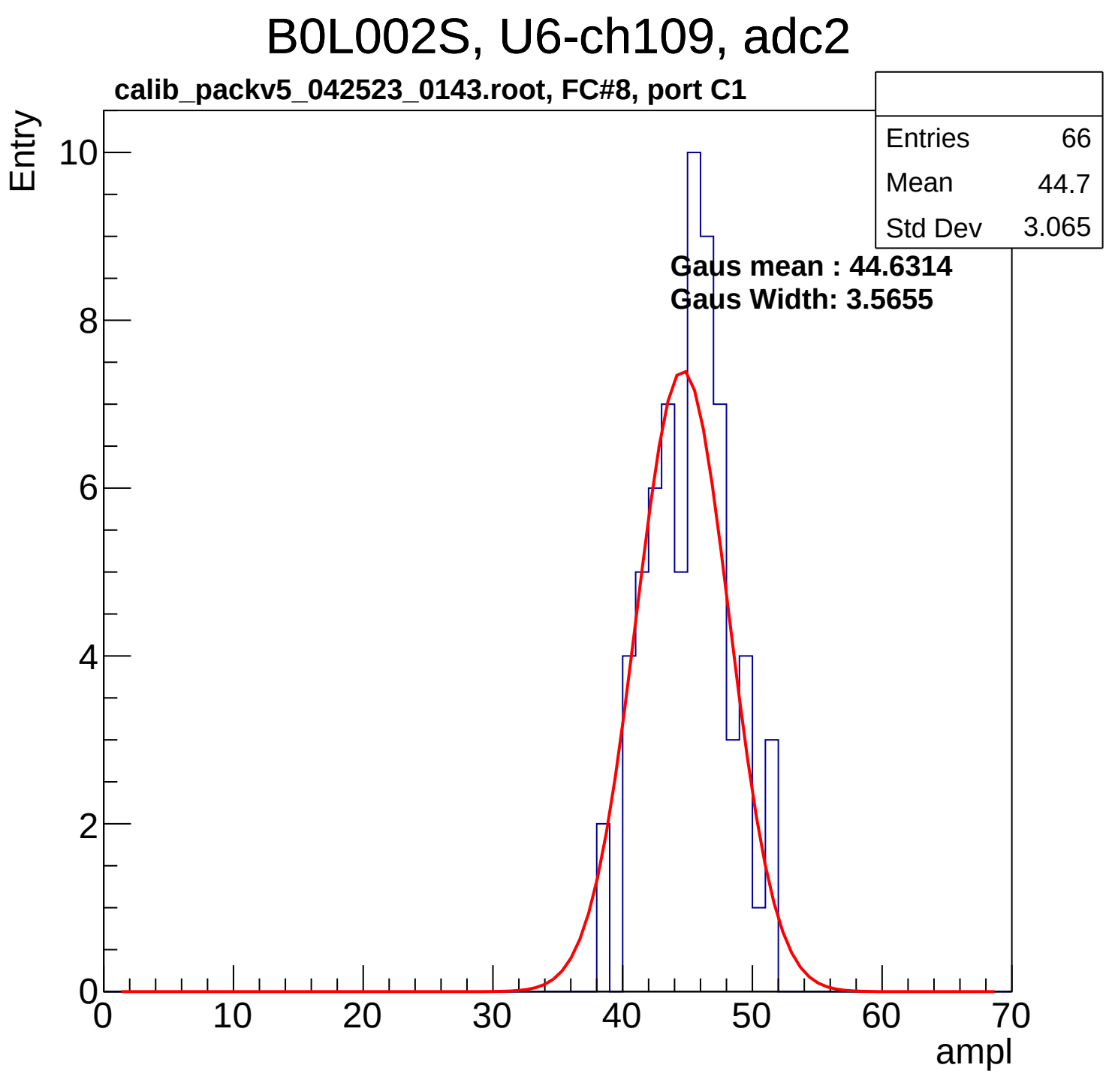
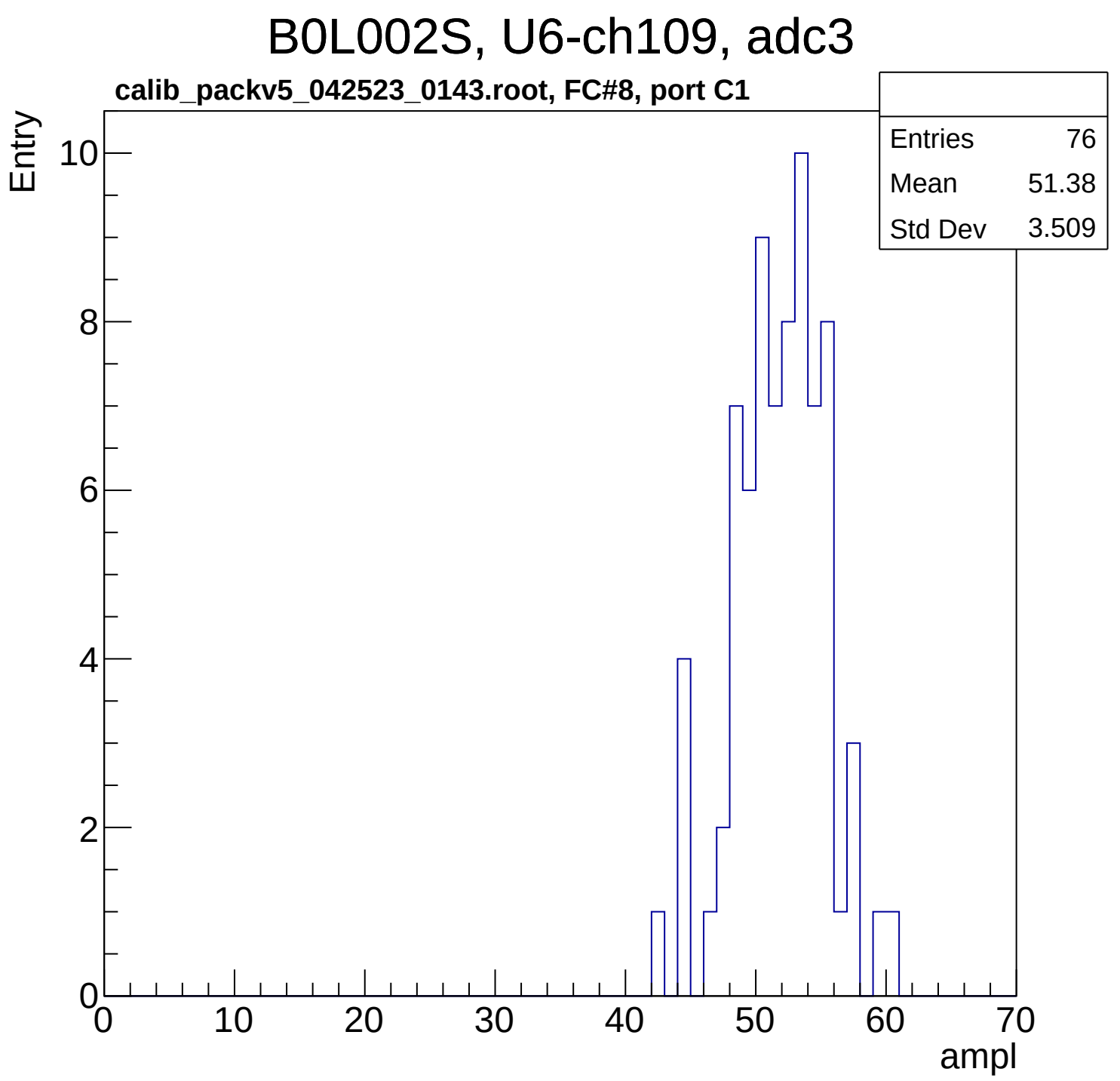
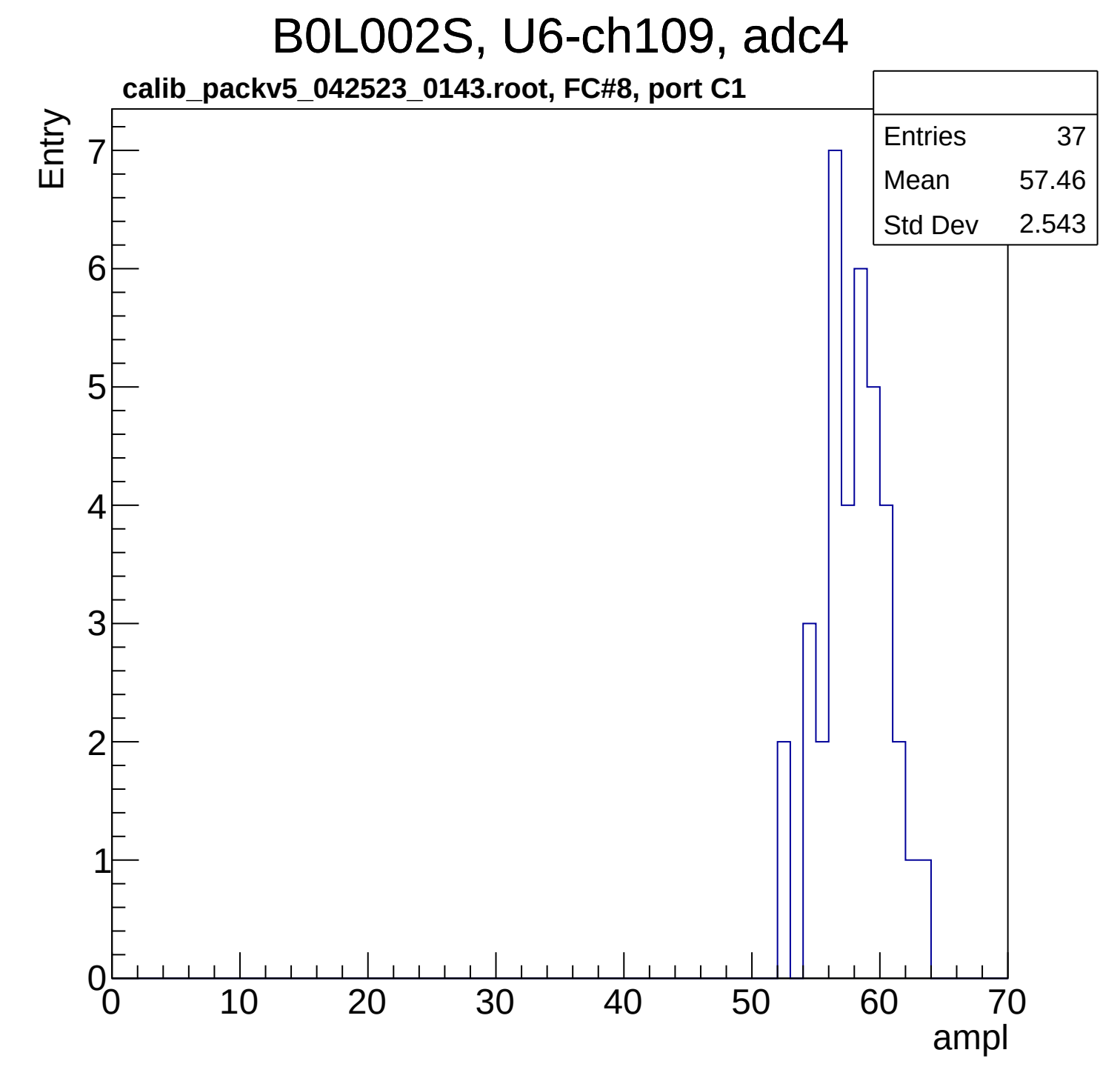

B0L002S, U6-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

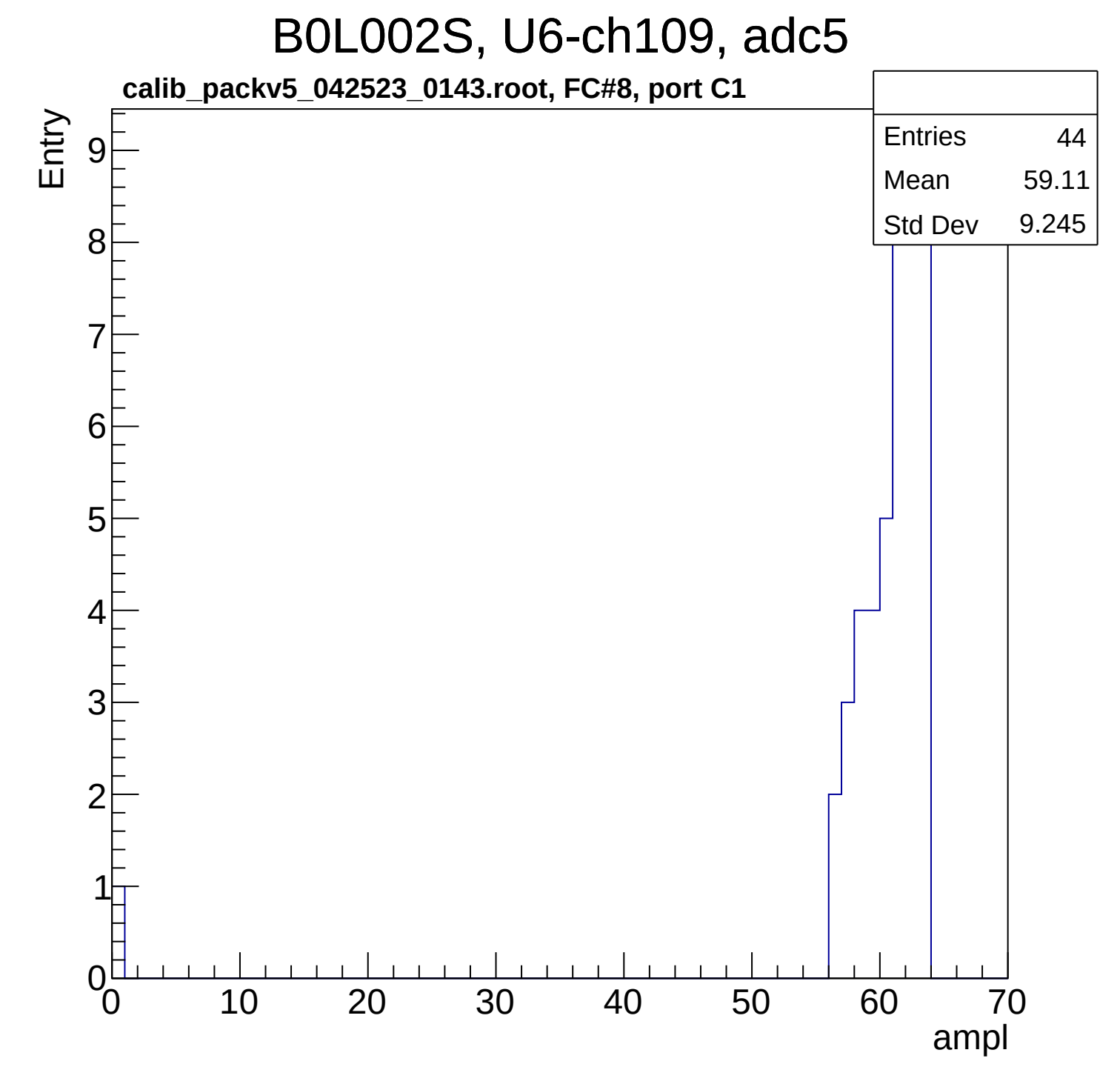

B0L002S, U6-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 30.43
Std Dev 2.76
Gaus mean : 31.1606
Gaus Width: 3.0707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

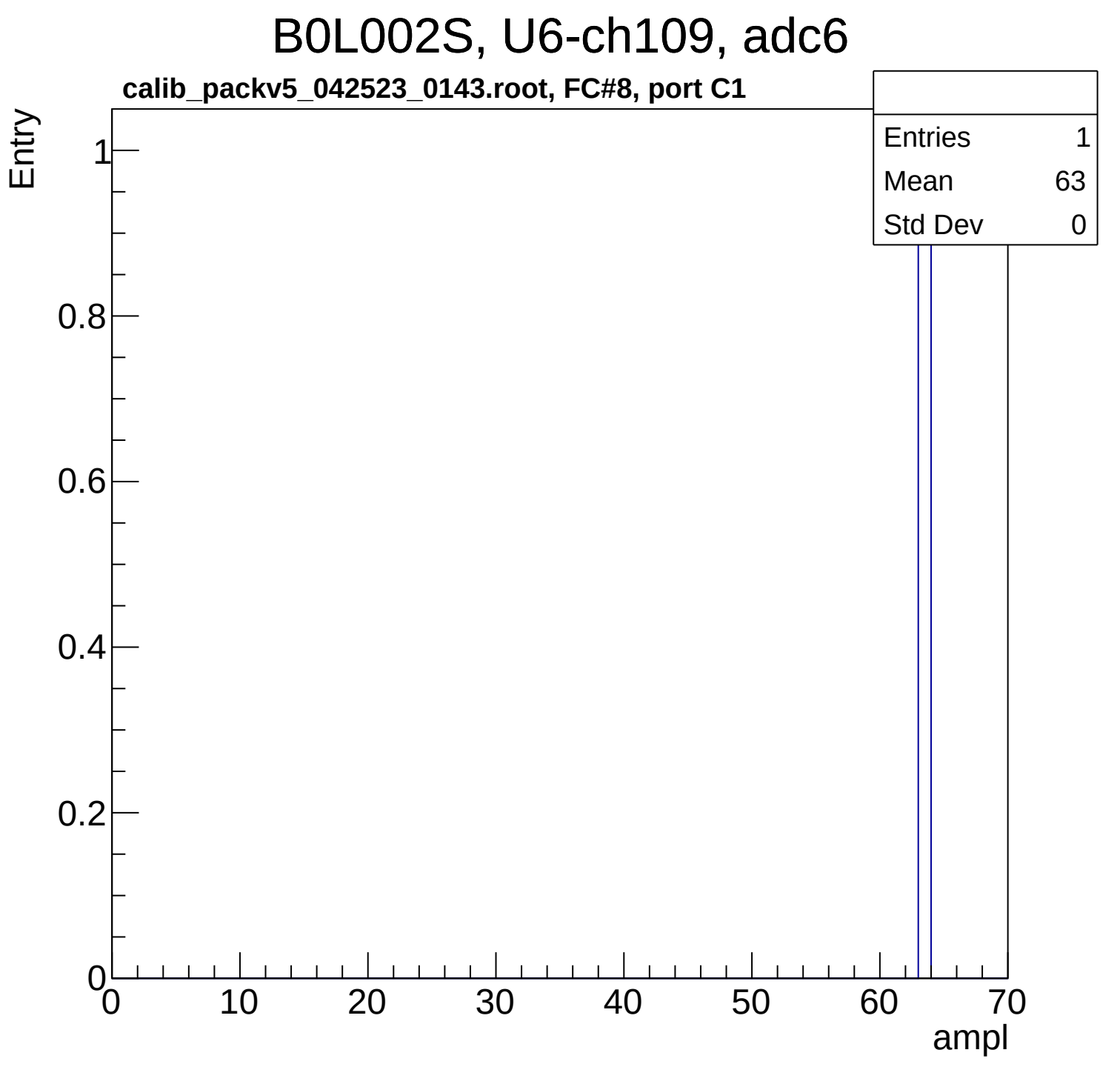

B0L002S, U6-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 37.37
Std Dev 3.219
Gaus mean : 37.9213
Gaus Width: 3.1649
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

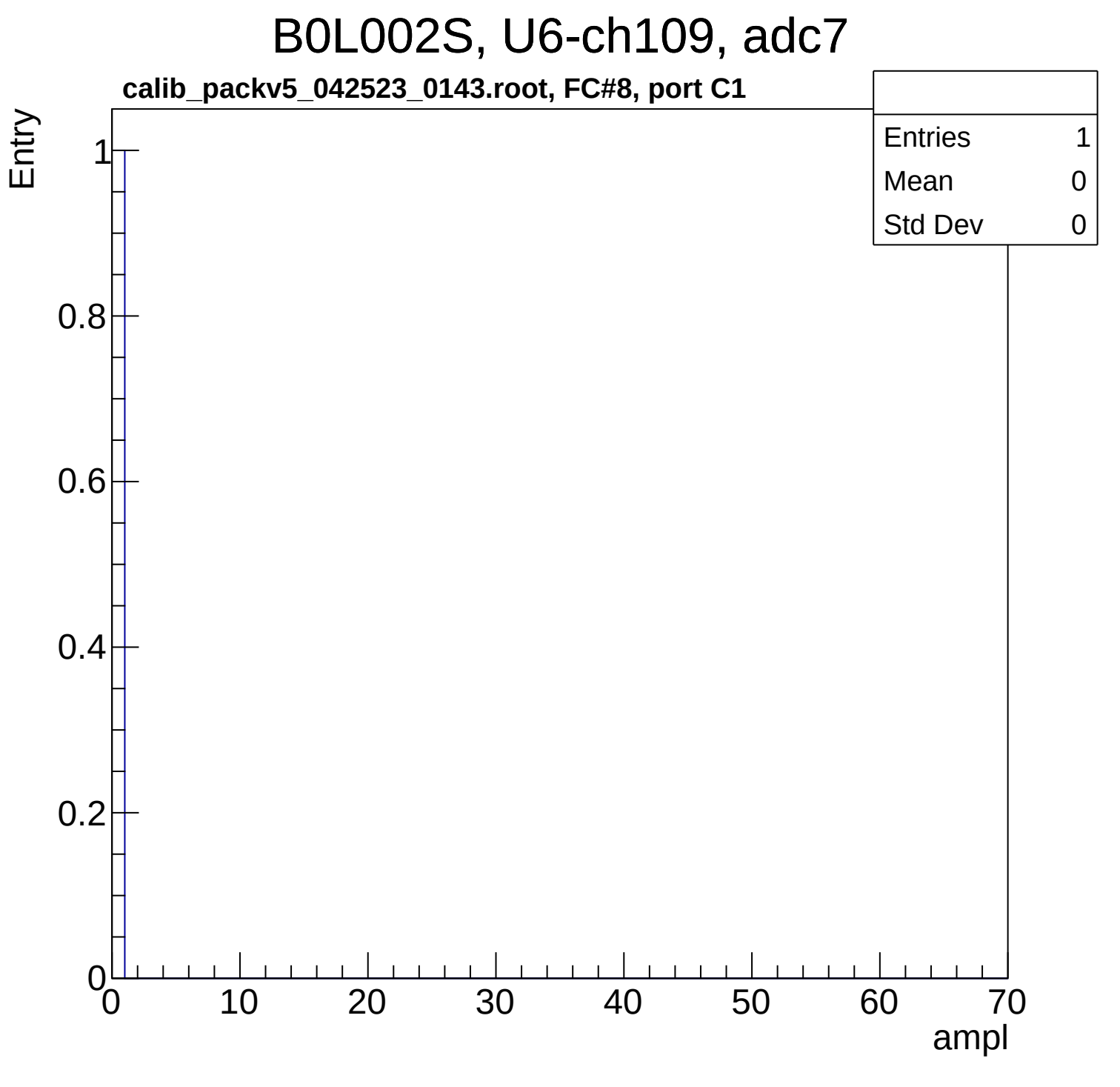

B0L002S, U6-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.7
Std Dev 3.065
Gaus mean : 44.6314
Gaus Width: 3.5655
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

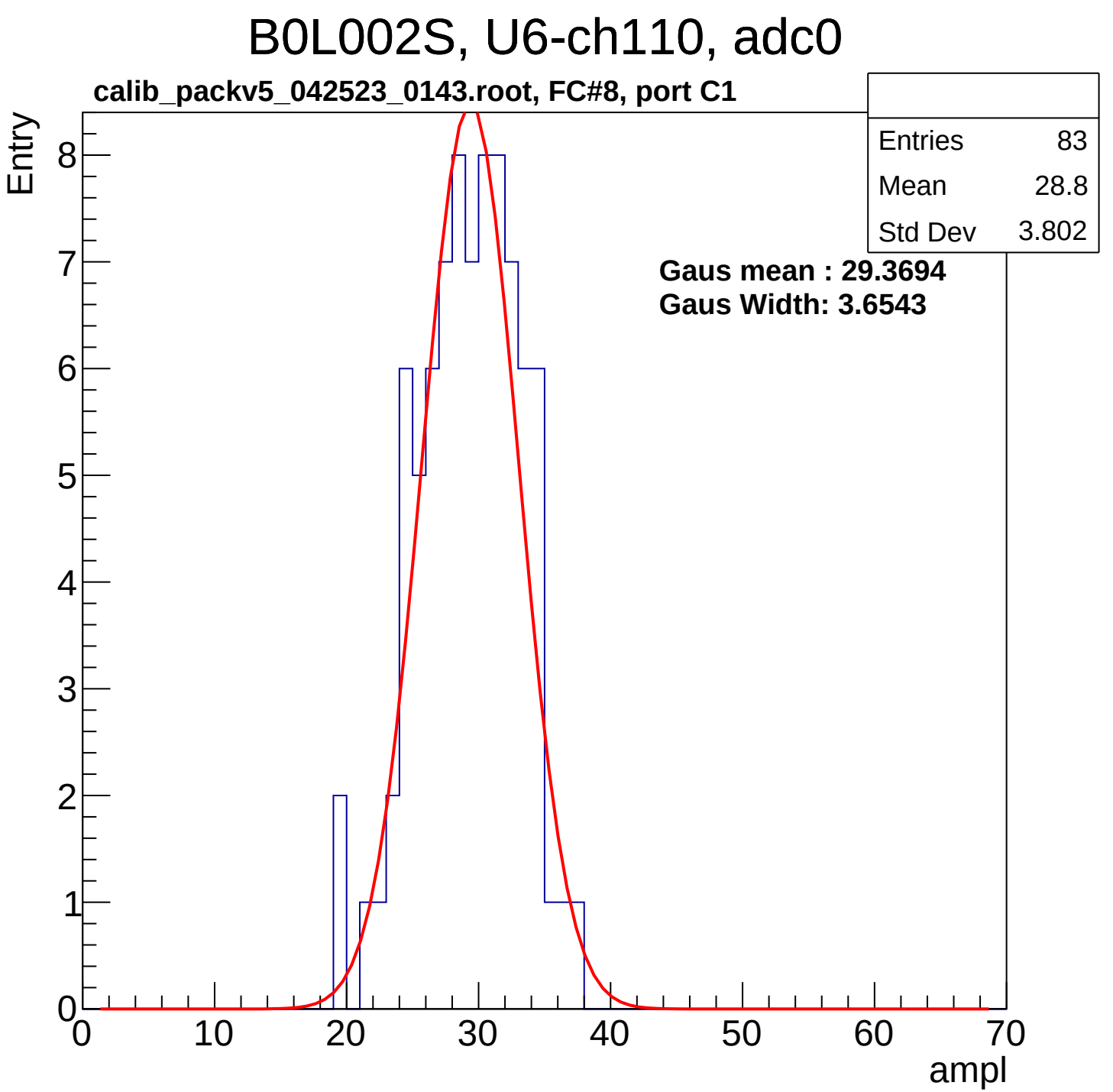

B0L002S, U6-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.38
Std Dev 3.509
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 57.46
Std Dev 2.543
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.11
Std Dev 9.245
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 28.8
Std Dev 3.802
Gaus mean : 29.3694
Gaus Width: 3.6543
Entry
ampl
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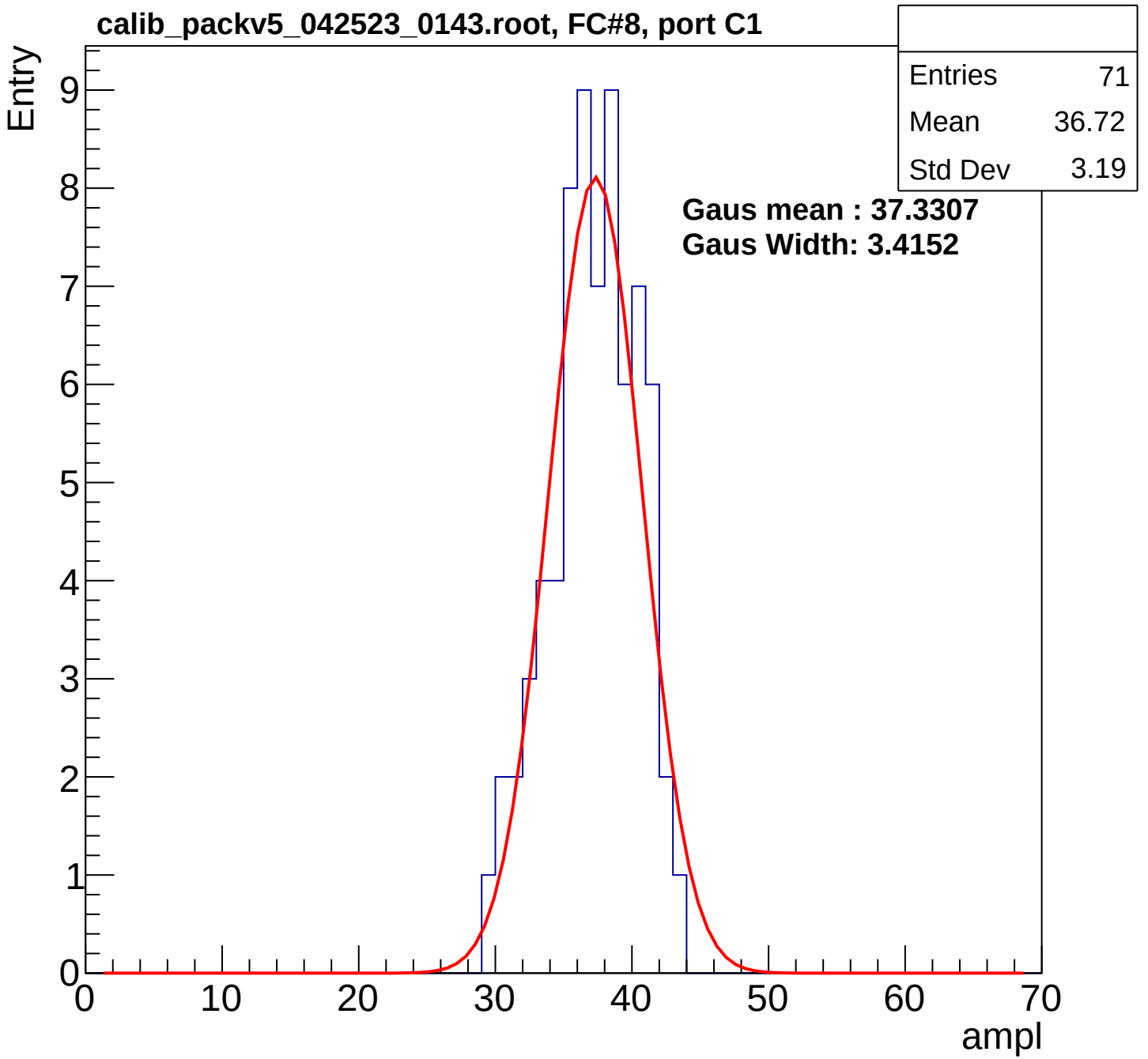
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.72
Std Dev 3.19
Gaus mean : 37.3307
Gaus Width: 3.4152
Entry
ampl
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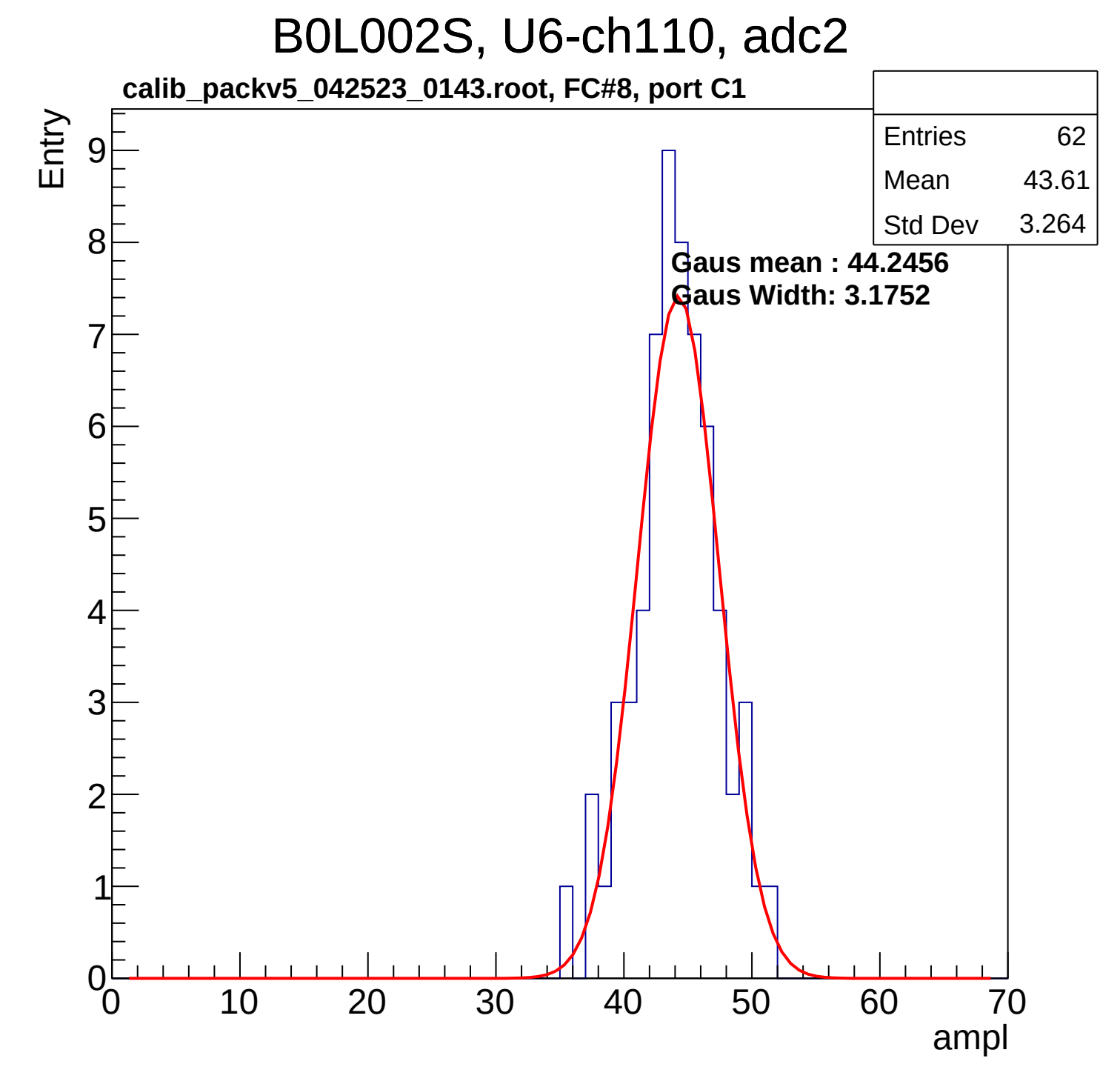
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 43.61
Std Dev 3.264
Gaus mean : 44.2456
Gaus Width: 3.1752
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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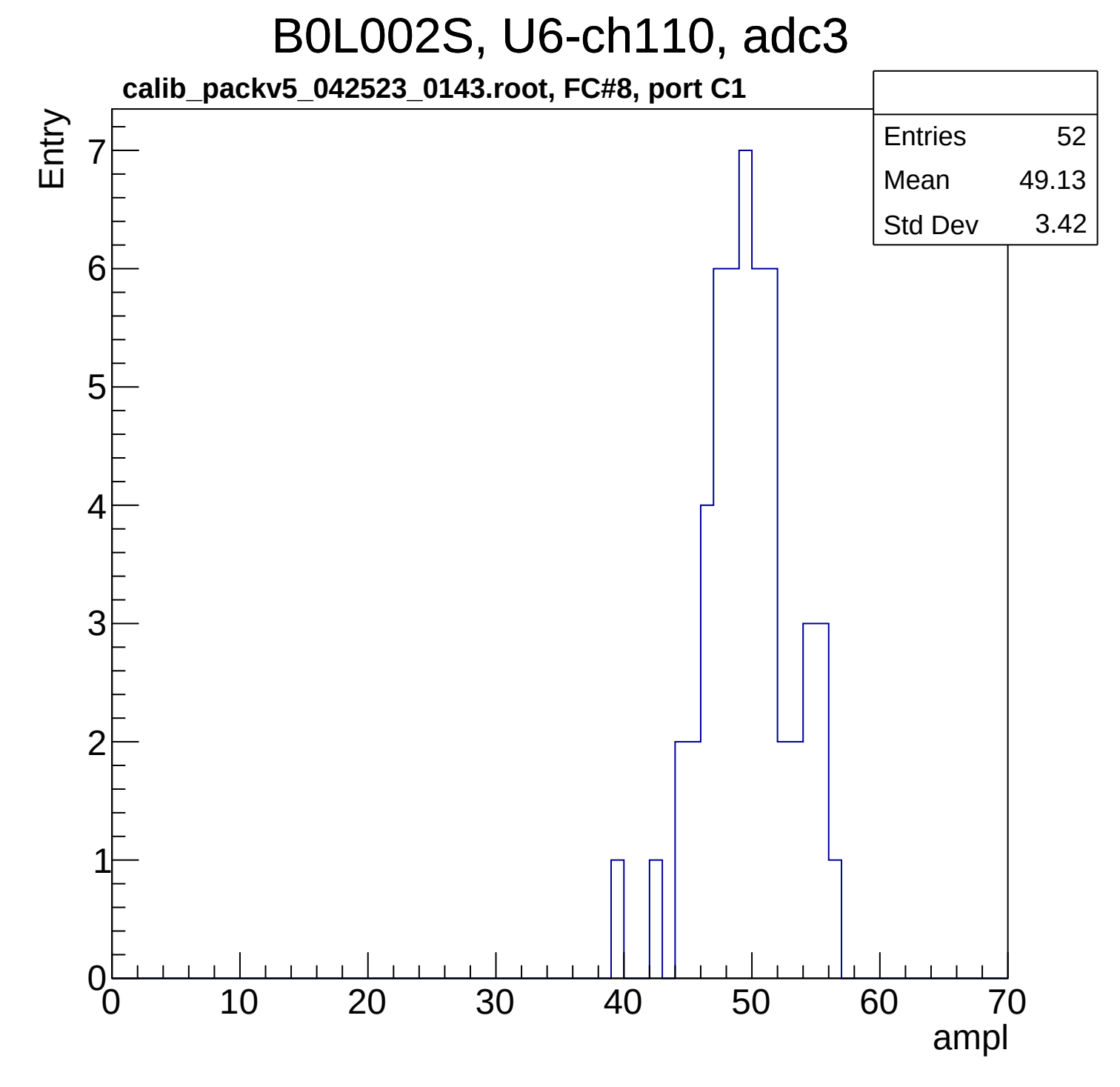
B0L002S, U6-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 49.13
Std Dev 3.42
Entry
ampl
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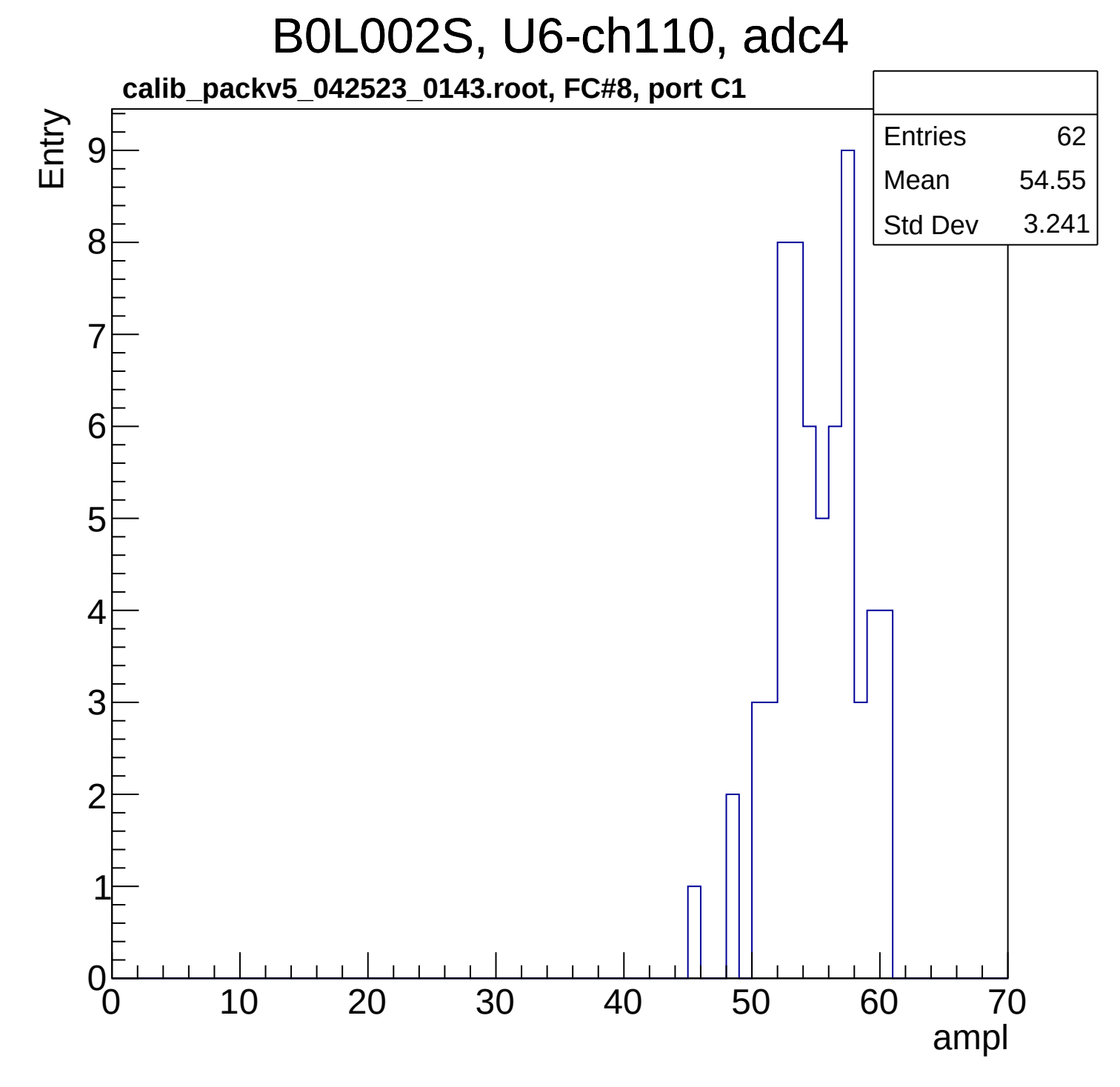
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 54.55
Std Dev 3.241
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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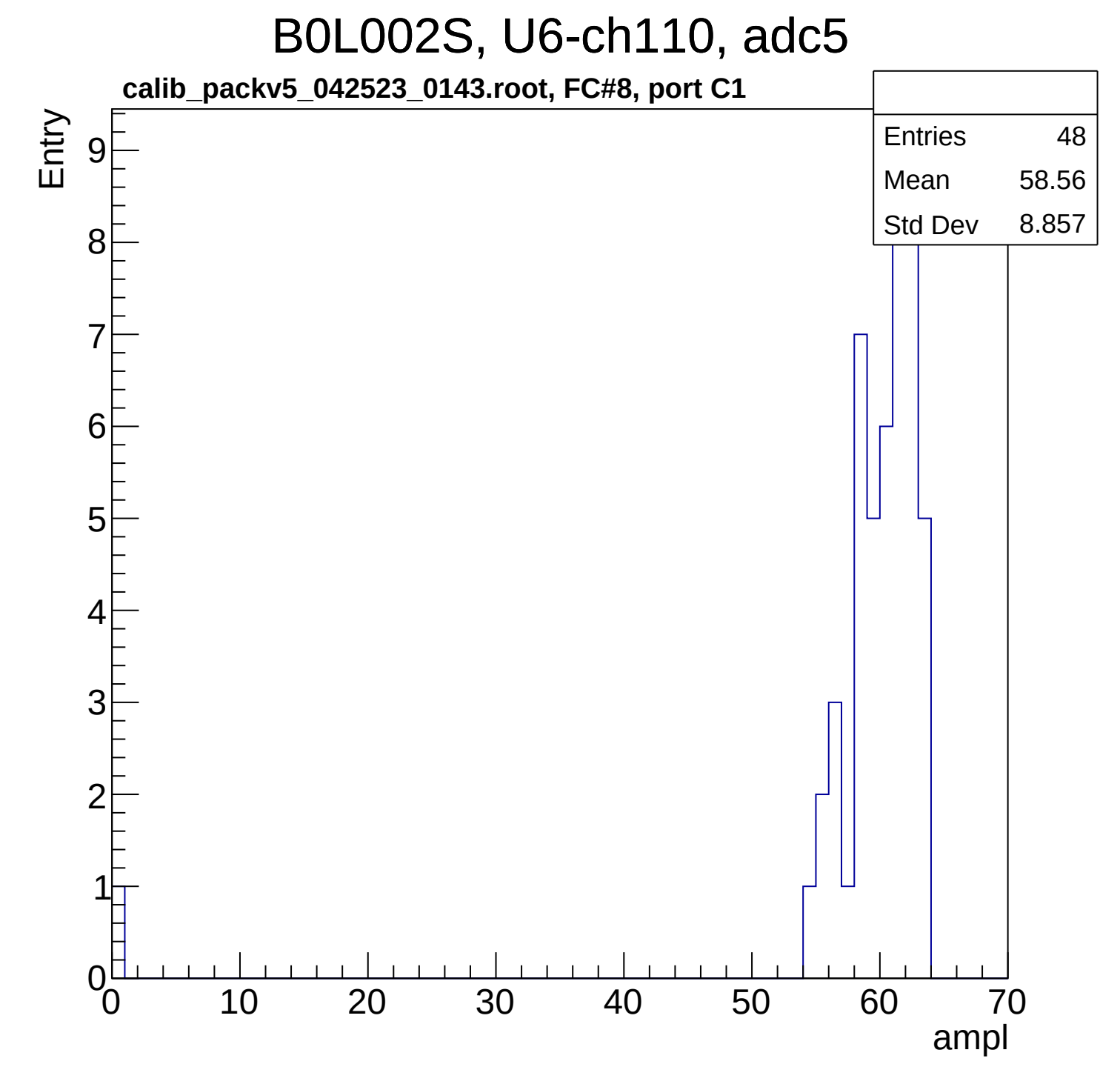
B0L002S, U6-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 58.56
Std Dev 8.857
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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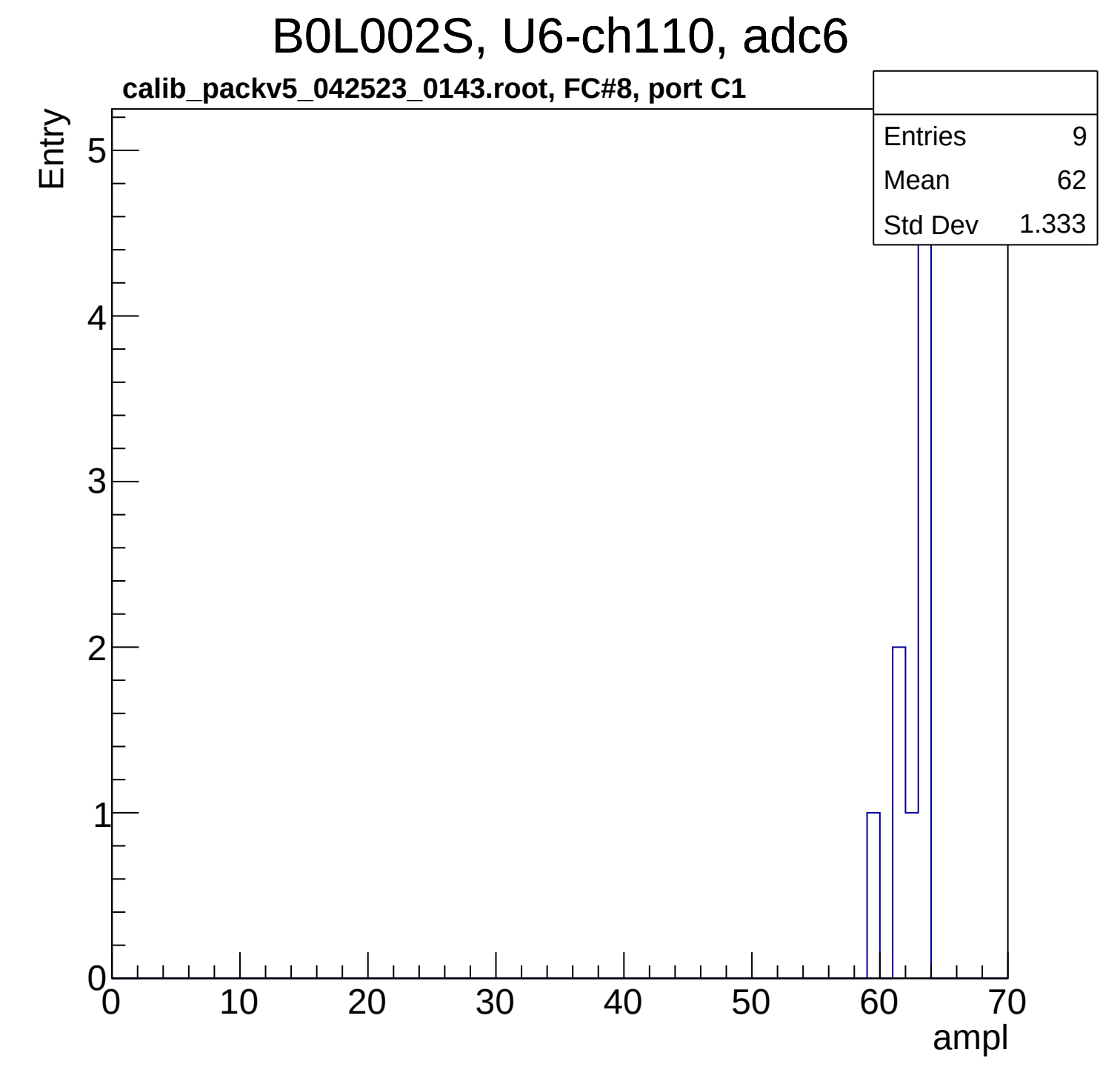
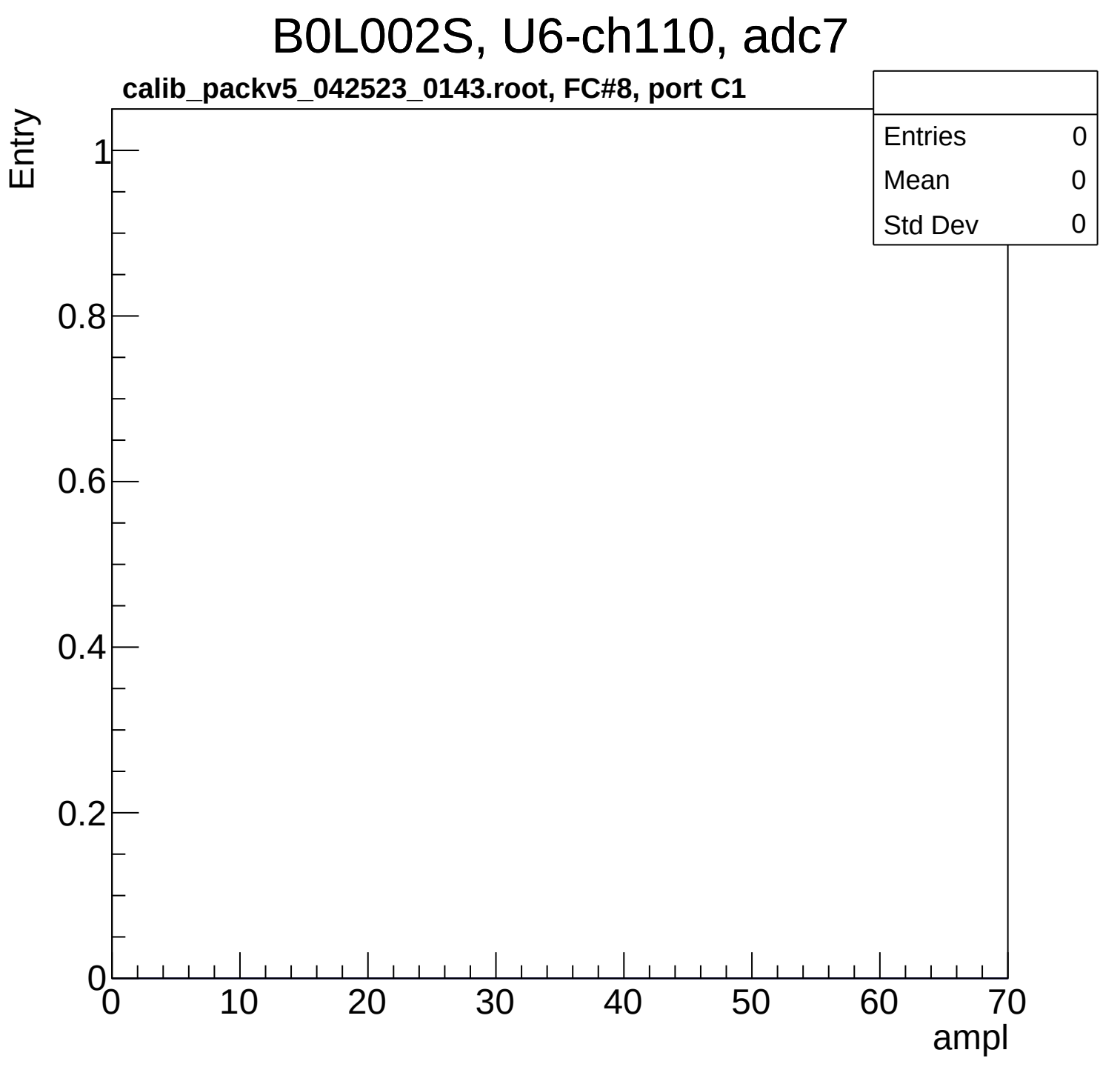

B0L002S, U6-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 62
Std Dev 1.333
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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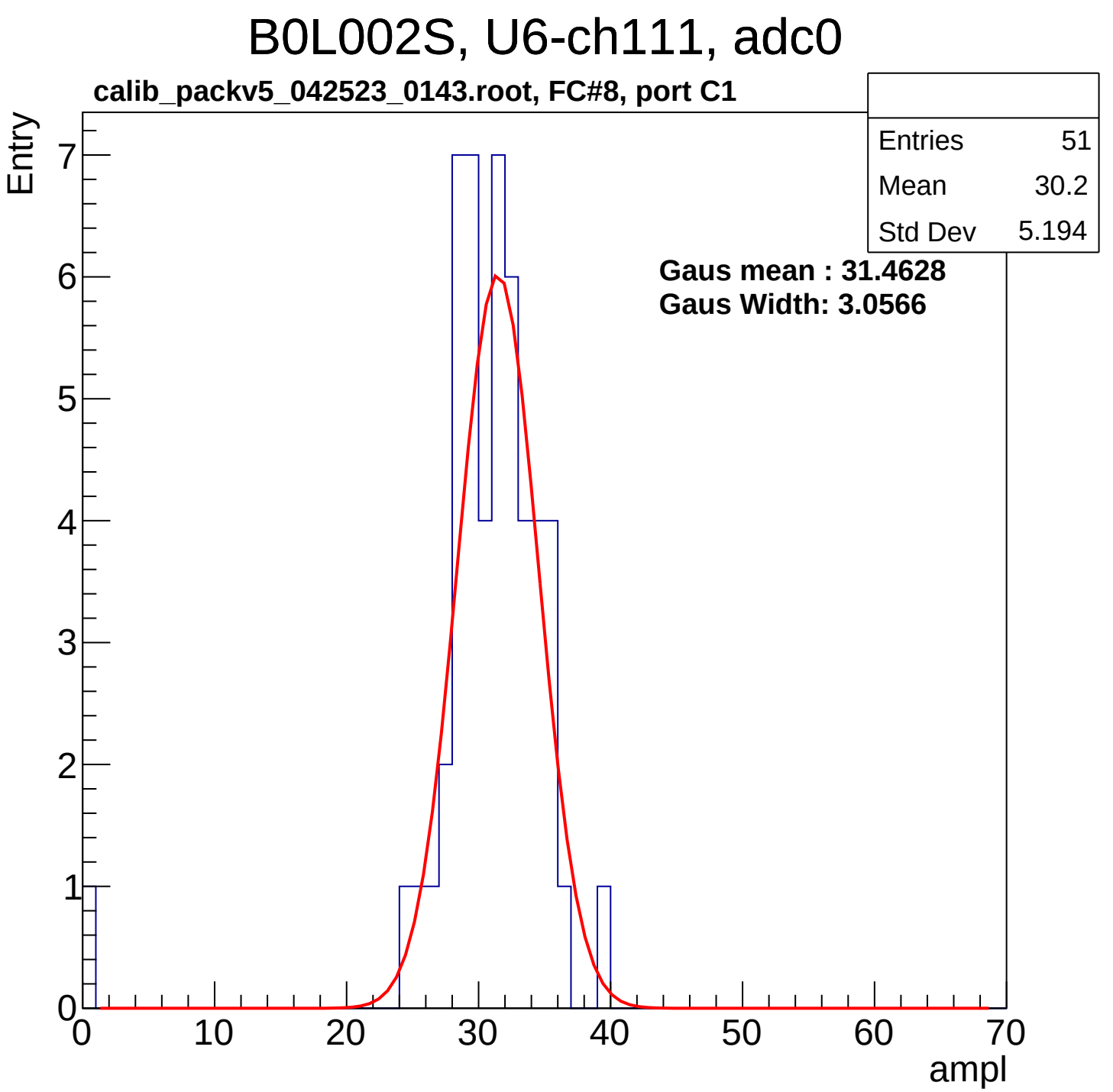
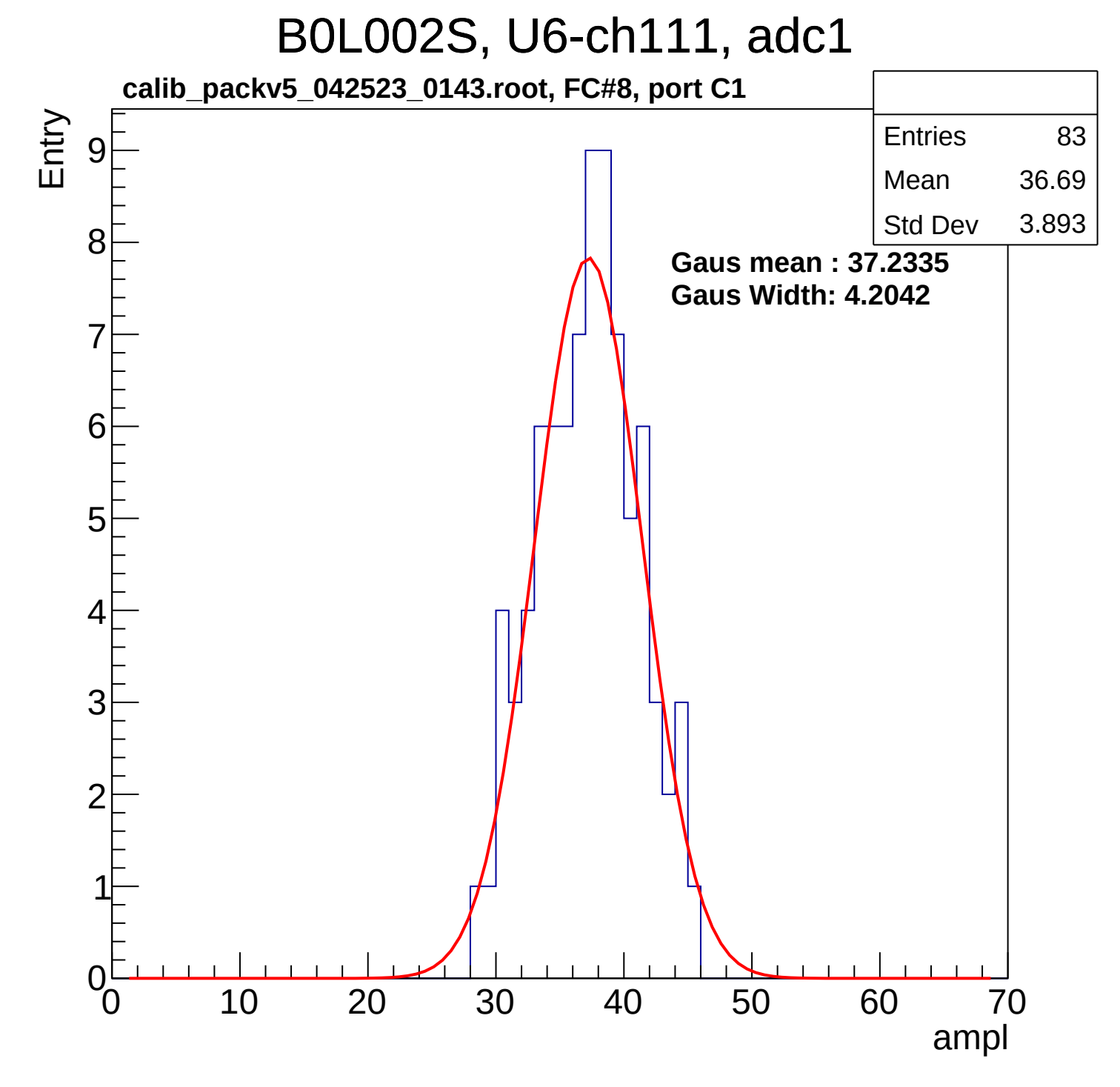
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 30.2
Std Dev 5.194
Gaus mean : 31.4628
Gaus Width: 3.0566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 36.69
Std Dev 3.893
Gaus mean : 37.2335
Gaus Width: 4.2042
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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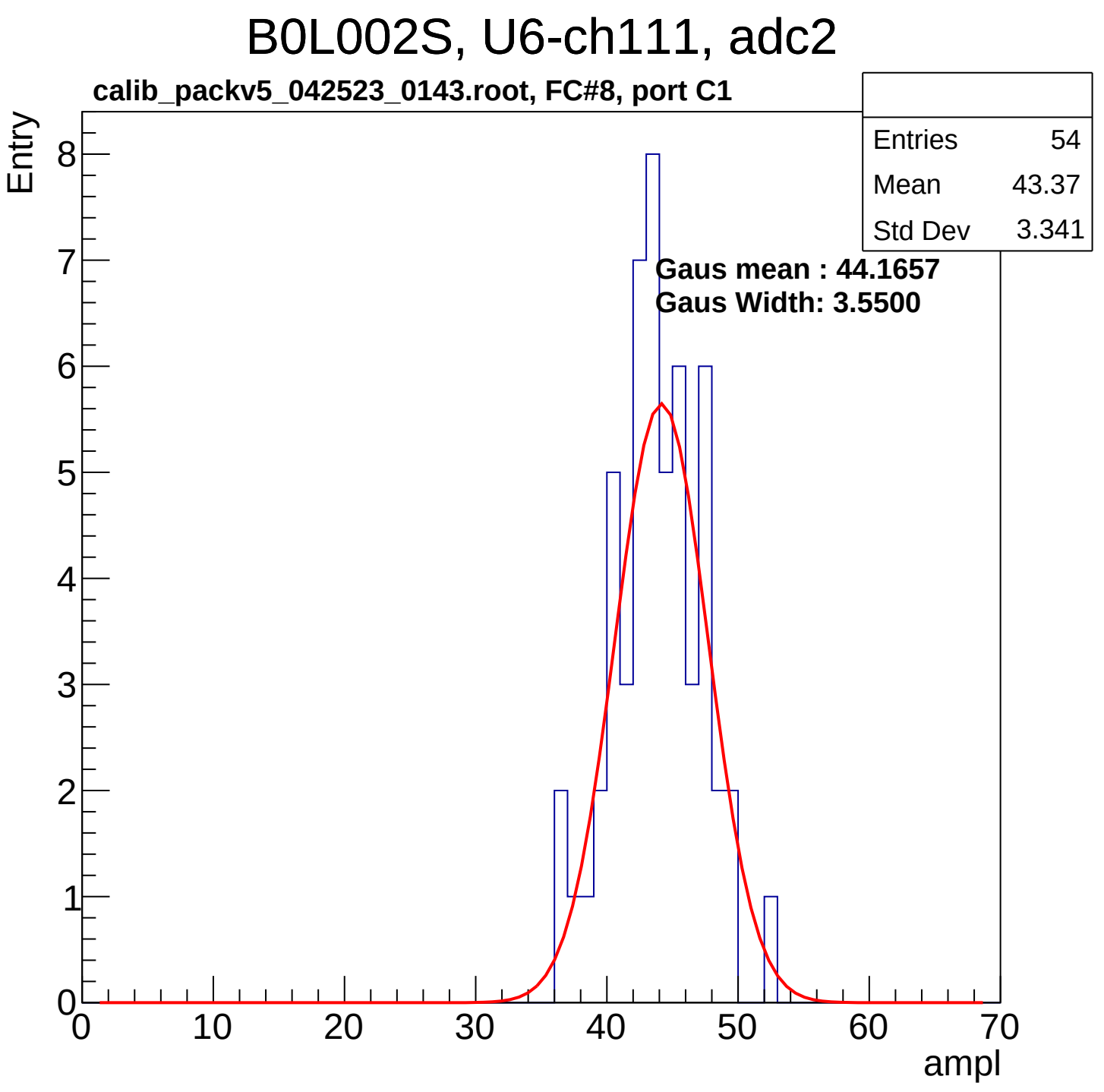
B0L002S, U6-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 43.37
Std Dev 3.341
Gaus mean : 44.1657
Gaus Width: 3.5500
Entry
ampl
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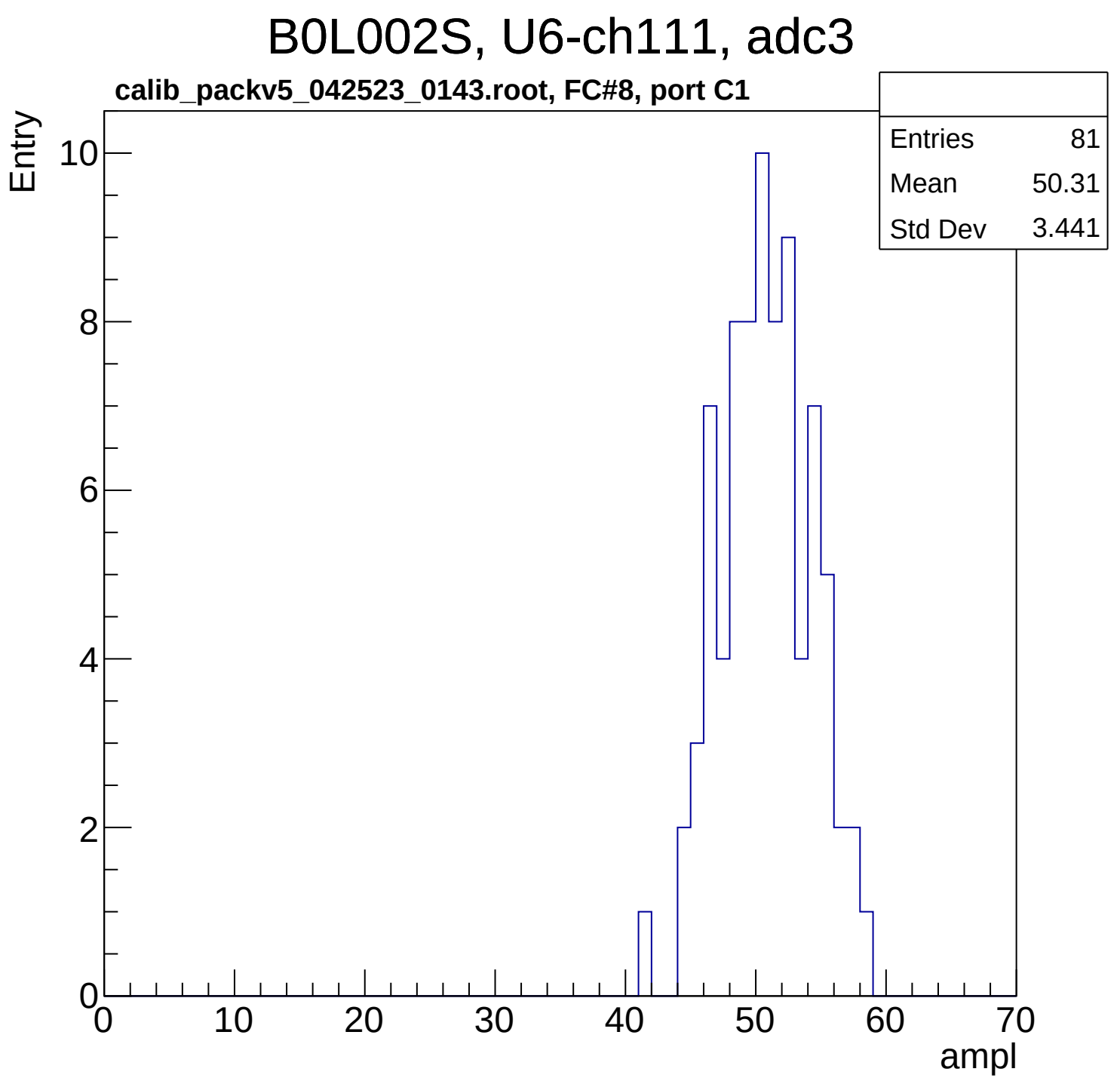
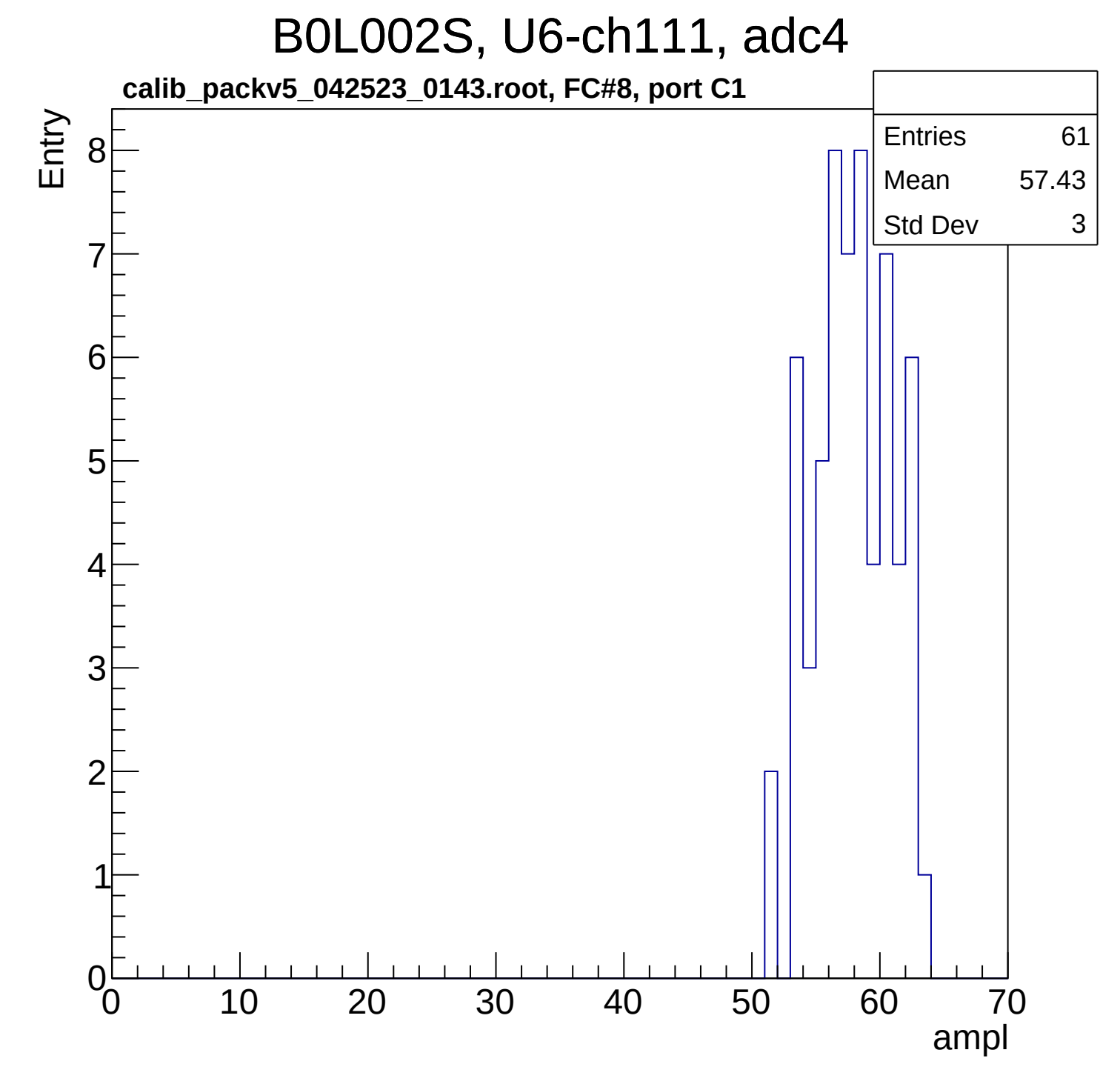
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 50.31
Std Dev 3.441
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.43
Std Dev 3
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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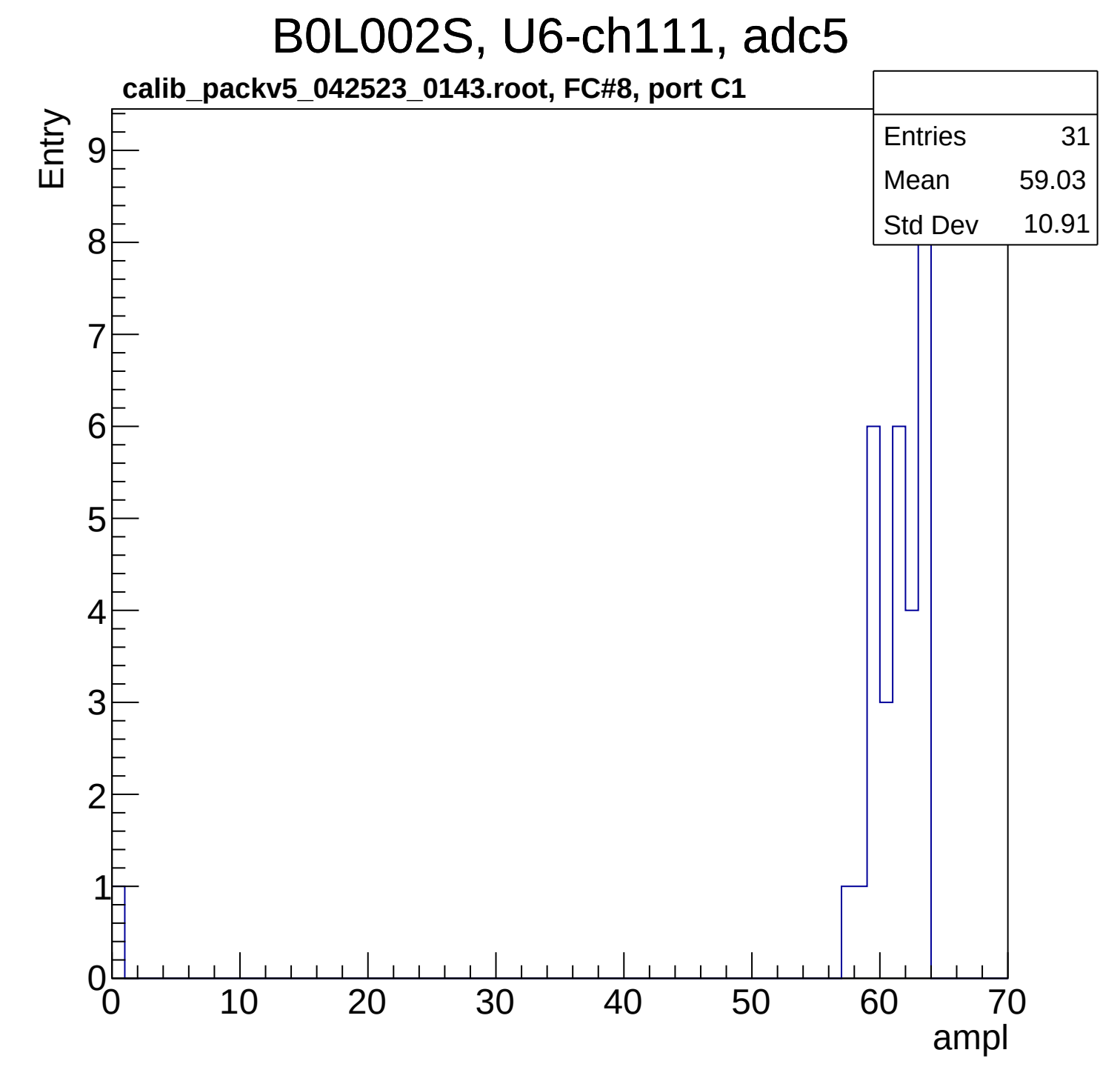
B0L002S, U6-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.03
Std Dev 10.91
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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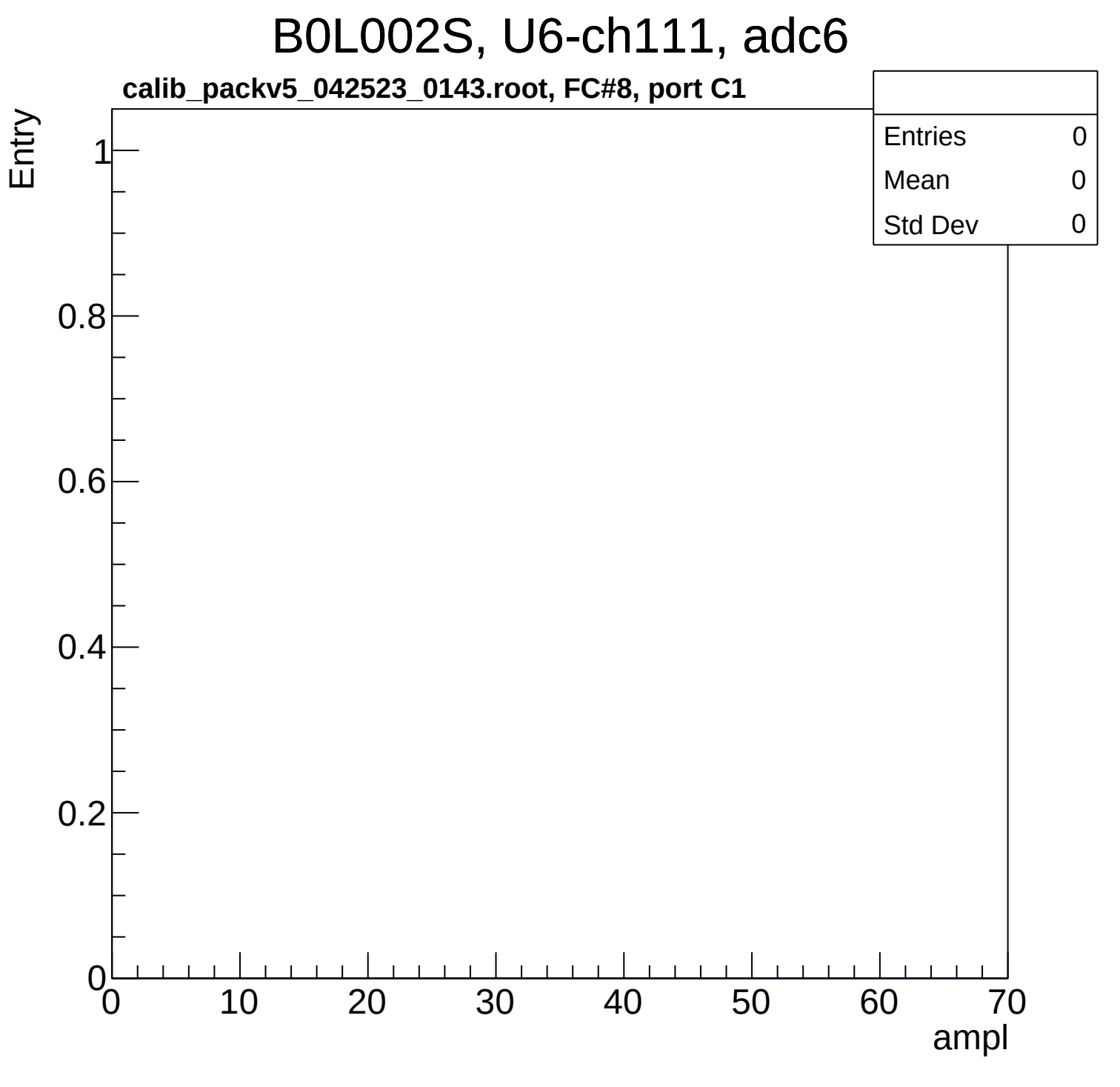
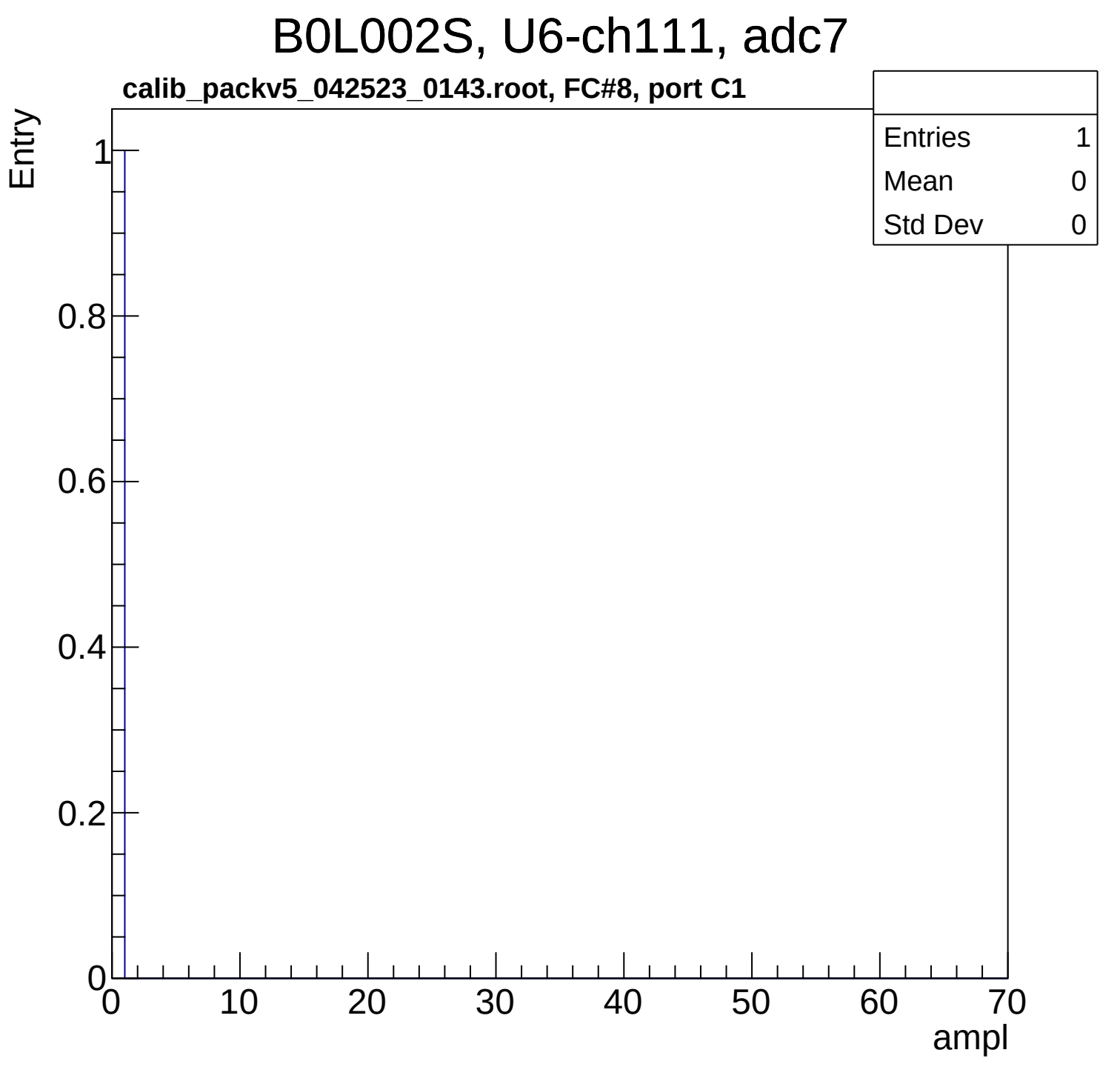
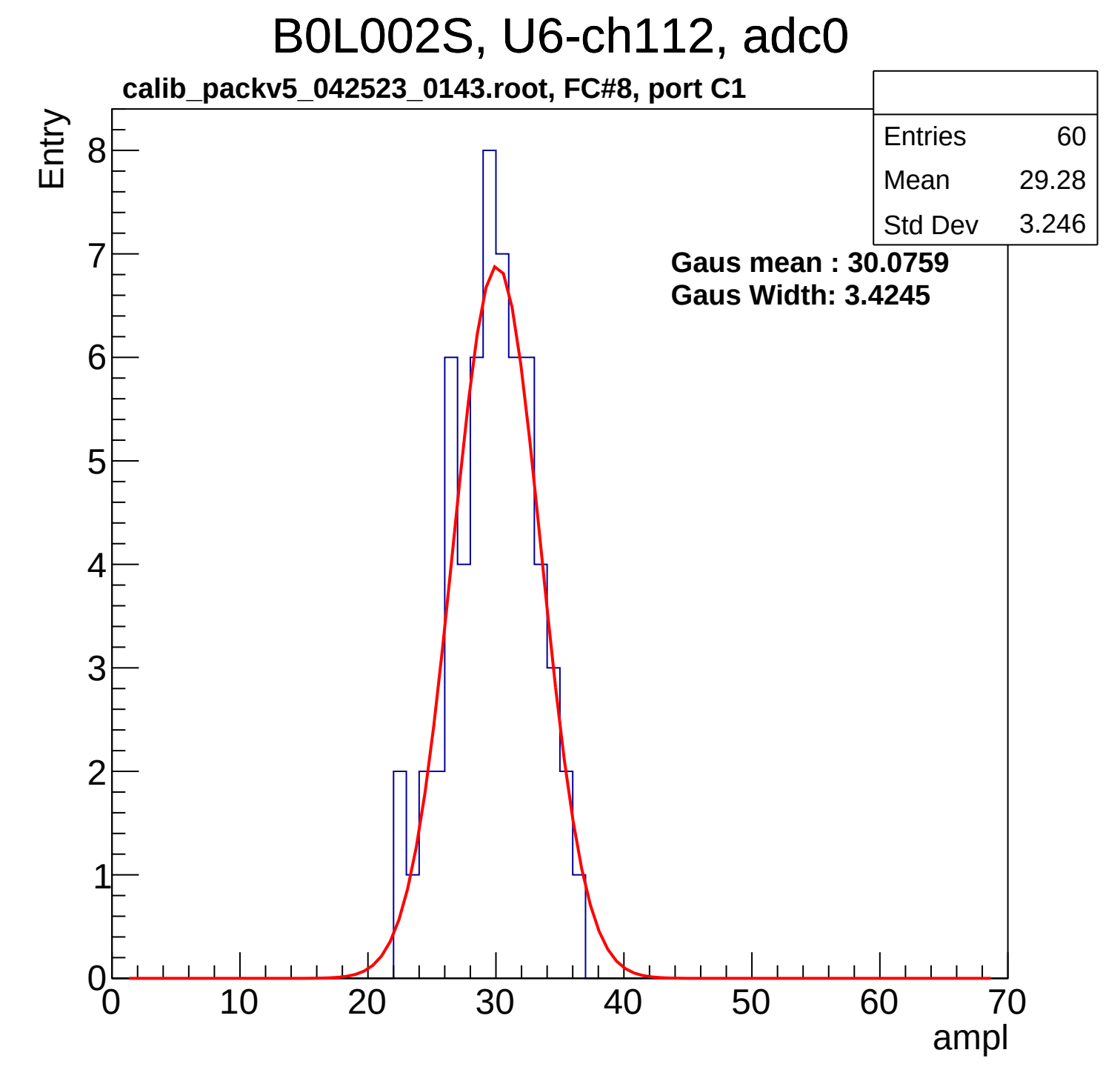
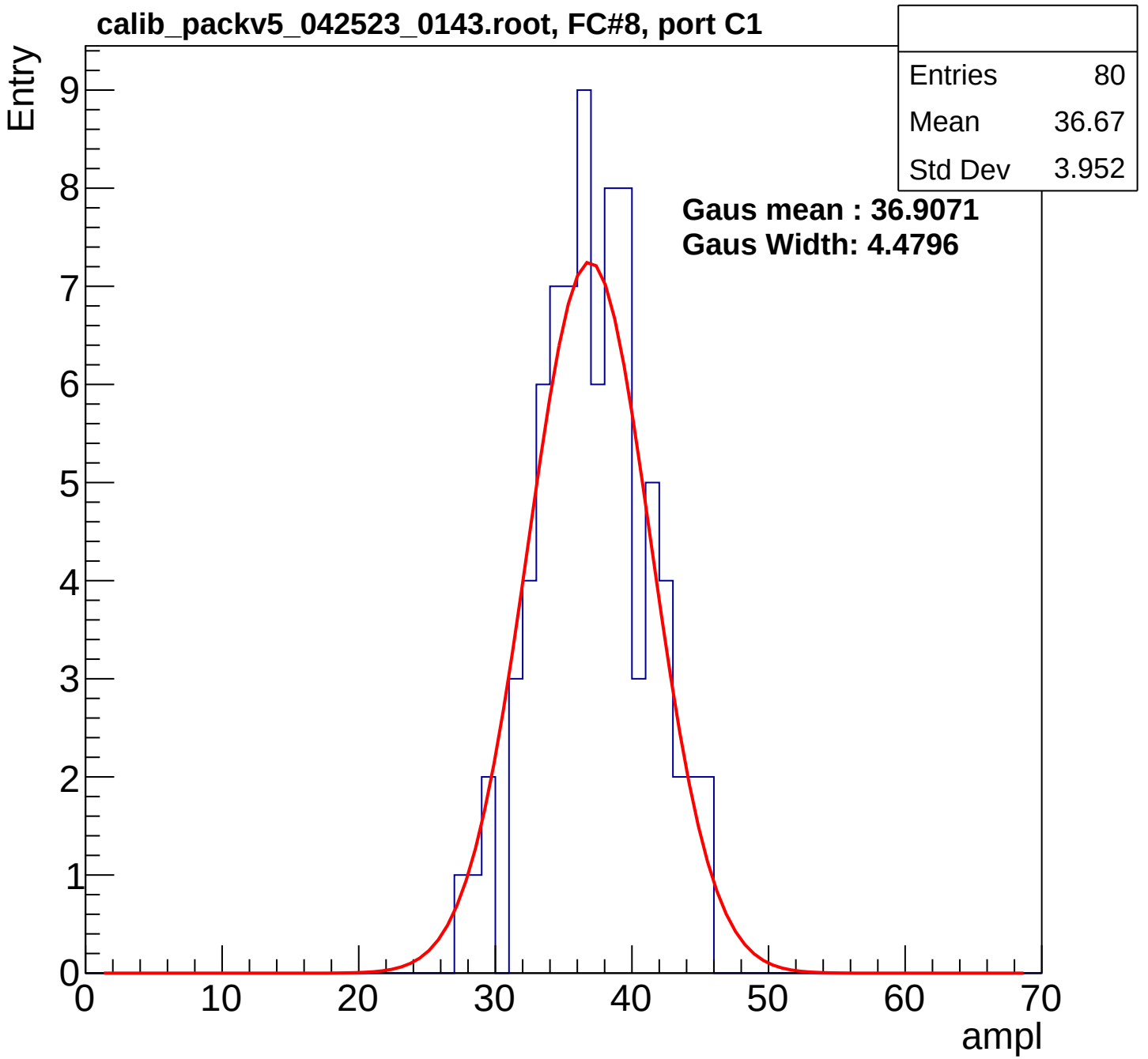

B0L002S, U6-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 29.28
Std Dev 3.246
Gaus mean : 30.0759
Gaus Width: 3.4245
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 36.67
Std Dev 3.952
Gaus mean : 36.9071
Gaus Width: 4.4796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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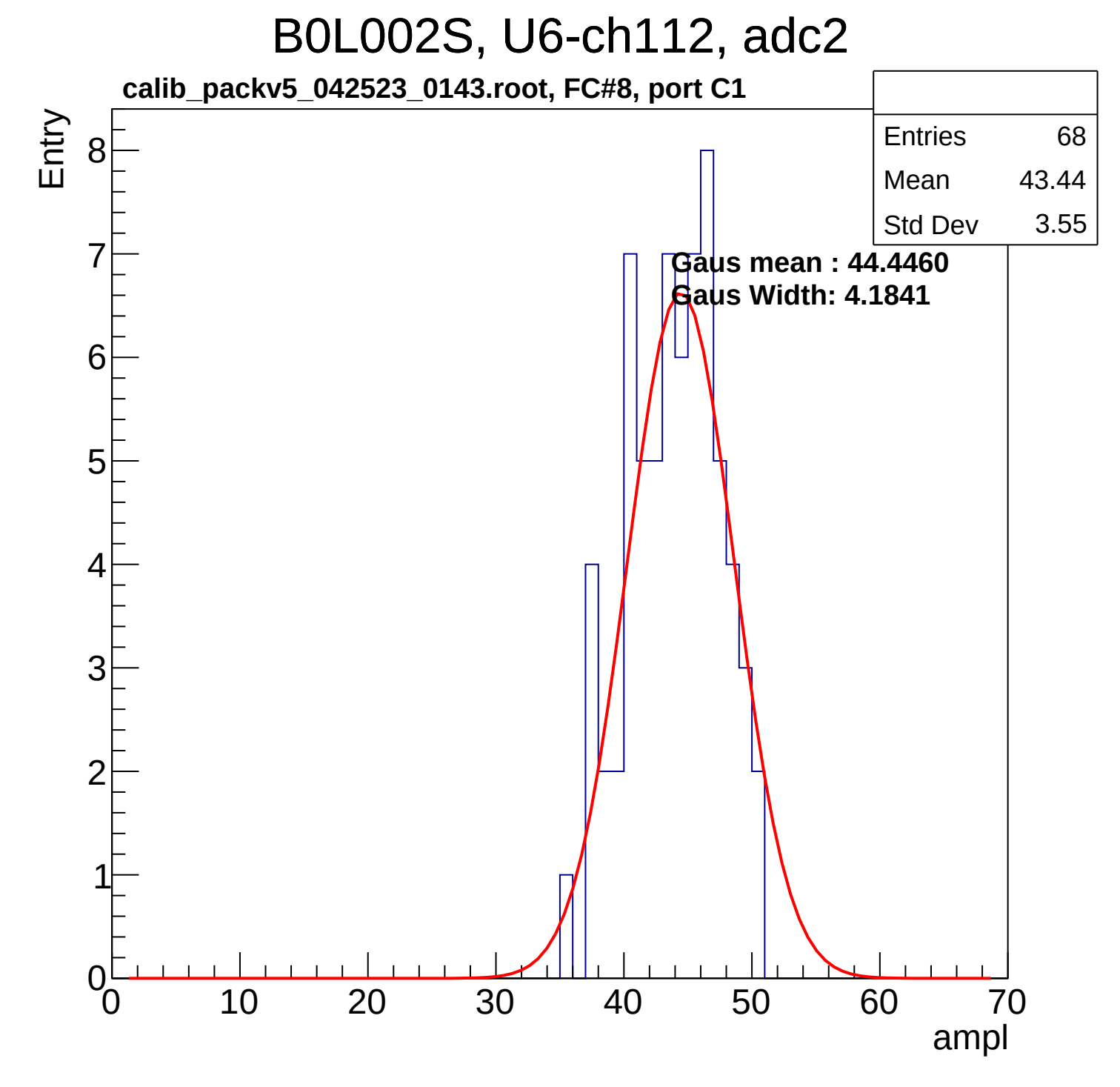
B0L002S, U6-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.44
Std Dev 3.55
Gaus mean : 44.4460
Gaus Width: 4.1841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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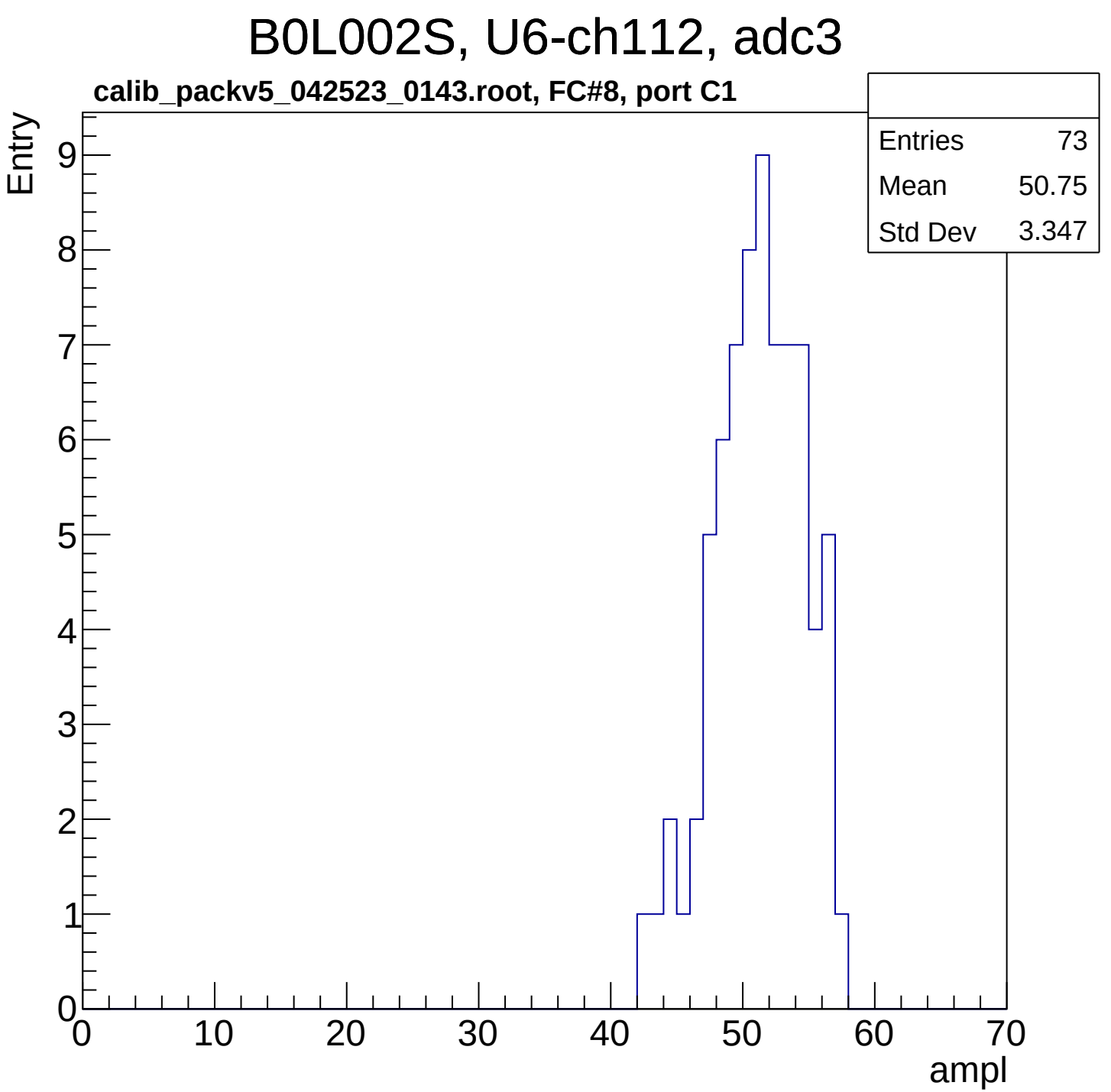
B0L002S, U6-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.75
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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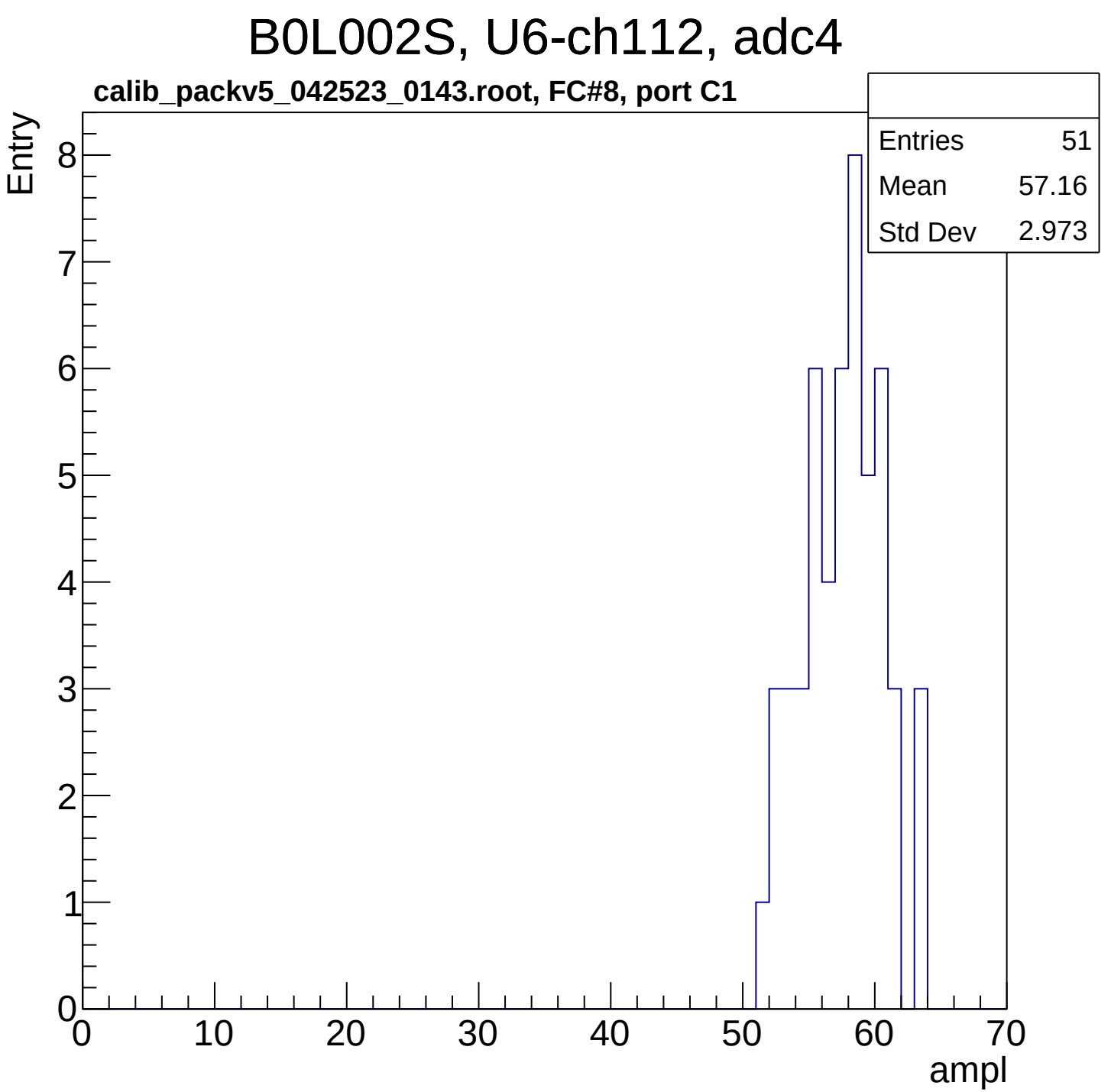
B0L002S, U6-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.16
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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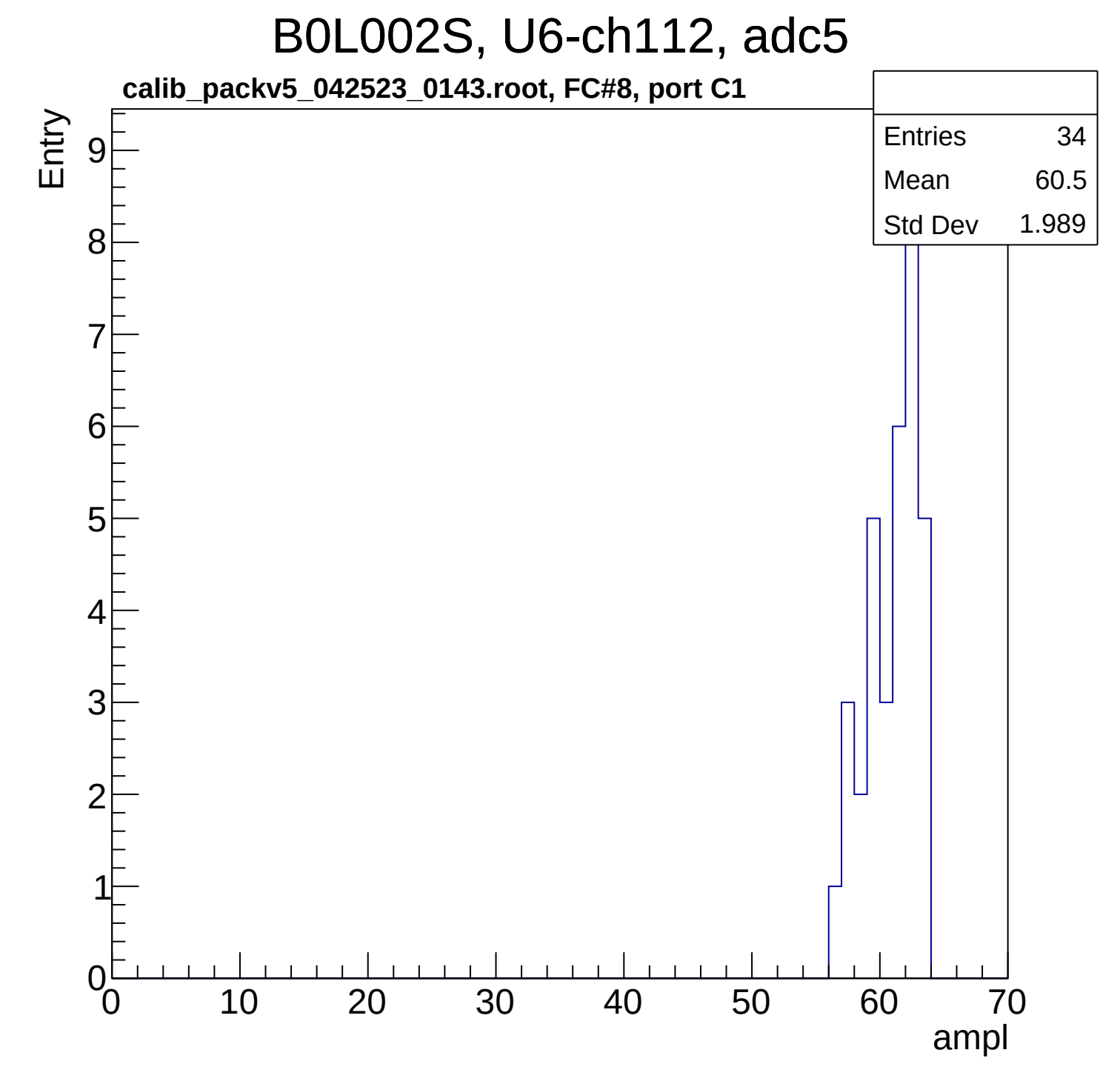
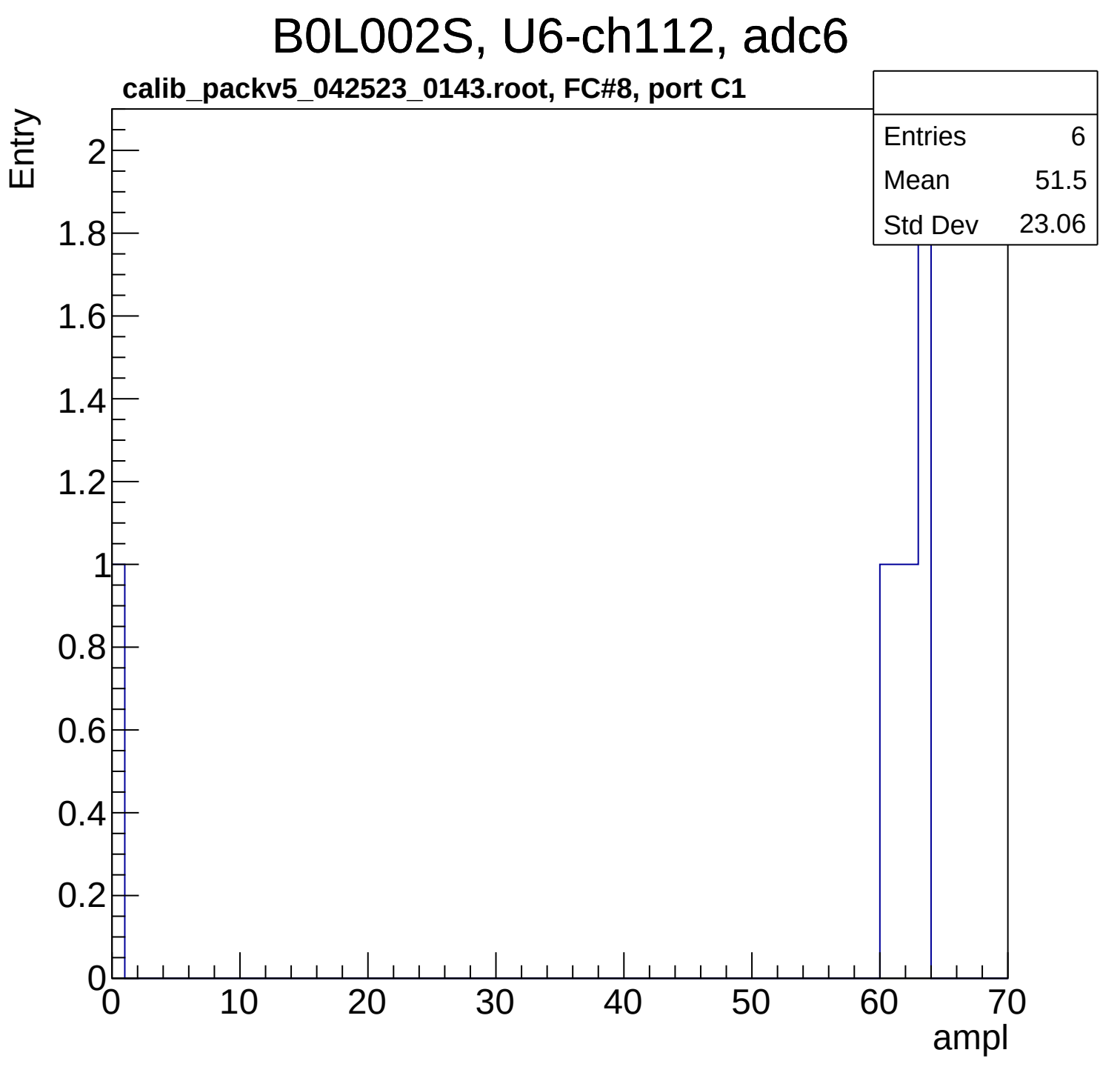
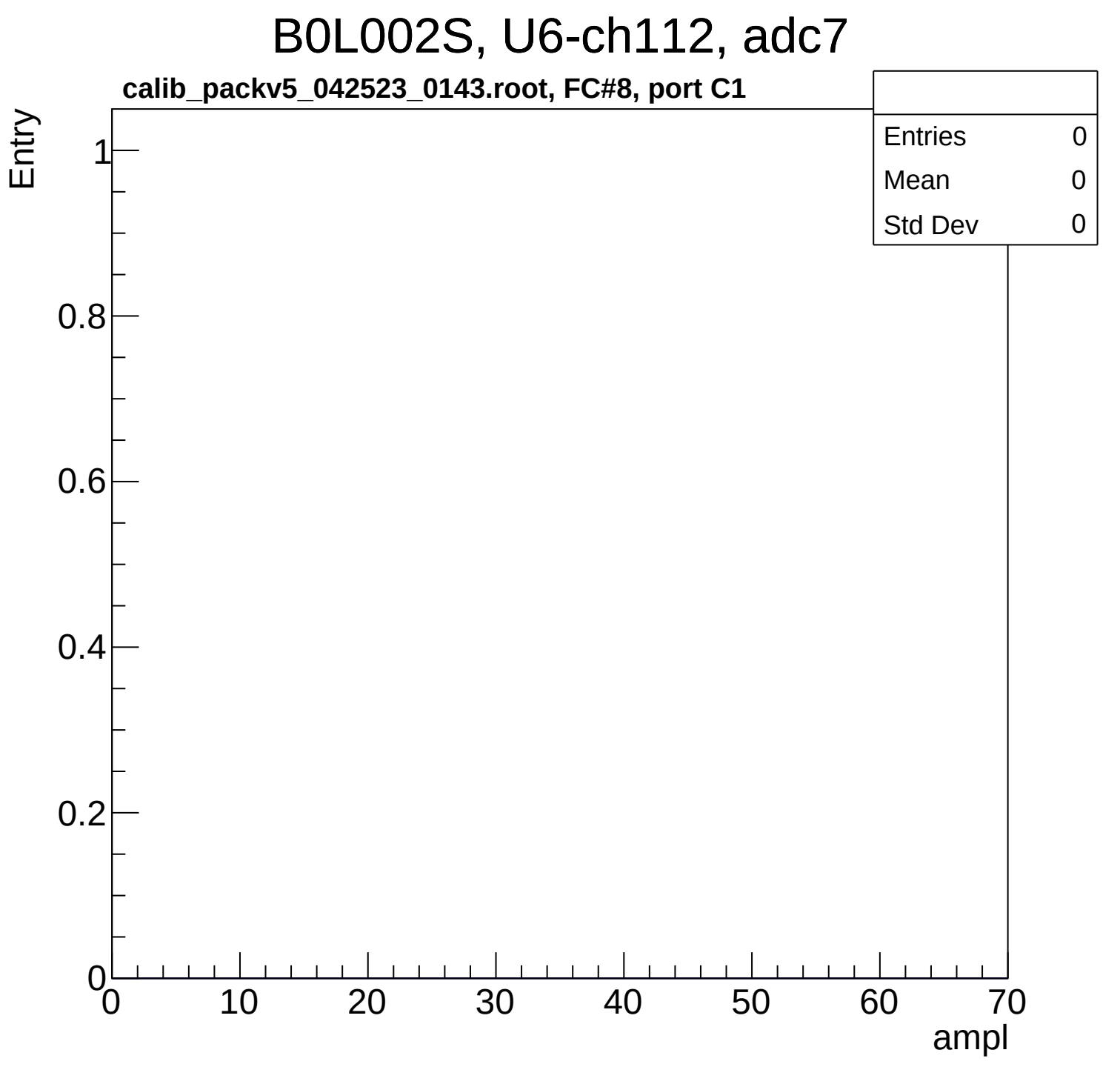
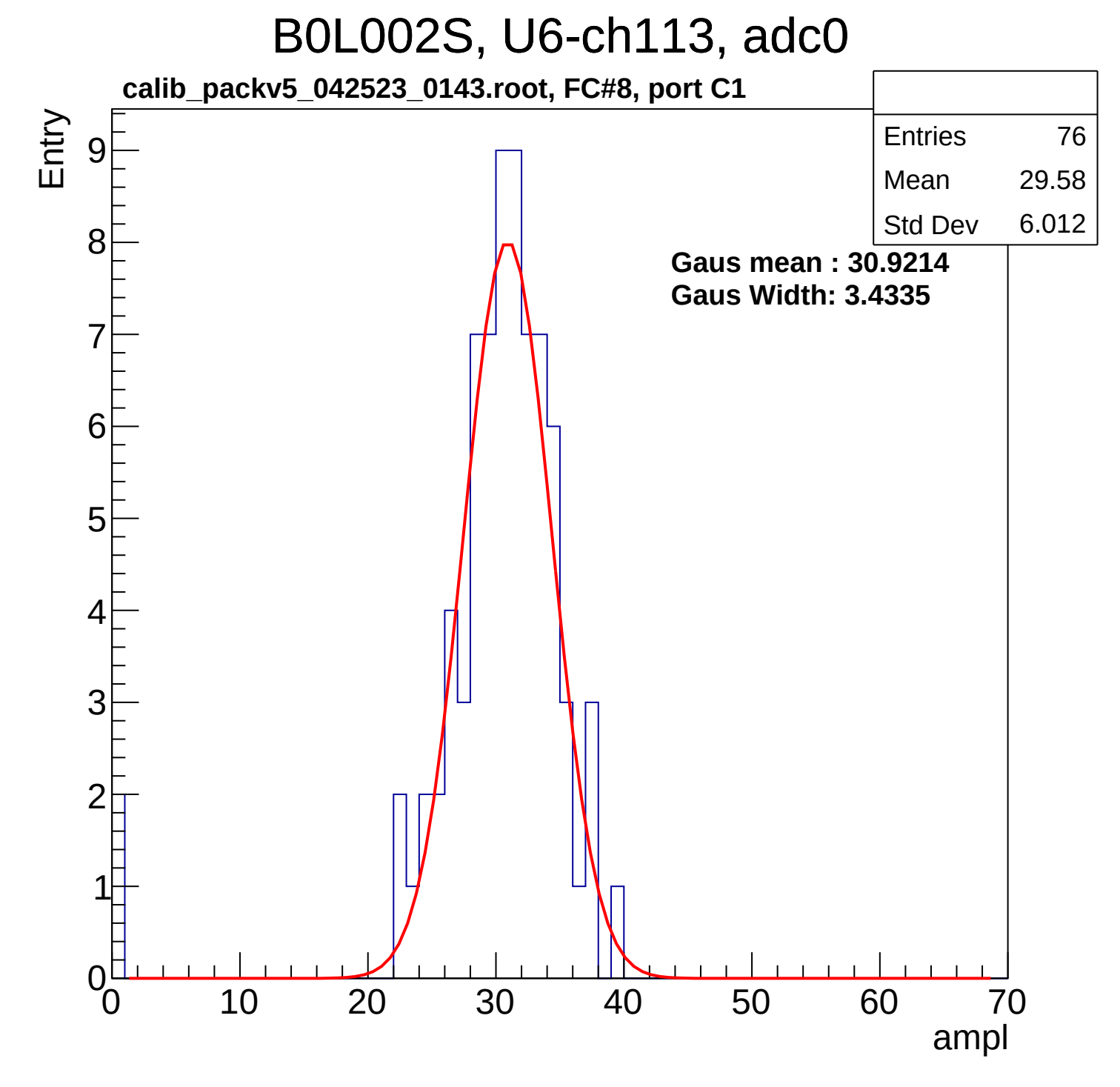
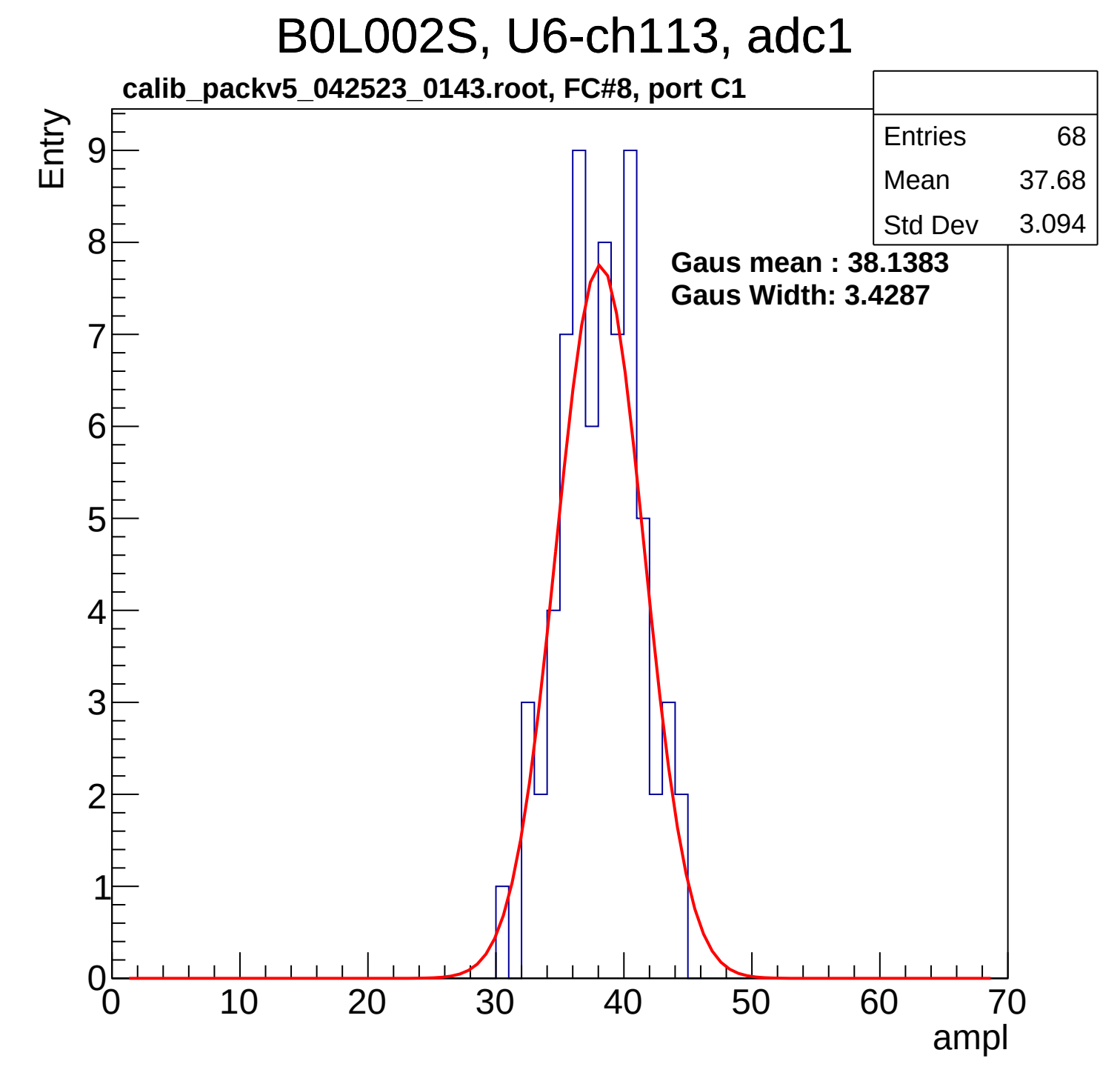

B0L002S, U6-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.5
Std Dev 1.989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 29.58
Std Dev 6.012
Gaus mean : 30.9214
Gaus Width: 3.4335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.68
Std Dev 3.094
Gaus mean : 38.1383
Gaus Width: 3.4287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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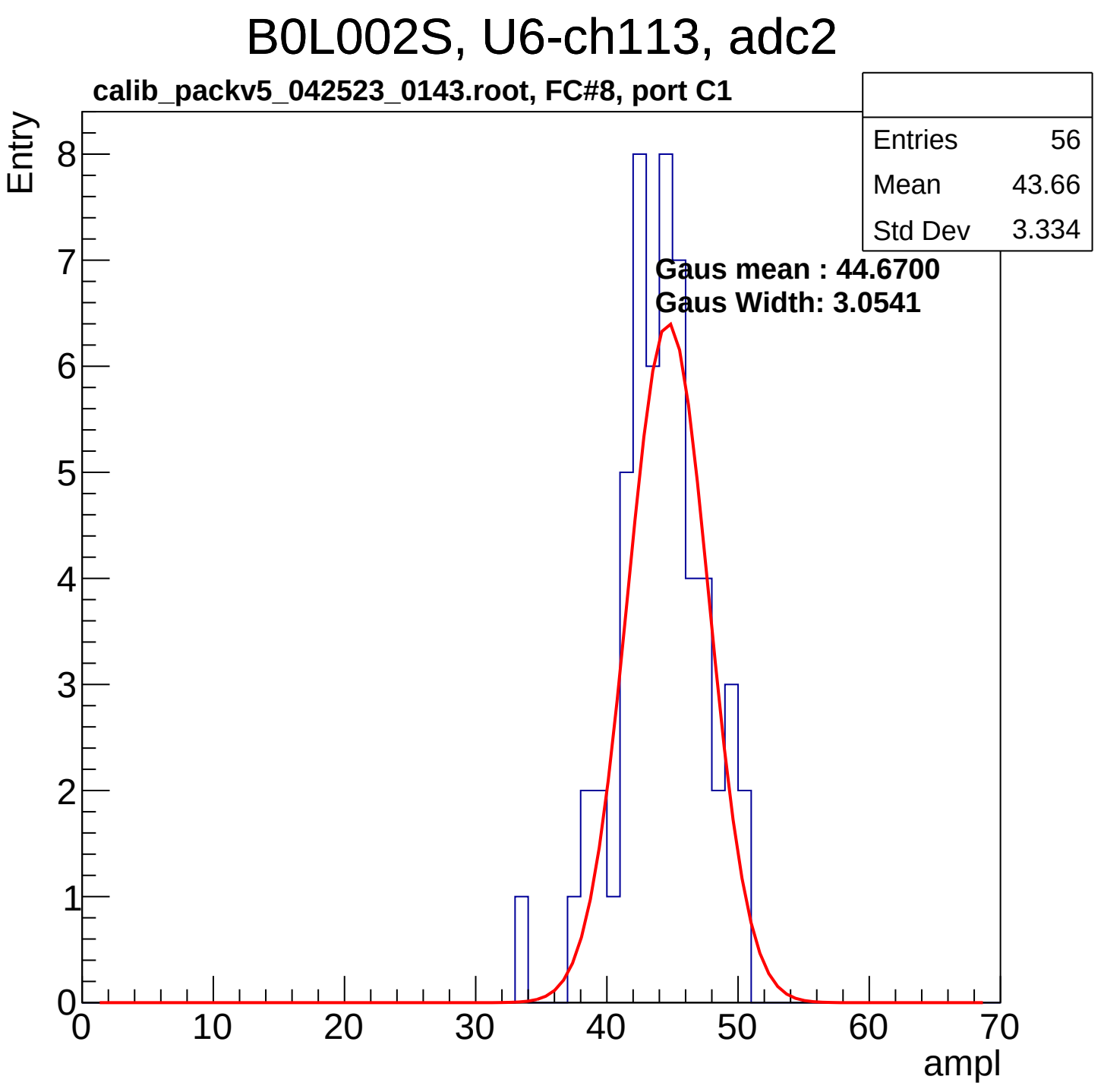
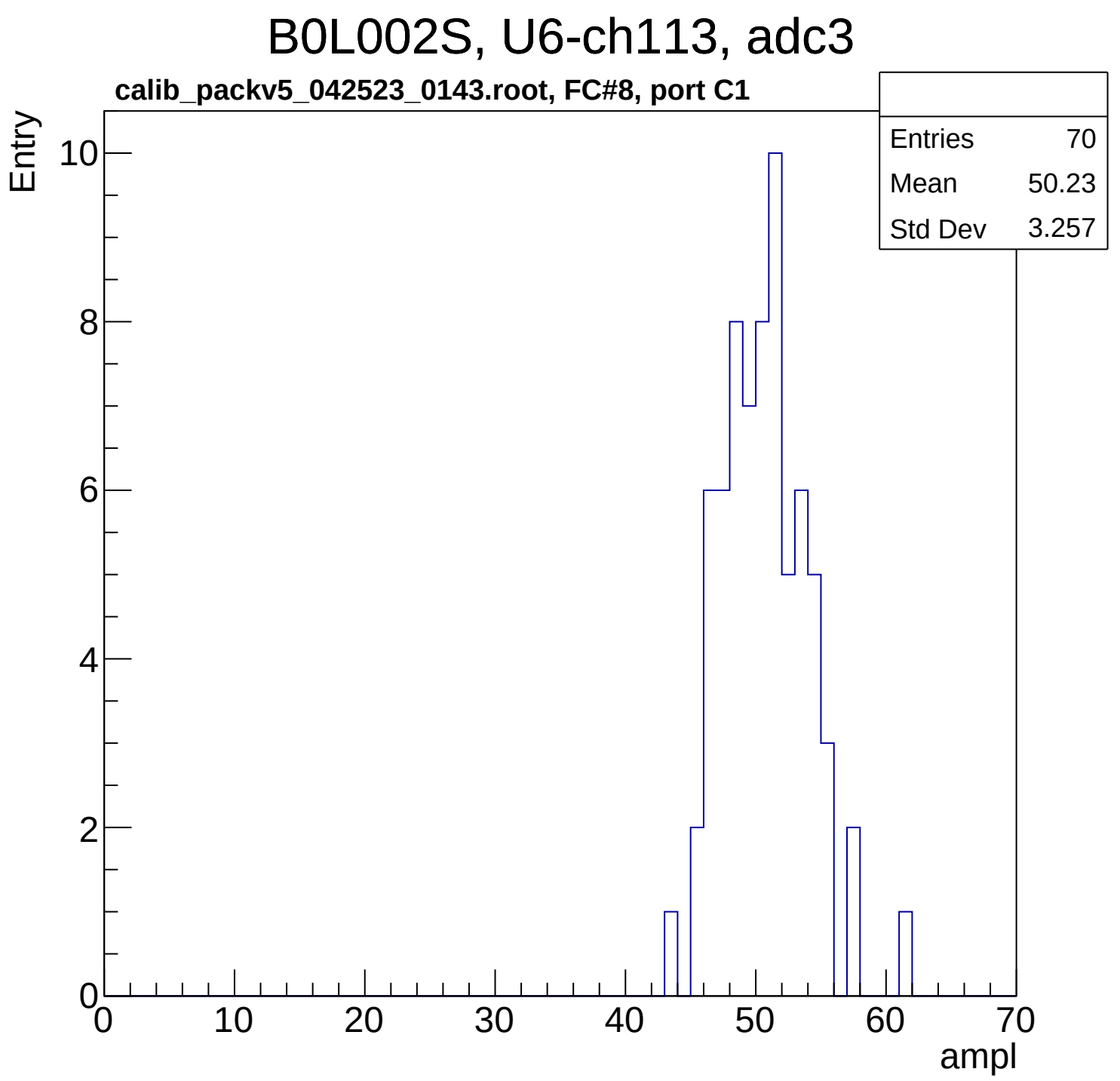
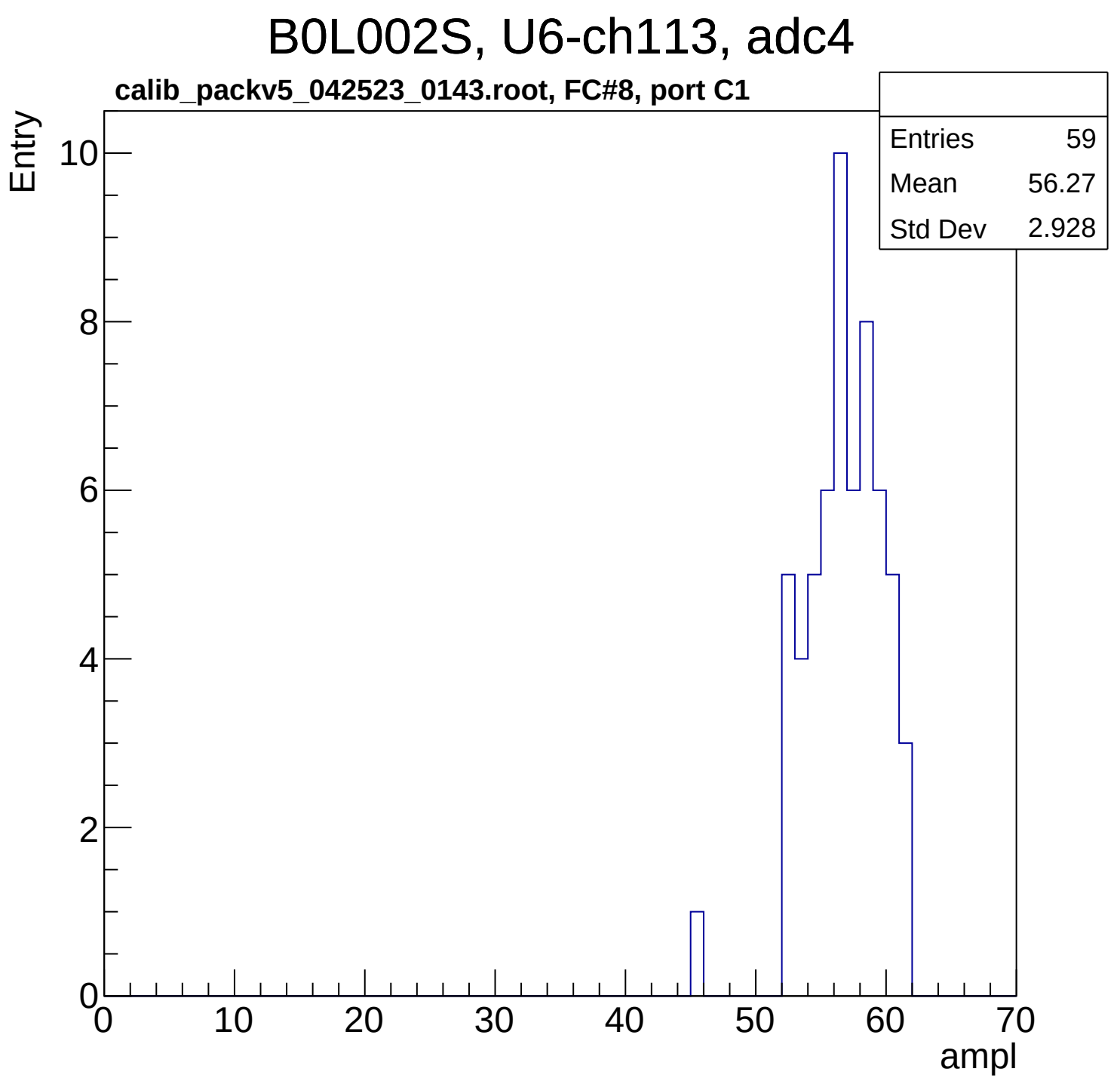
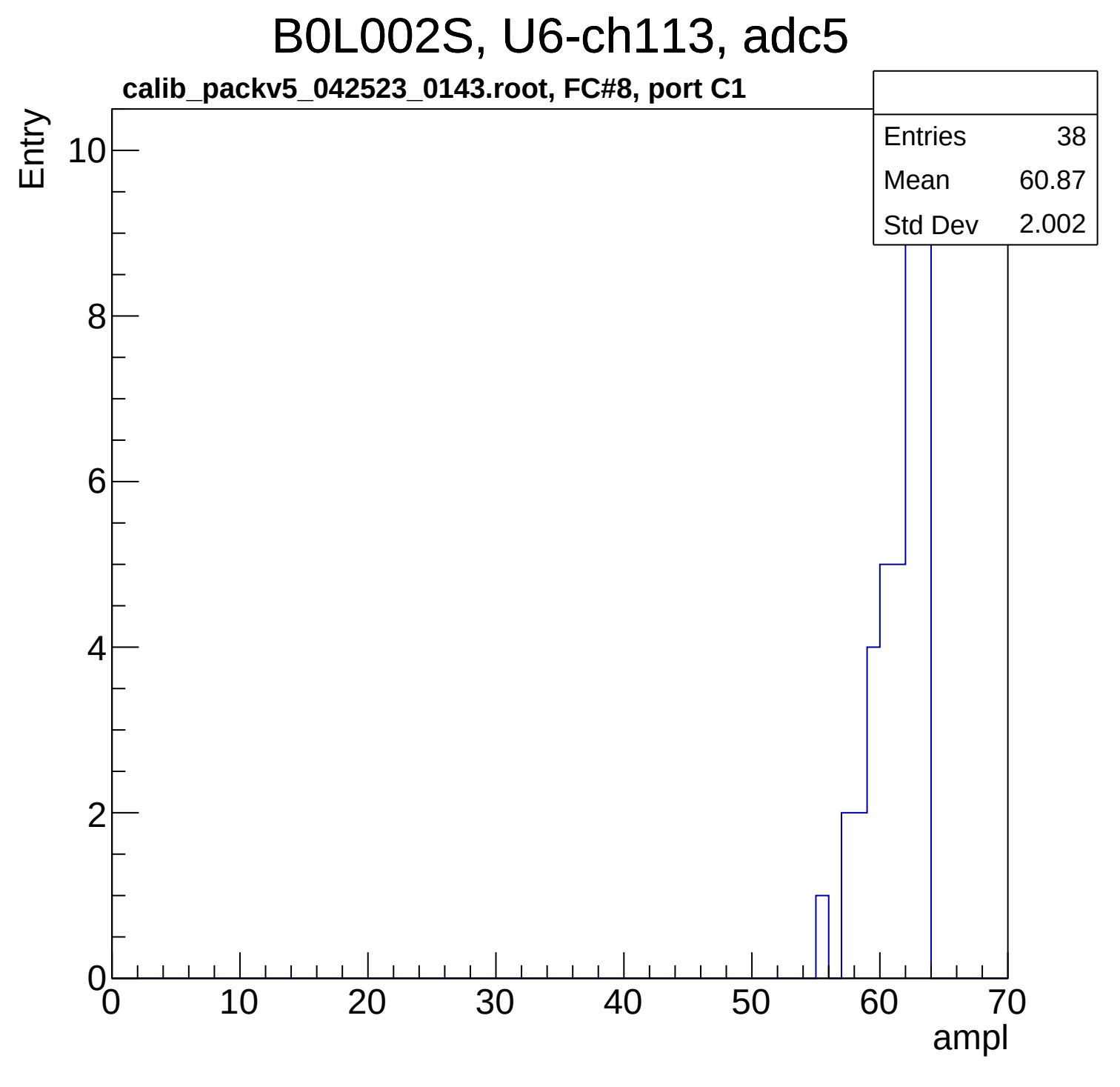
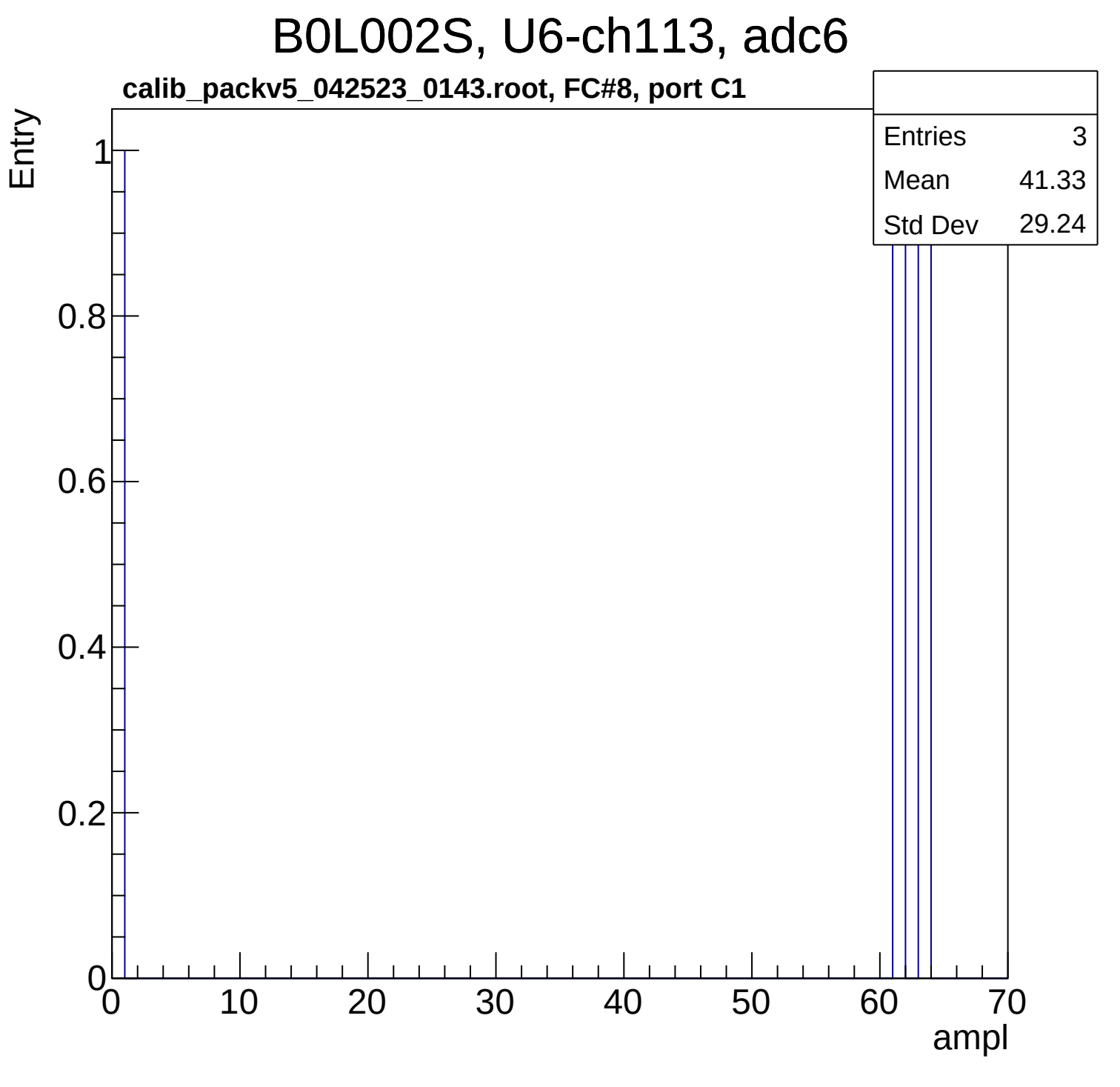
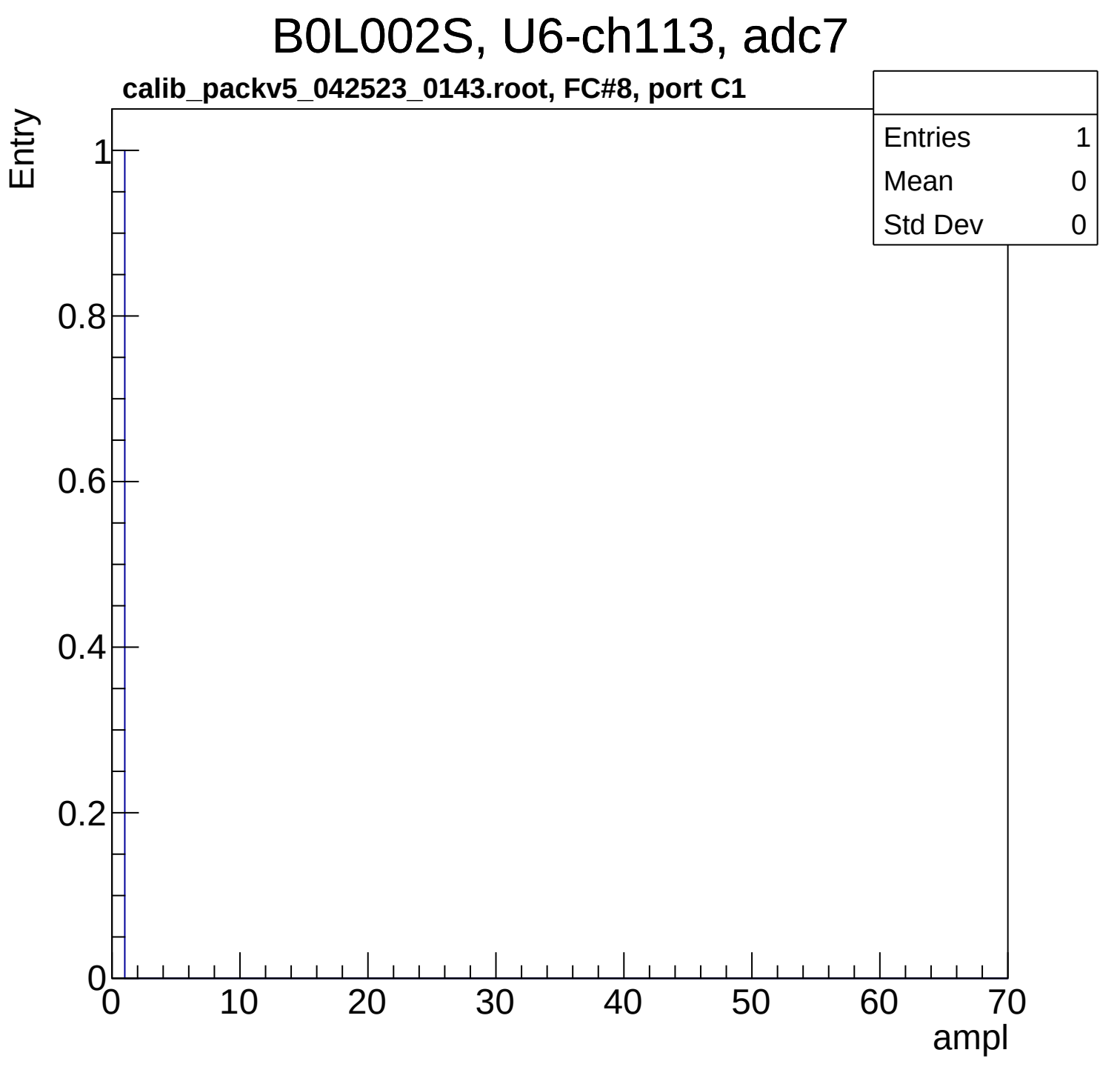

B0L002S, U6-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 43.66
Std Dev 3.334
Gaus mean : 44.6700
Gaus Width: 3.0541
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

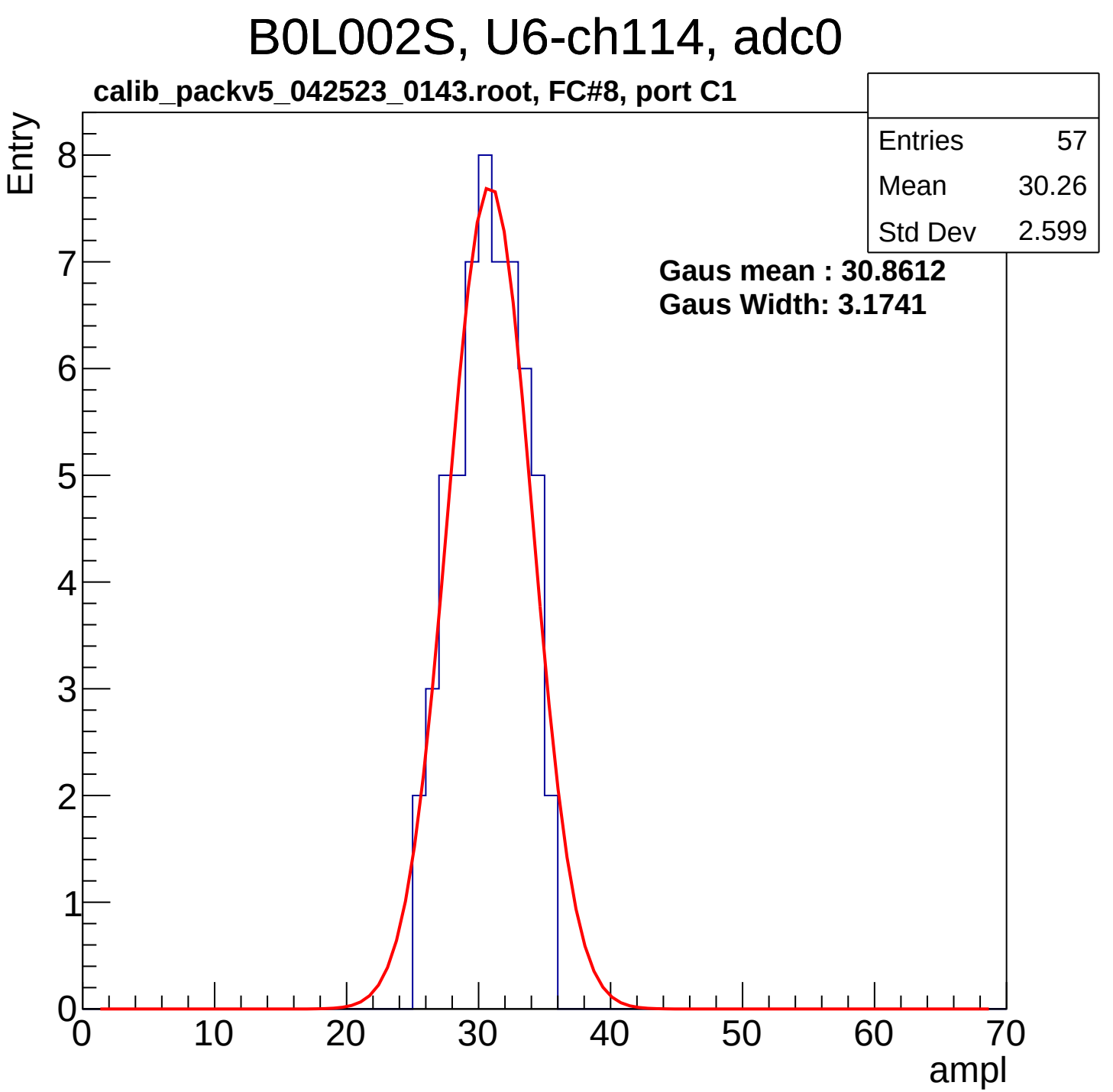

B0L002S, U6-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 50.23
Std Dev 3.257
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.27
Std Dev 2.928
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.87
Std Dev 2.002
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 30.26
Std Dev 2.599
Gaus mean : 30.8612
Gaus Width: 3.1741
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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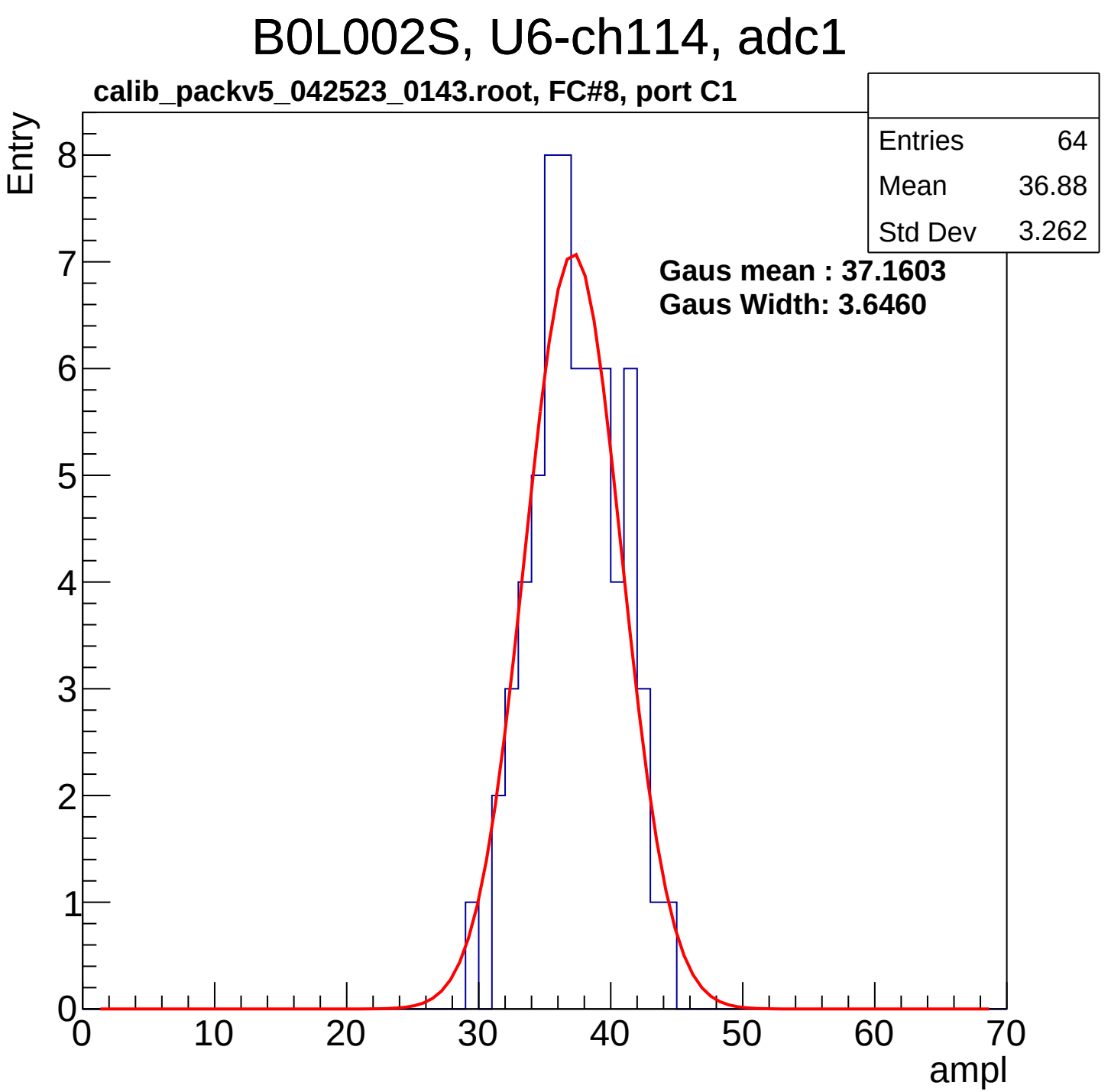
B0L002S, U6-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 36.88
Std Dev 3.262
Gaus mean : 37.1603
Gaus Width: 3.6460
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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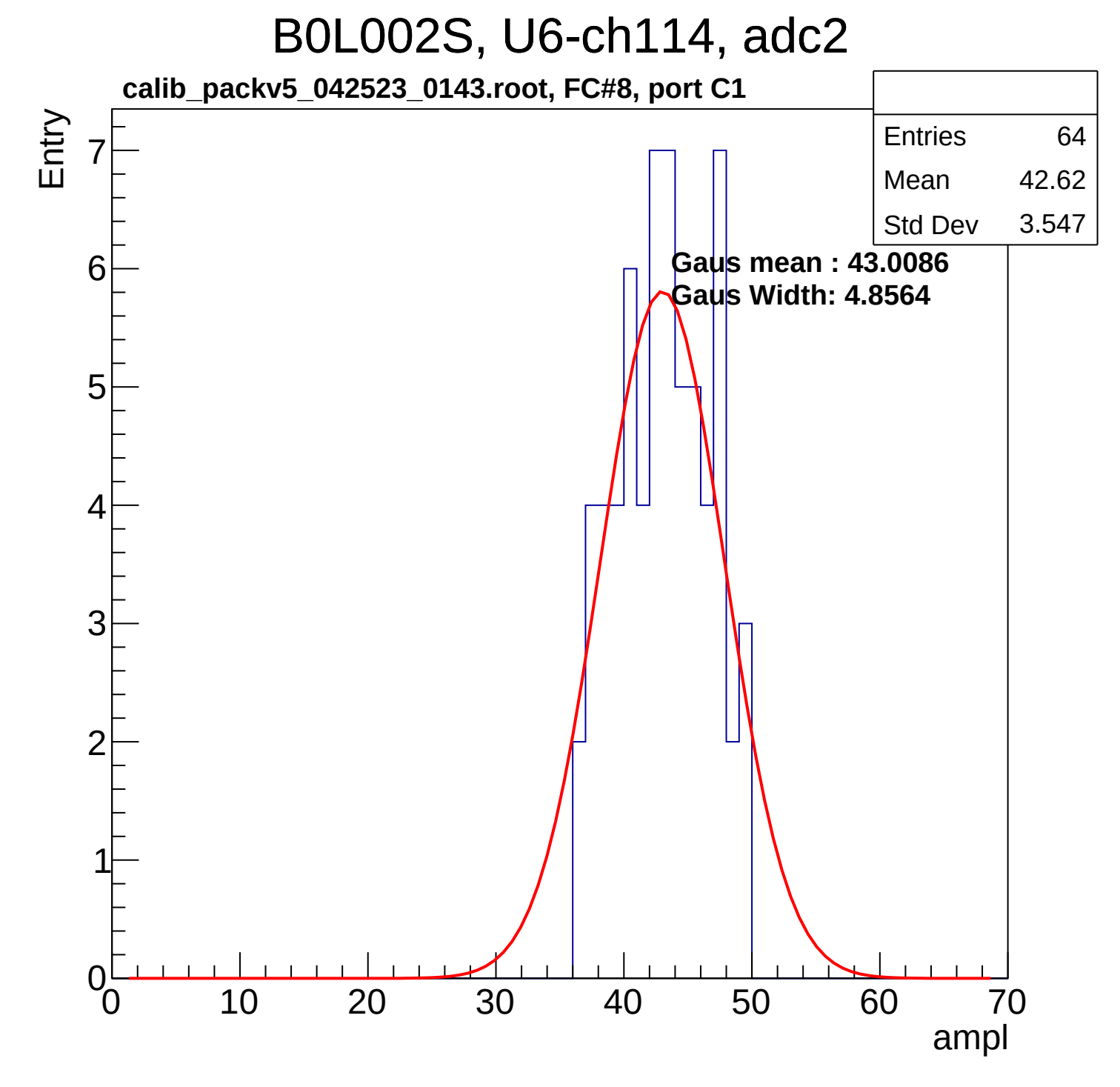
B0L002S, U6-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 42.62
Std Dev 3.547
Gaus mean : 43.0086
Gaus Width: 4.8564
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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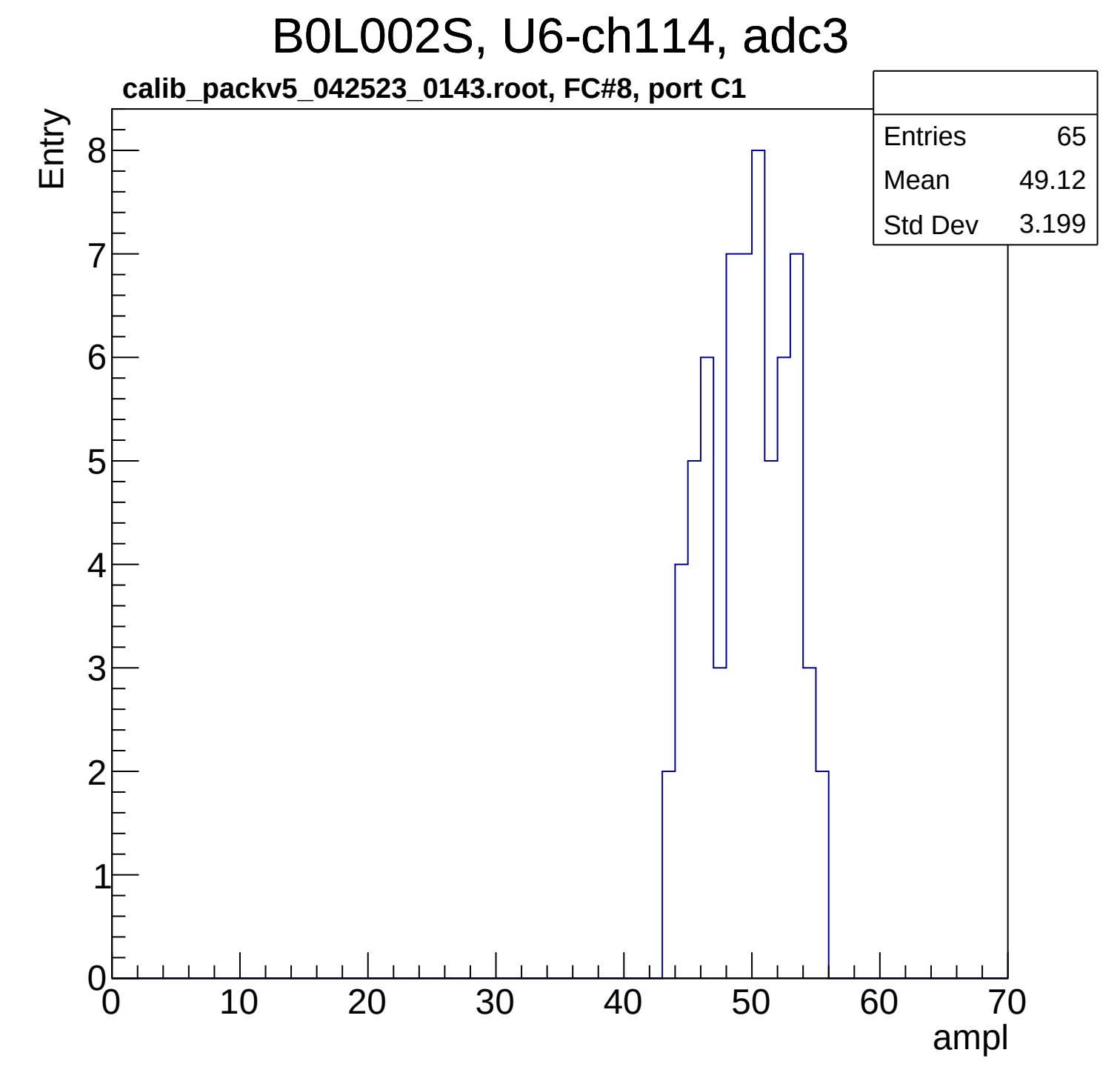
B0L002S, U6-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.12
Std Dev 3.199
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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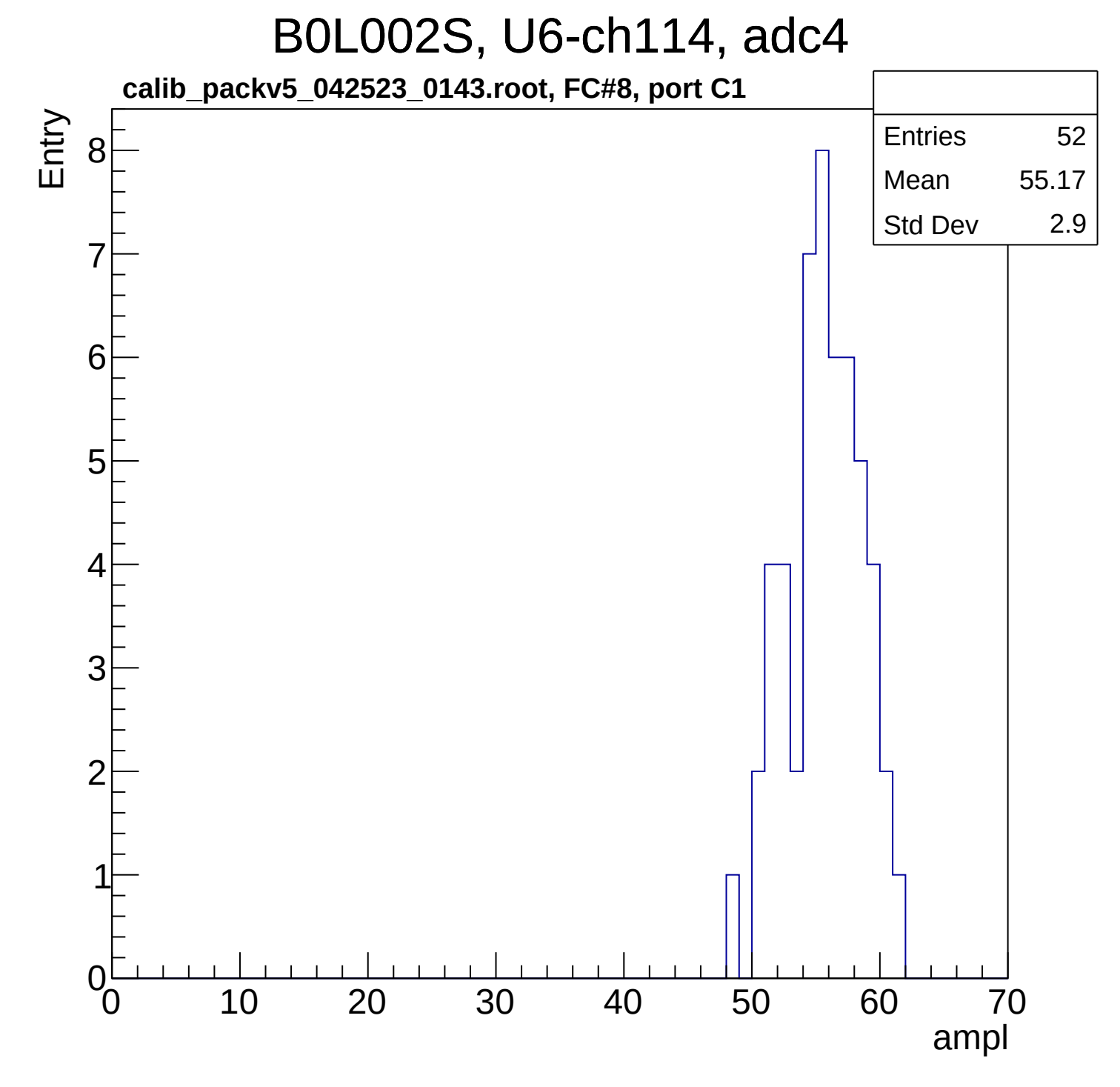
B0L002S, U6-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 55.17
Std Dev 2.9
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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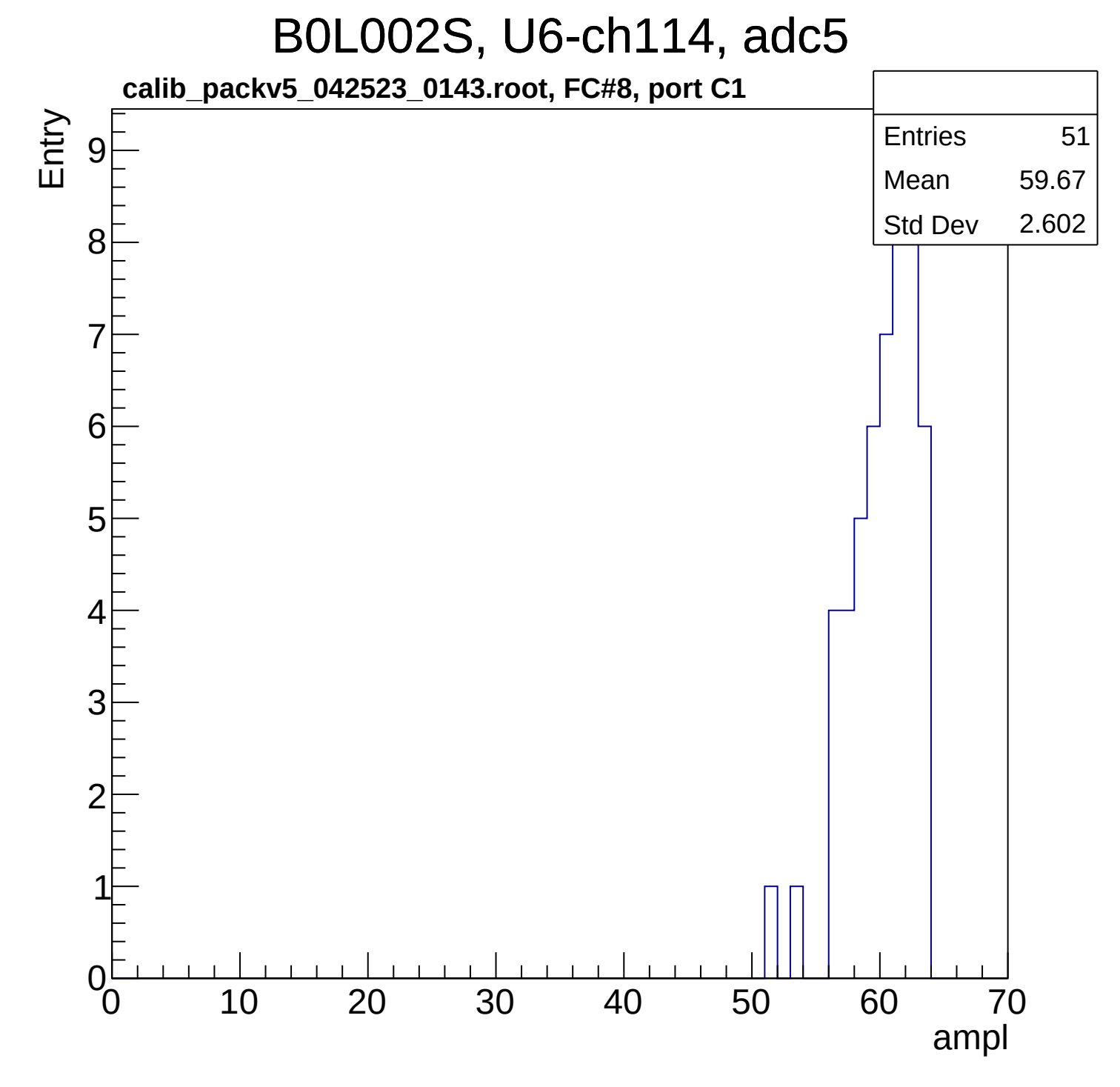
B0L002S, U6-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 59.67
Std Dev 2.602
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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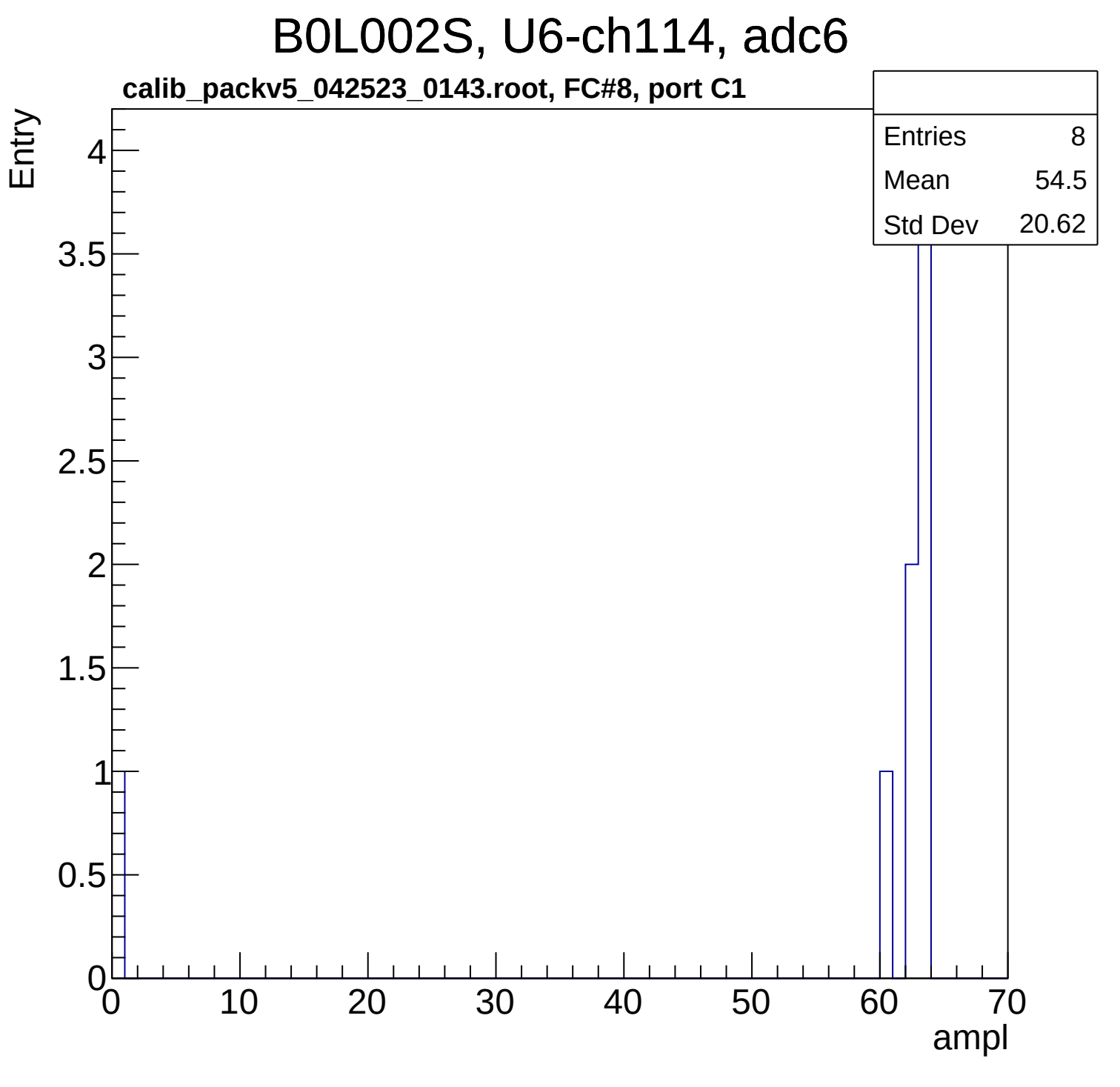
B0L002S, U6-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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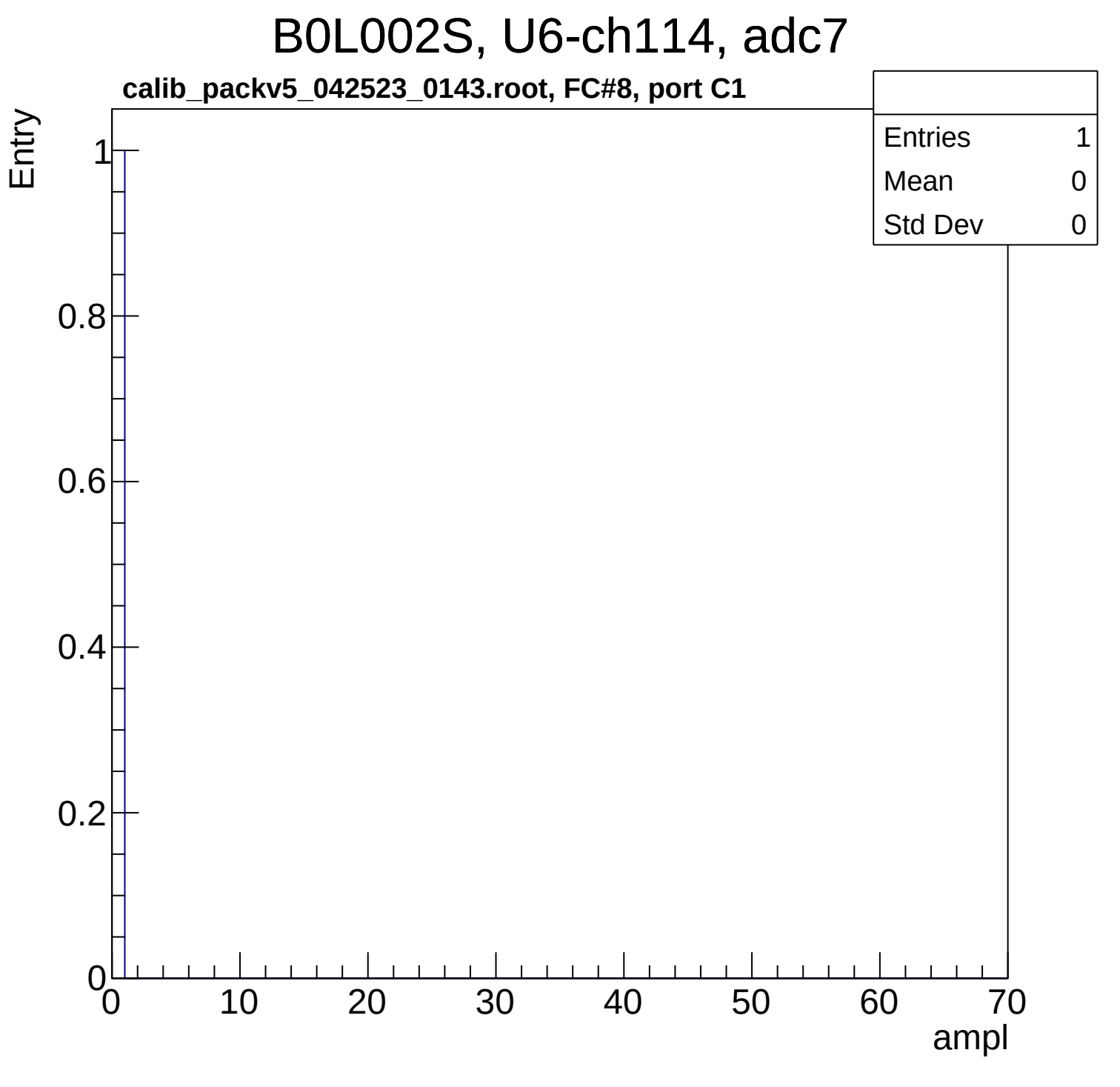
40
50
60
70


B0L002S, U6-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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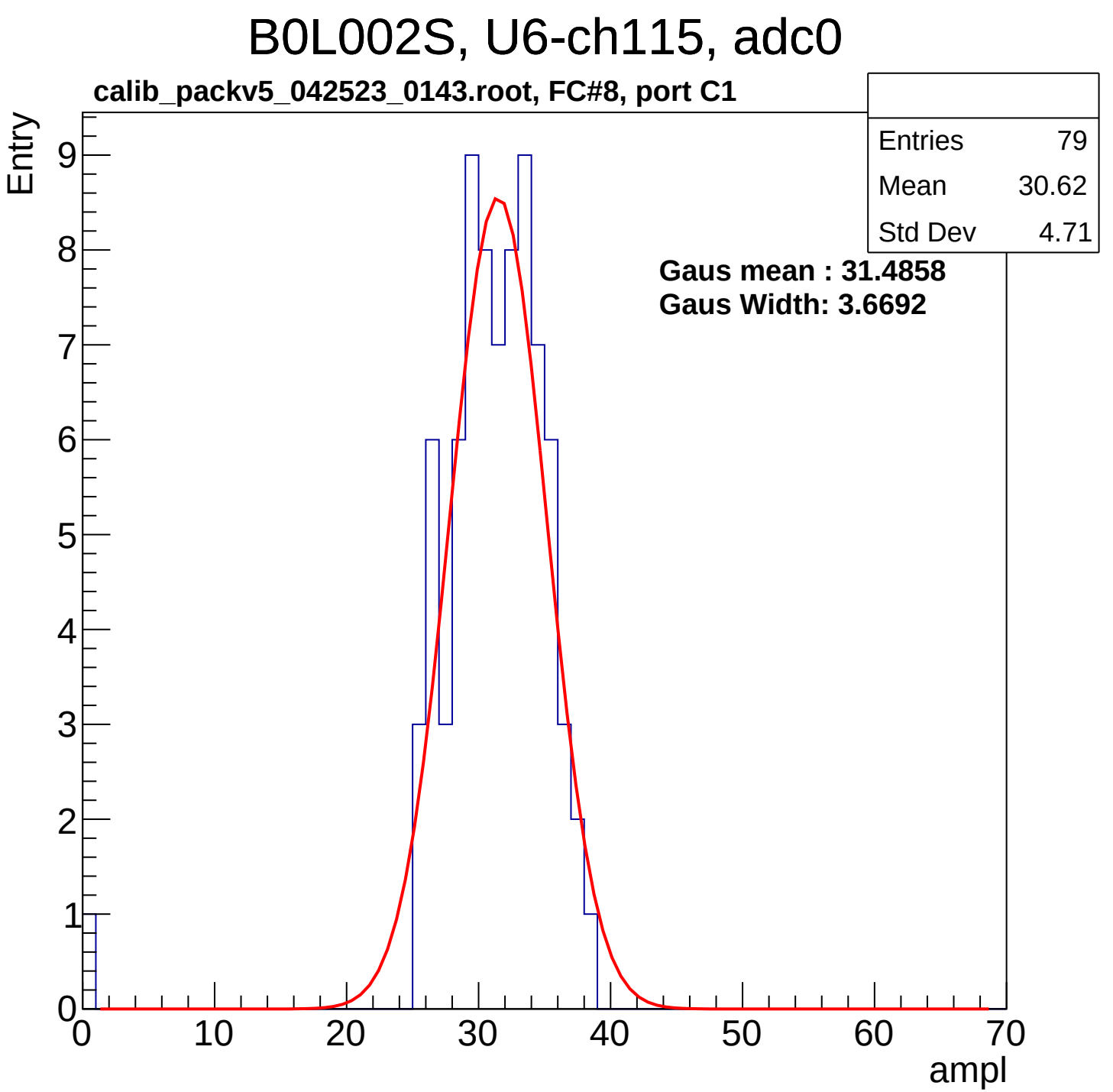
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.62
Std Dev 4.71
Gaus mean : 31.4858
Gaus Width: 3.6692
Entry
ampl
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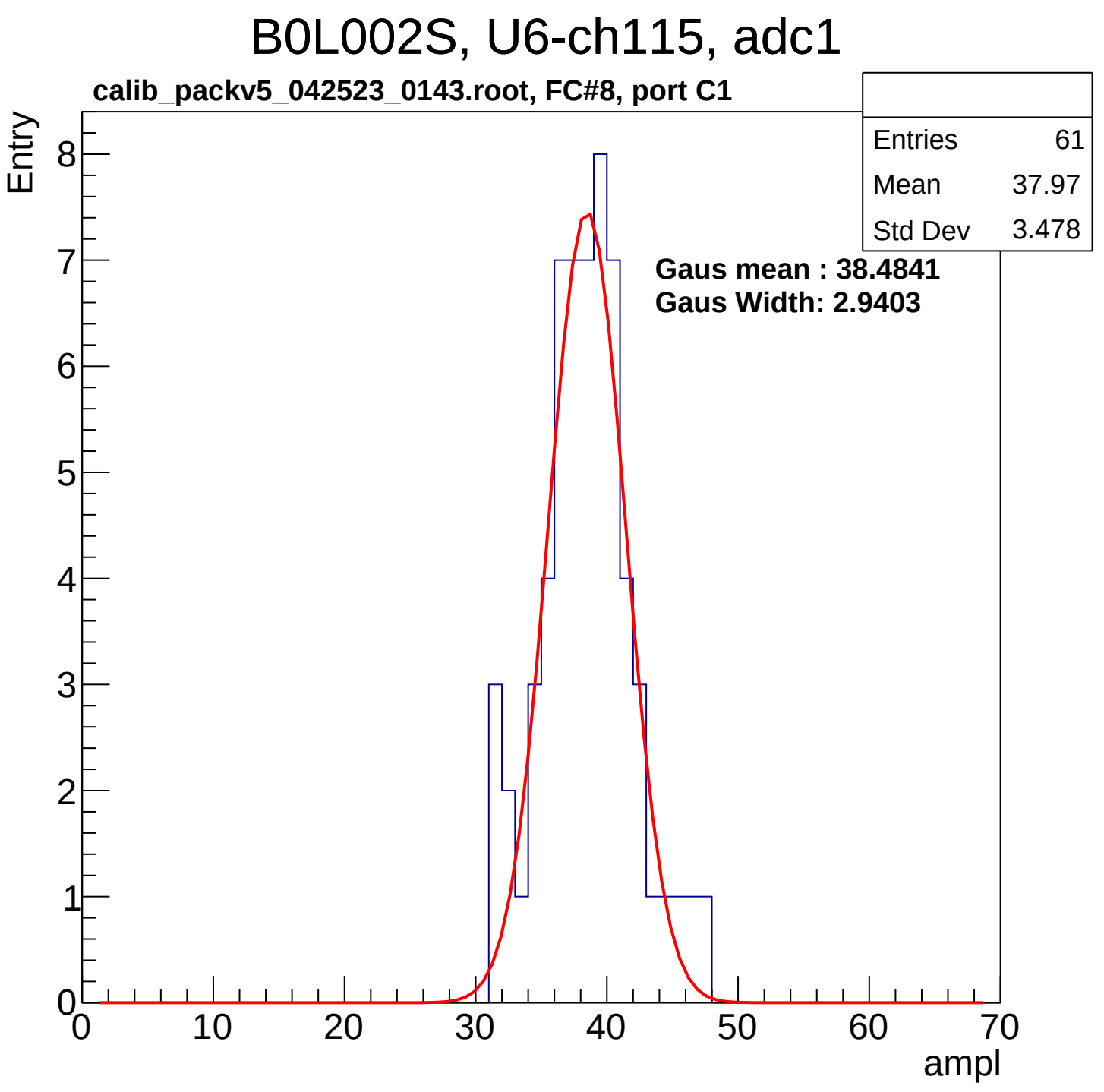
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.97
Std Dev 3.478
Gaus mean : 38.4841
Gaus Width: 2.9403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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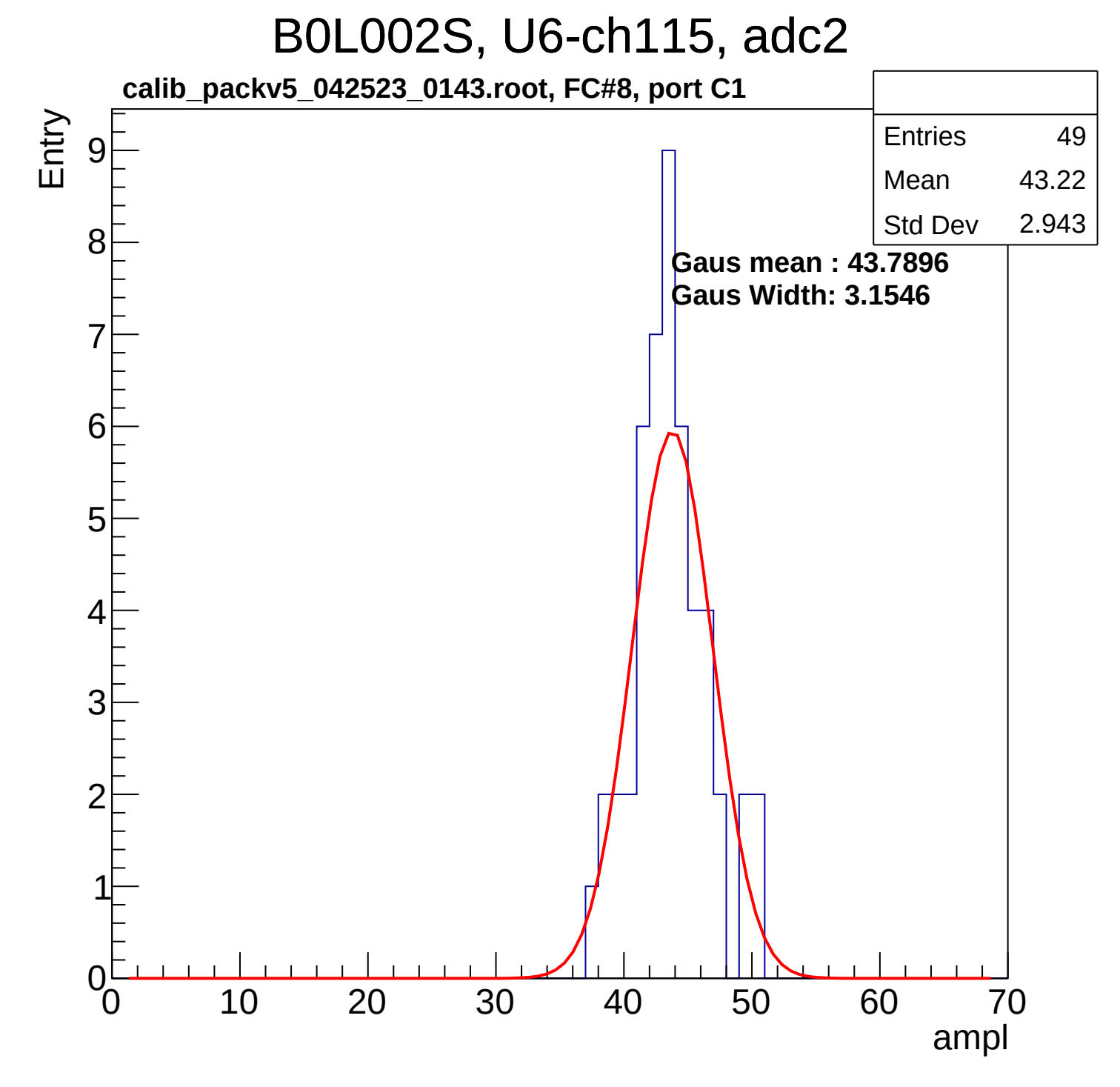
B0L002S, U6-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 43.22
Std Dev 2.943
Gaus mean : 43.7896
Gaus Width: 3.1546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
50
60
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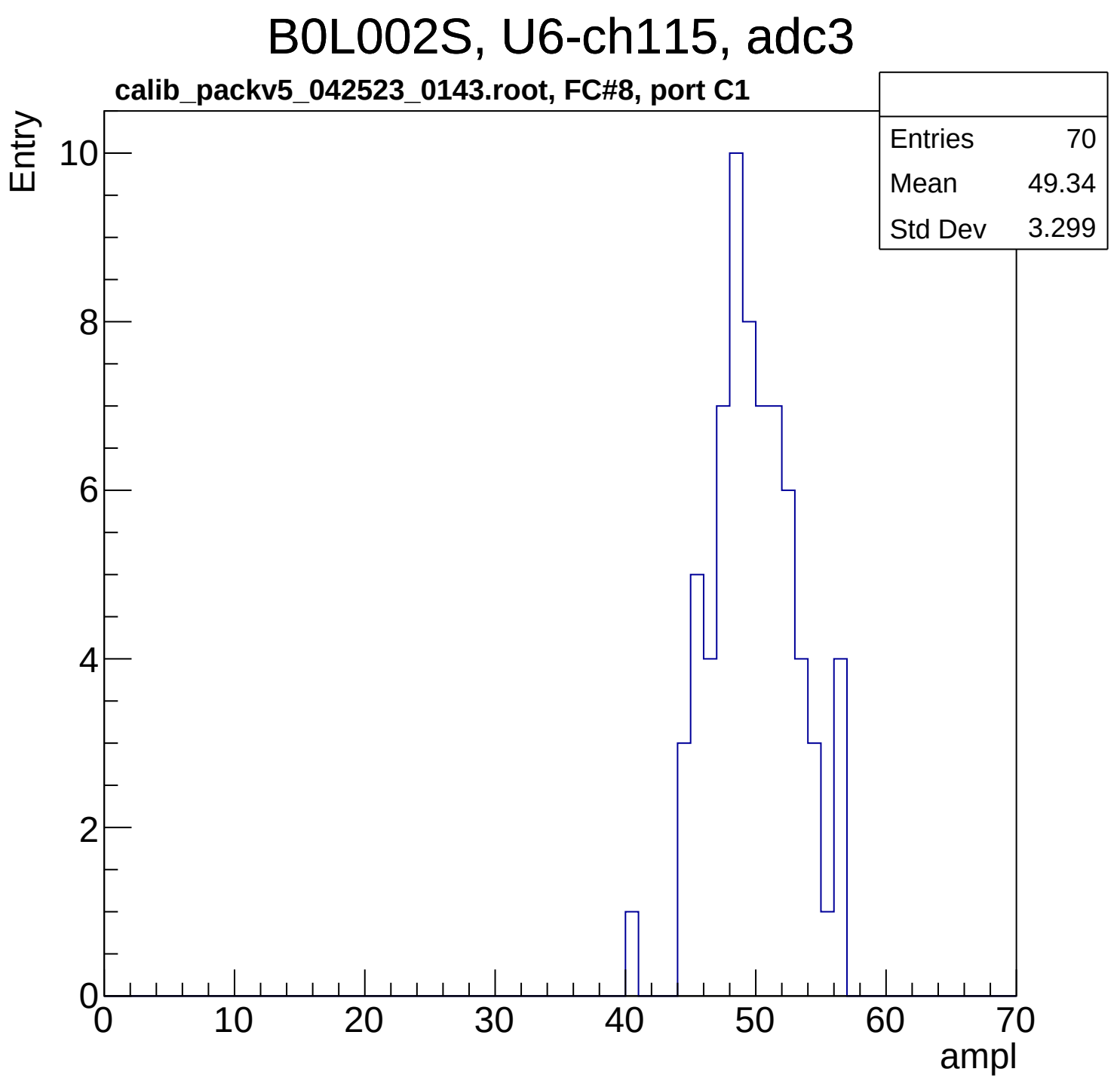
B0L002S, U6-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 49.34
Std Dev 3.299
Entry
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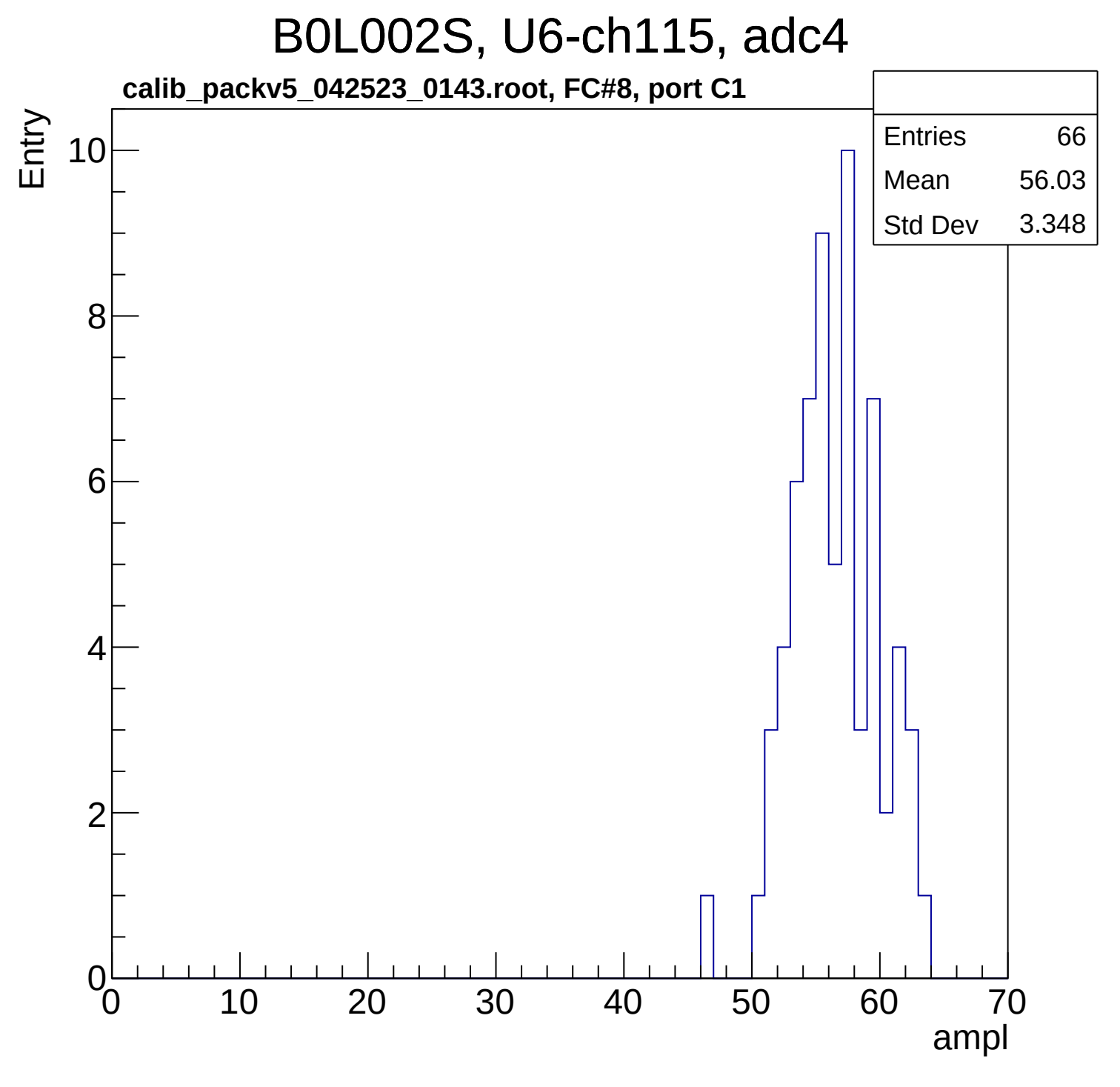
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 56.03
Std Dev 3.348
Entry
ampl
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40
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60
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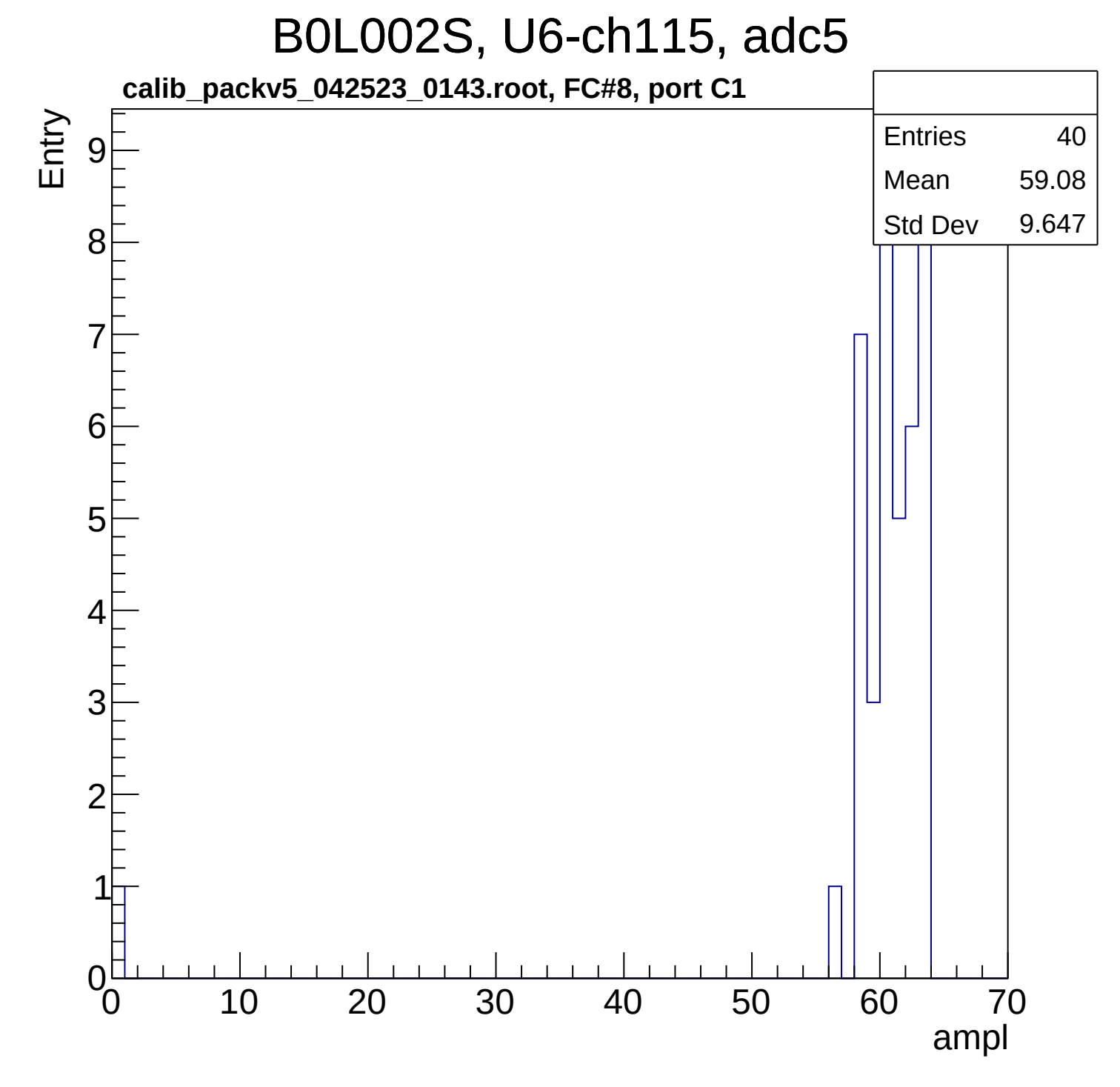
B0L002S, U6-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.08
Std Dev 9.647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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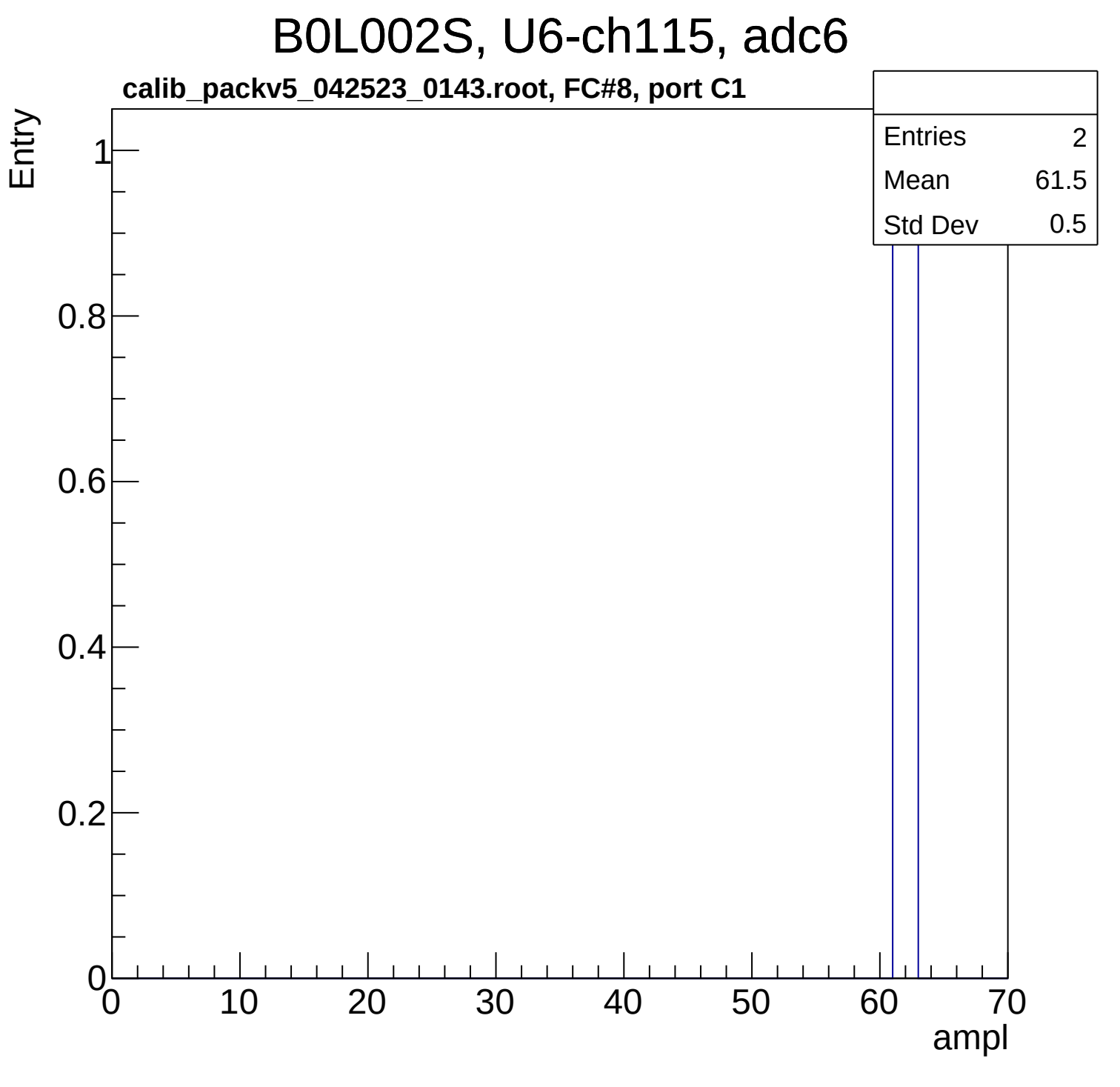
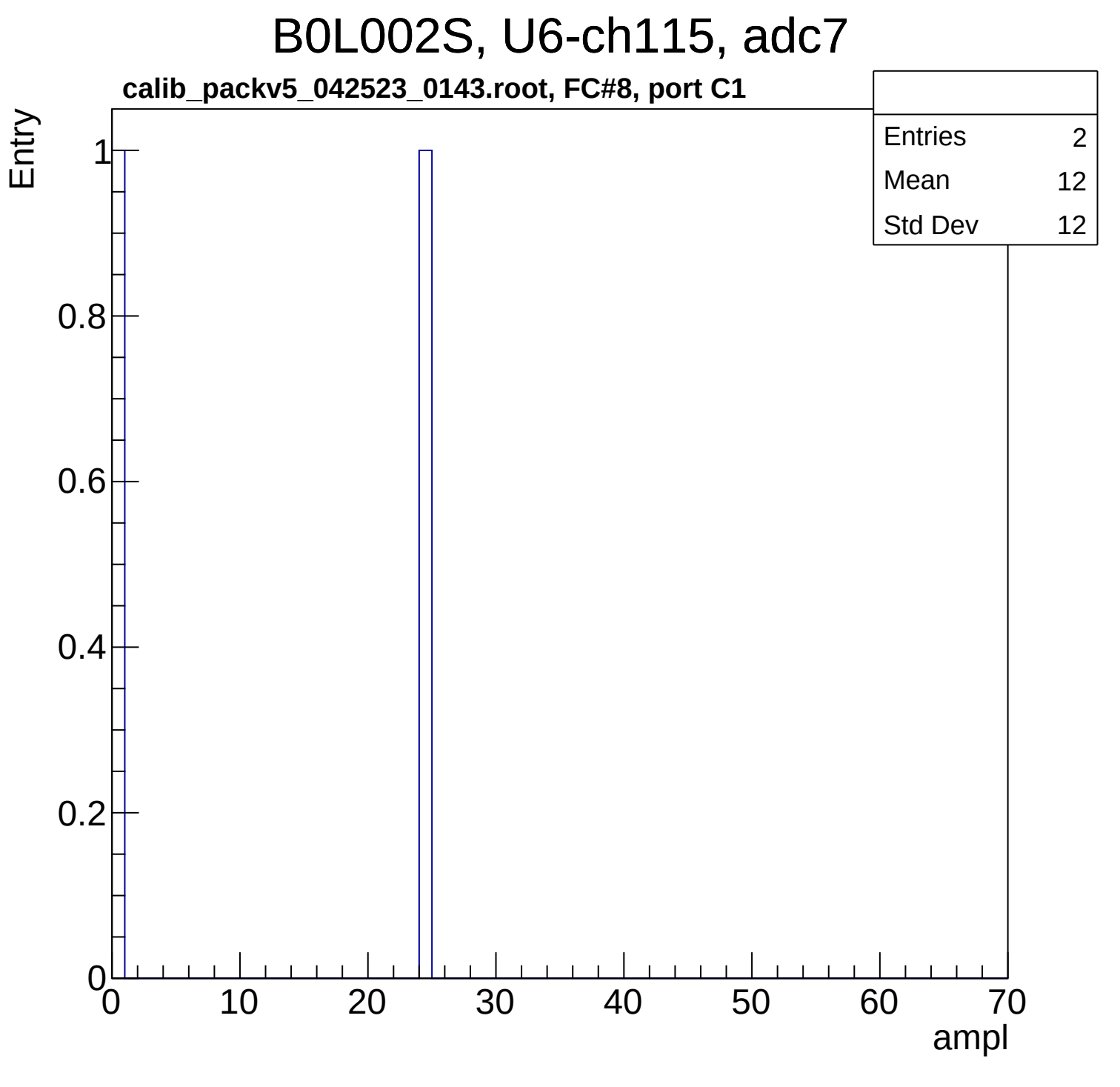
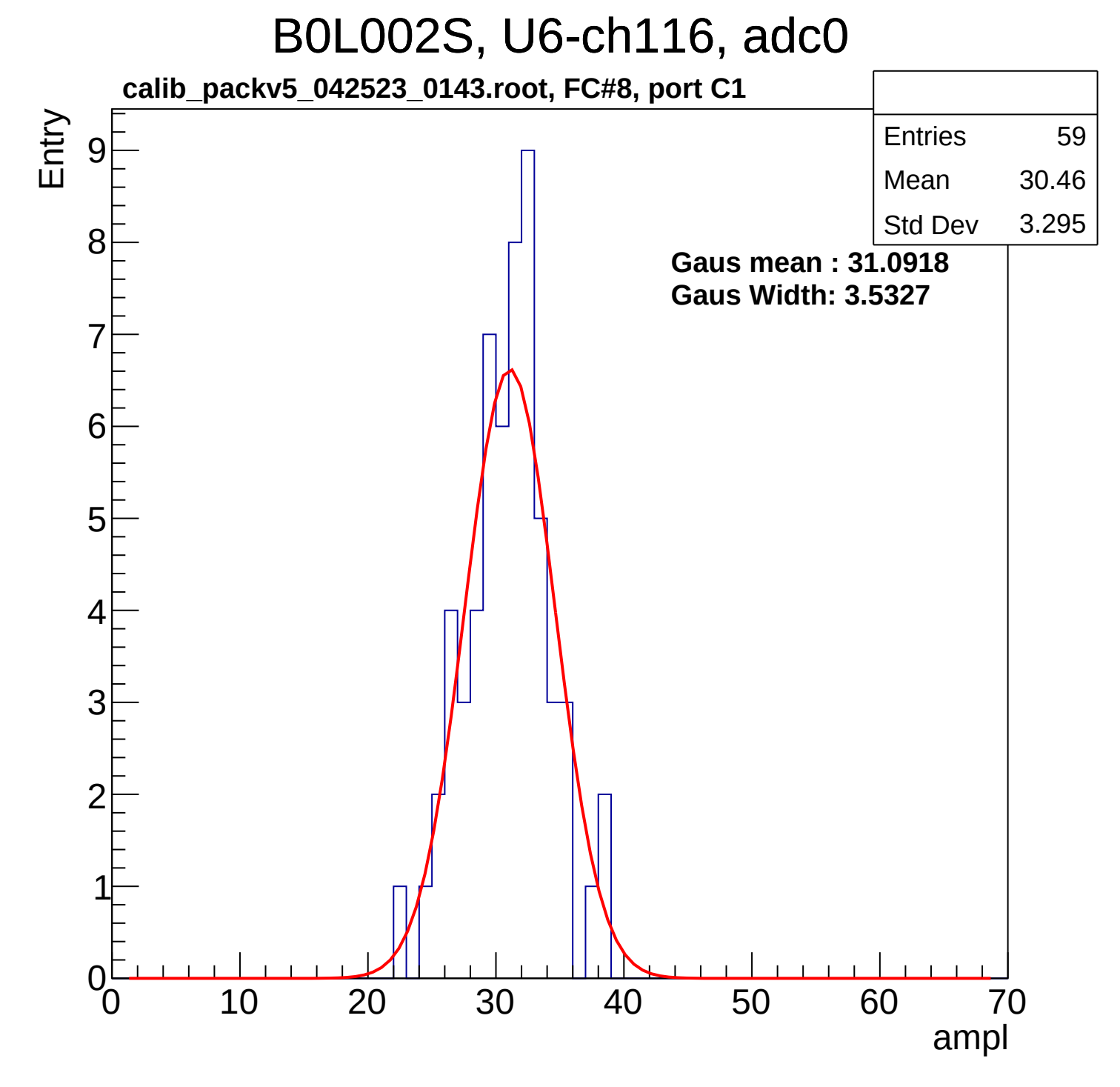

B0L002S, U6-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.46
Std Dev 3.295
Gaus mean : 31.0918
Gaus Width: 3.5327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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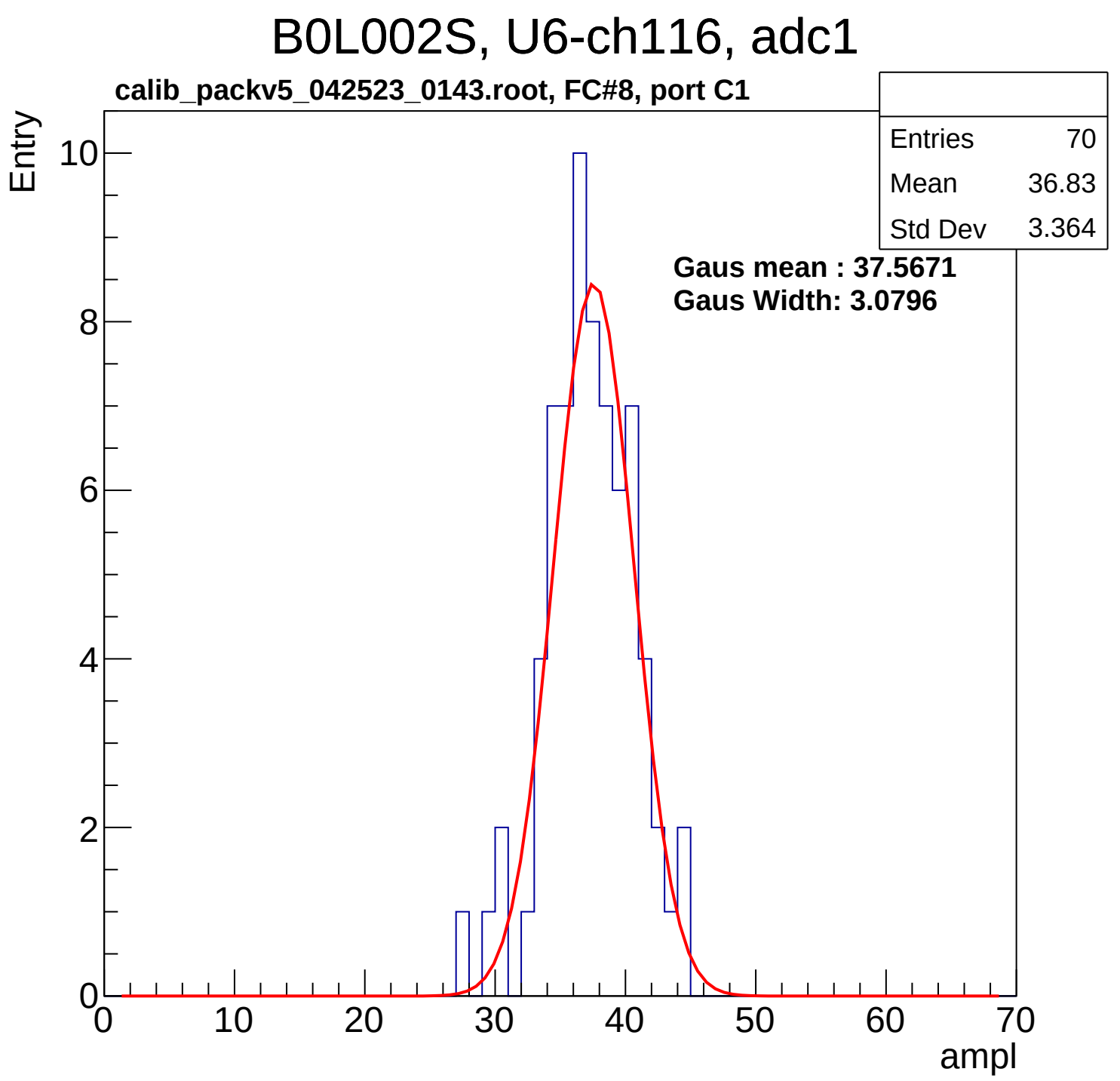
B0L002S, U6-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.83
Std Dev 3.364
Gaus mean : 37.5671
Gaus Width: 3.0796
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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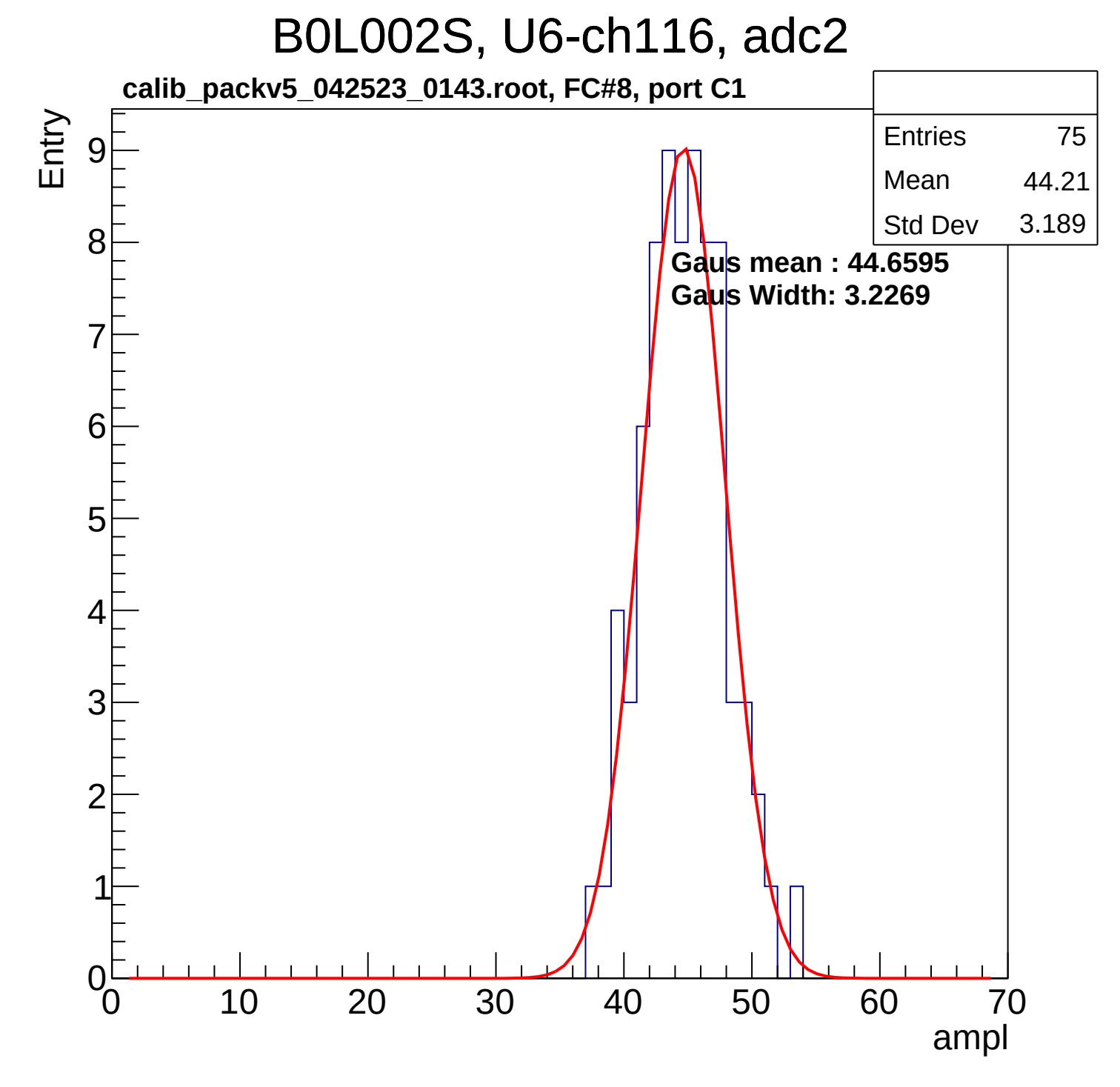
B0L002S, U6-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.21
Std Dev 3.189
Gaus mean : 44.6595
Gaus Width: 3.2269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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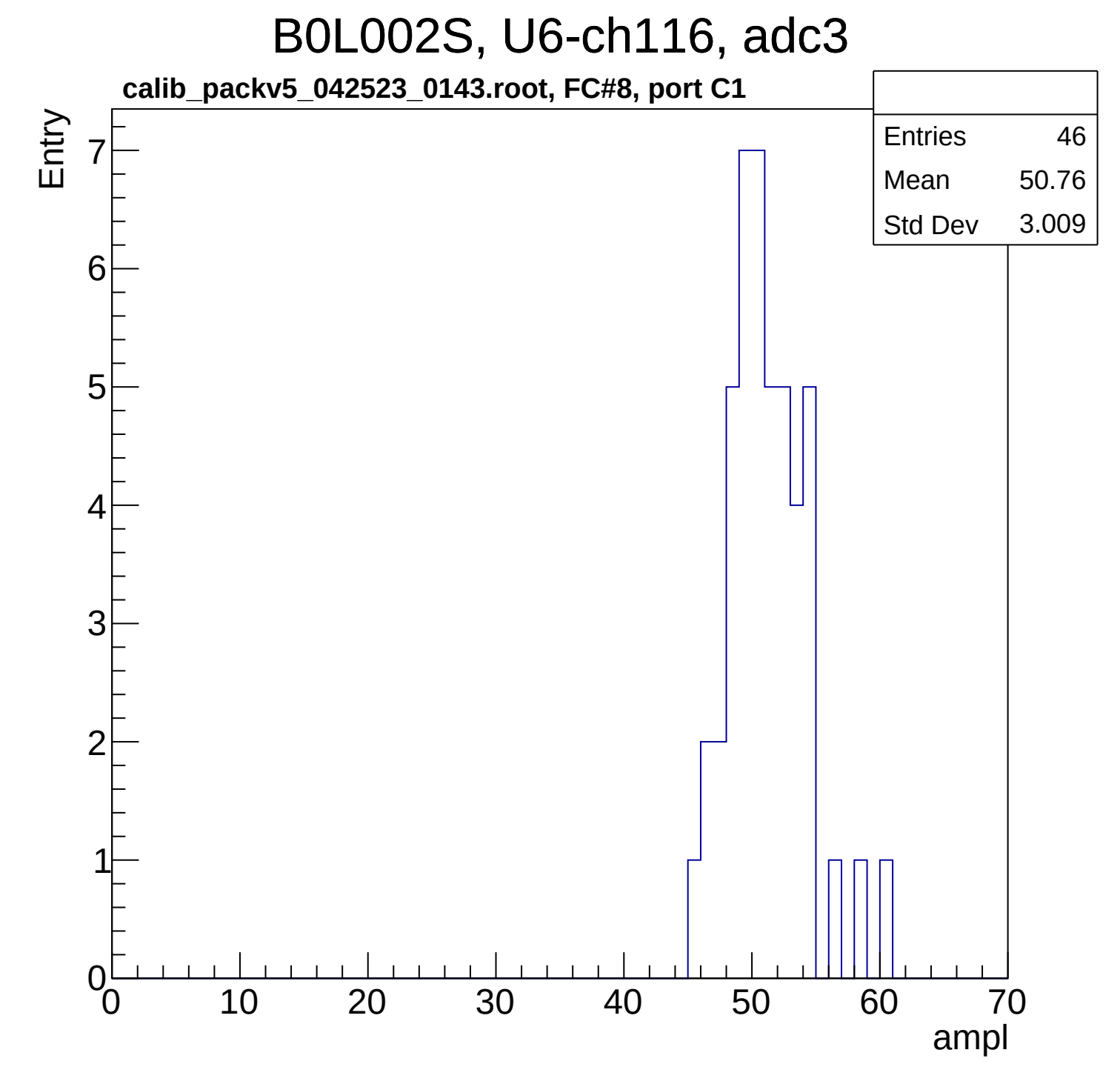
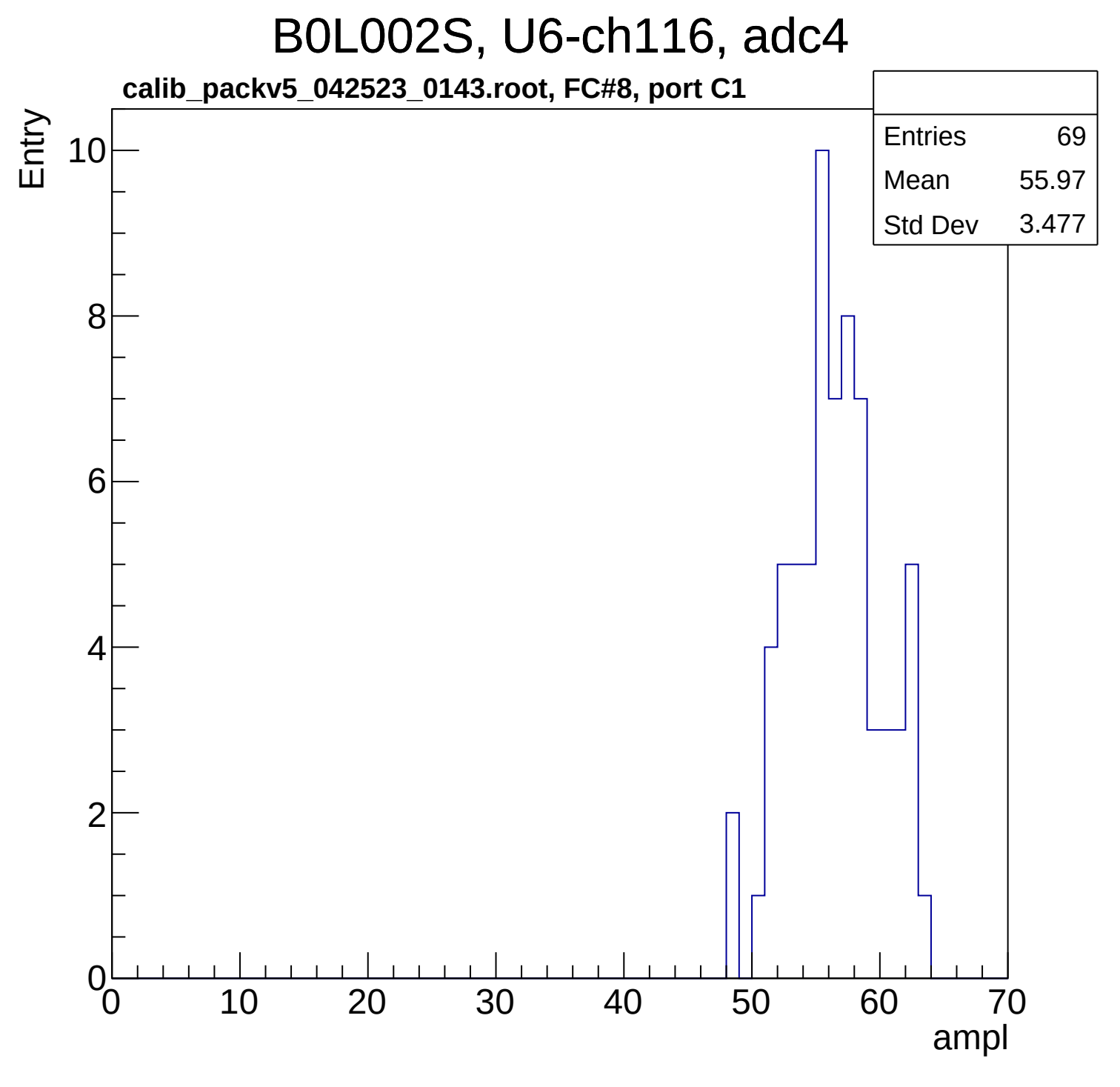

B0L002S, U6-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 50.76
Std Dev 3.009
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 55.97
Std Dev 3.477
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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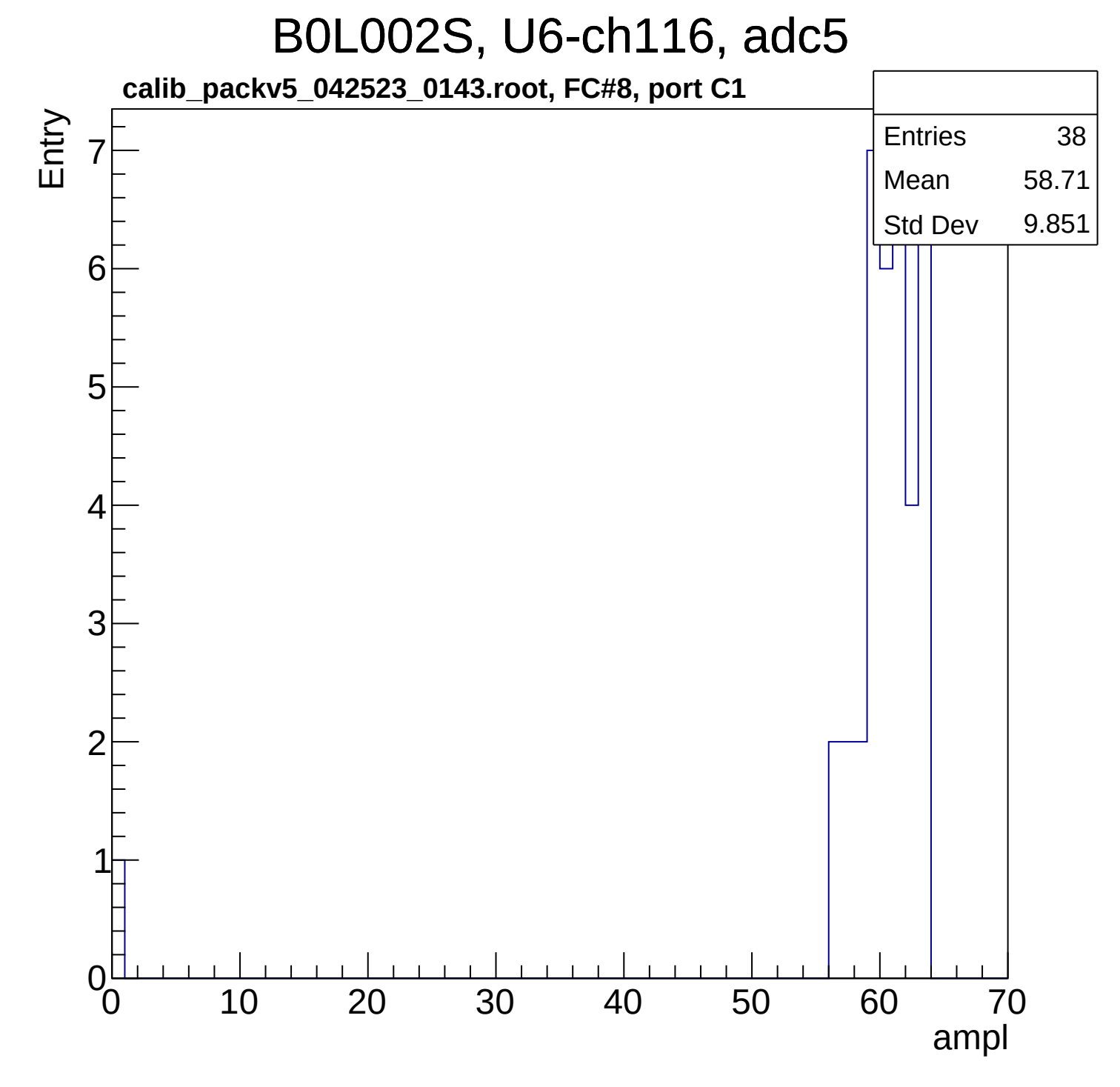
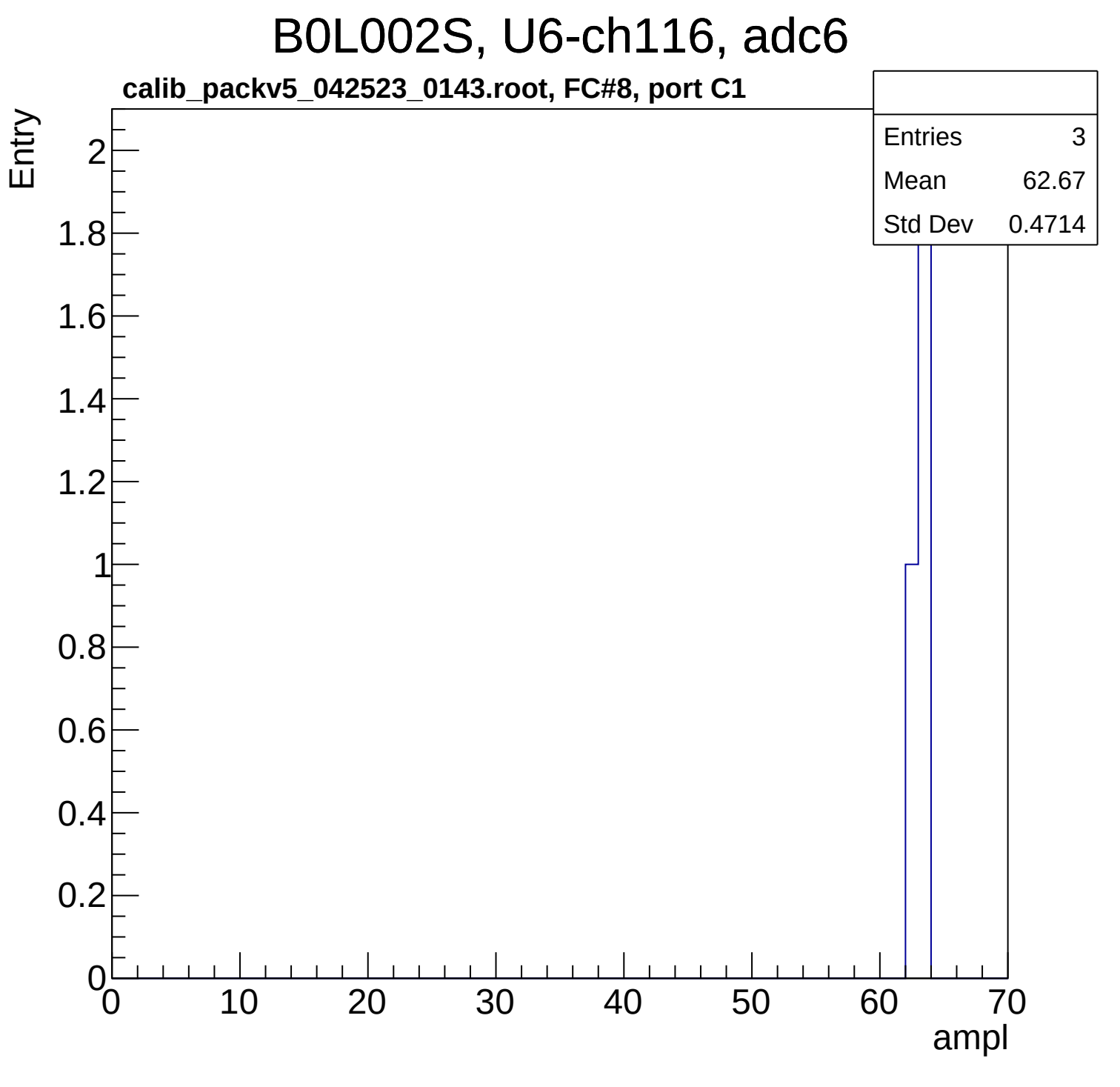
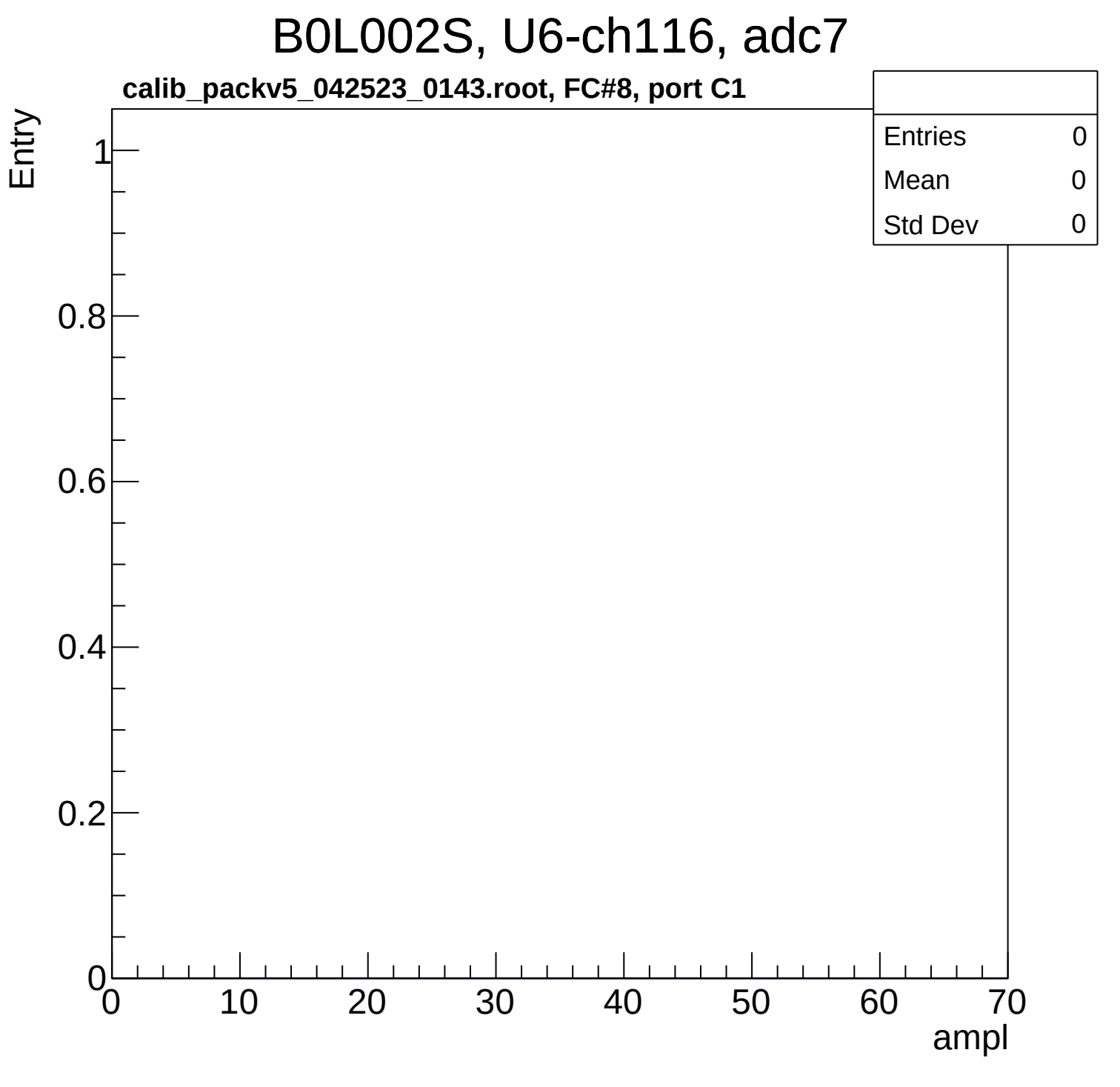
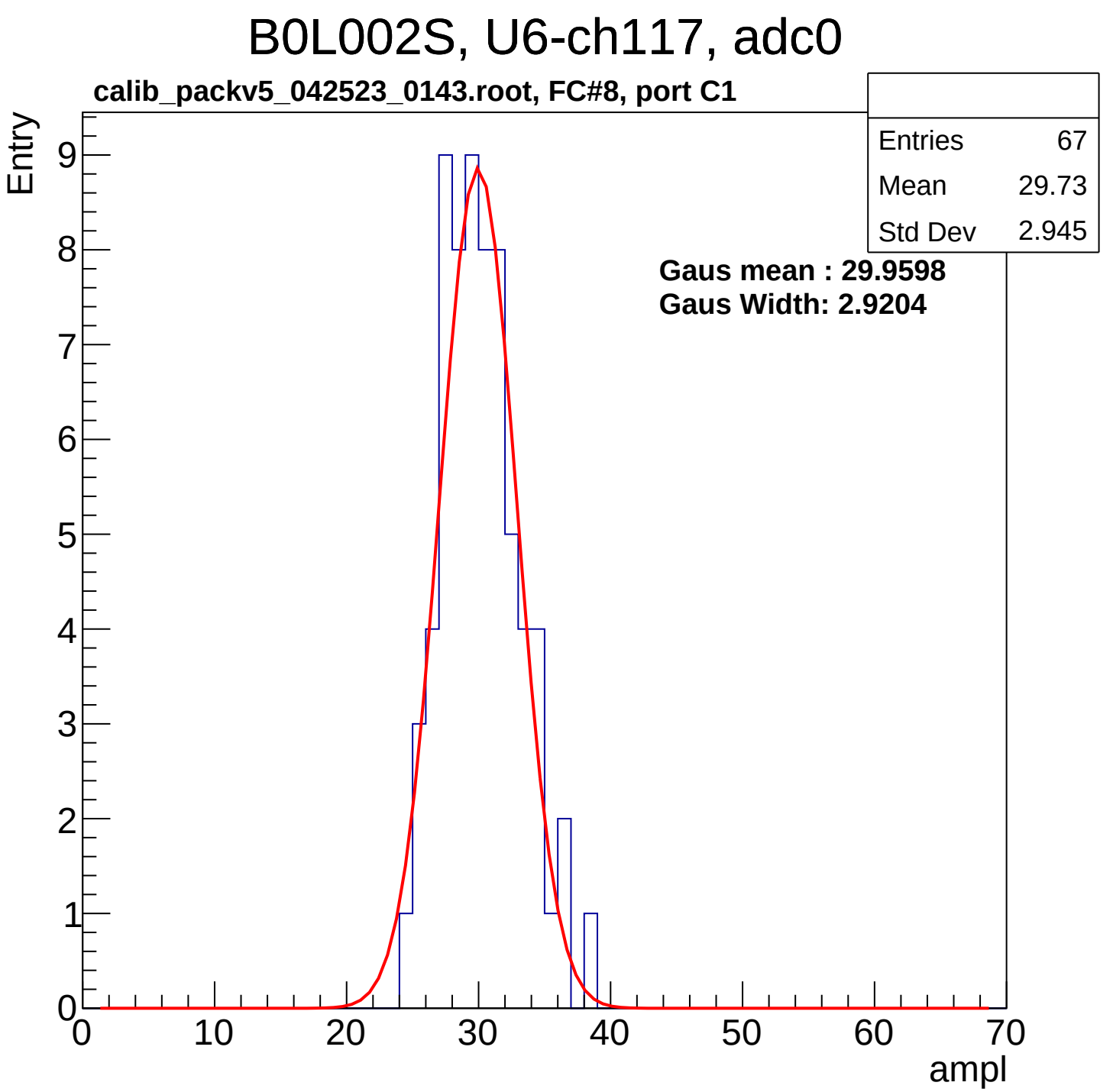
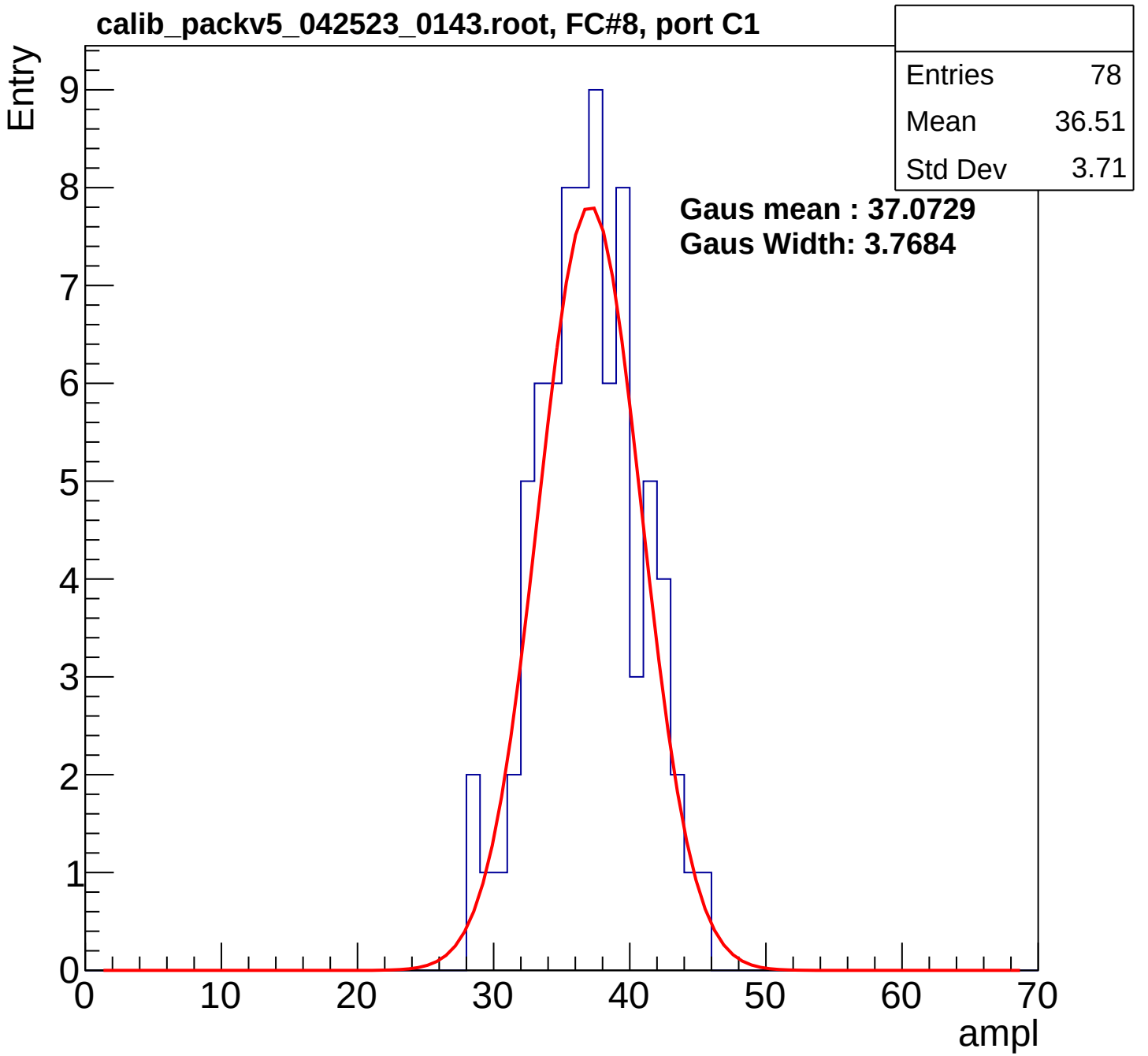

B0L002S, U6-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 58.71
Std Dev 9.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.73
Std Dev 2.945
Gaus mean : 29.9598
Gaus Width: 2.9204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 36.51
Std Dev 3.71
Gaus mean : 37.0729
Gaus Width: 3.7684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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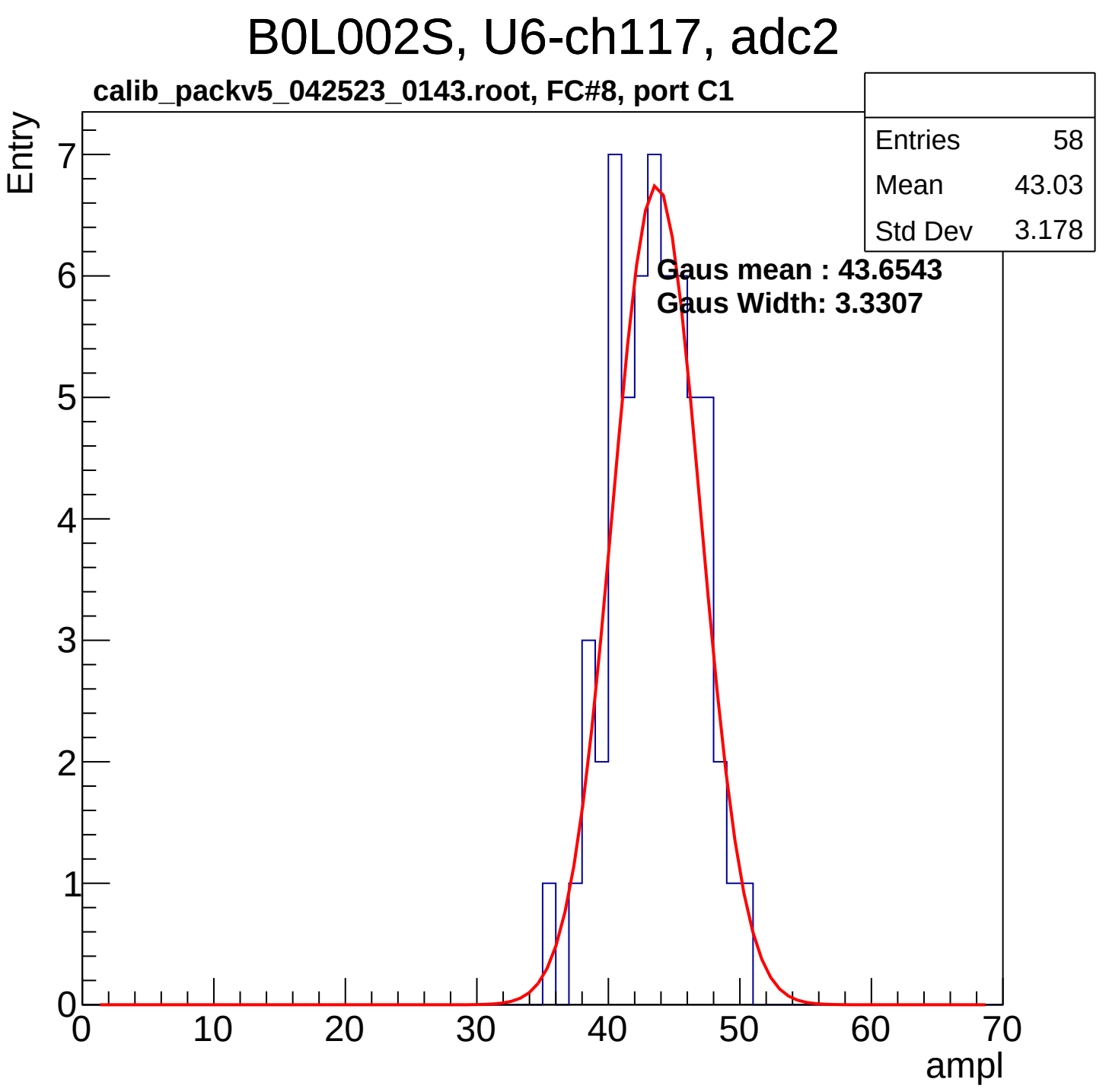
B0L002S, U6-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.03
Std Dev 3.178
Gaus mean : 43.6543
Gaus Width: 3.3307
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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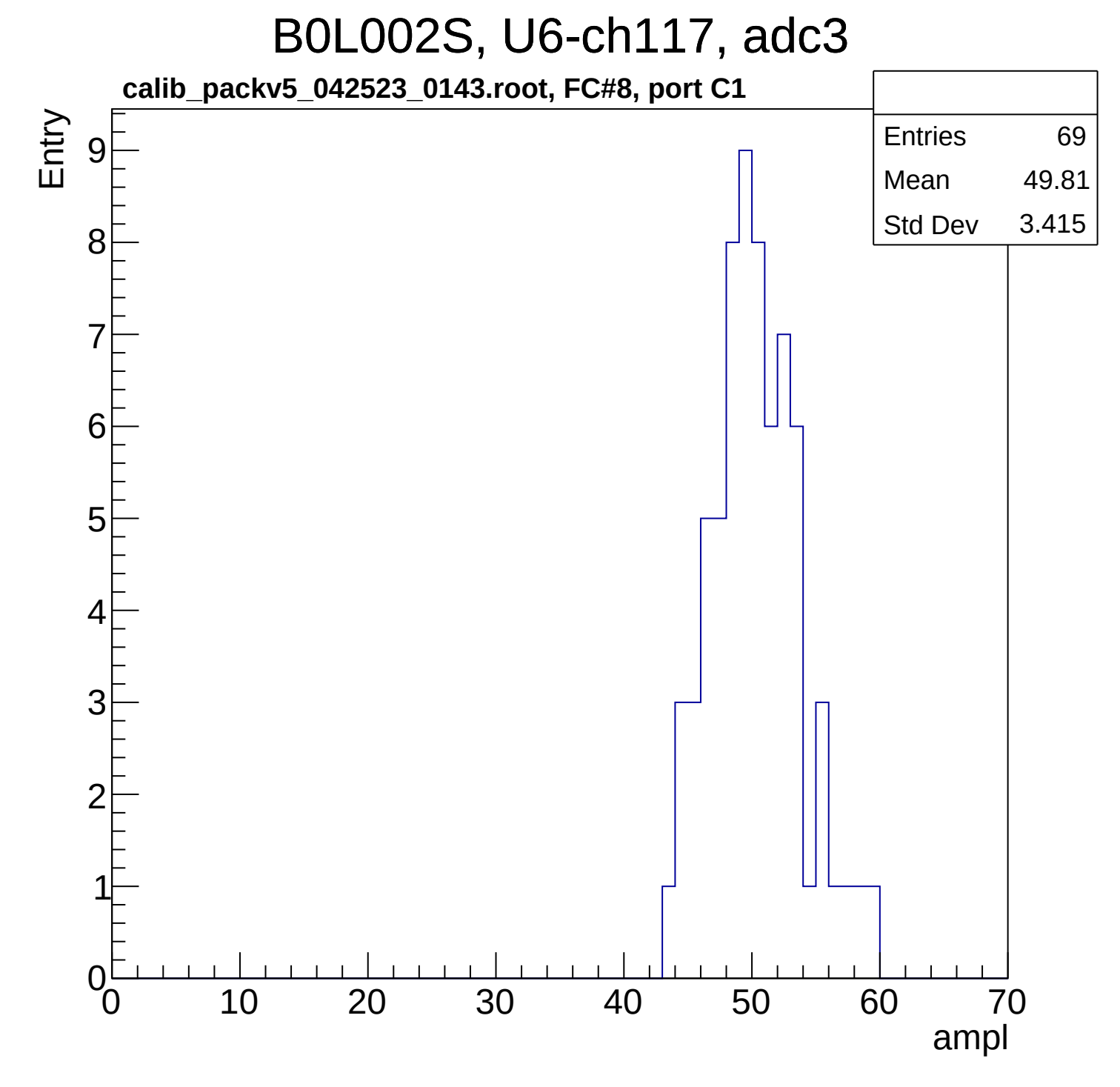
B0L002S, U6-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 49.81
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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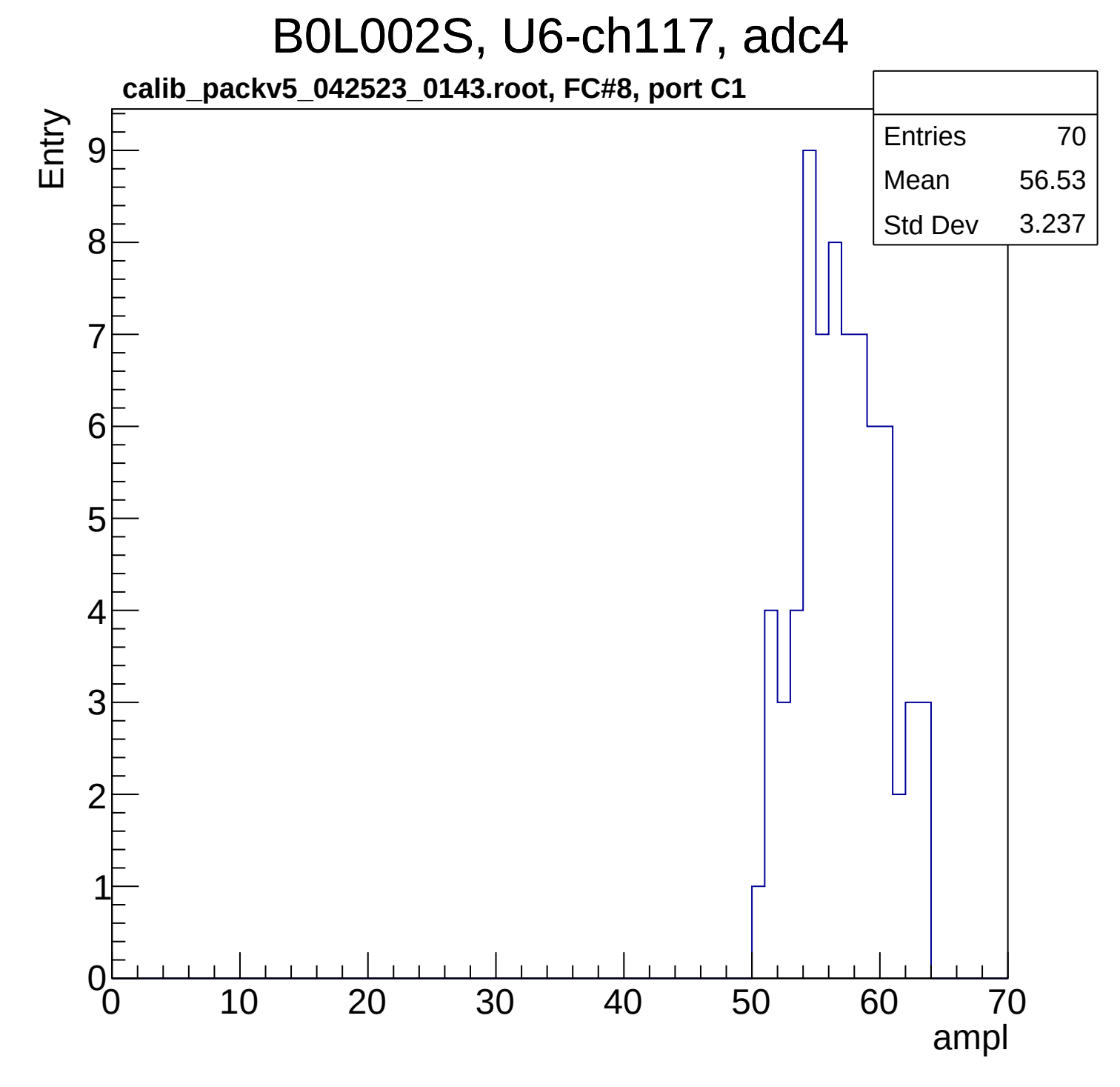
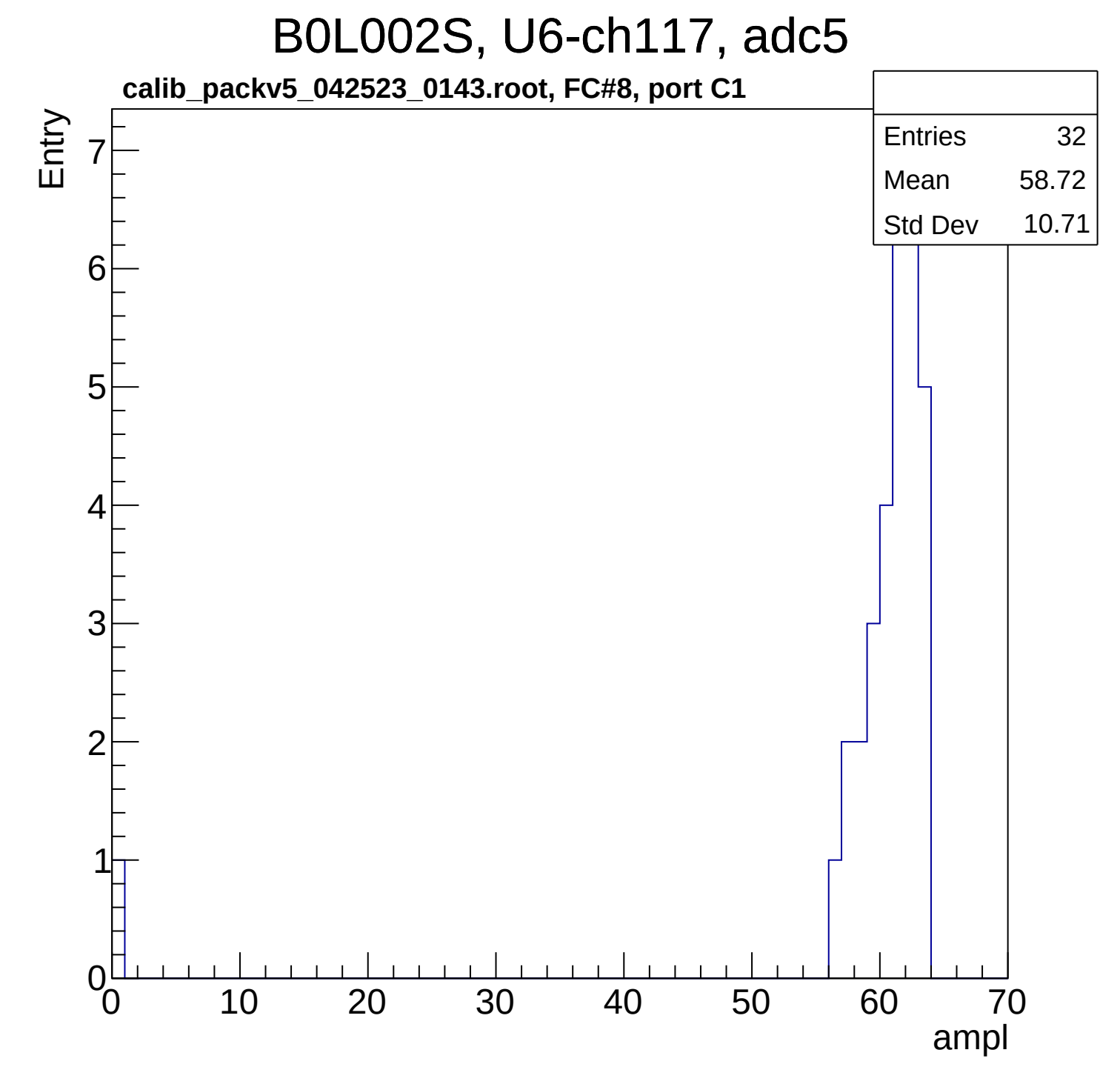
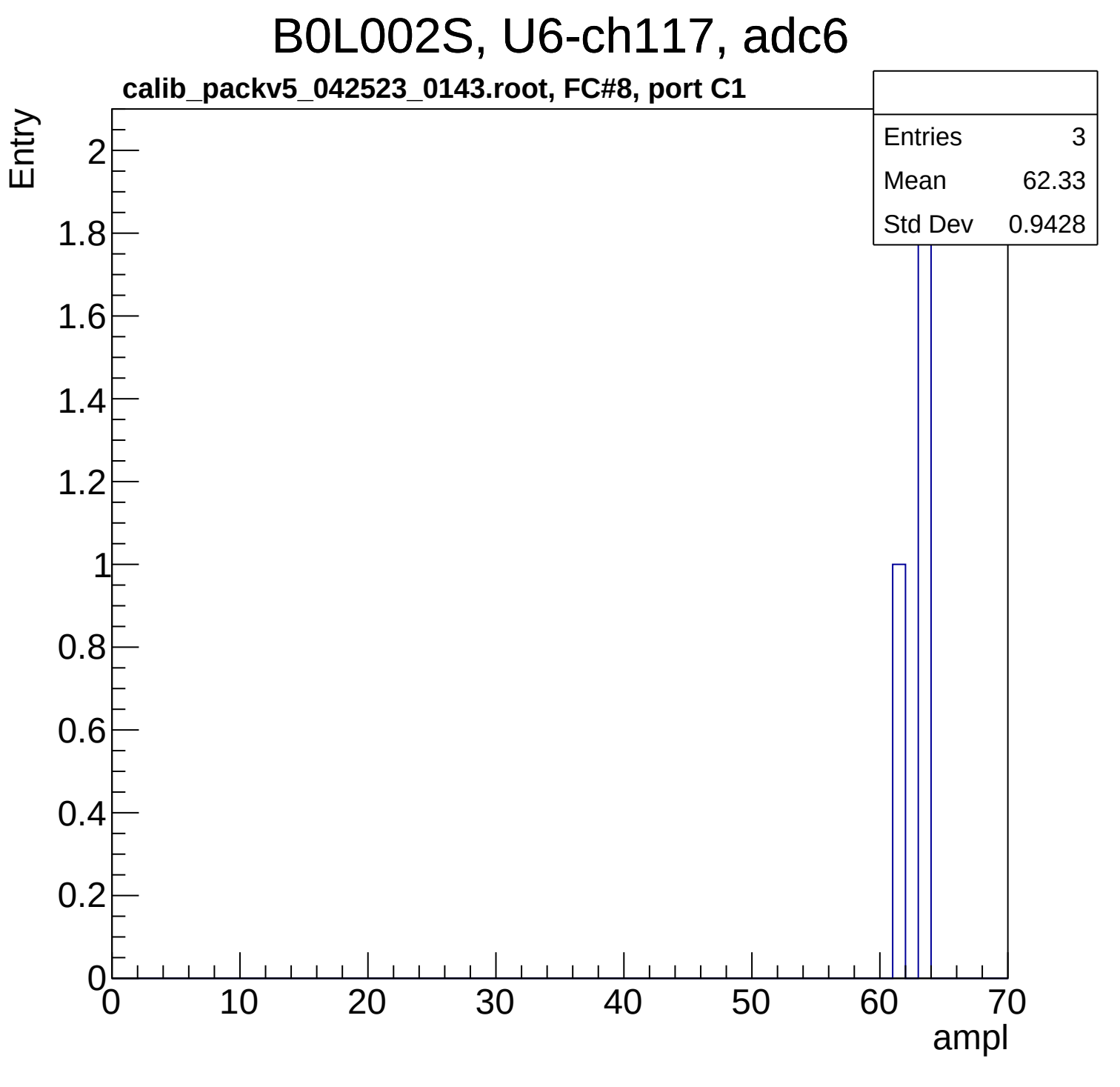
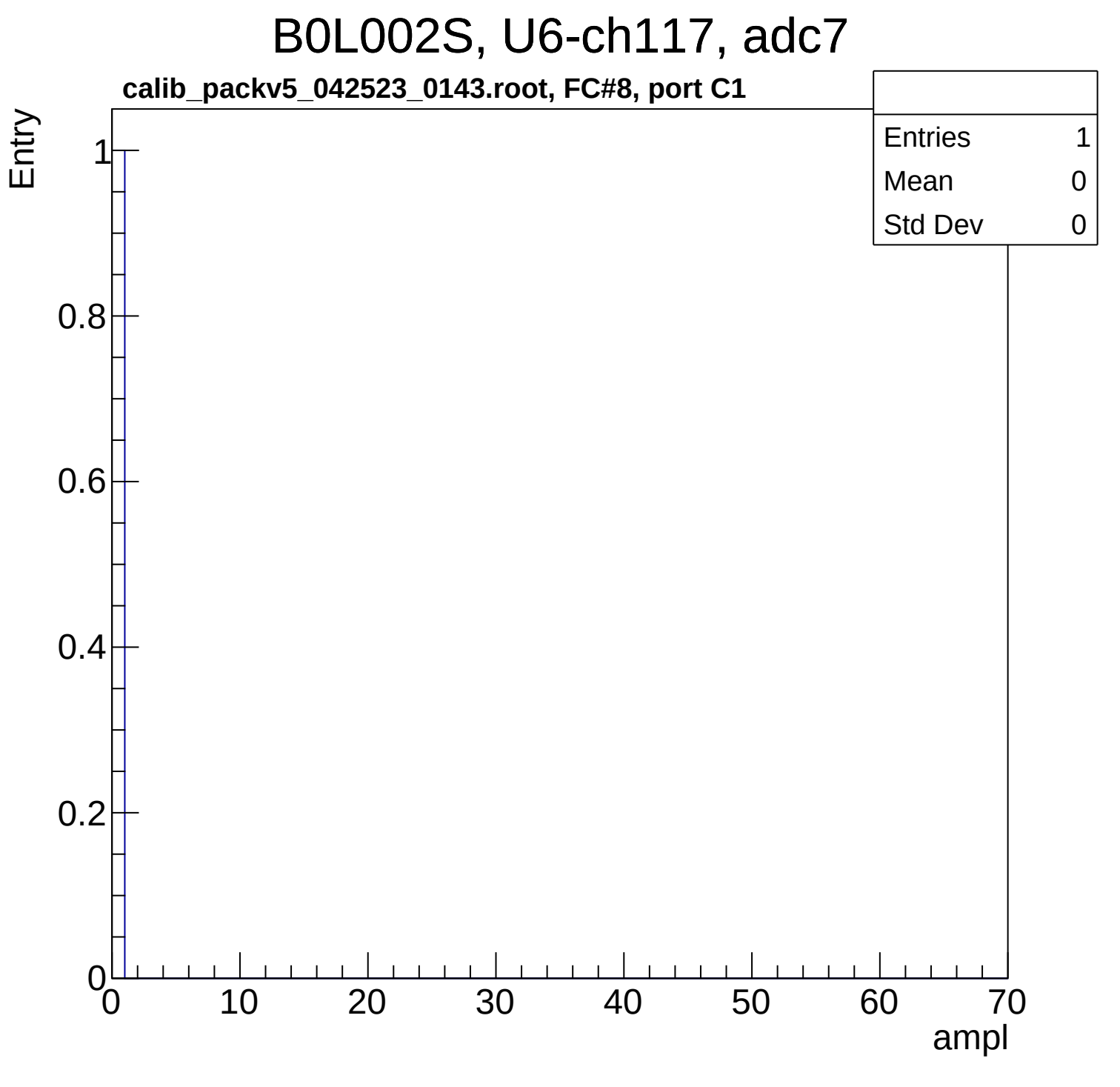
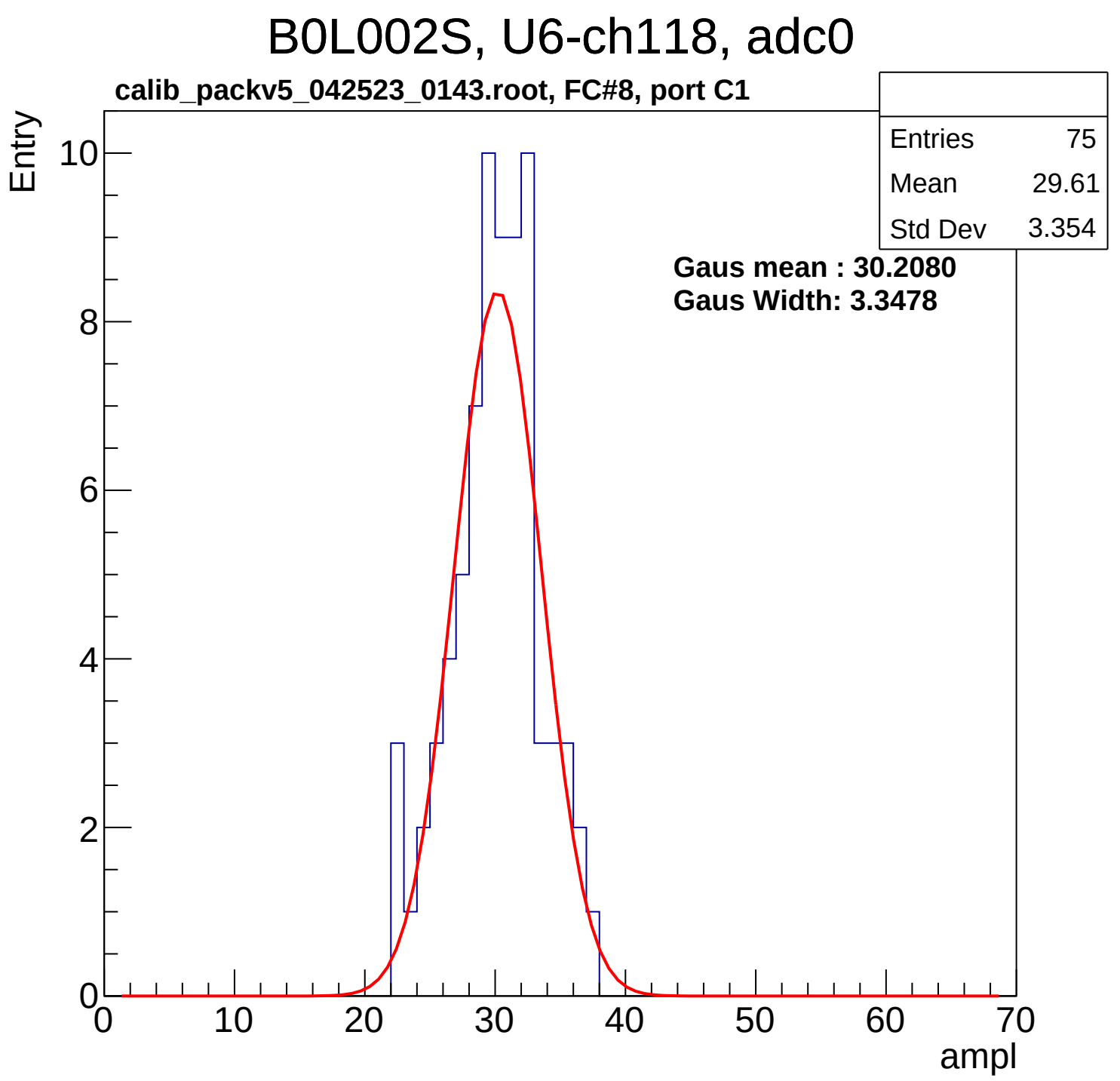

B0L002S, U6-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 56.53
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.72
Std Dev 10.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.61
Std Dev 3.354
Gaus mean : 30.2080
Gaus Width: 3.3478
Entry
ampl
0
2
4
6
8
10
0
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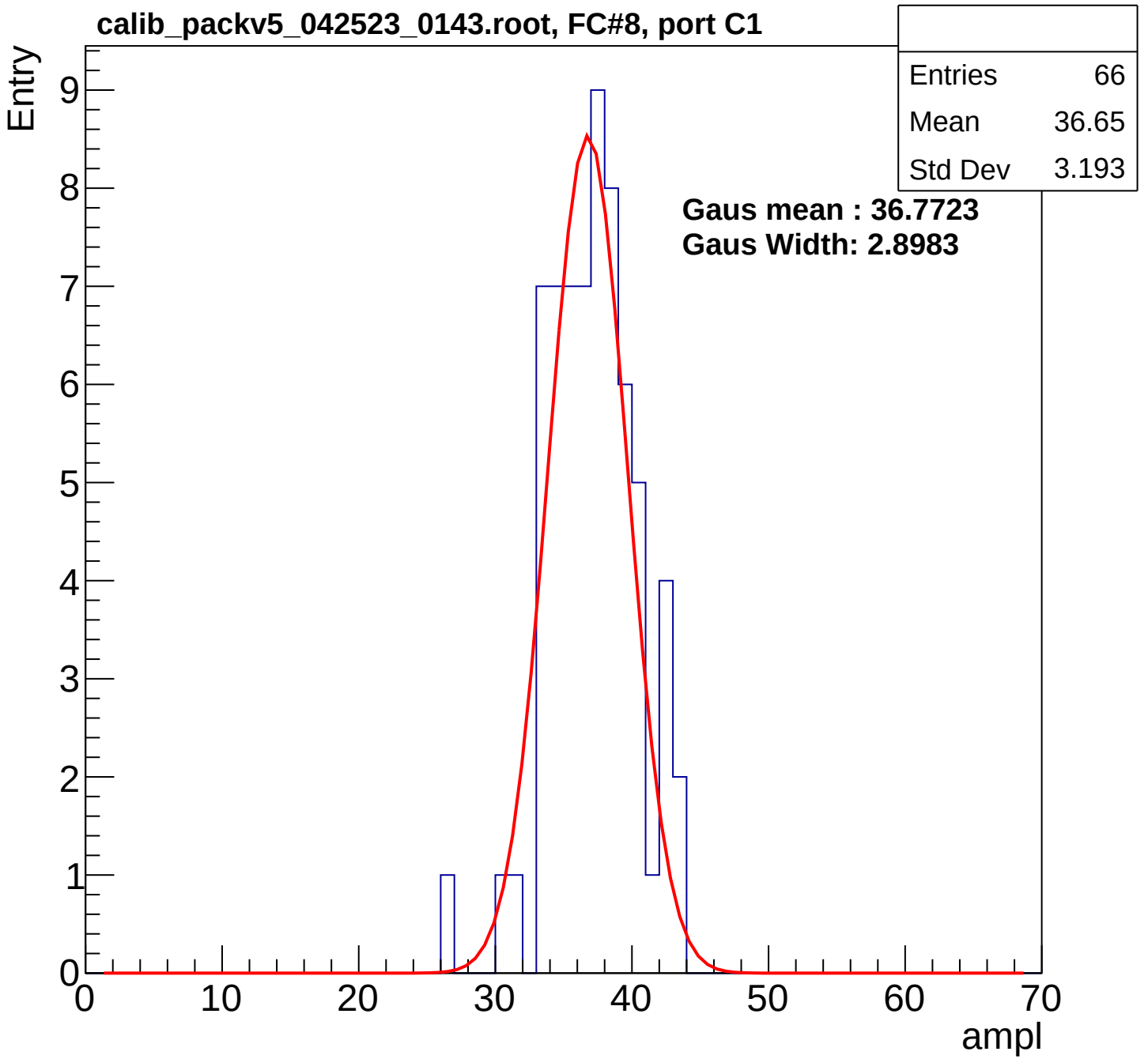
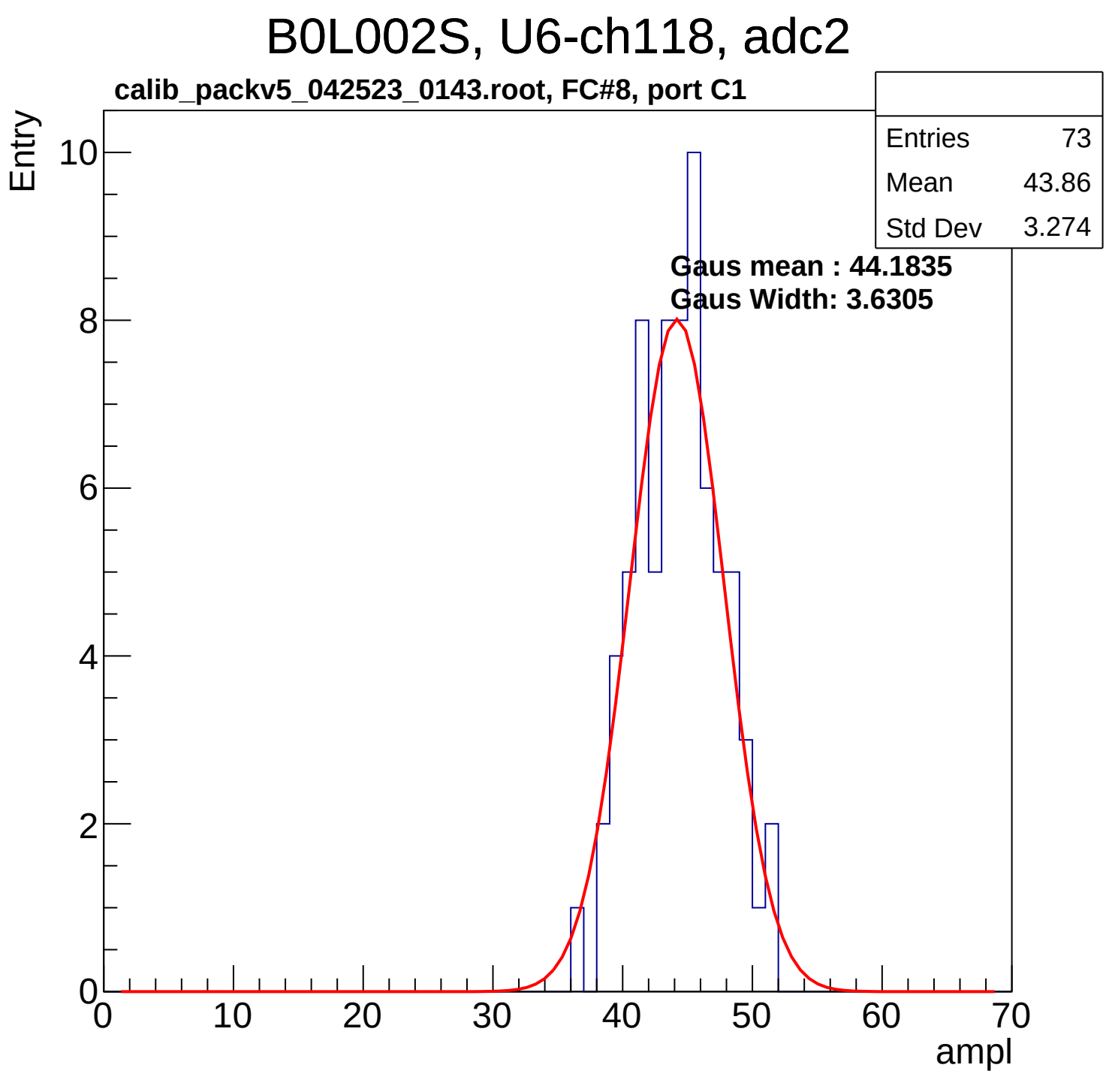
20
30
40
50
60
70


B0L002S, U6-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.65
Std Dev 3.193
Gaus mean : 36.7723
Gaus Width: 2.8983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.86
Std Dev 3.274
Gaus mean : 44.1835
Gaus Width: 3.6305
Entry
ampl
0
2
4
6
8
10
0
10
20
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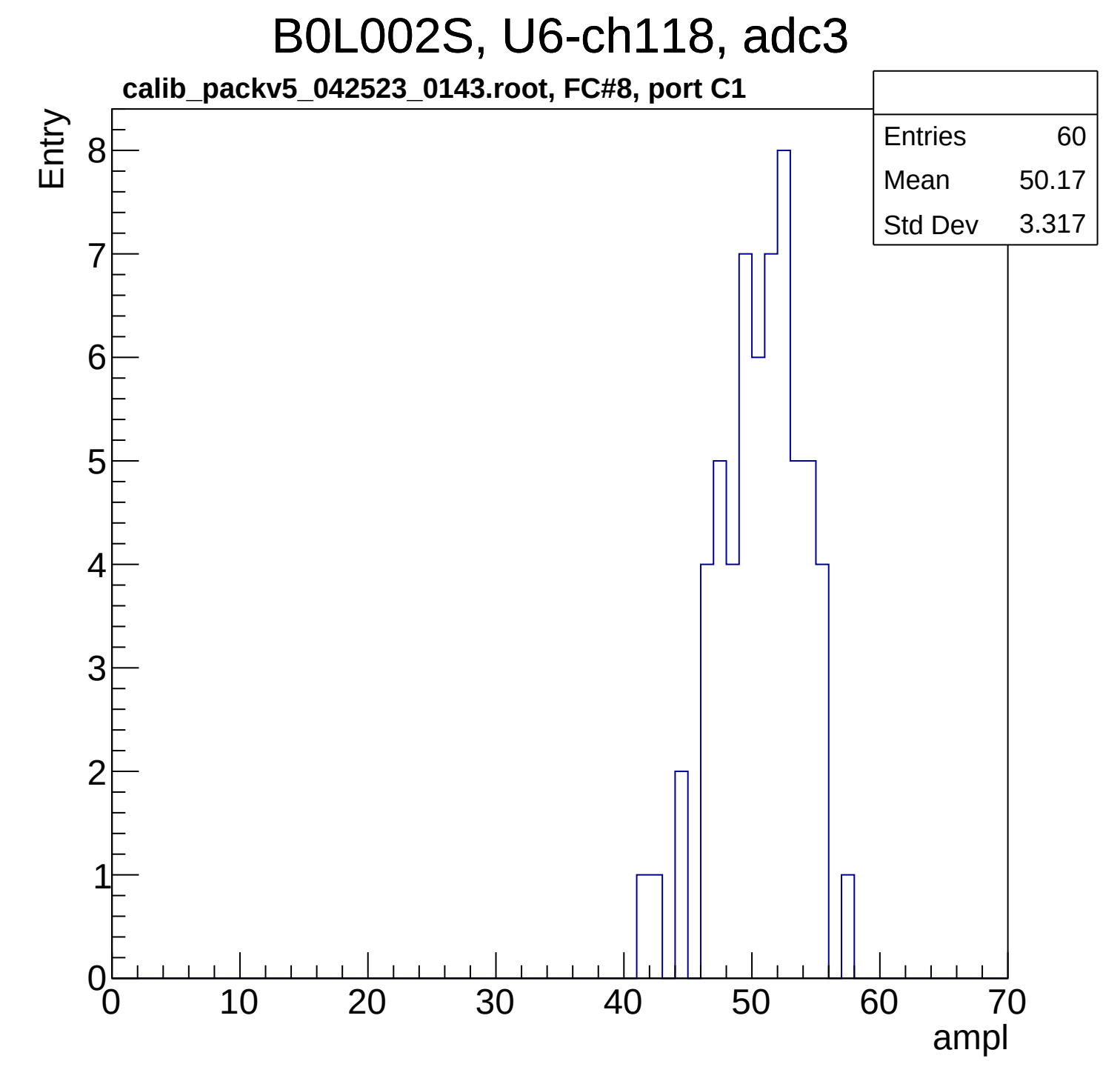
40
50
60
70


B0L002S, U6-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.17
Std Dev 3.317
Entry
ampl
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3
4
5
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0
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40
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60
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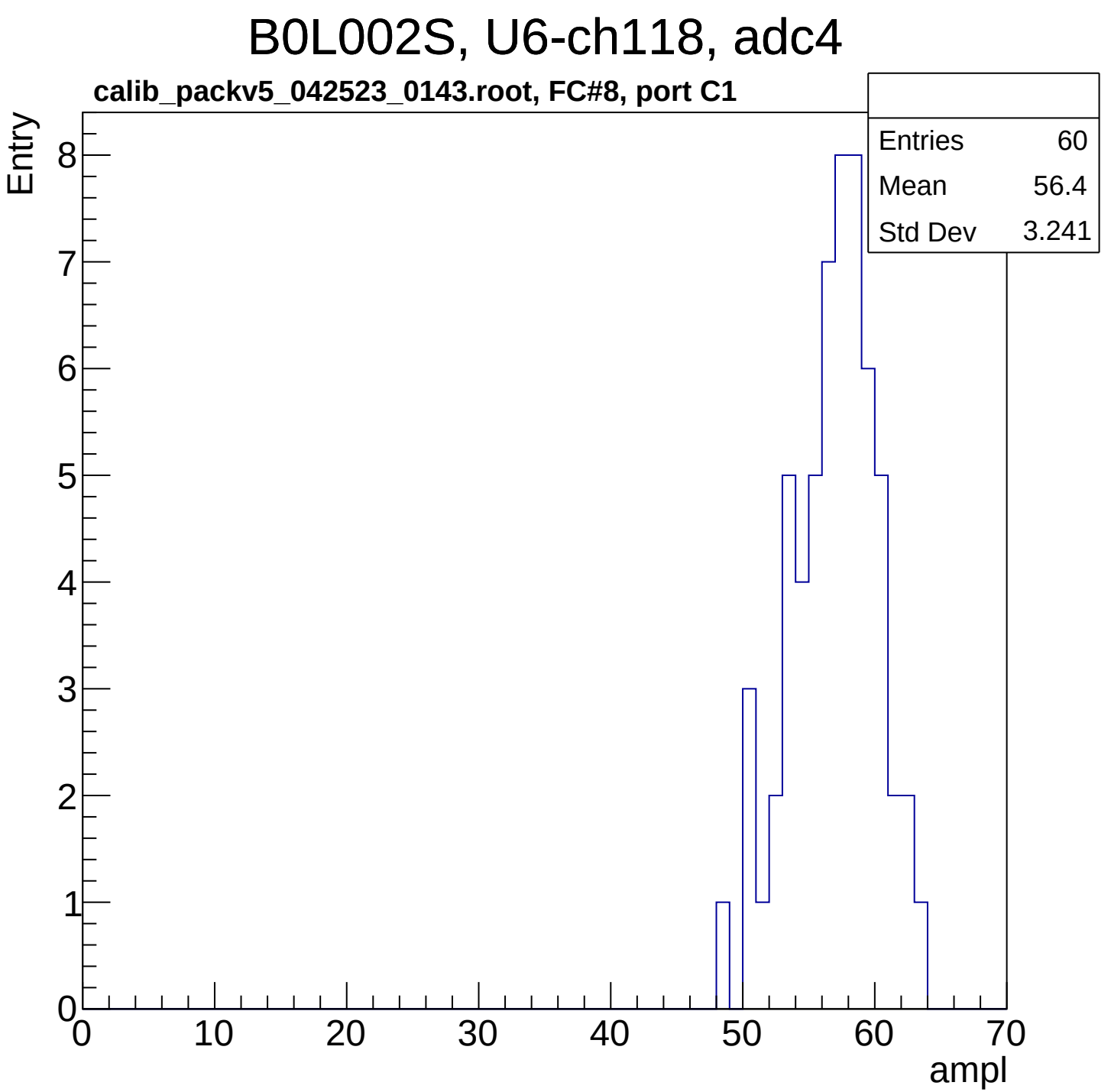
B0L002S, U6-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.4
Std Dev 3.241
Entry
ampl
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1
2
3
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0
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40
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60
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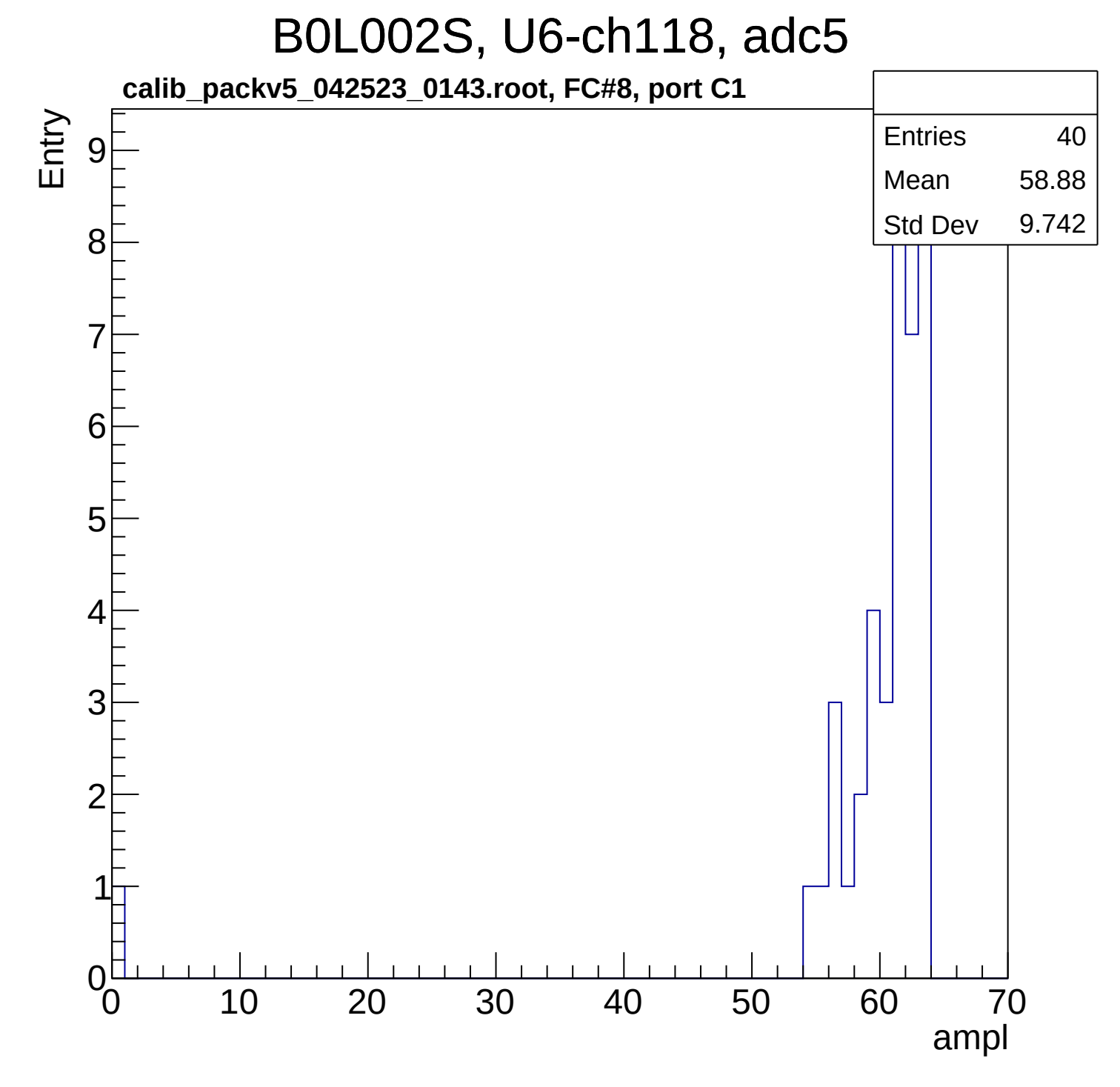
B0L002S, U6-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.88
Std Dev 9.742
Entry
ampl
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2
3
4
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6
7
8
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0
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20
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40
50
60
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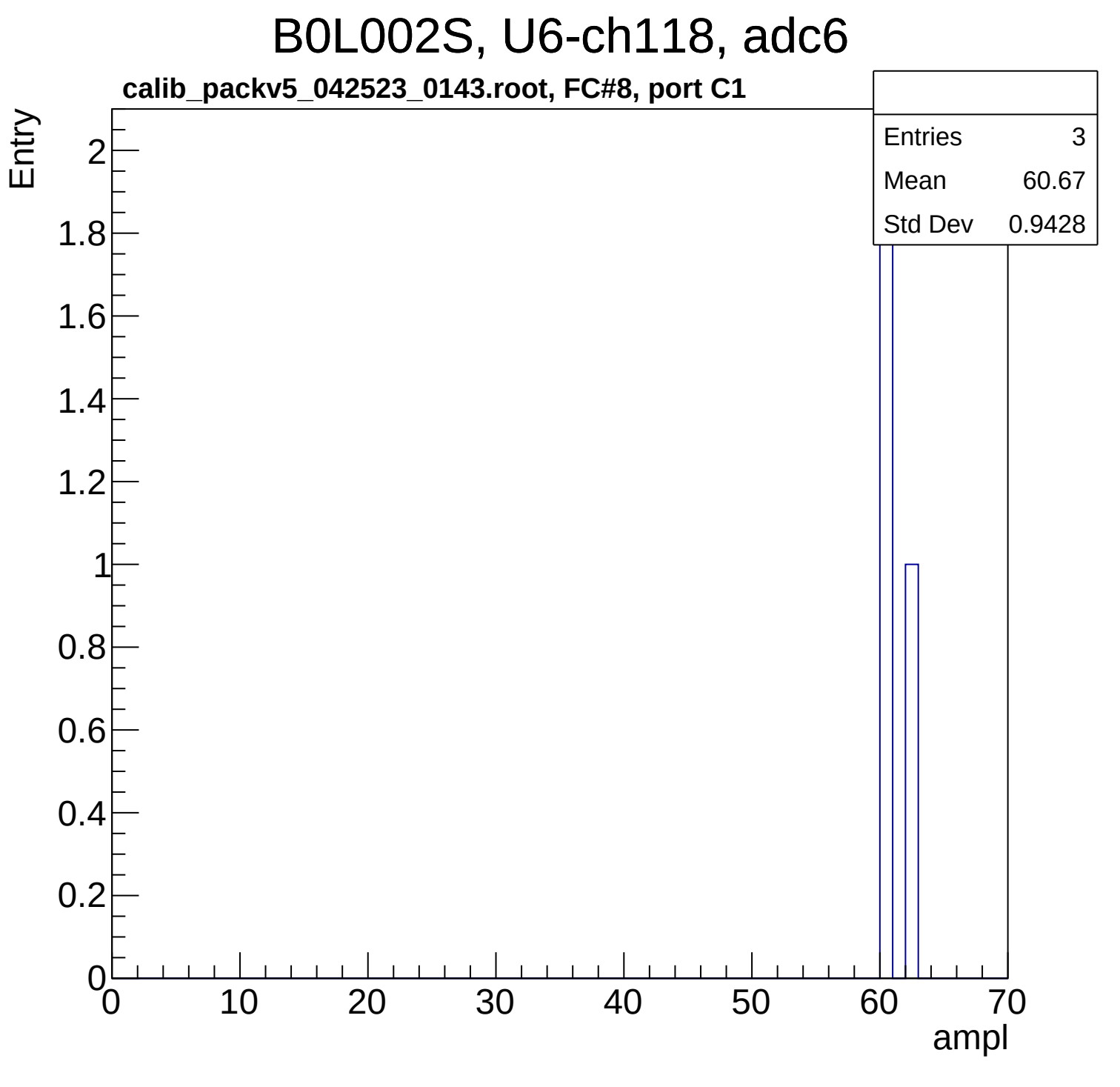
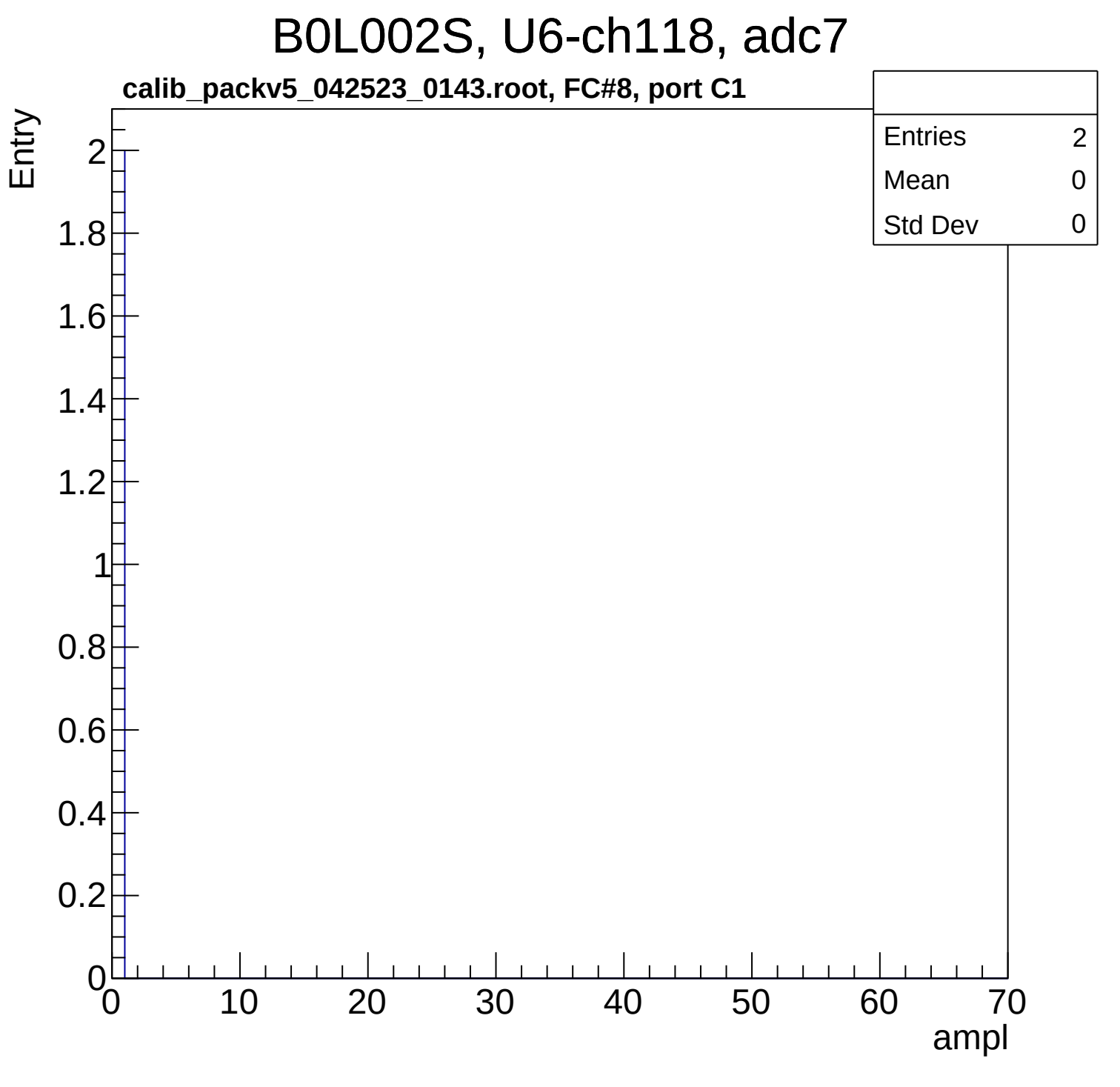

B0L002S, U6-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 60.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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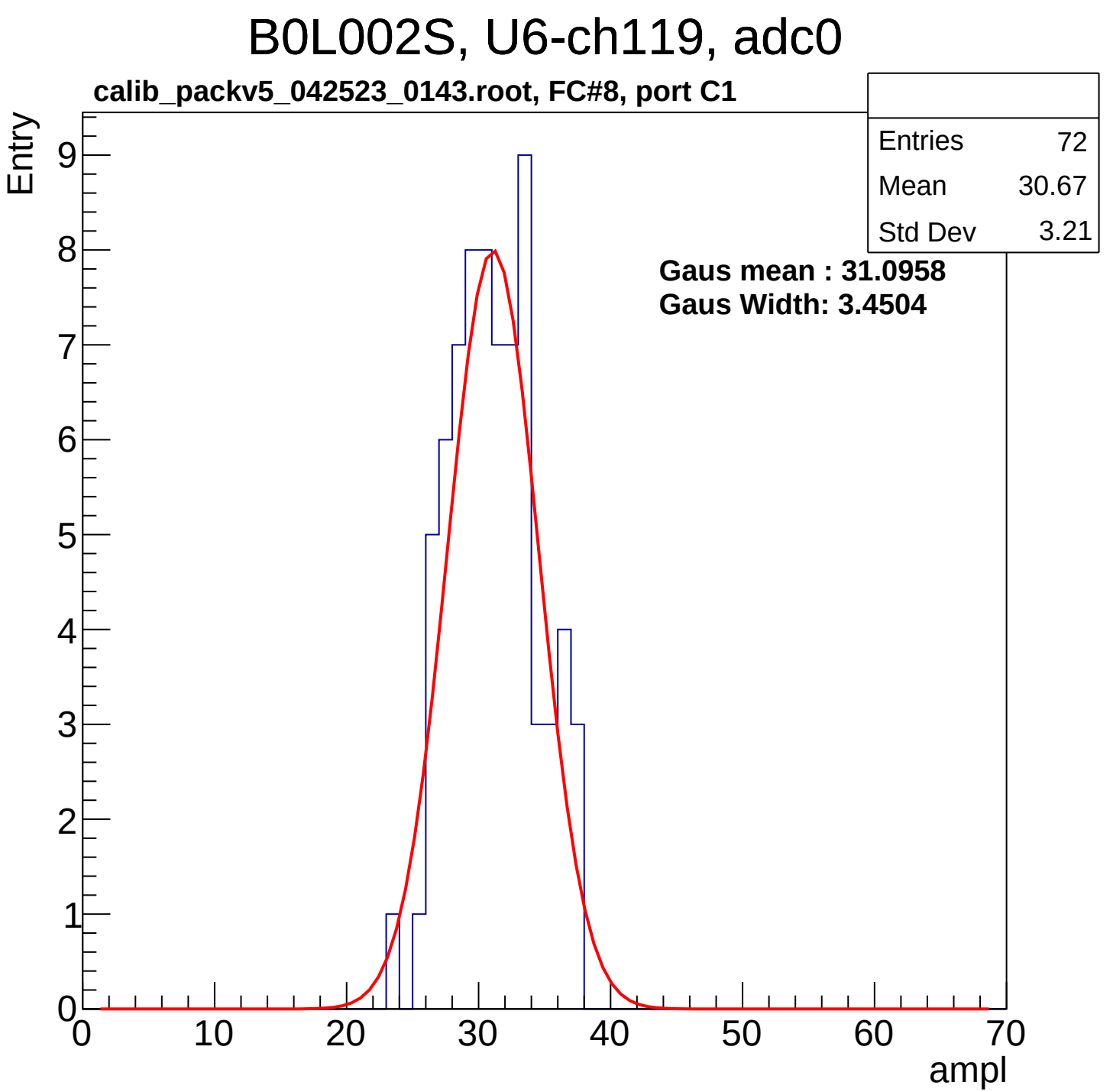
B0L002S, U6-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.67
Std Dev 3.21
Gaus mean : 31.0958
Gaus Width: 3.4504
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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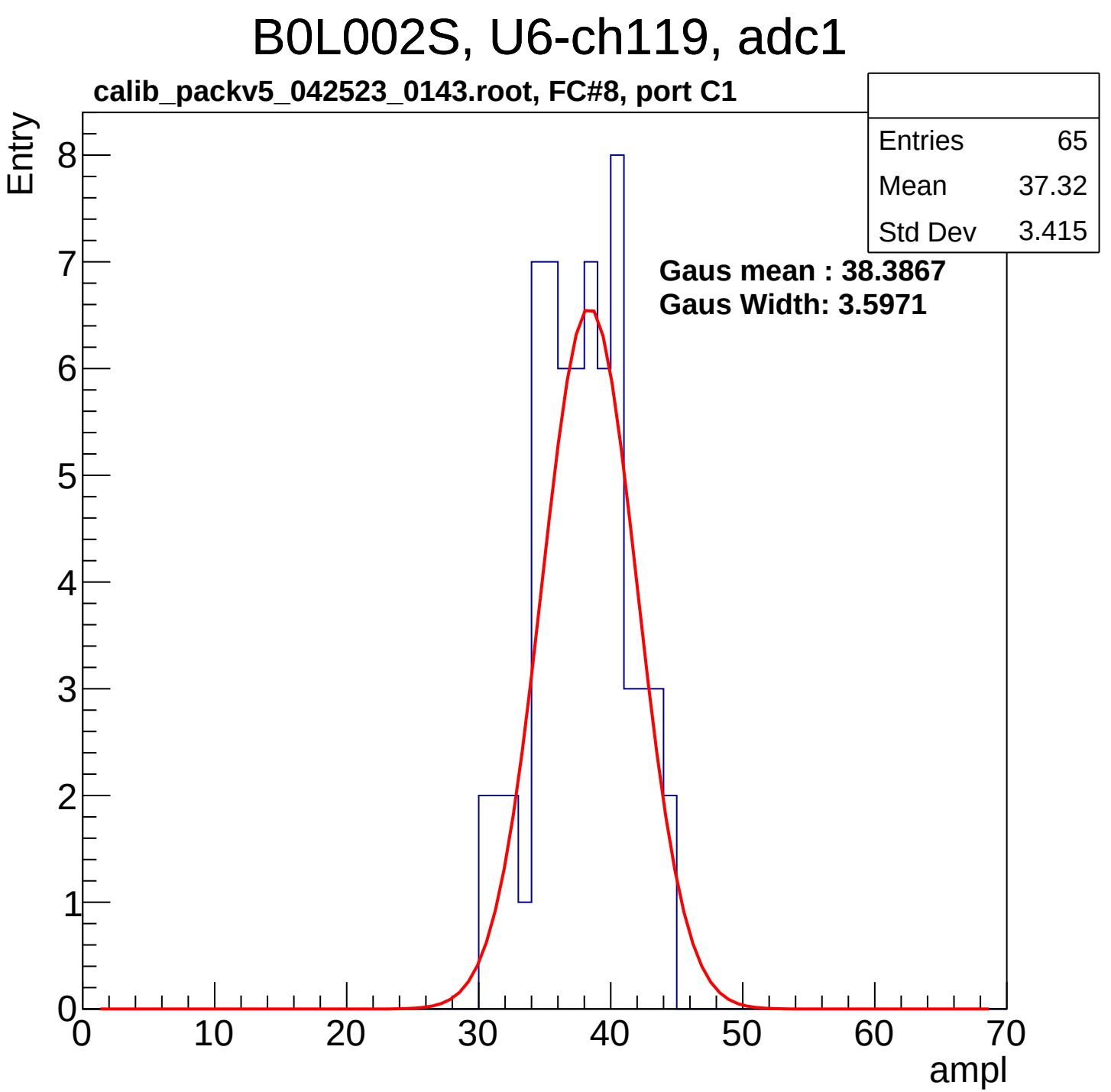
B0L002S, U6-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.32
Std Dev 3.415
Gaus mean : 38.3867
Gaus Width: 3.5971
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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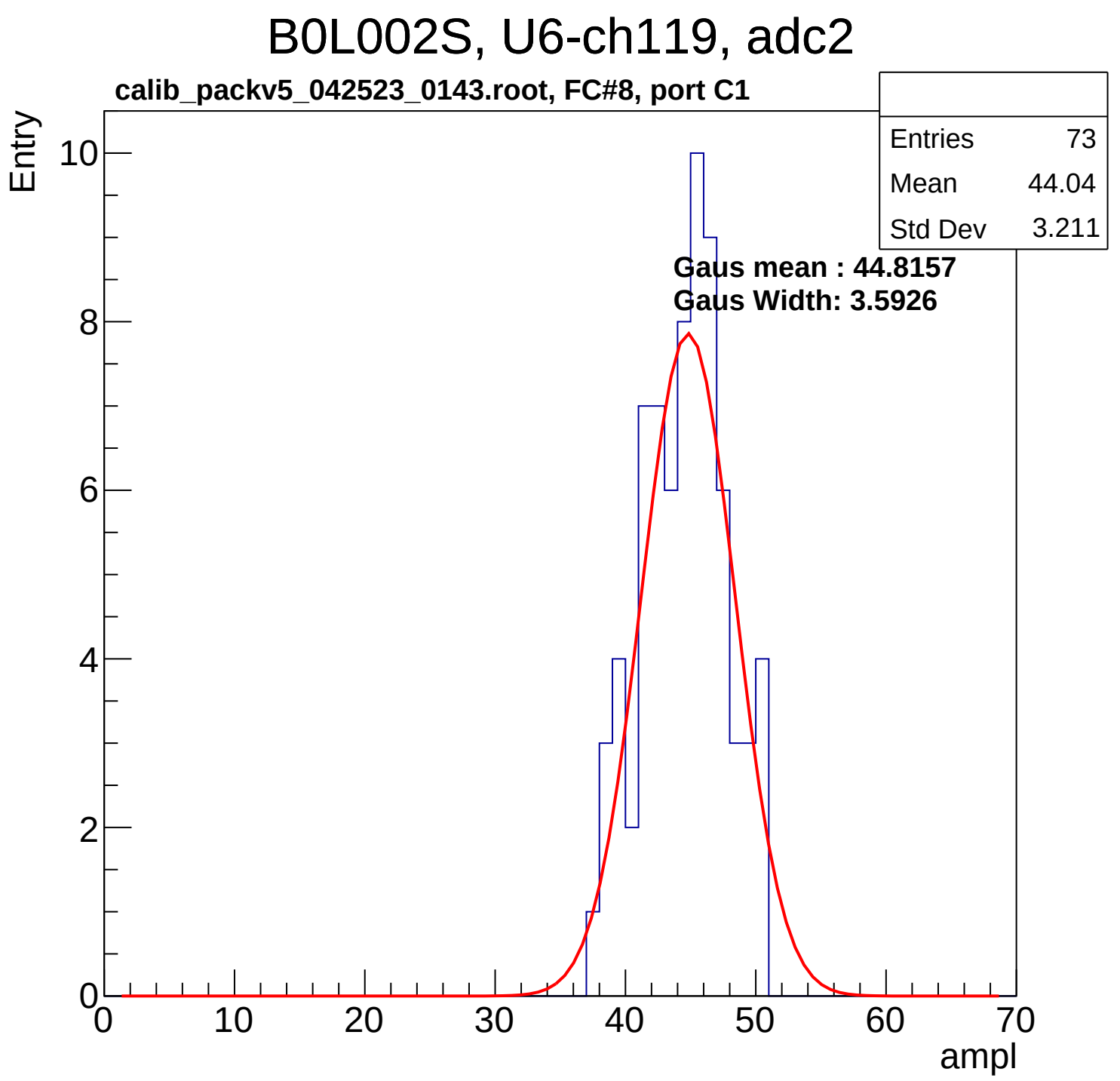
B0L002S, U6-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.04
Std Dev 3.211
Gaus mean : 44.8157
Gaus Width: 3.5926
Entry
ampl
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2
4
6
8
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0
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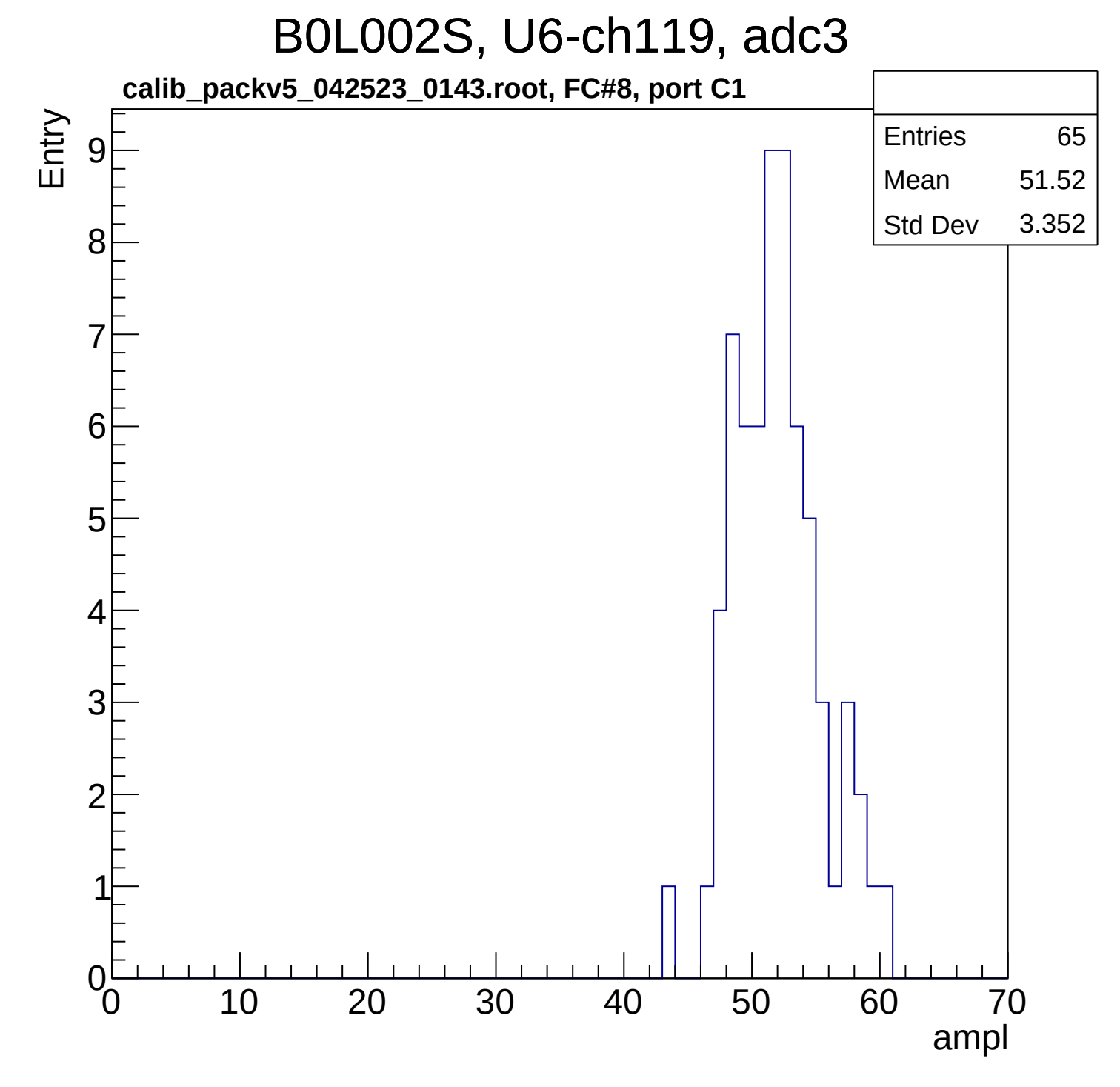
B0L002S, U6-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.52
Std Dev 3.352
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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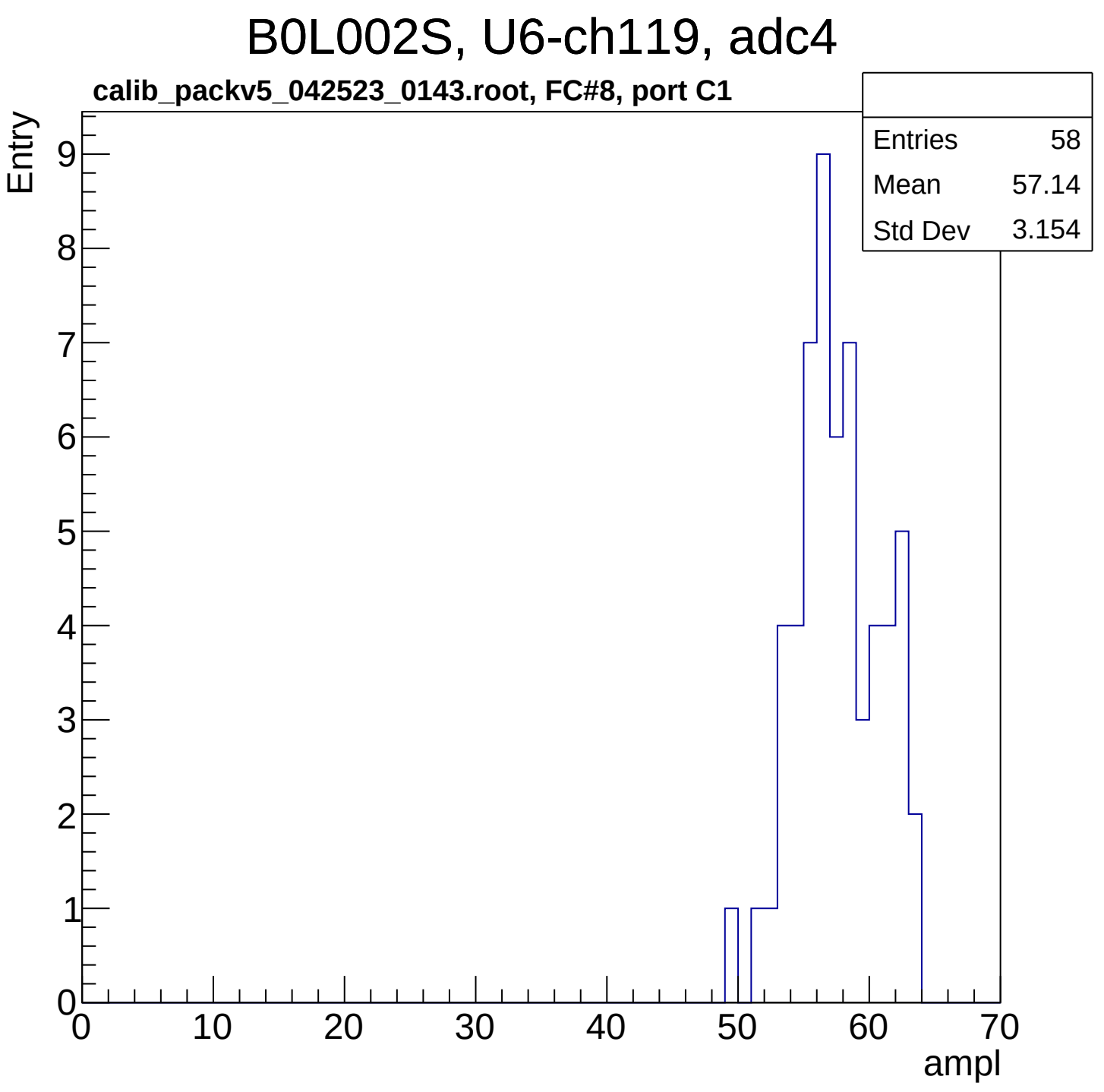
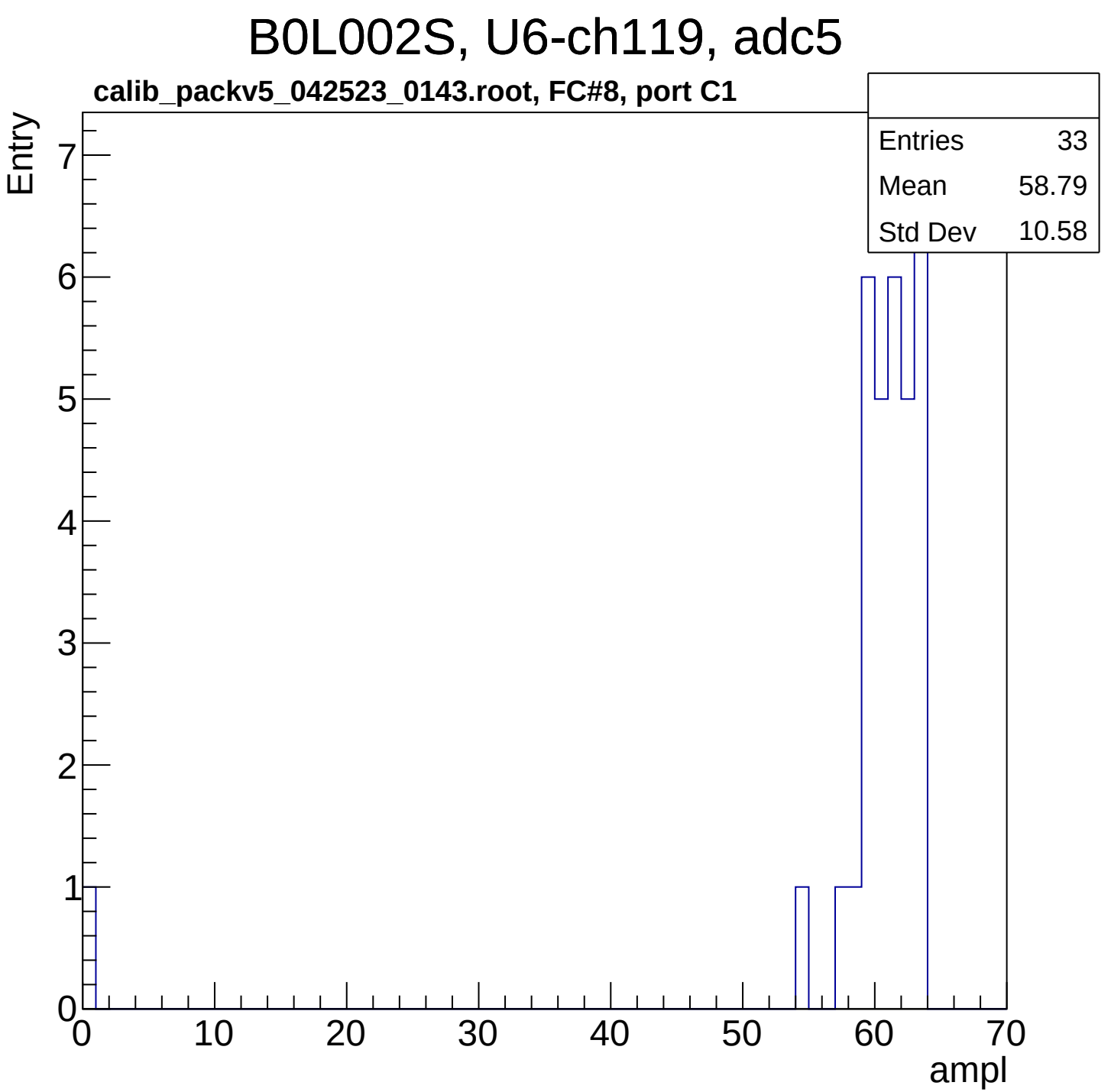

B0L002S, U6-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.14
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.79
Std Dev 10.58
Entry
ampl
0
1
2
3
4
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60
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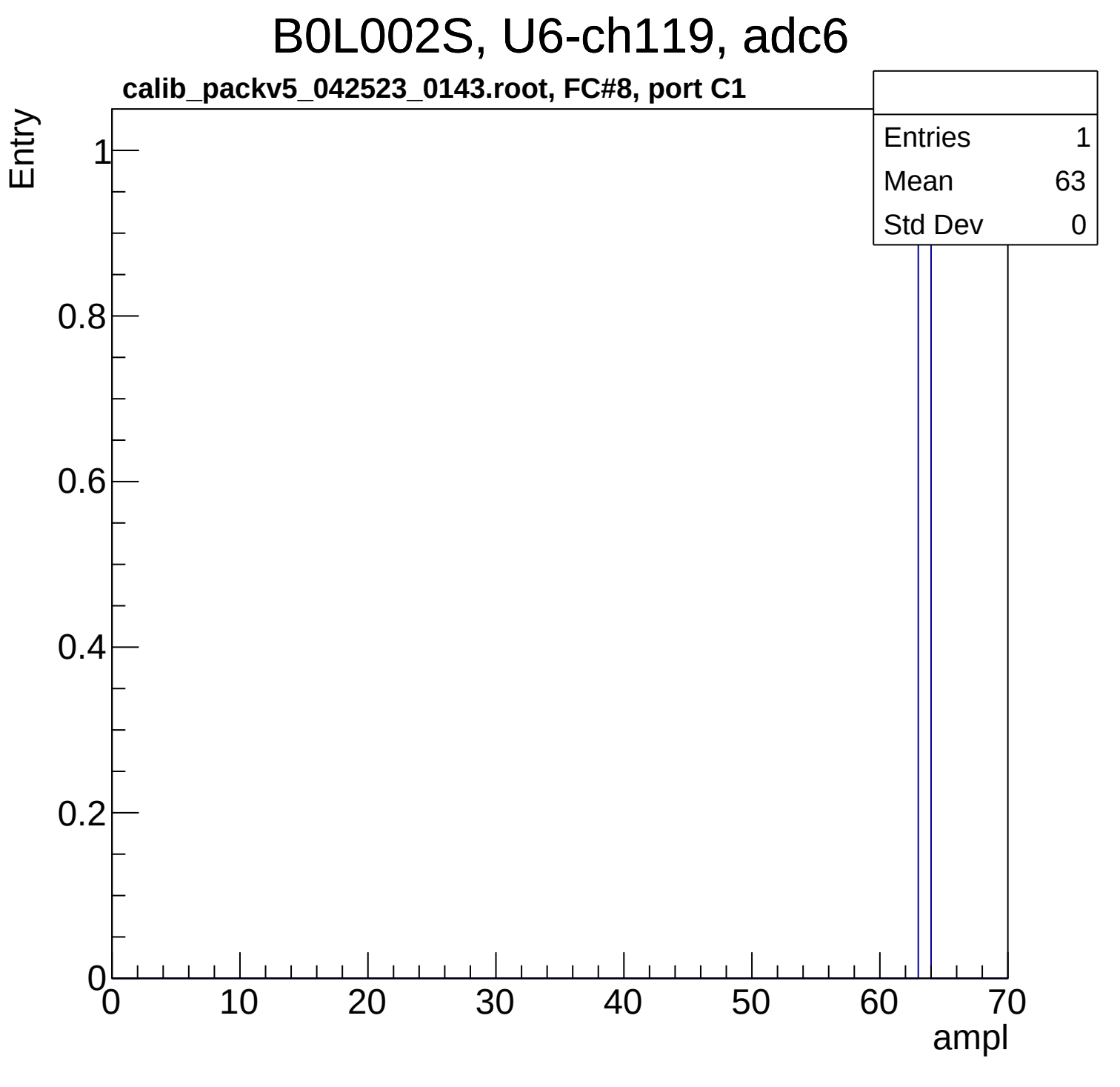
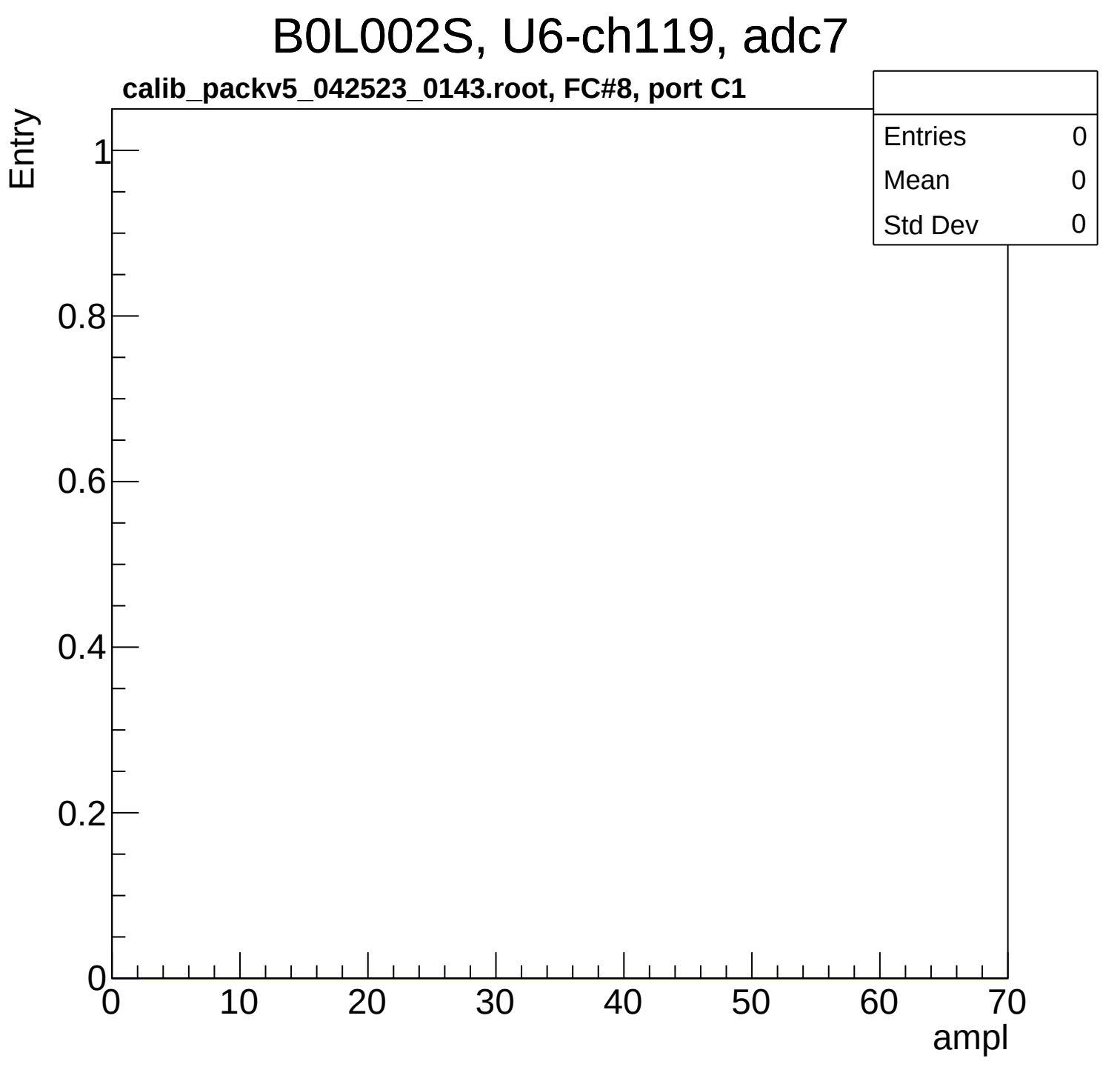
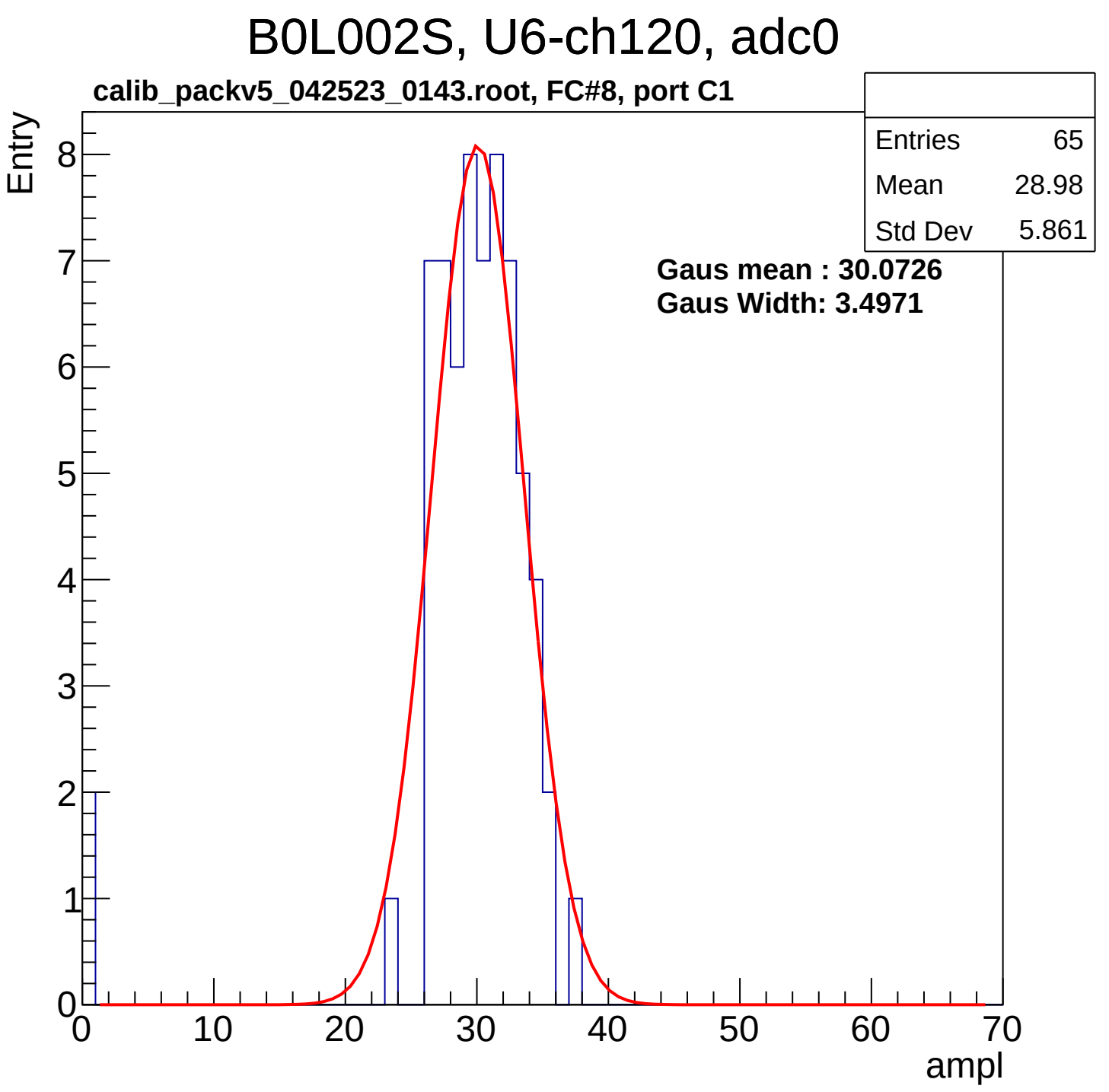

B0L002S, U6-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 28.98
Std Dev 5.861
Gaus mean : 30.0726
Gaus Width: 3.4971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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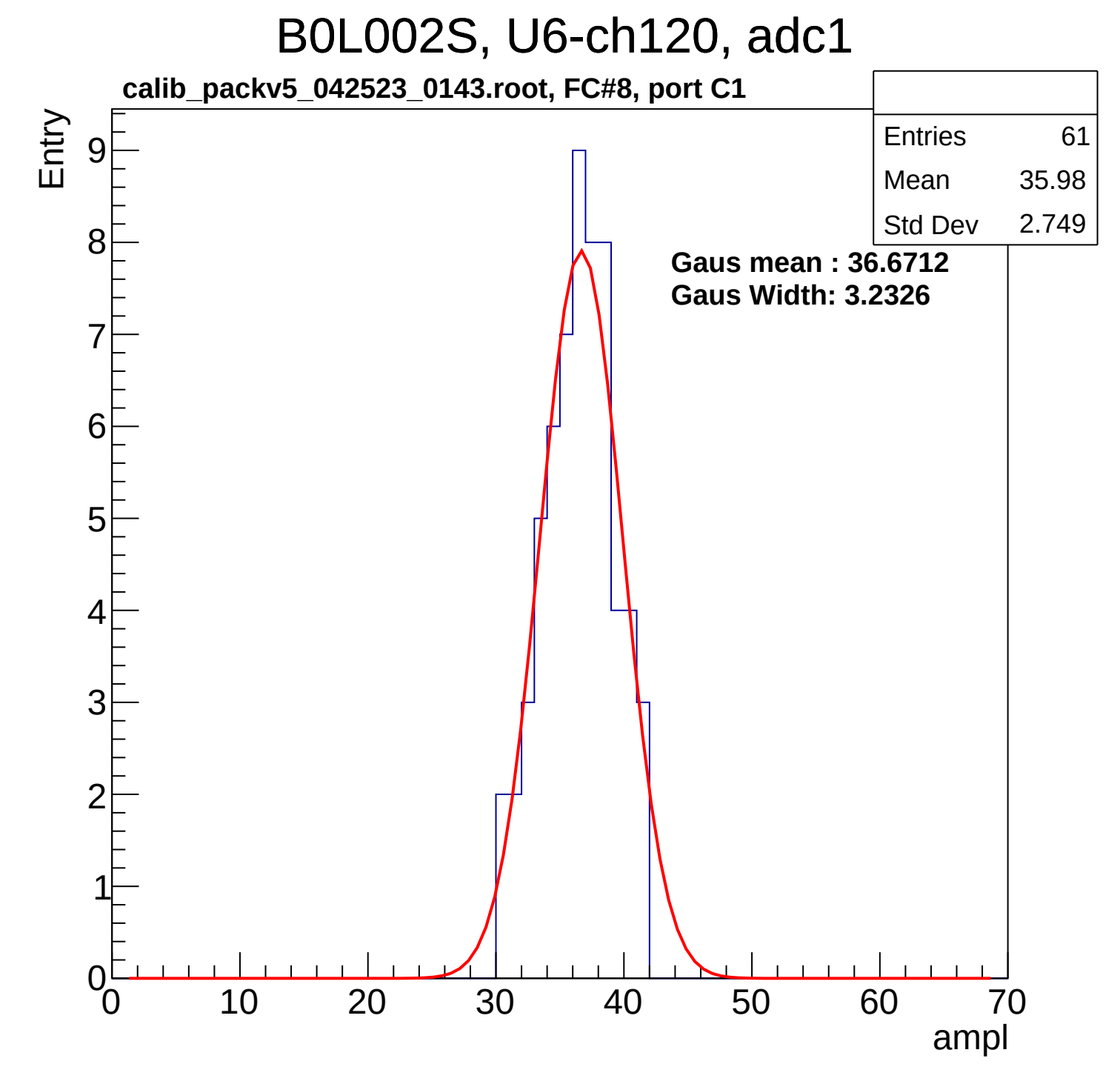
B0L002S, U6-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 35.98
Std Dev 2.749
Gaus mean : 36.6712
Gaus Width: 3.2326
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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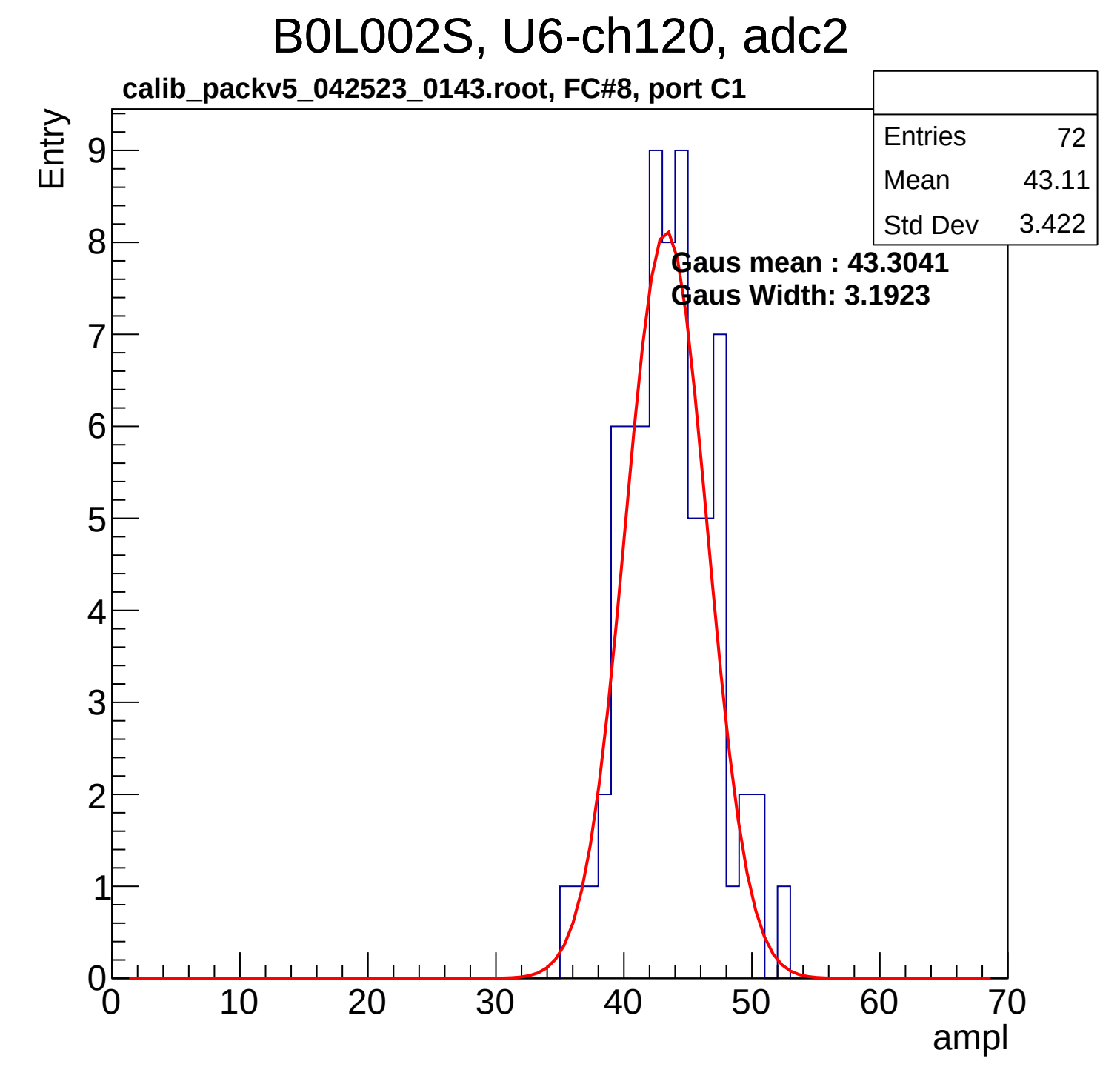
B0L002S, U6-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.11
Std Dev 3.422
Gaus mean : 43.3041
Gaus Width: 3.1923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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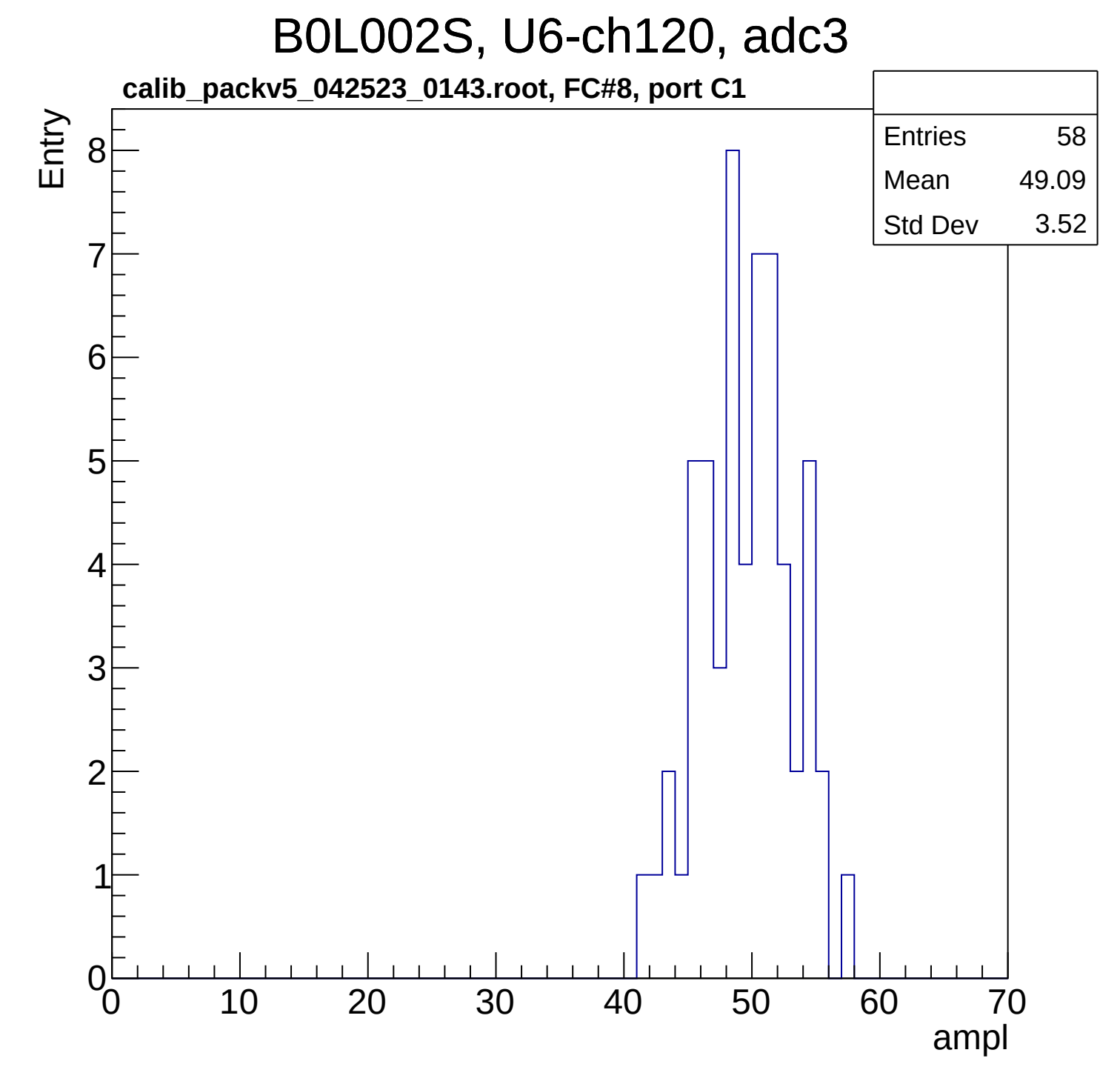
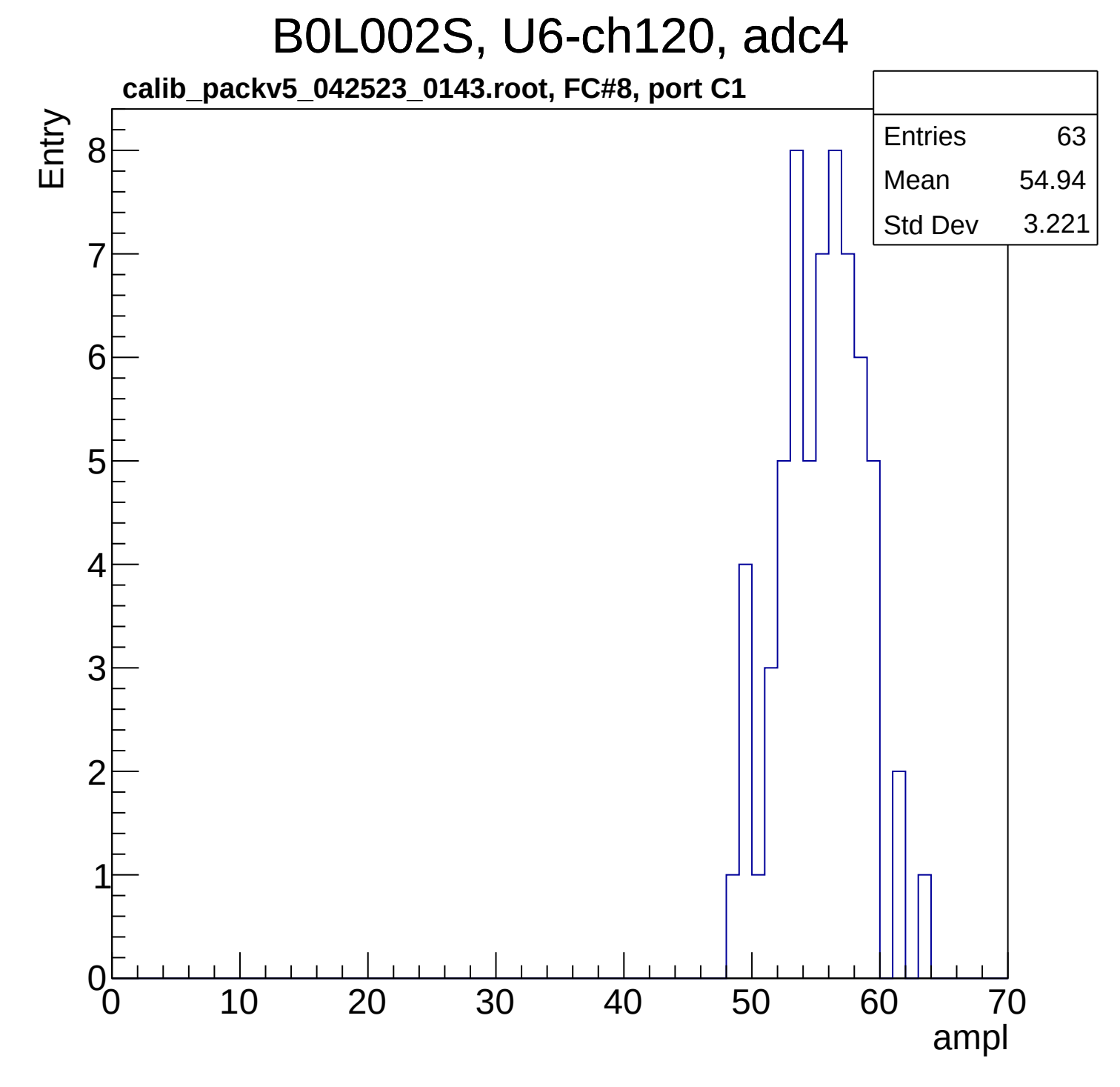
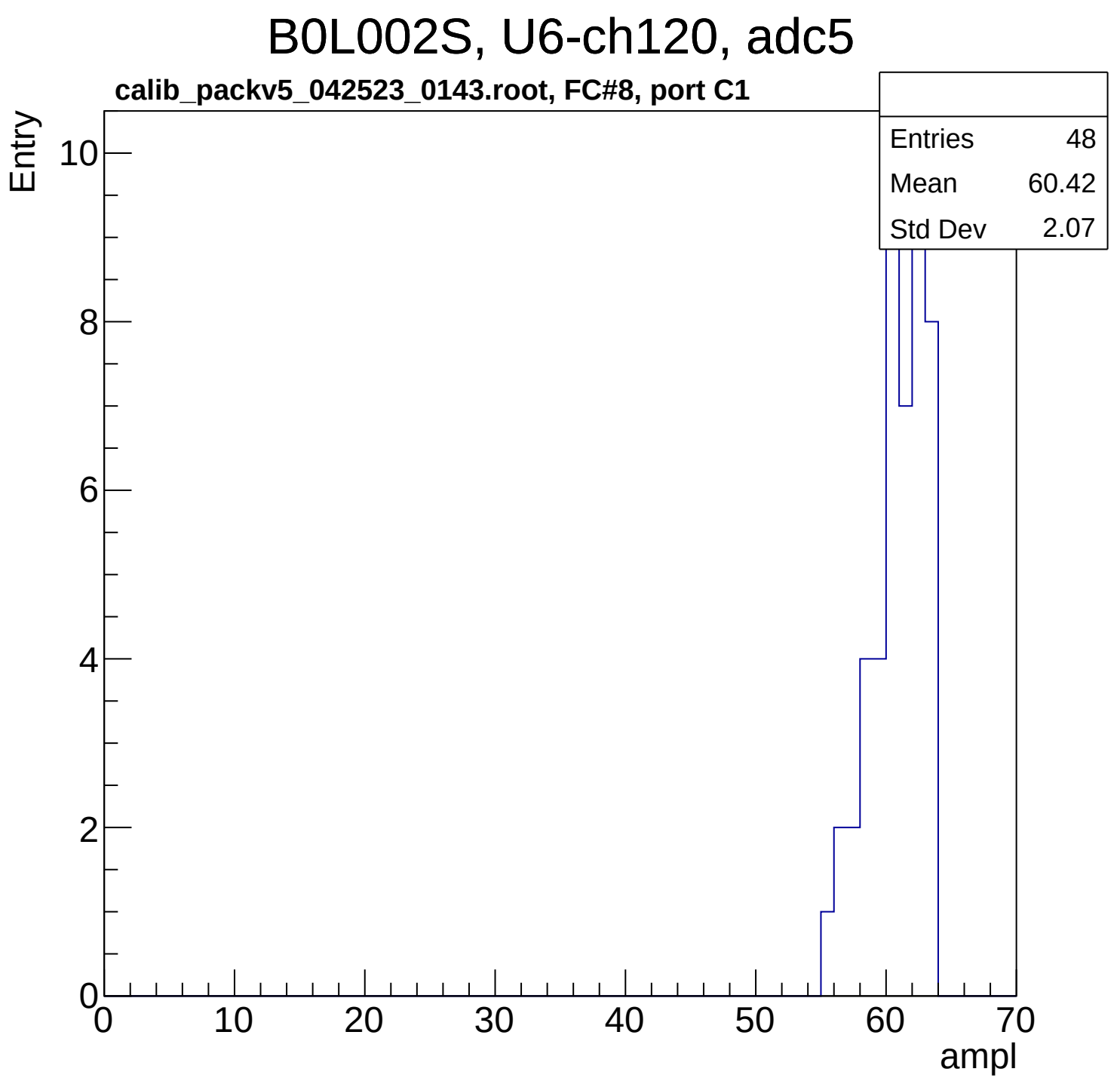
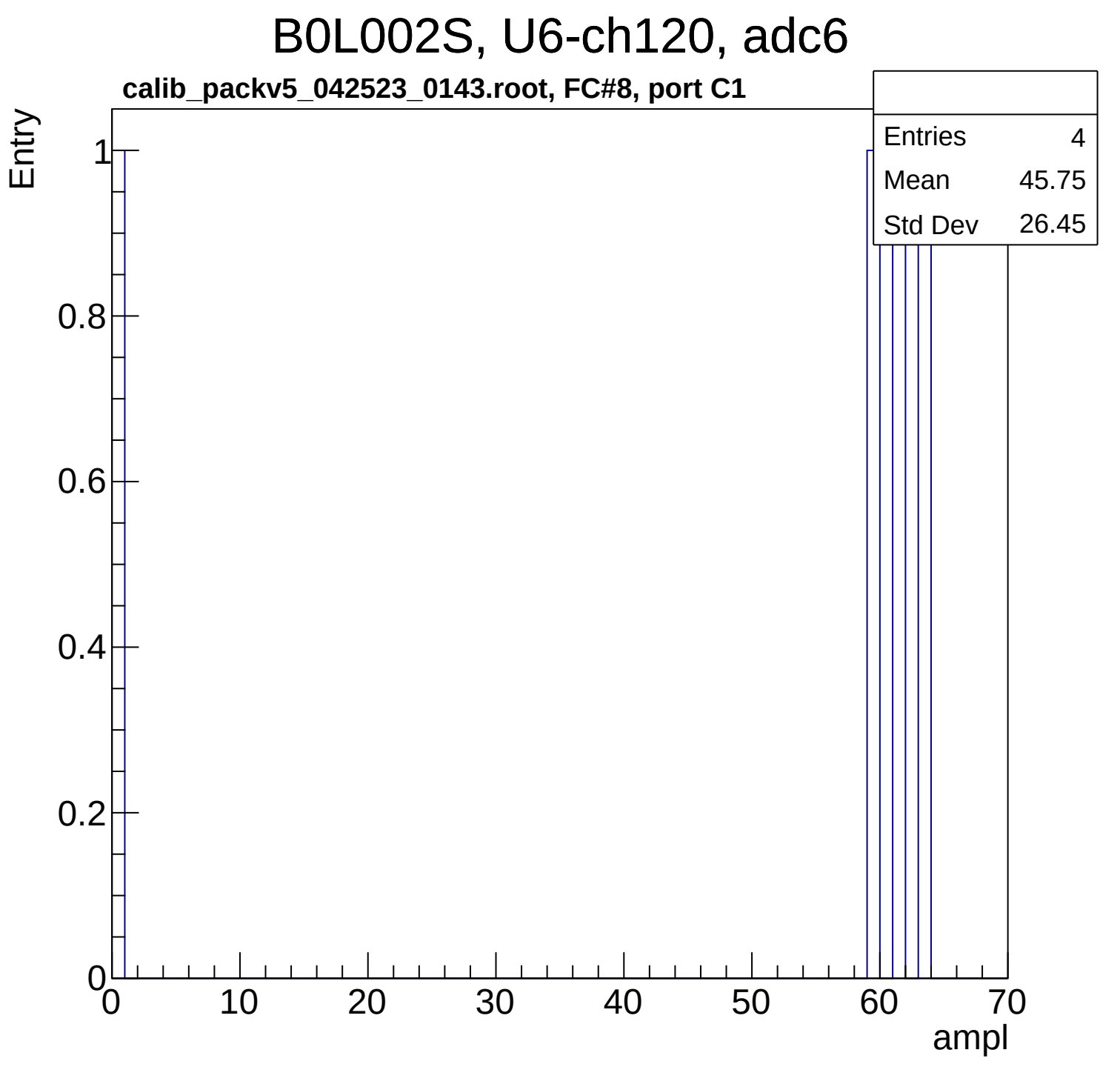

B0L002S, U6-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 49.09
Std Dev 3.52
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 54.94
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 60.42
Std Dev 2.07
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 45.75
Std Dev 26.45
Entry
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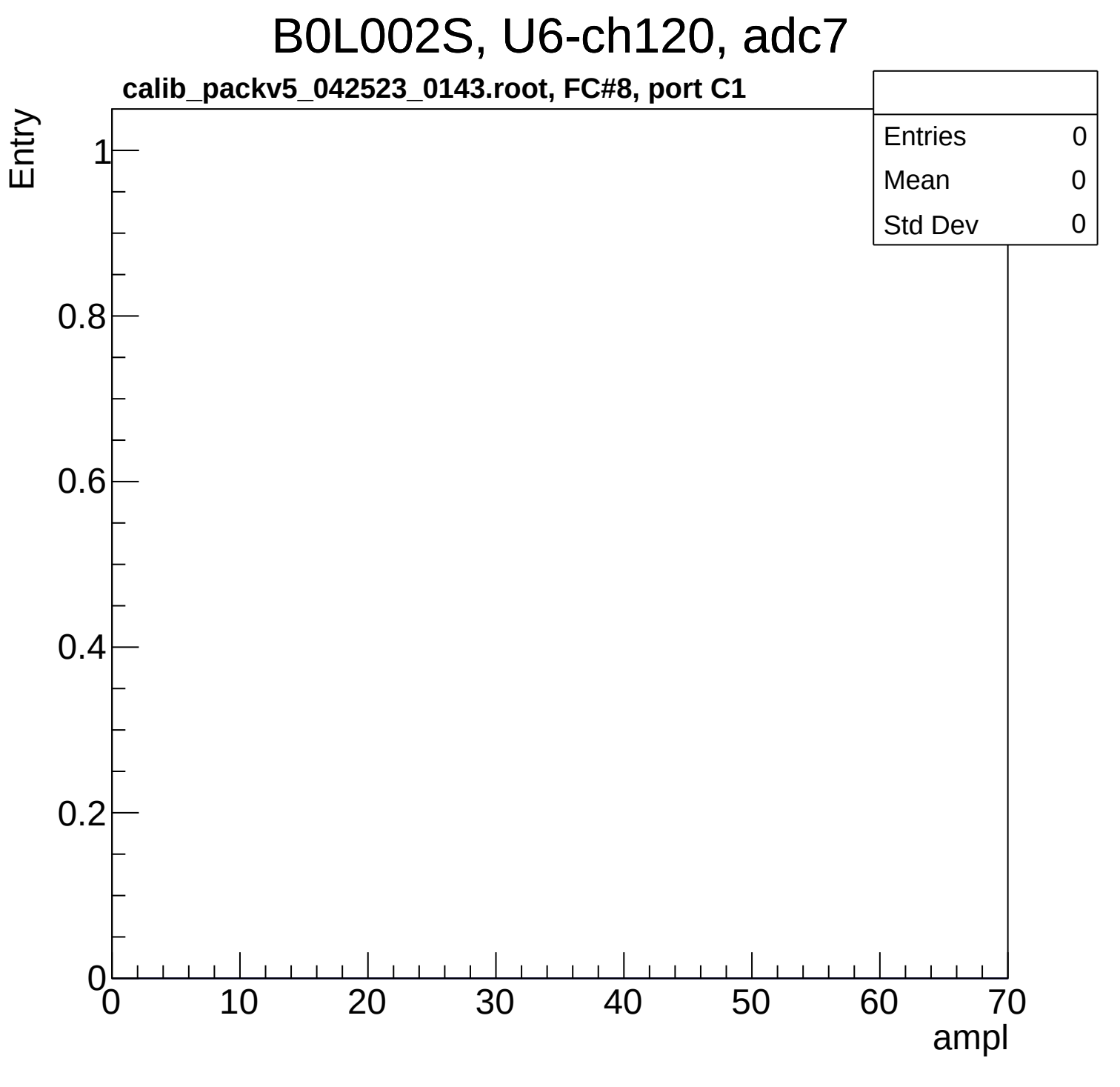
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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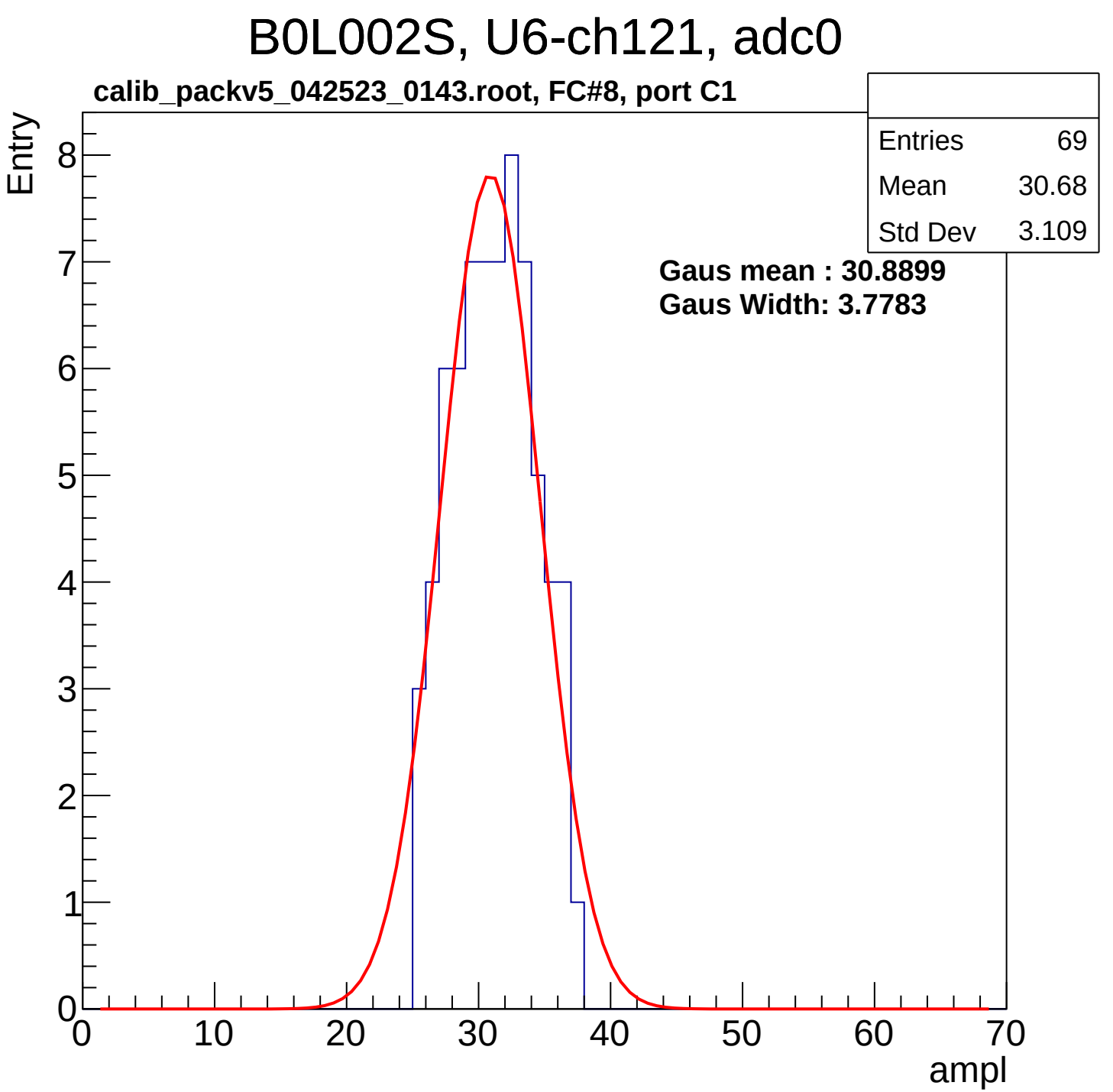
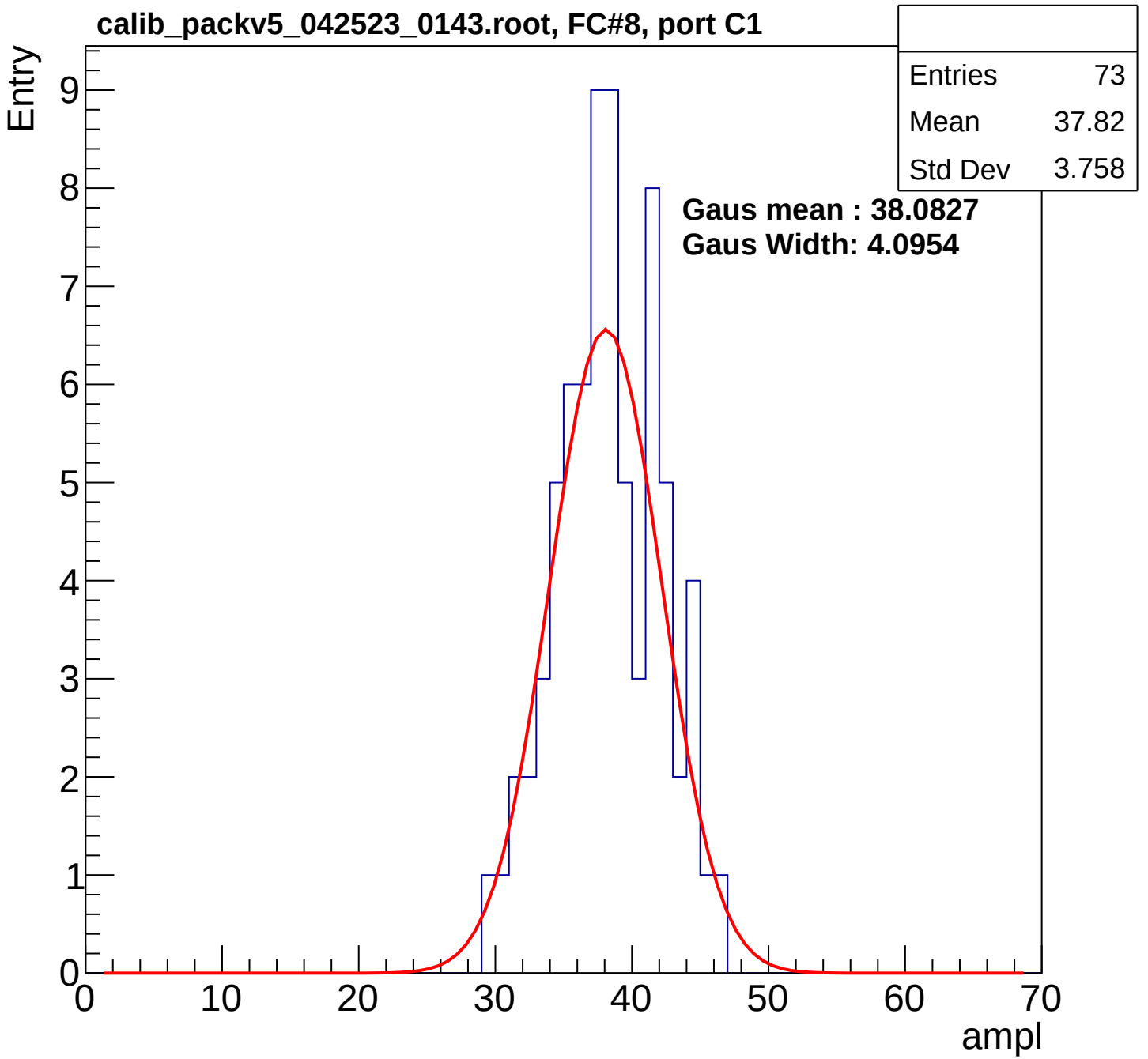
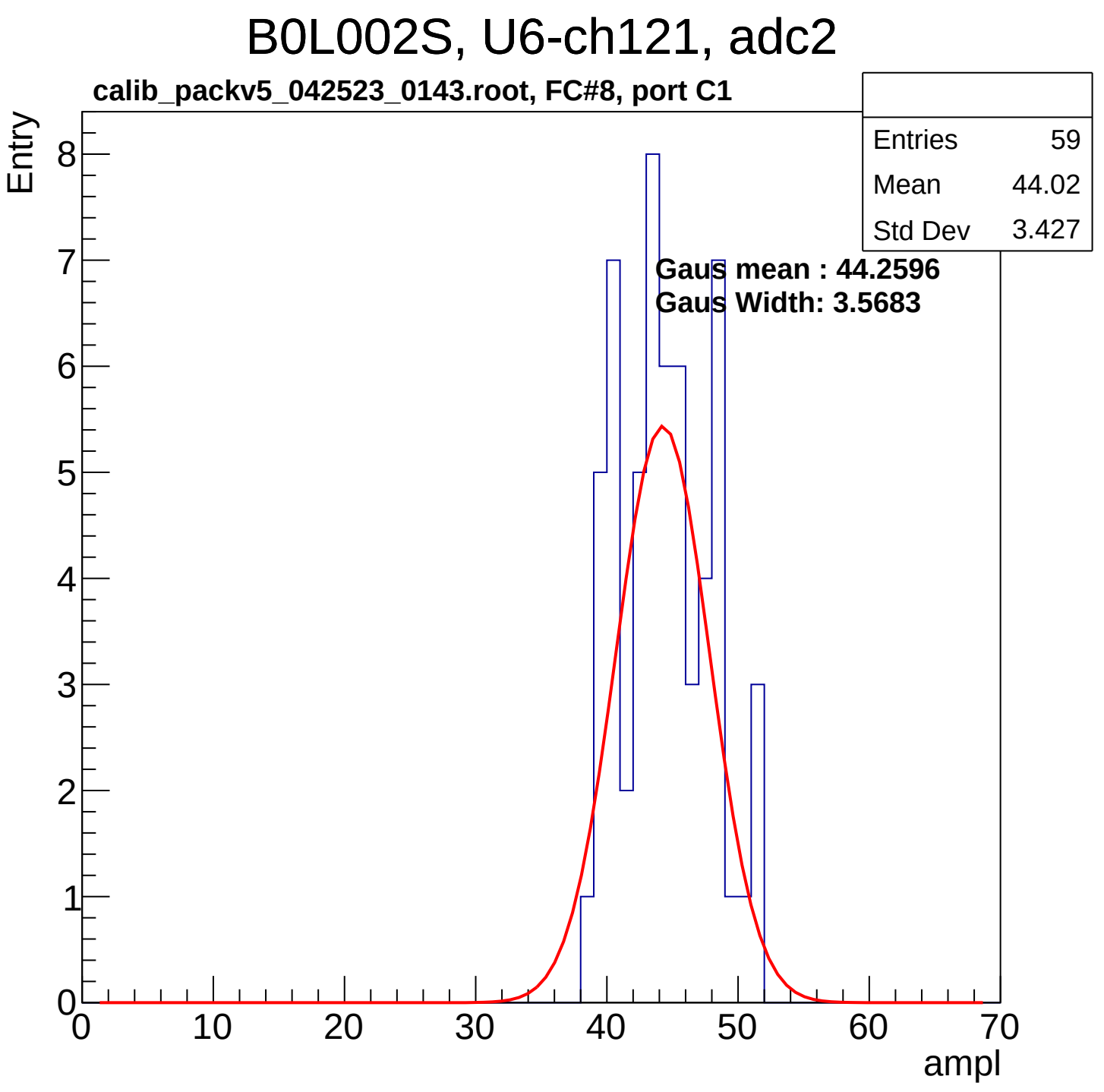
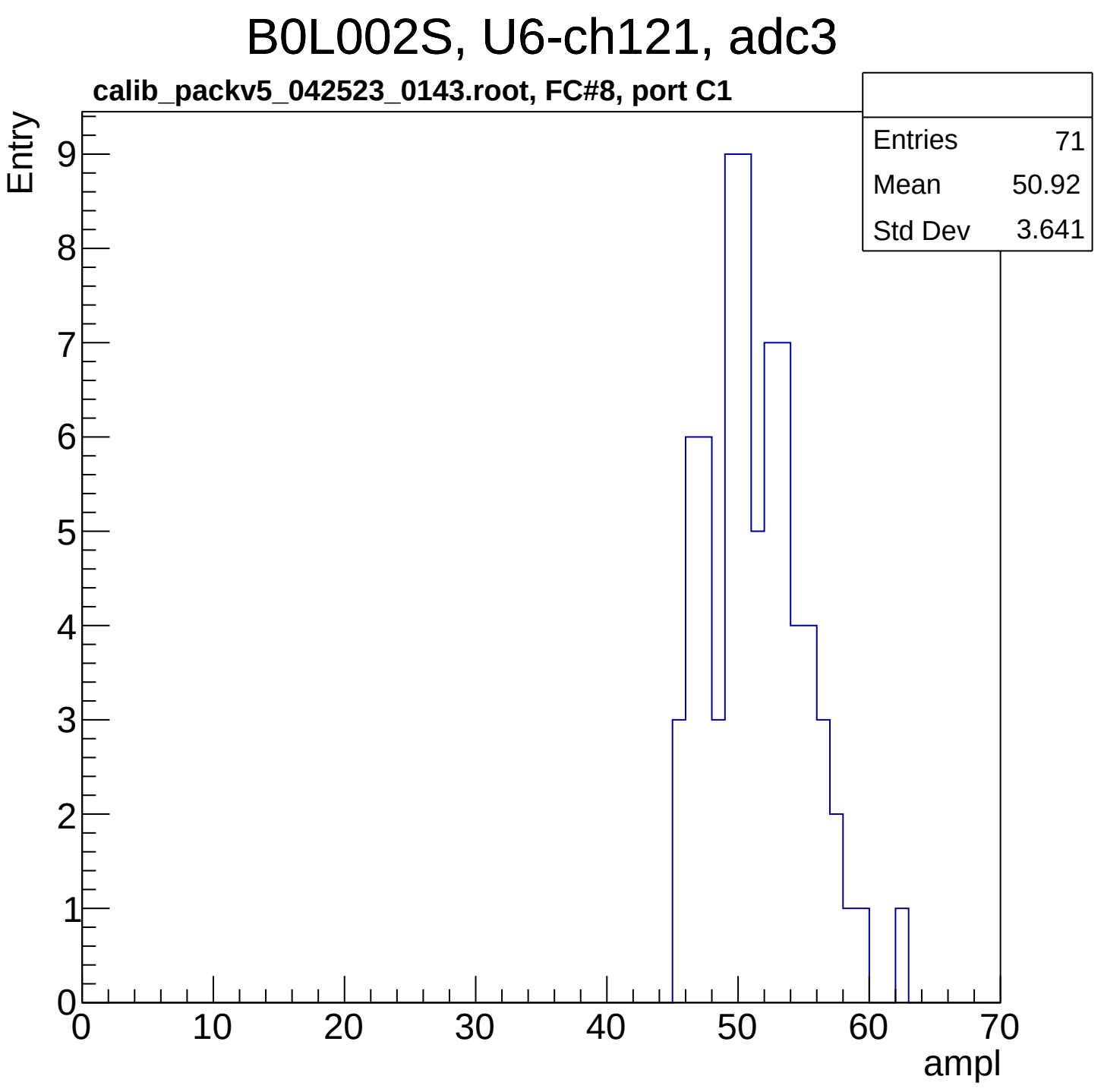
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.68
Std Dev 3.109
Gaus mean : 30.8899
Gaus Width: 3.7783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.82
Std Dev 3.758
Gaus mean : 38.0827
Gaus Width: 4.0954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 44.02
Std Dev 3.427
Gaus mean : 44.2596
Gaus Width: 3.5683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.92
Std Dev 3.641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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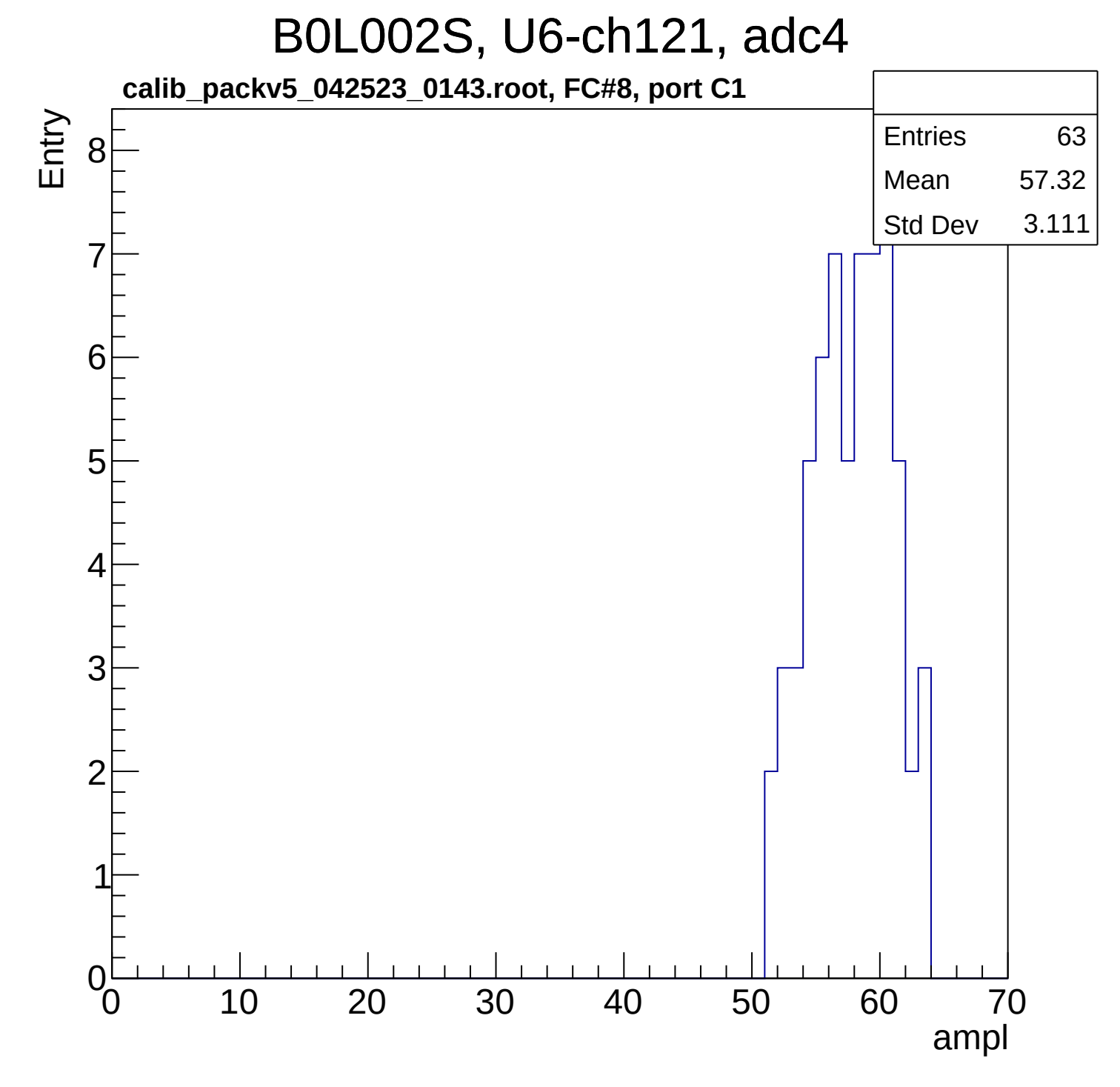
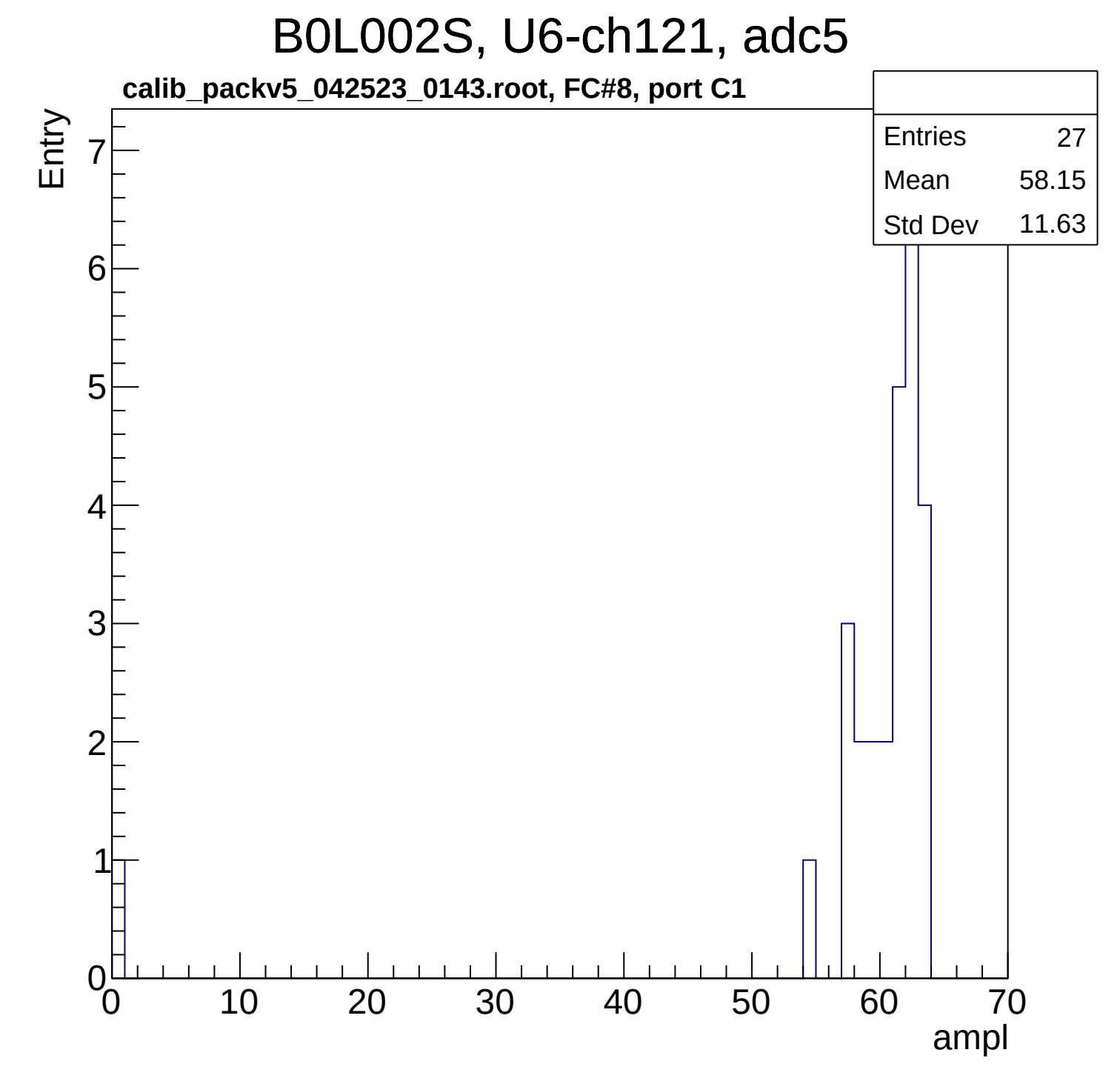
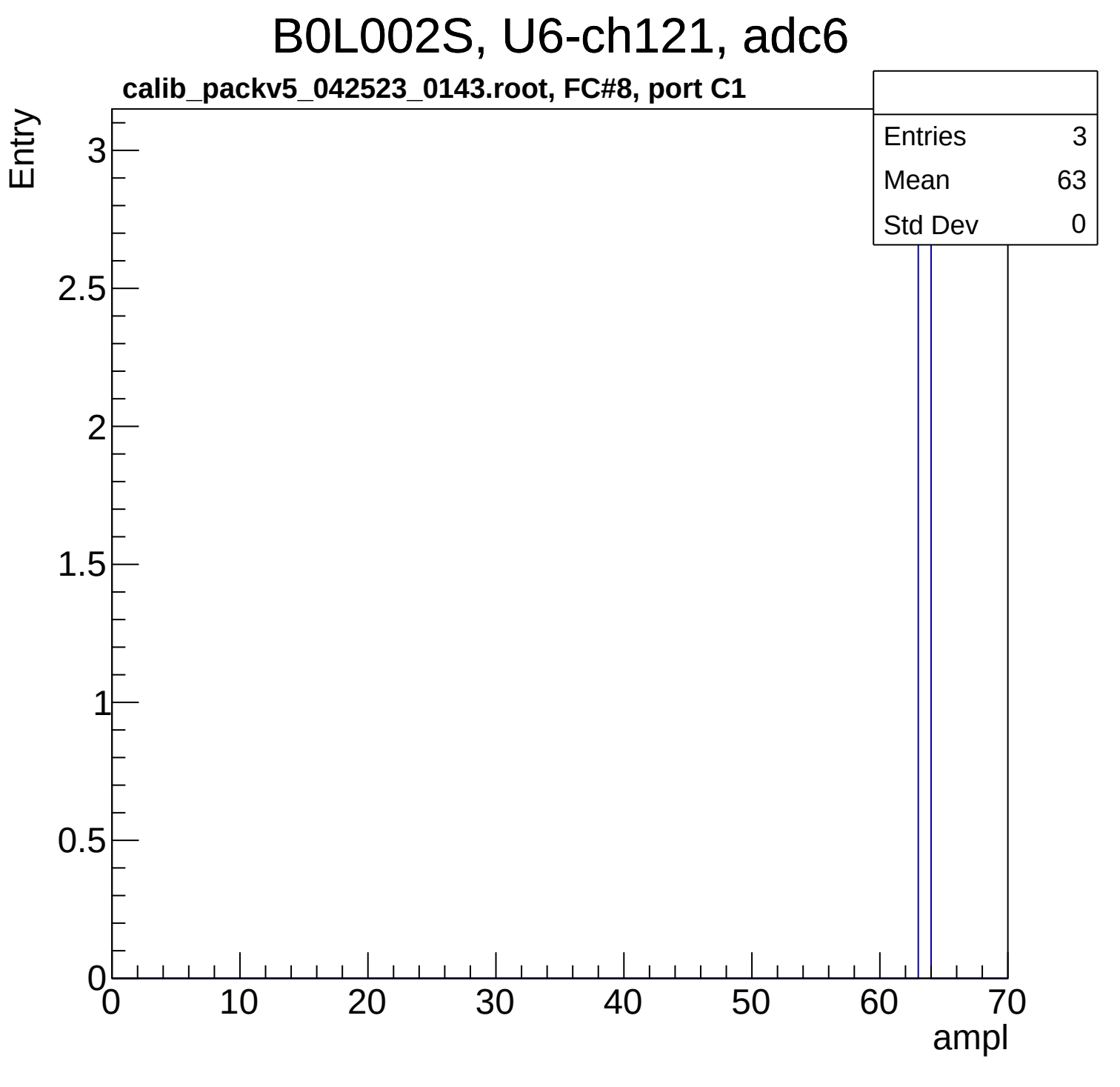
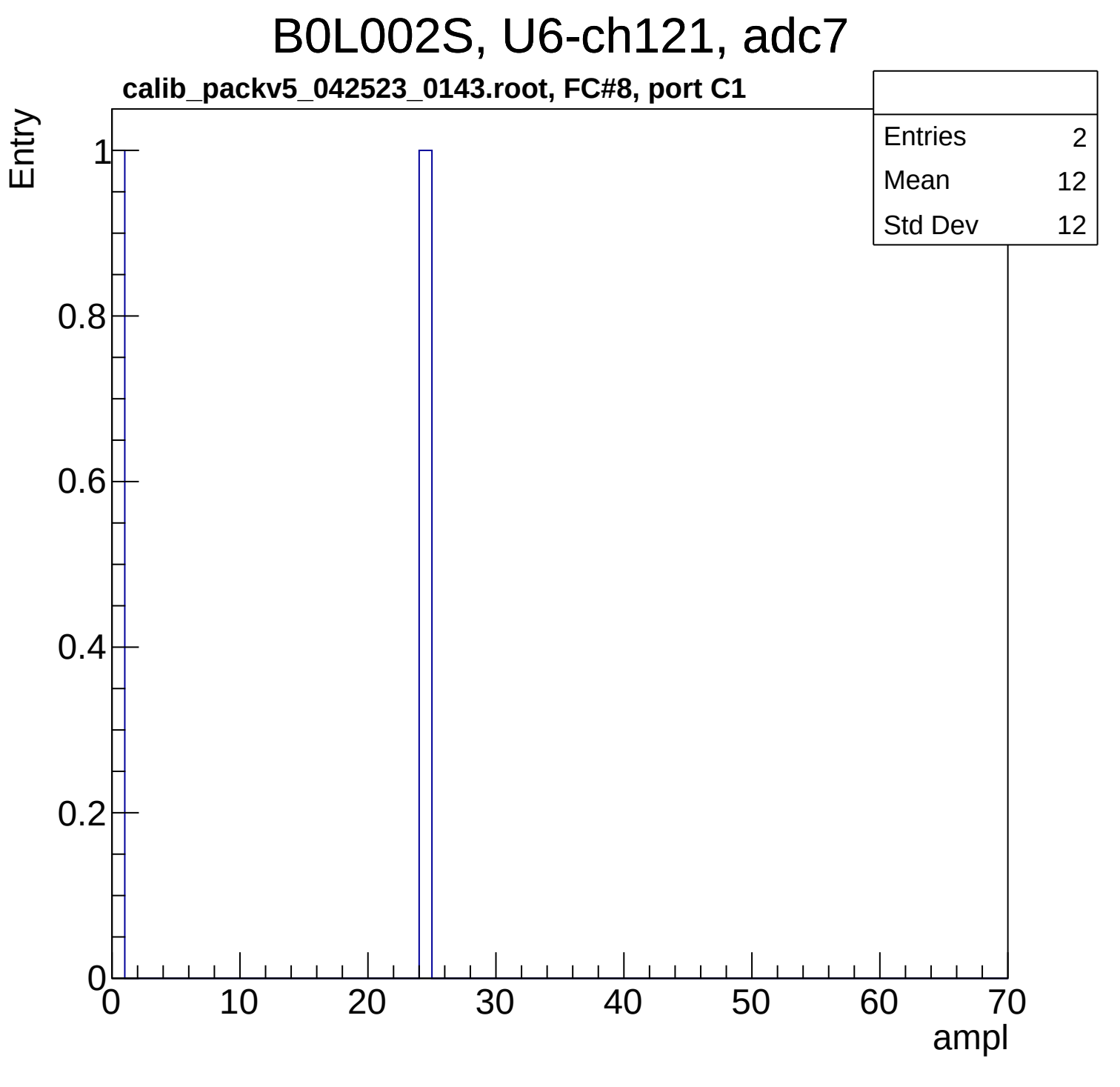

B0L002S, U6-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.32
Std Dev 3.111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.15
Std Dev 11.63
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U6-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
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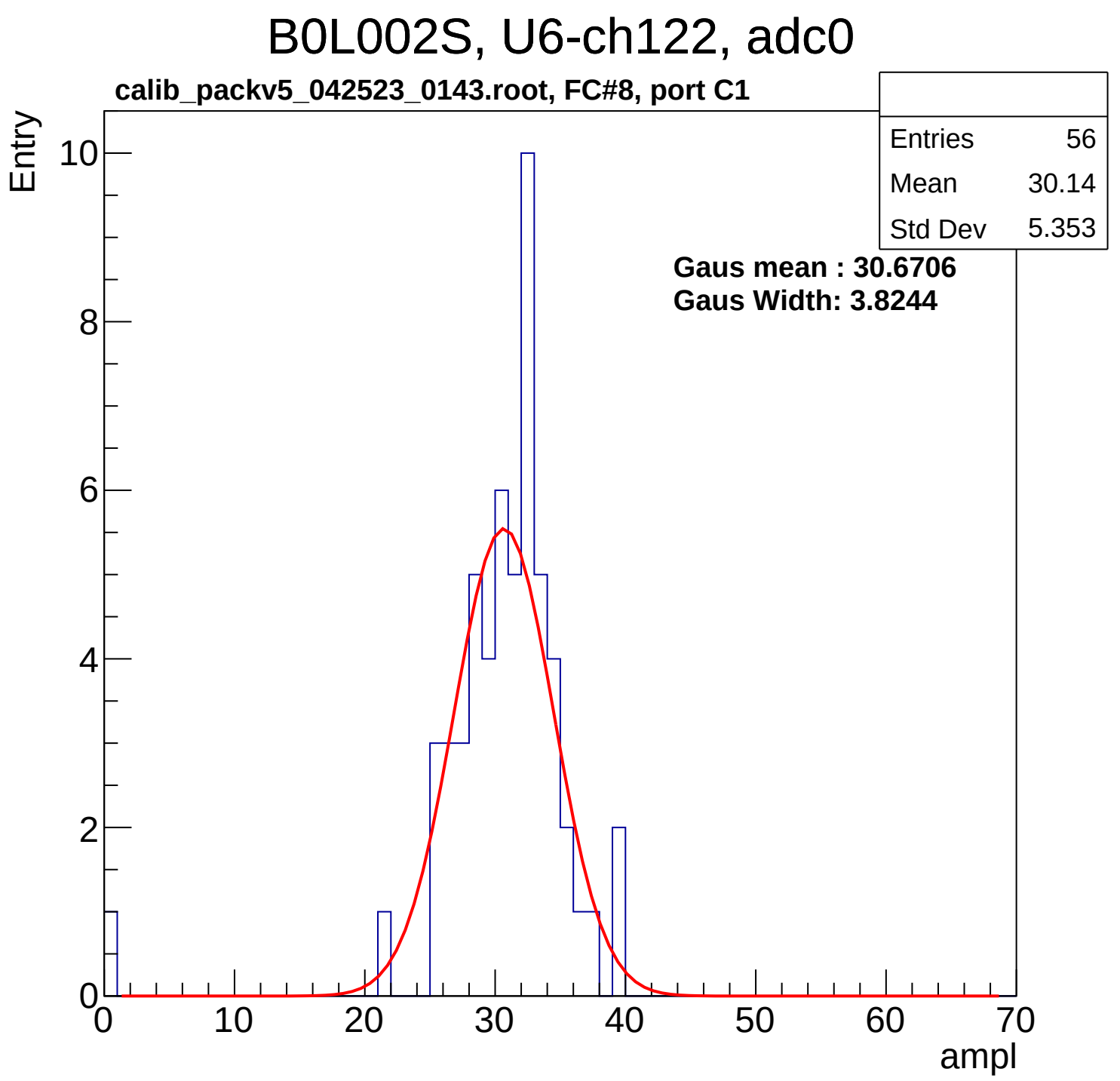
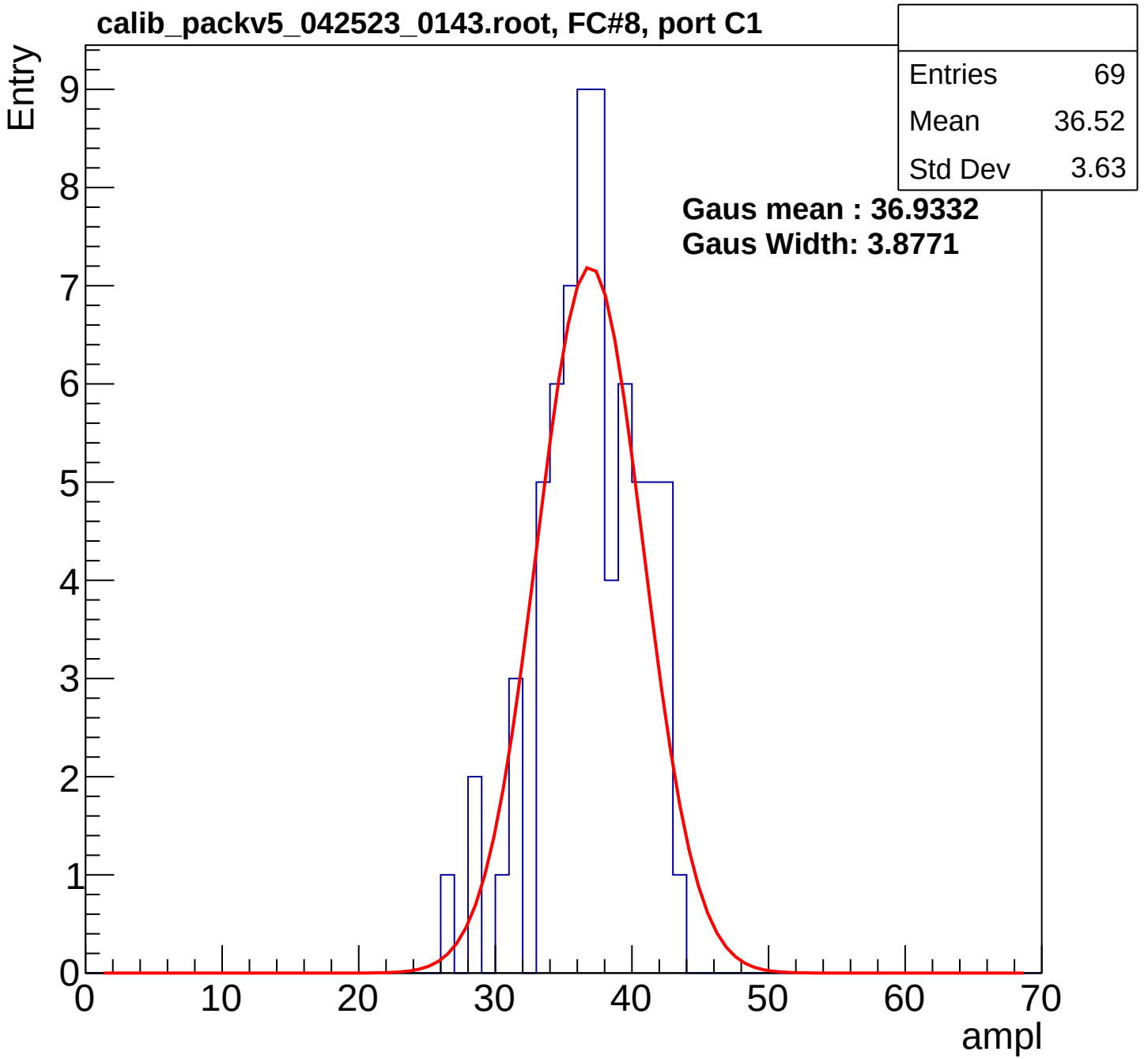
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 30.14
Std Dev 5.353
Gaus mean : 30.6706
Gaus Width: 3.8244
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.52
Std Dev 3.63
Gaus mean : 36.9332
Gaus Width: 3.8771
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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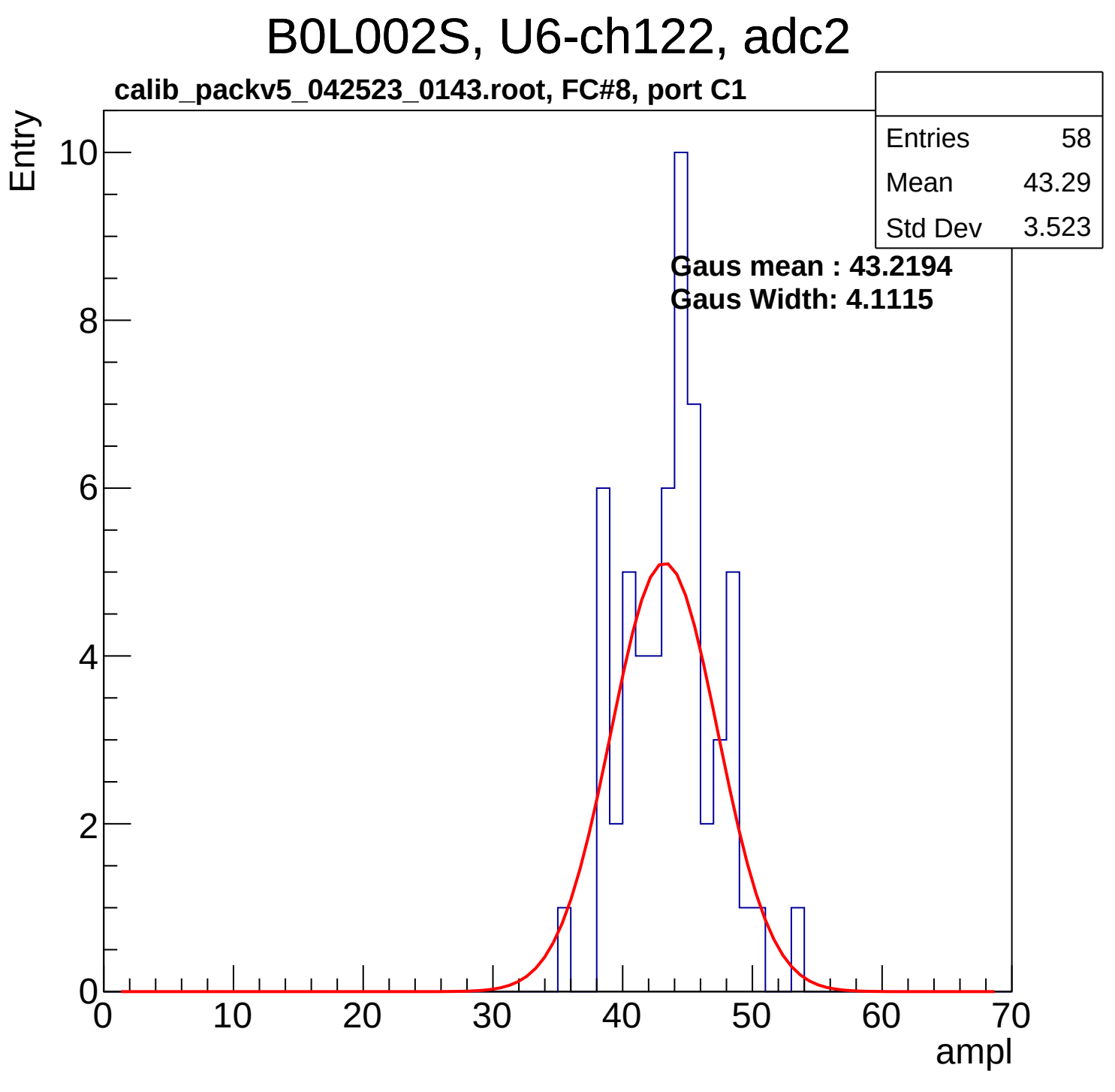
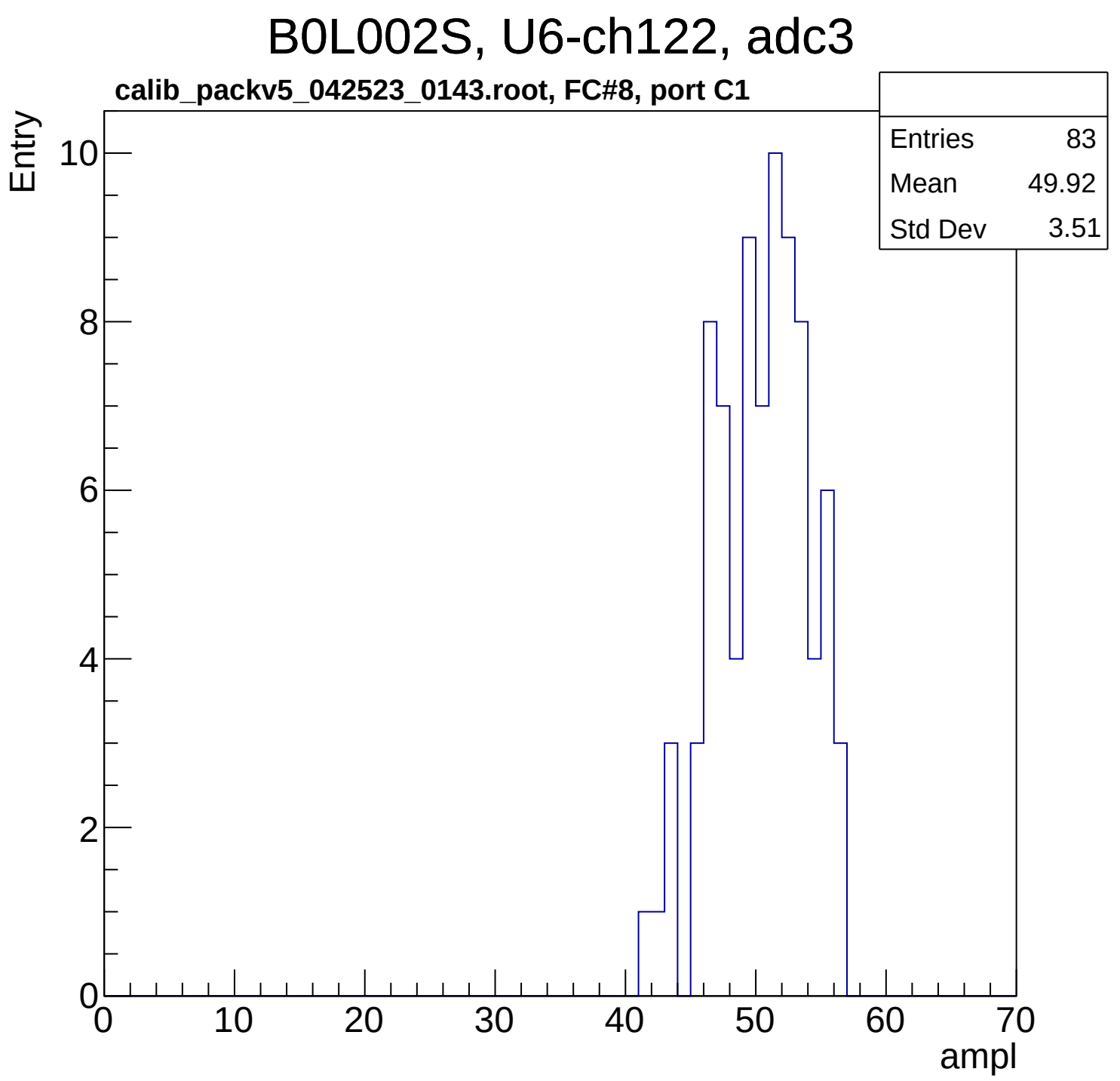
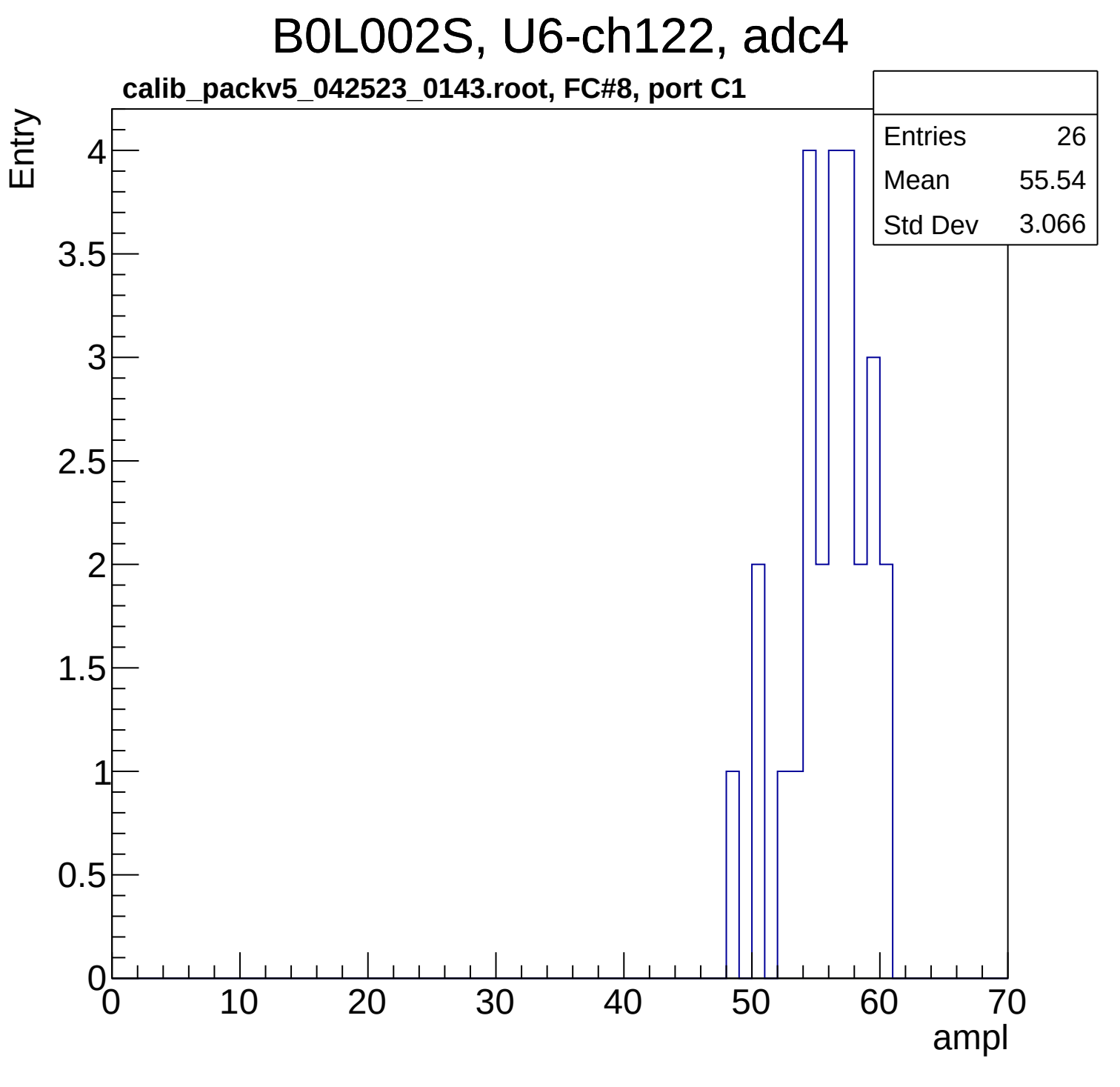
60
70


B0L002S, U6-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.29
Std Dev 3.523
Gaus mean : 43.2194
Gaus Width: 4.1115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 49.92
Std Dev 3.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 55.54
Std Dev 3.066
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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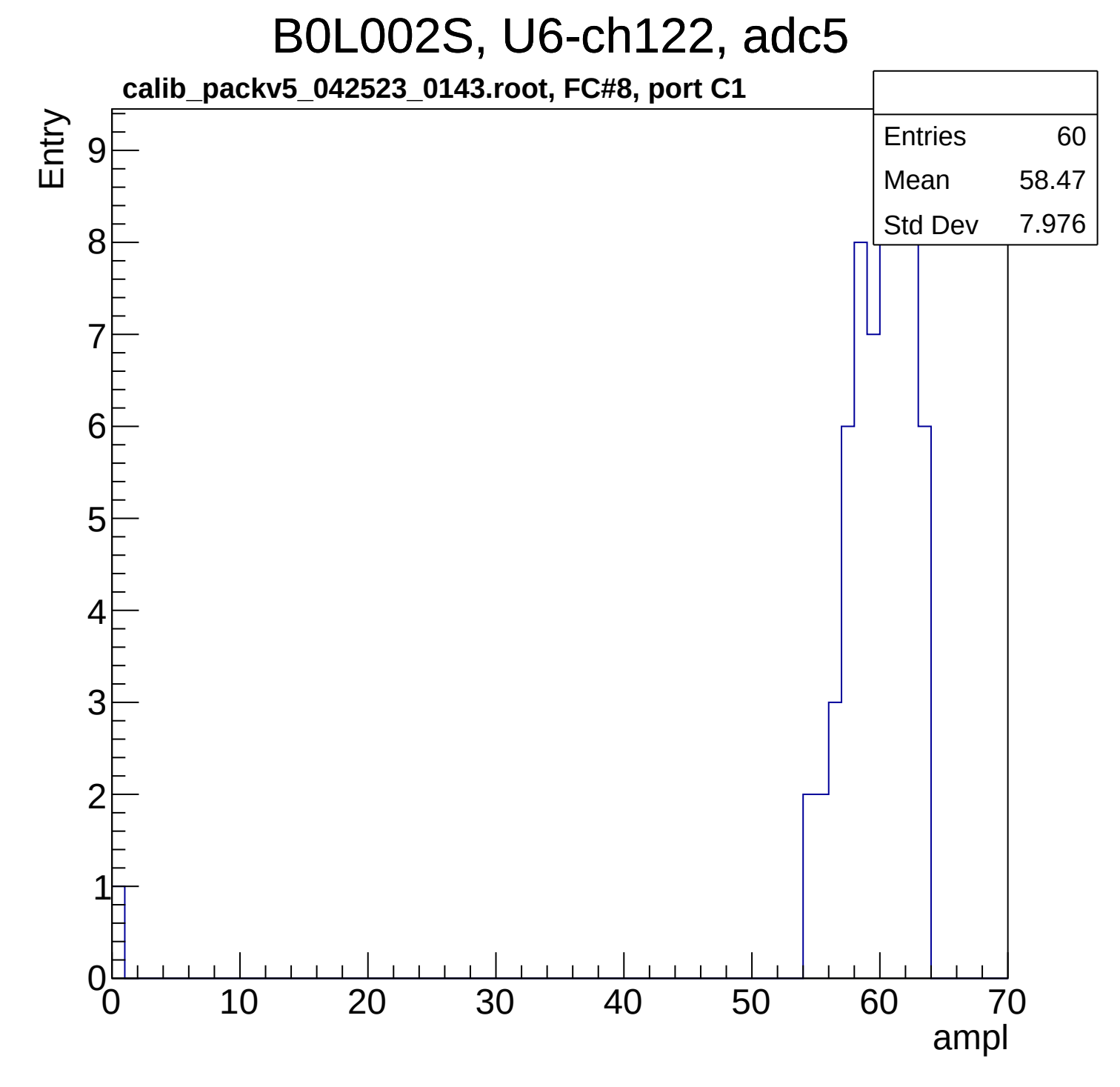
40
50
60
70


B0L002S, U6-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 58.47
Std Dev 7.976
Entry
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1
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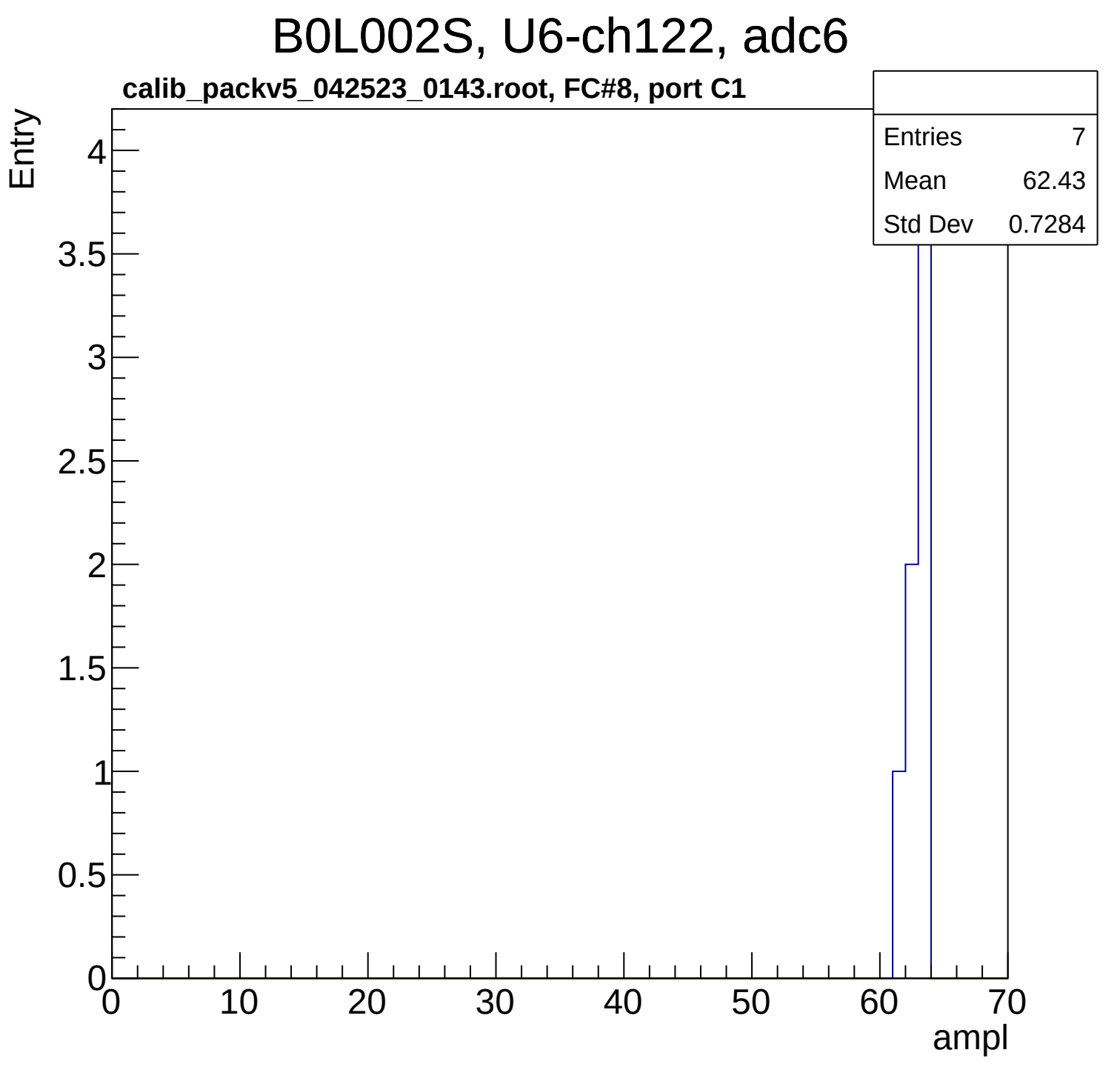
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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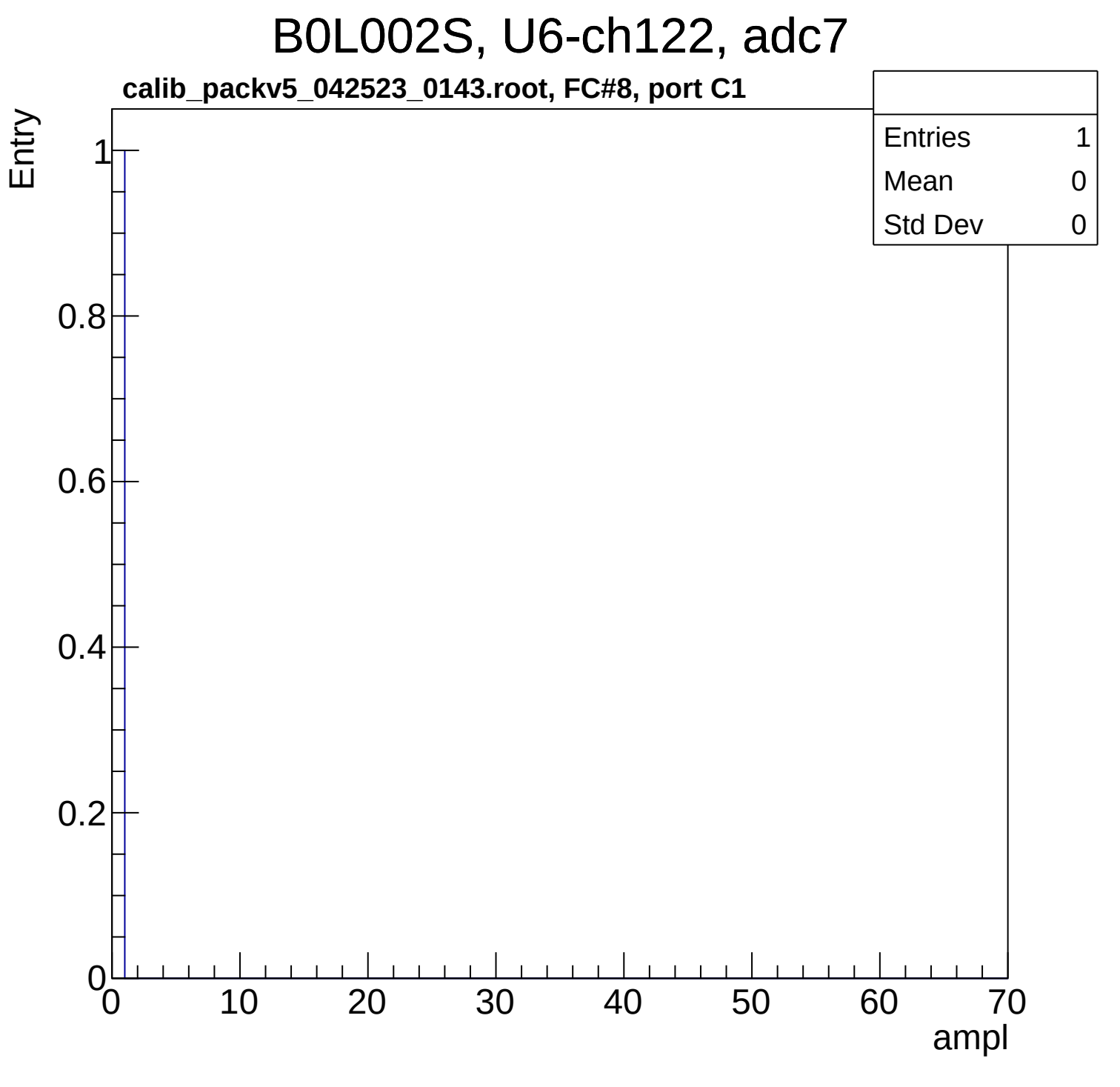
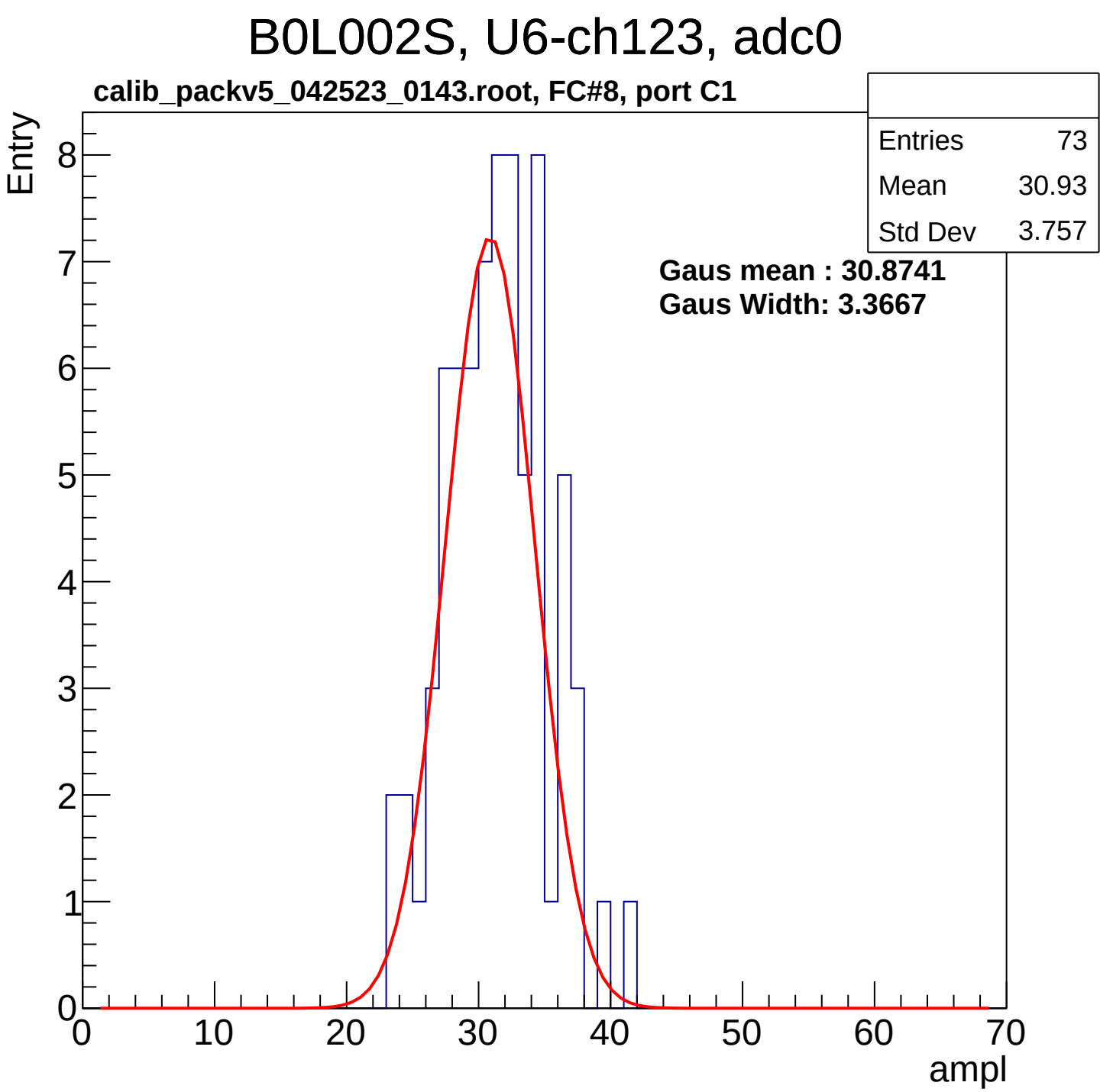
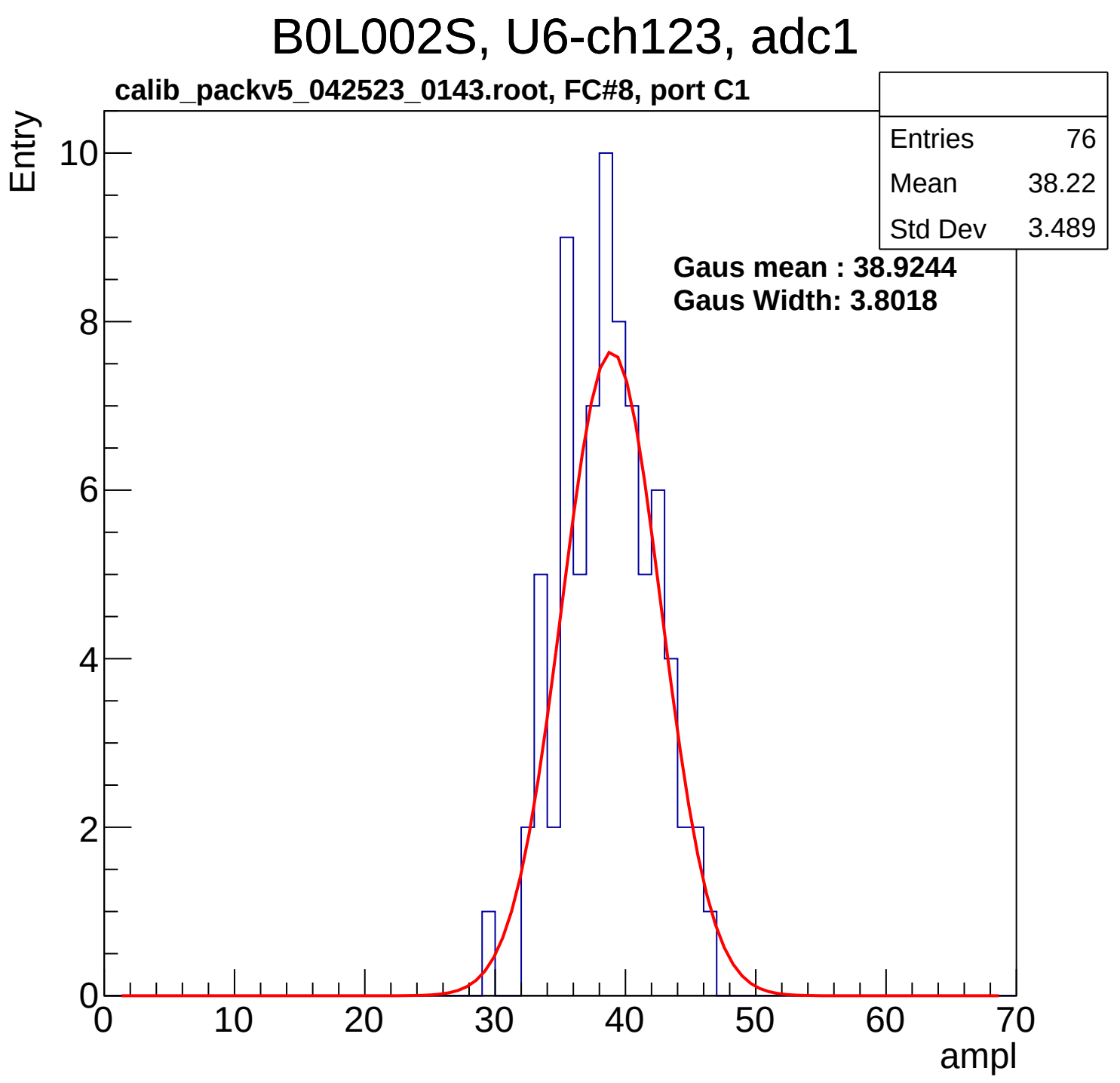
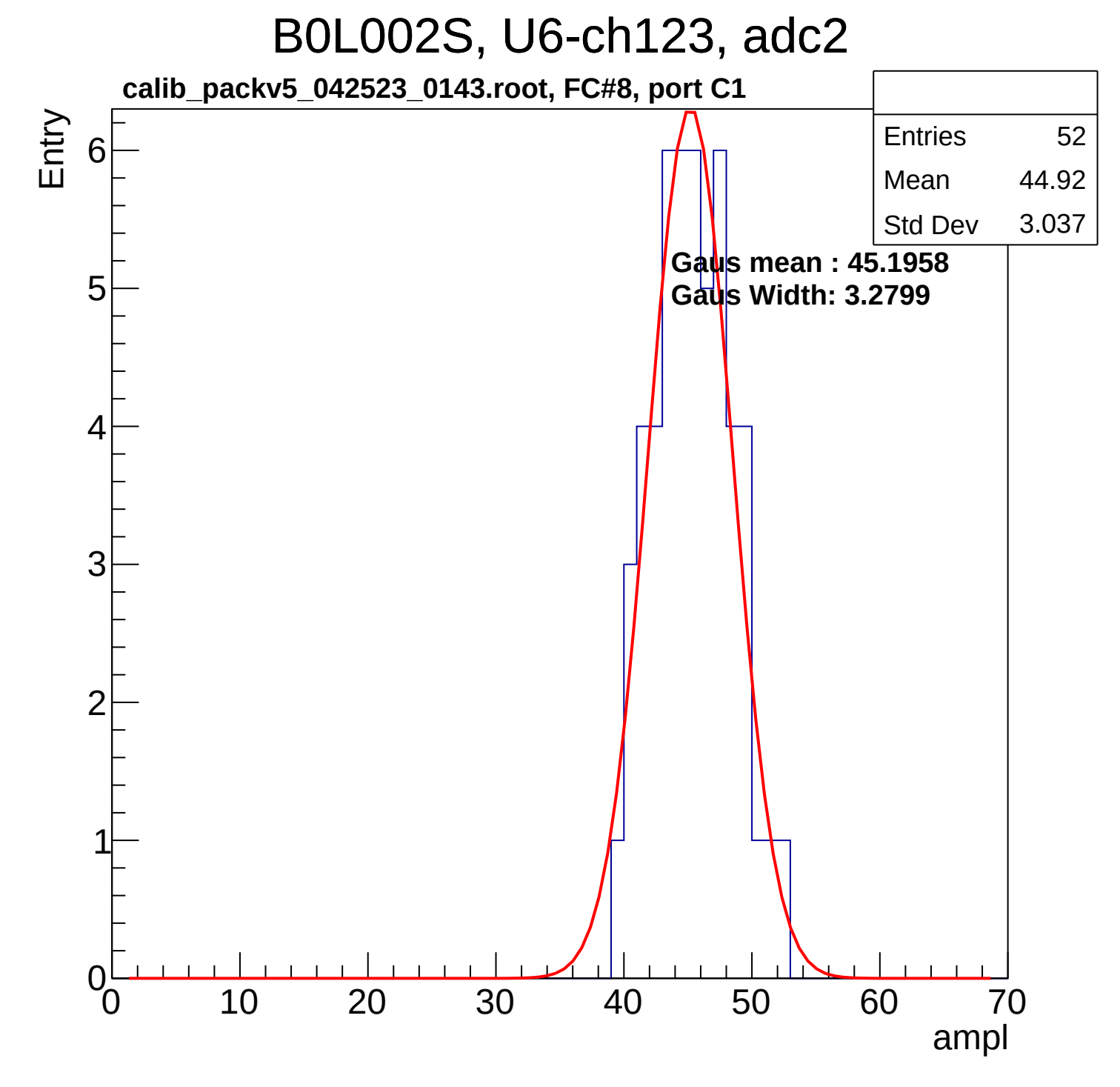
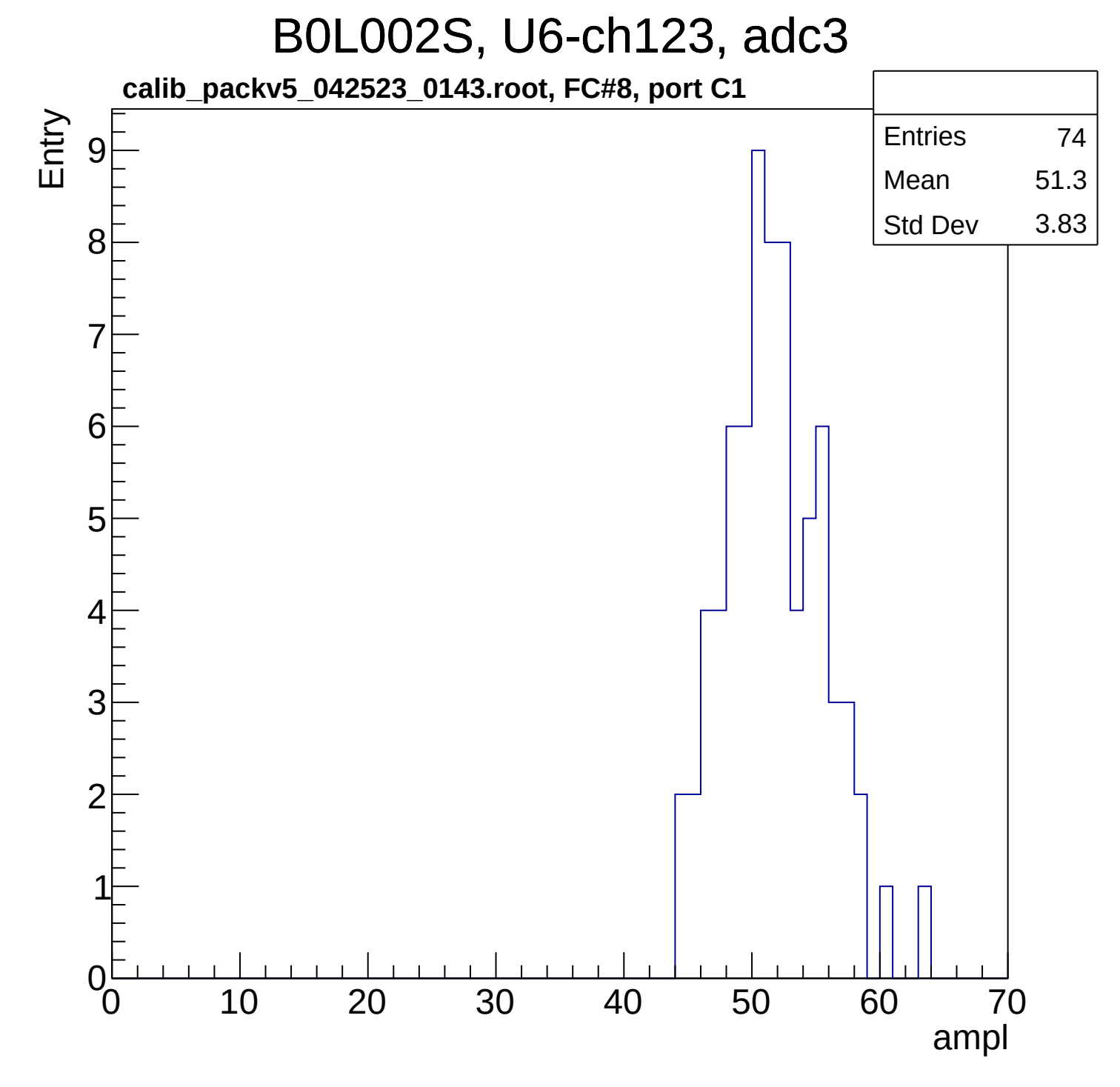
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U6-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.93
Std Dev 3.757
Gaus mean : 30.8741
Gaus Width: 3.3667
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 38.22
Std Dev 3.489
Gaus mean : 38.9244
Gaus Width: 3.8018
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 44.92
Std Dev 3.037
Gaus mean : 45.1958
Gaus Width: 3.2799
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U6-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 51.3
Std Dev 3.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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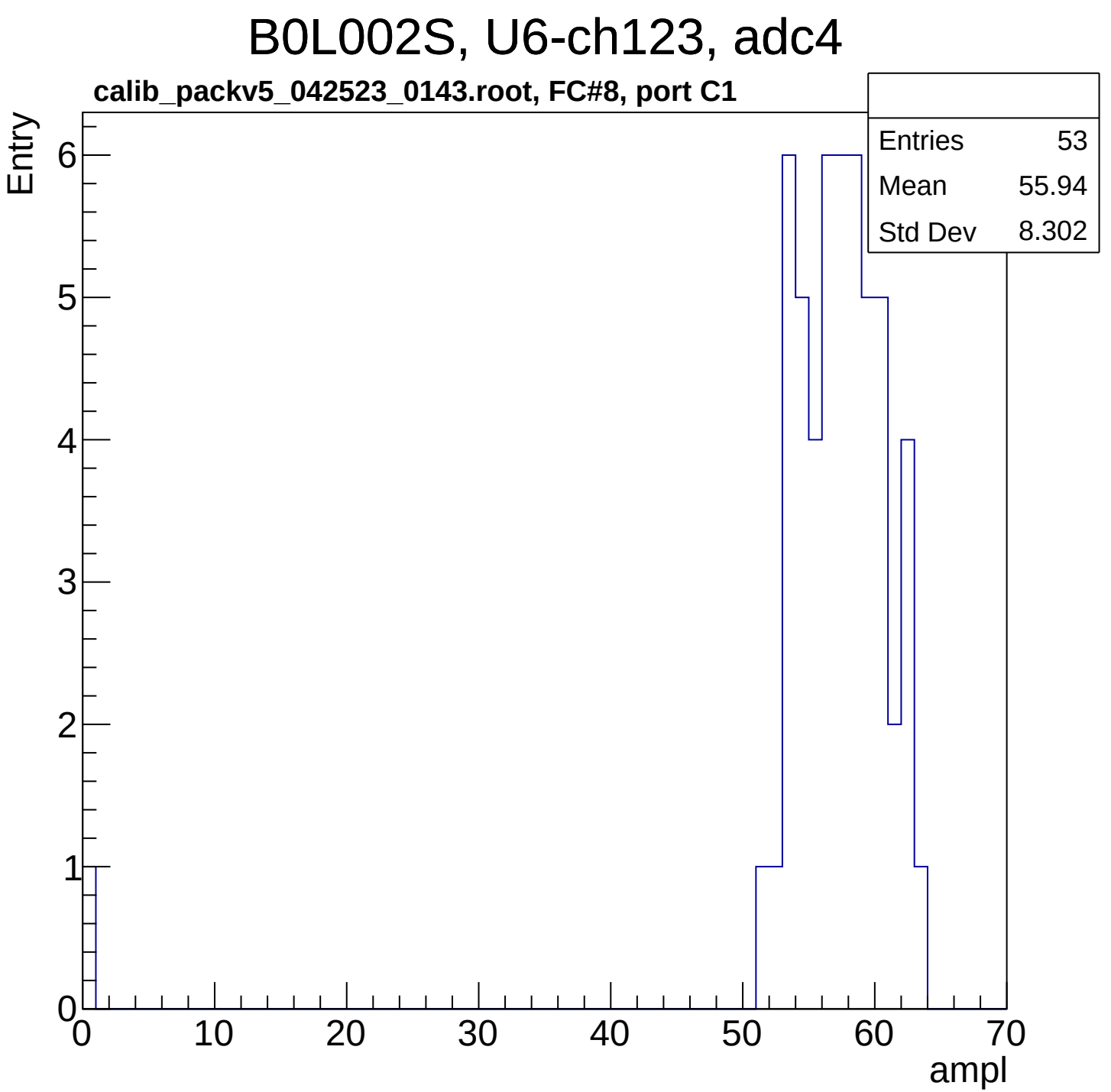
B0L002S, U6-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 55.94
Std Dev 8.302
Entry
0
1
2
3
4
5
6
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10
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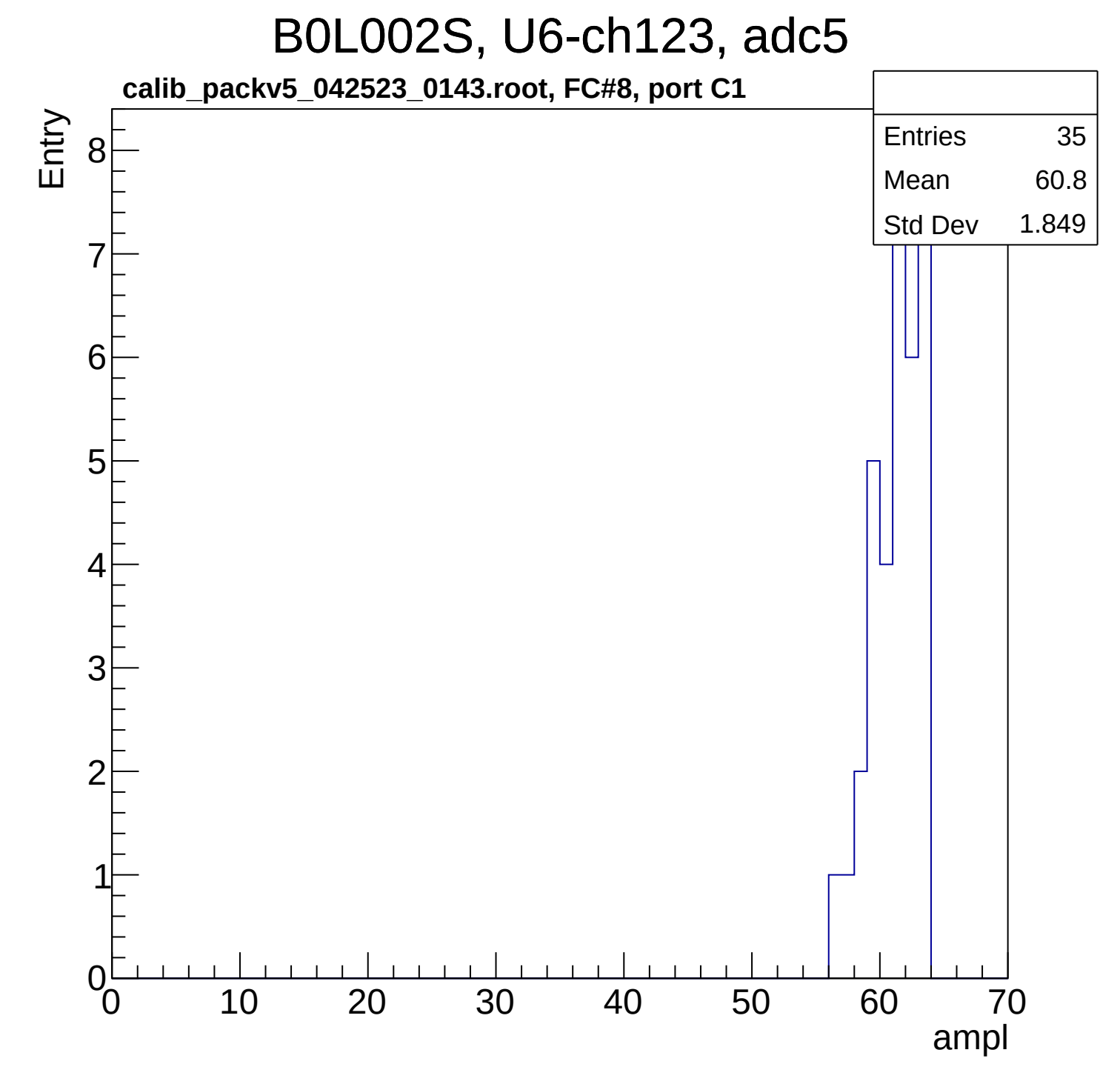
70
ampl


B0L002S, U6-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.8
Std Dev 1.849
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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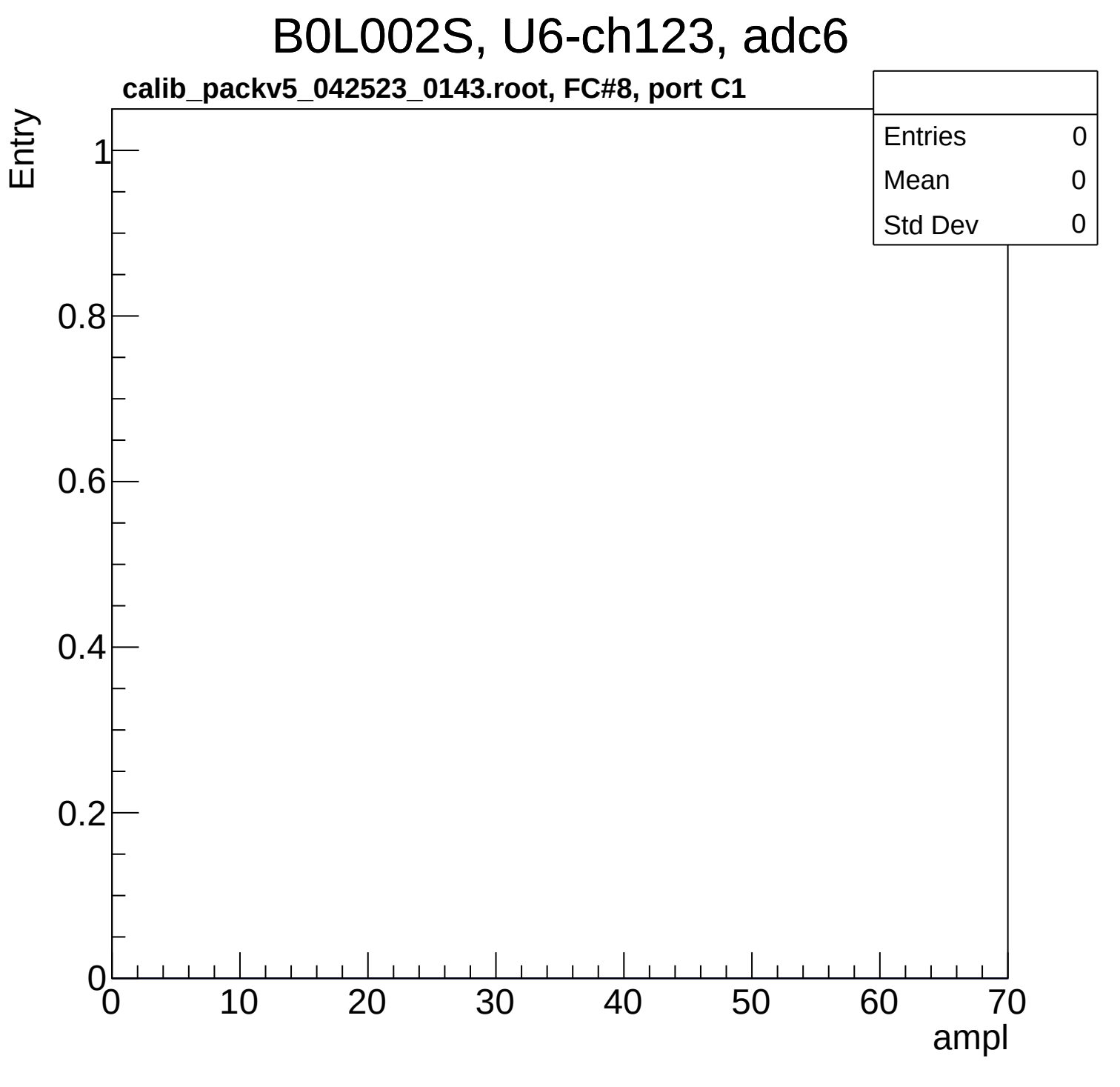
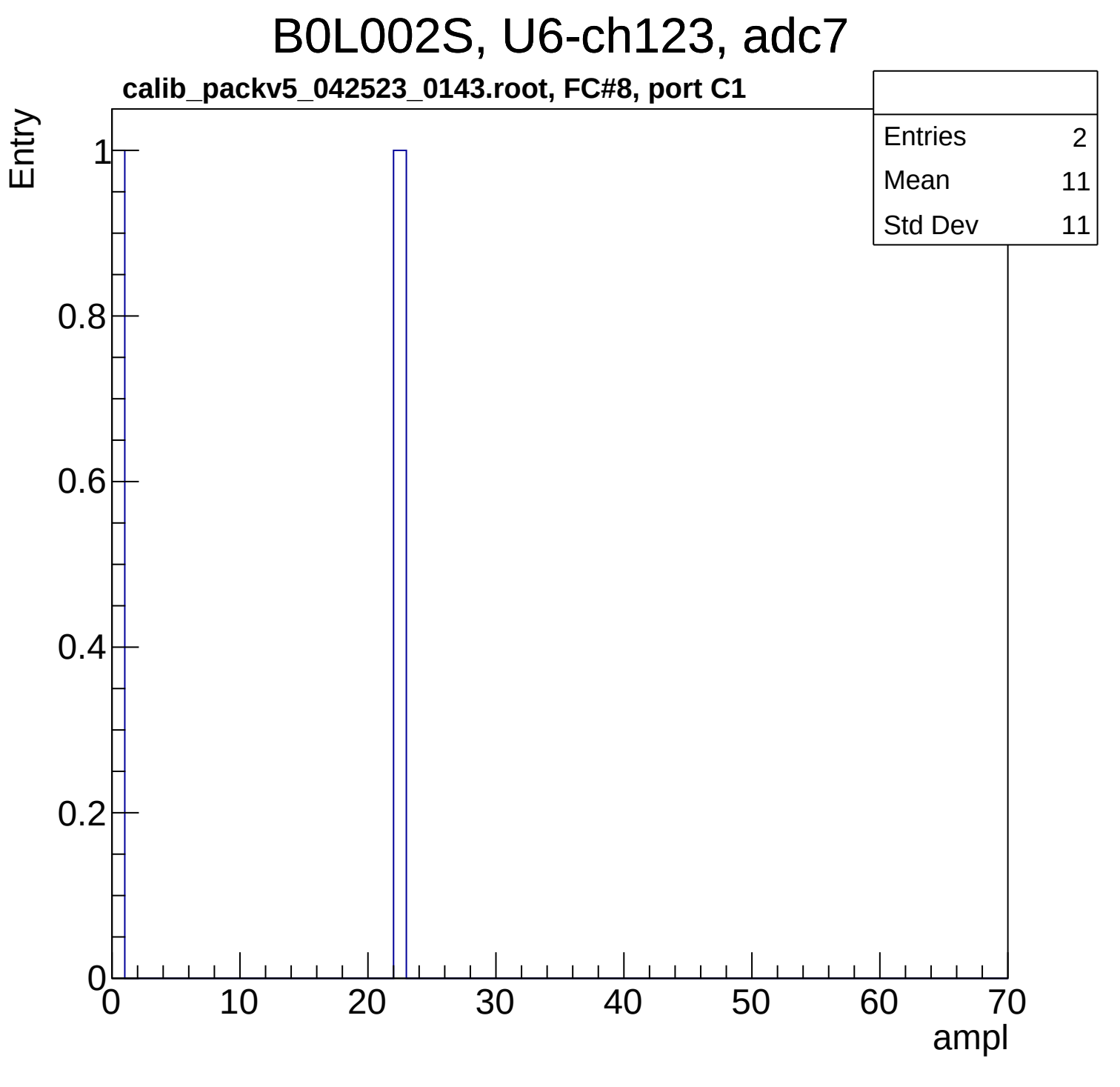

B0L002S, U6-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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0.8
0.6
0.4
0.2
0
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10
20
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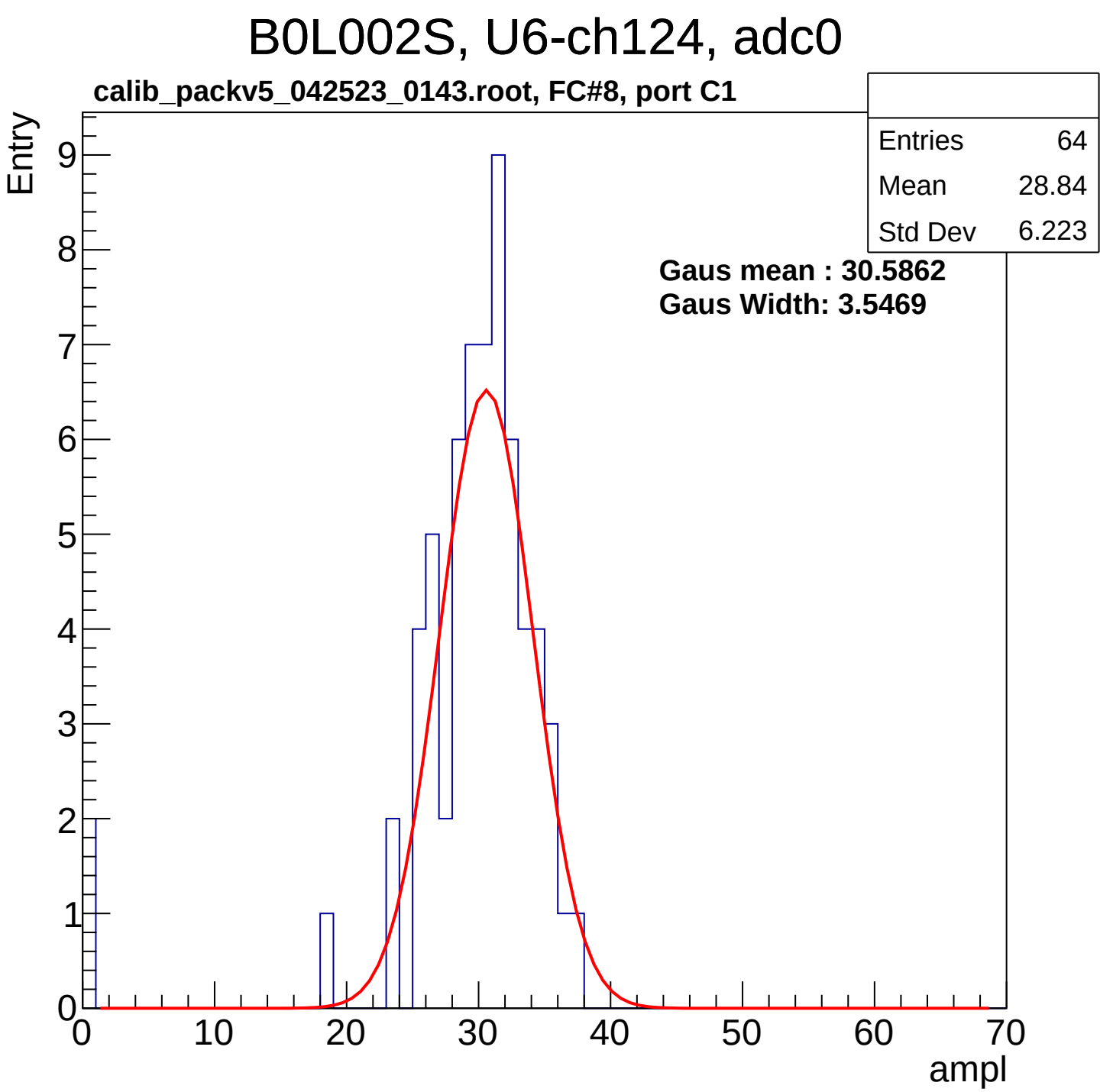
40
50
60
70
ampl


B0L002S, U6-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 28.84
Std Dev 6.223
Gaus mean : 30.5862
Gaus Width: 3.5469
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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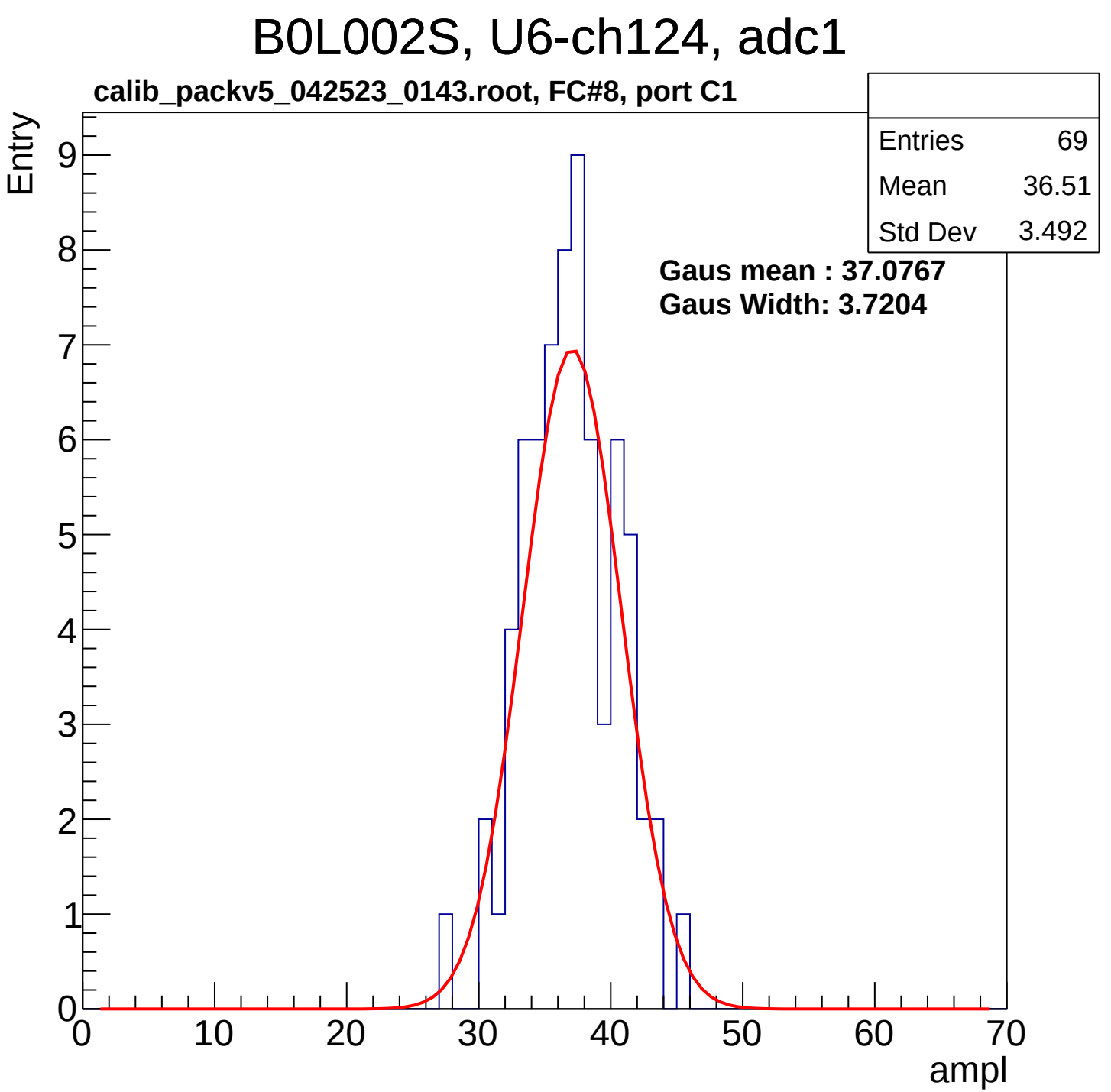
B0L002S, U6-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.51
Std Dev 3.492
Gaus mean : 37.0767
Gaus Width: 3.7204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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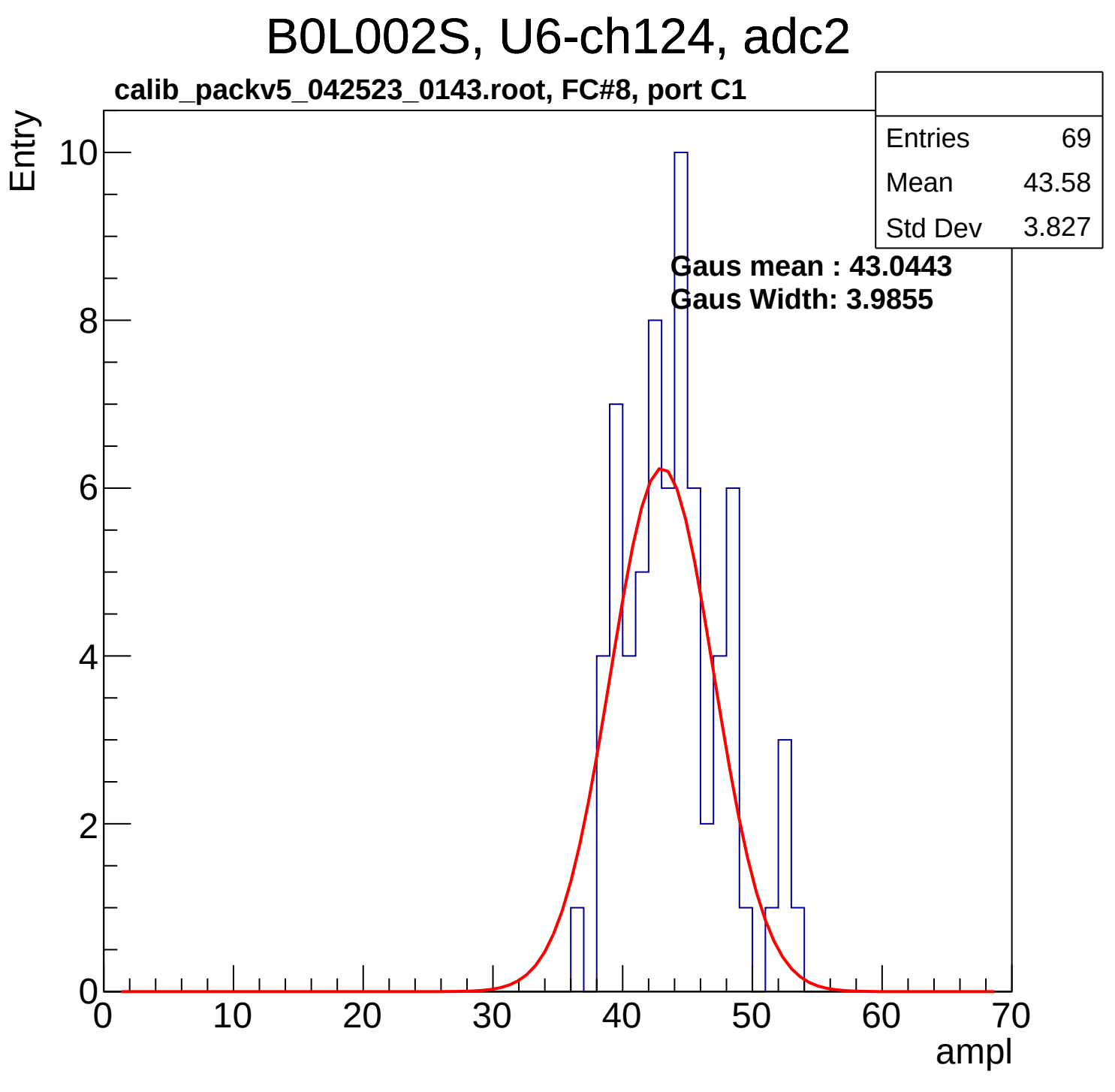
B0L002S, U6-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.58
Std Dev 3.827
Gaus mean : 43.0443
Gaus Width: 3.9855
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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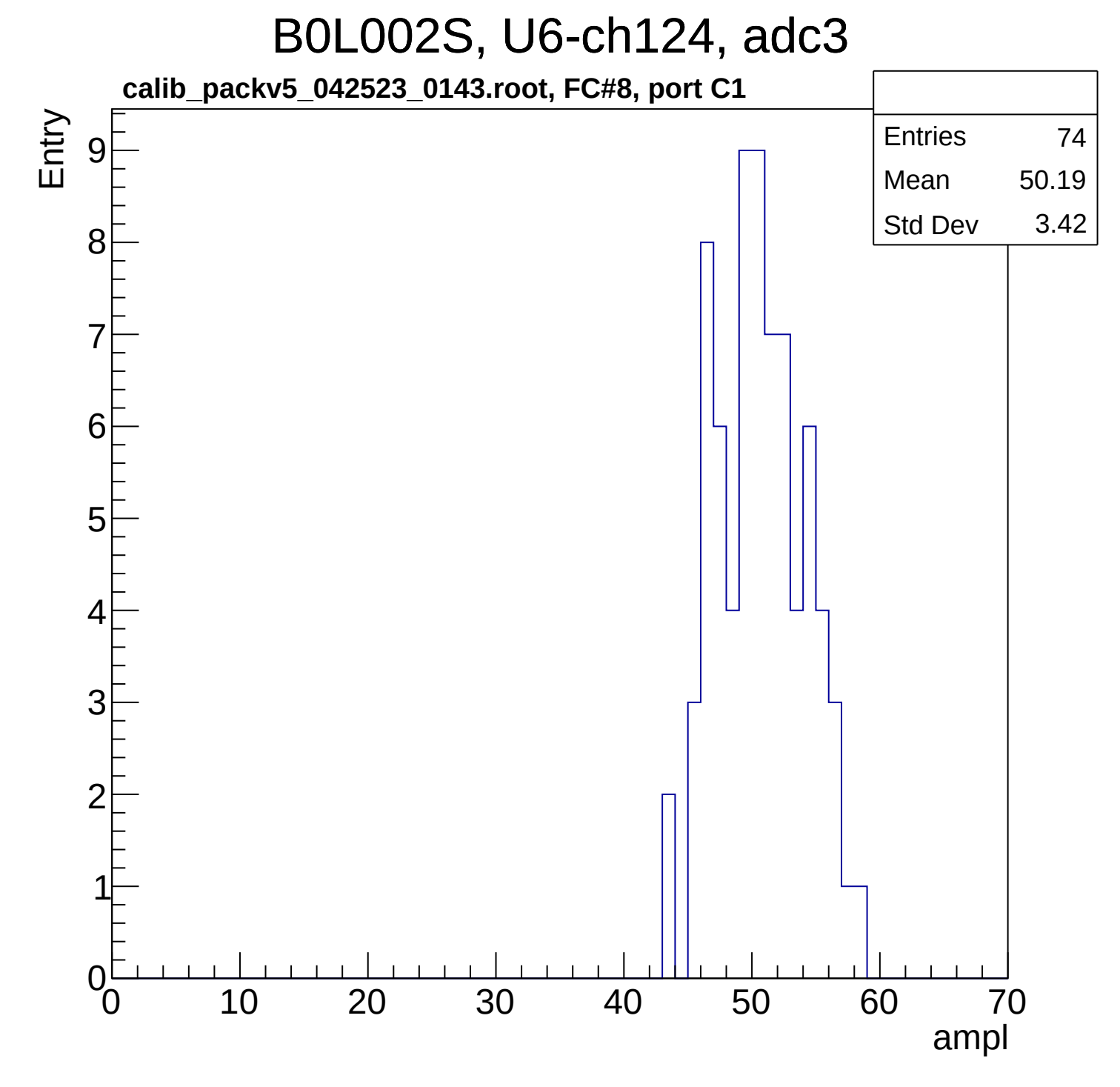
B0L002S, U6-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 50.19
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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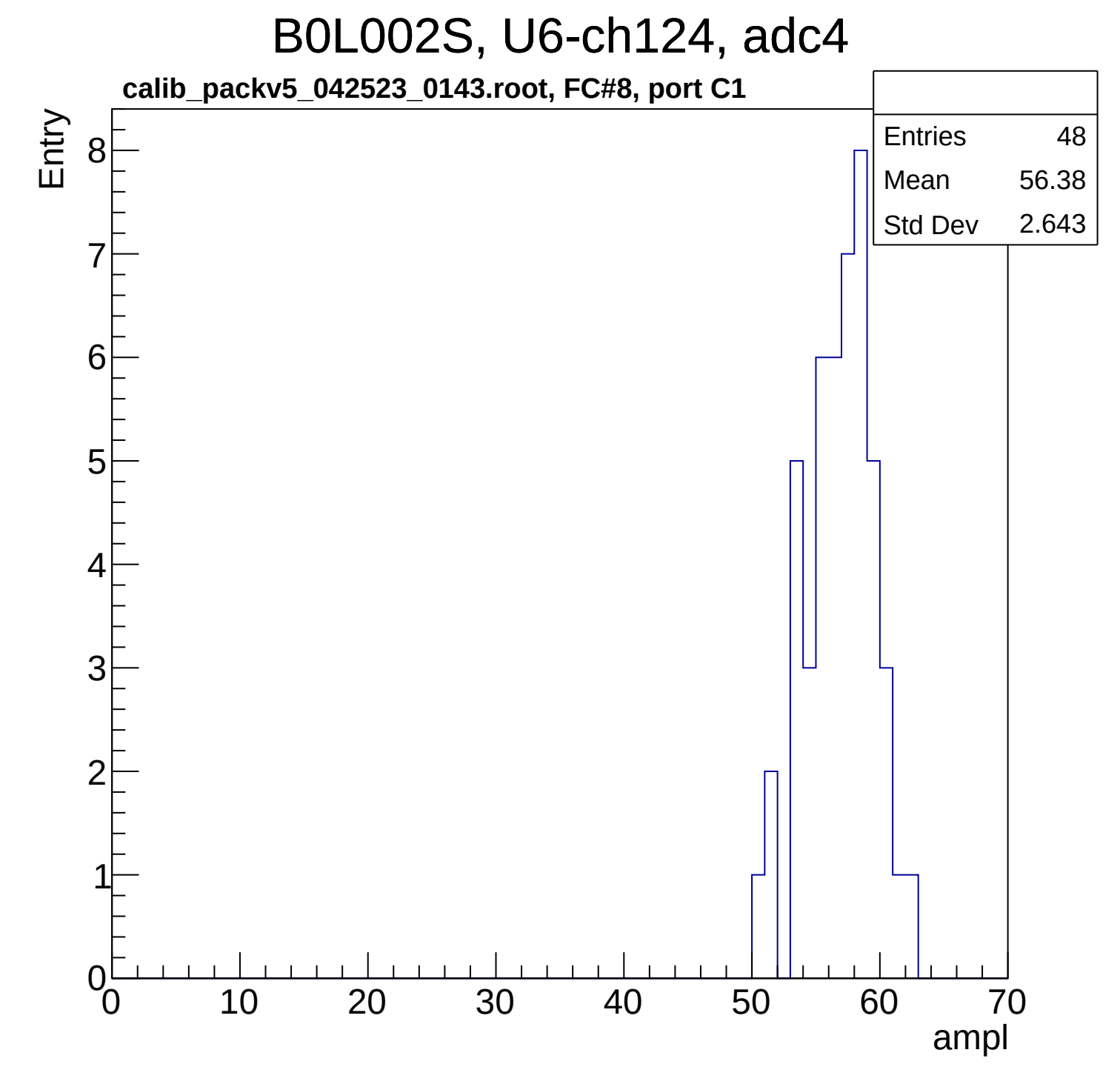
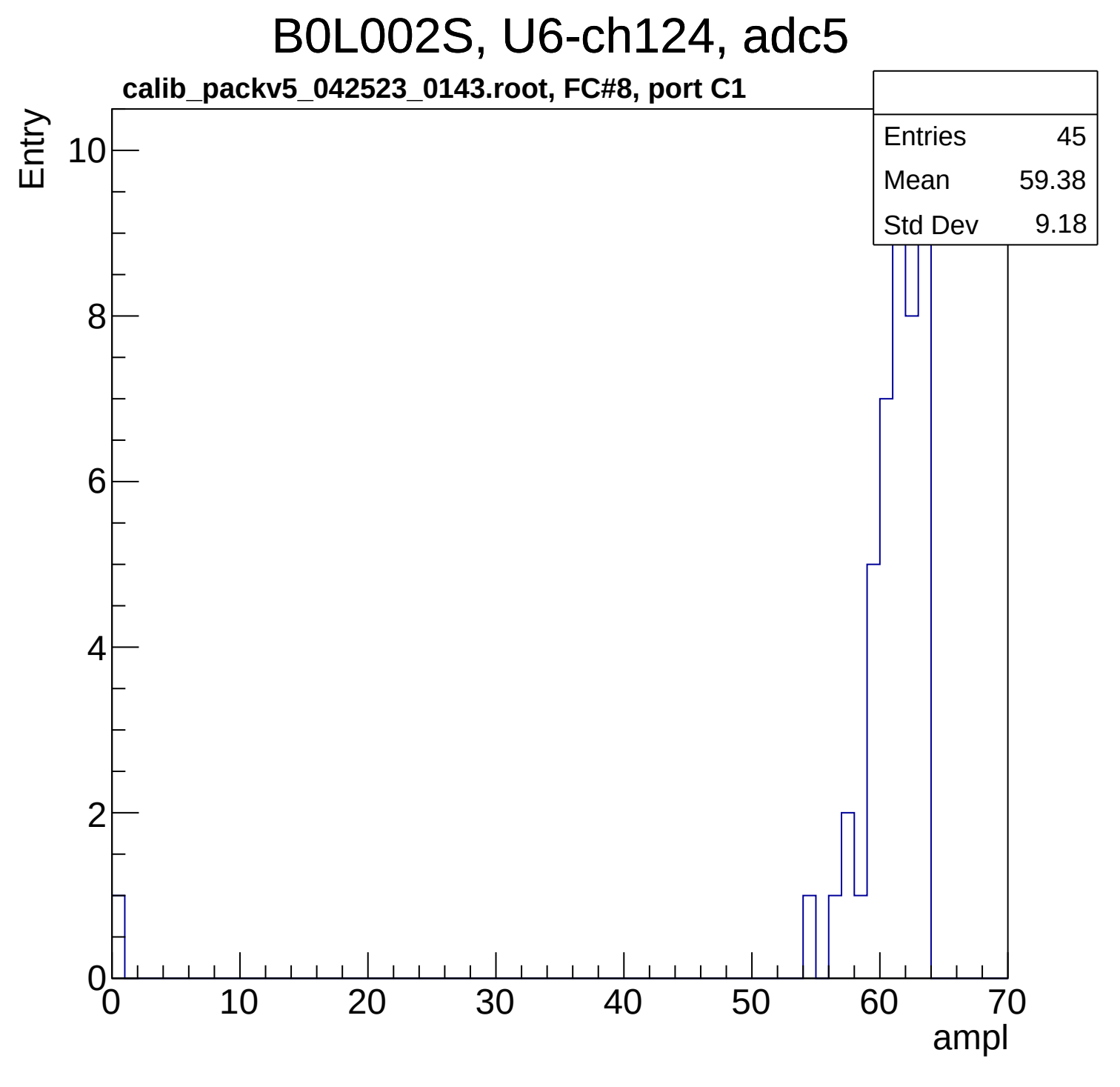
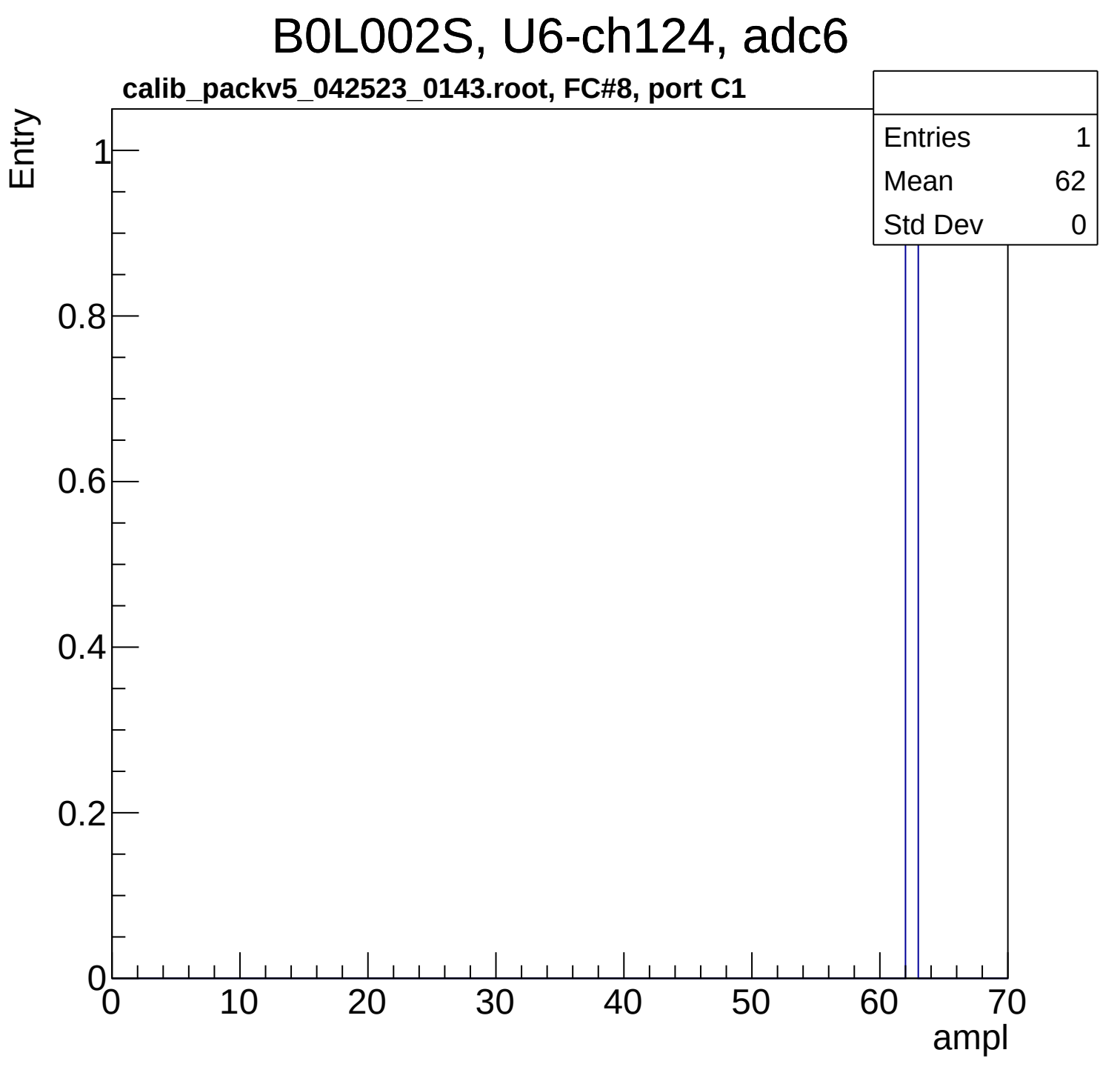
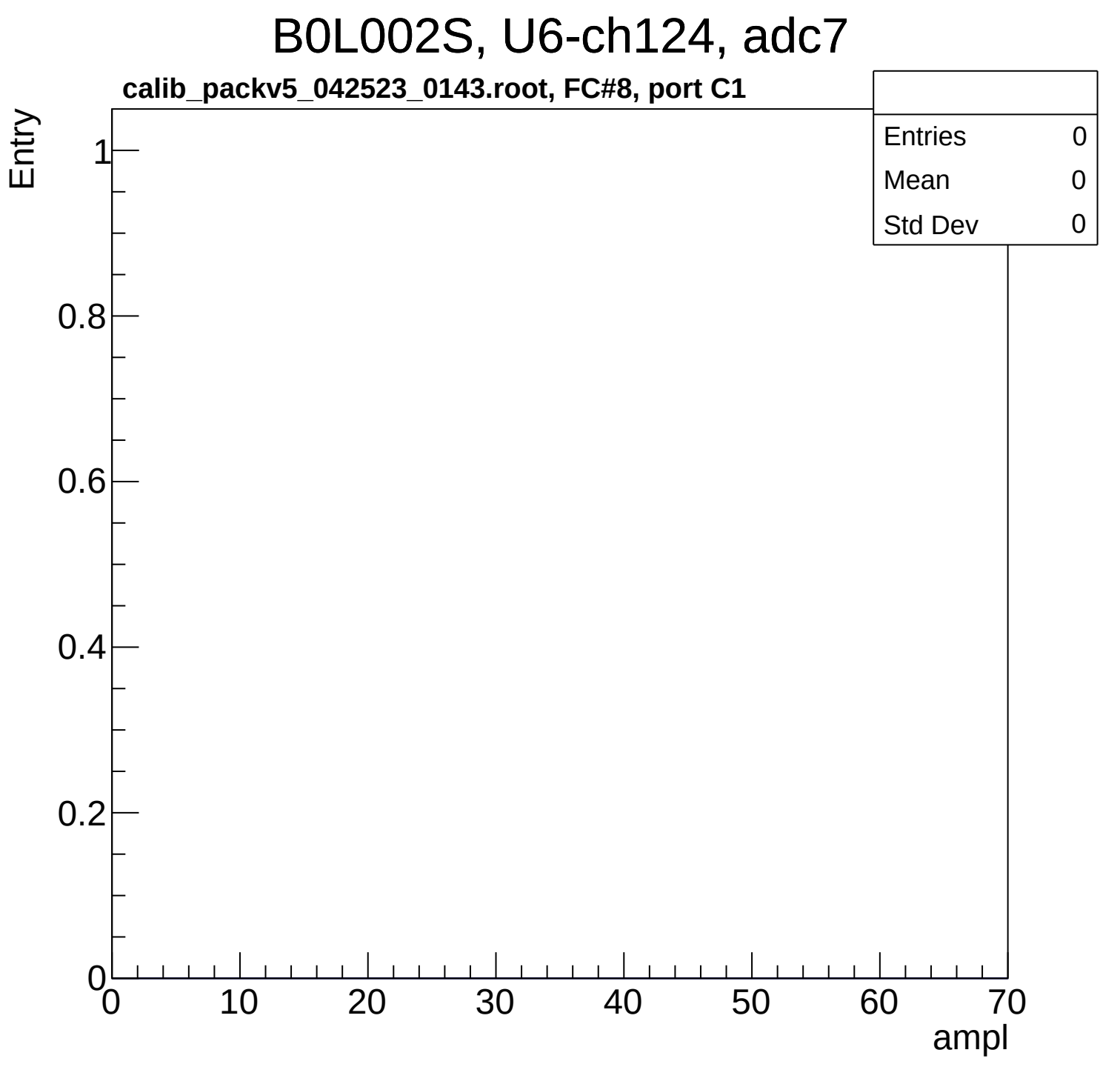

B0L002S, U6-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 56.38
Std Dev 2.643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.38
Std Dev 9.18
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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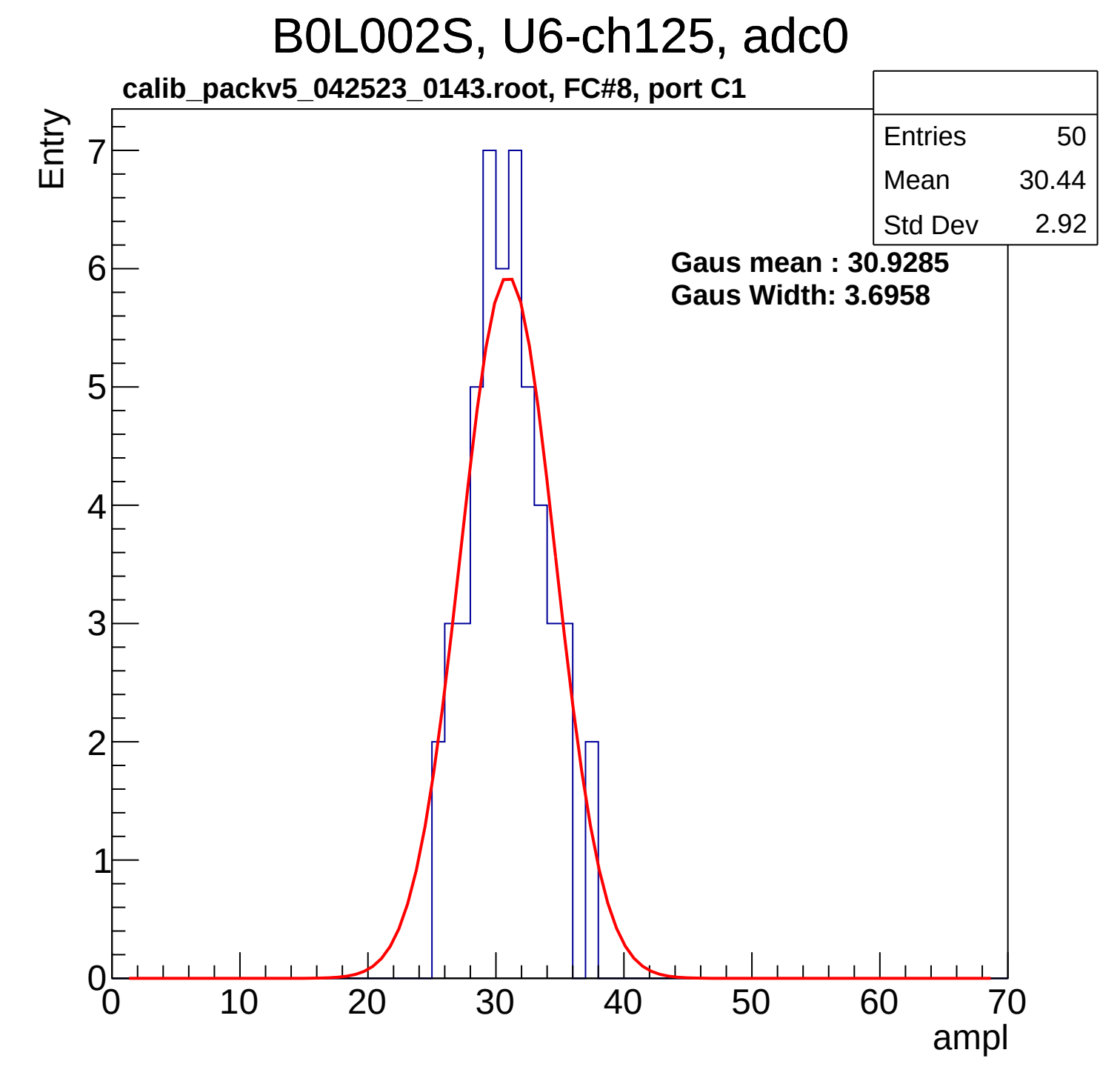
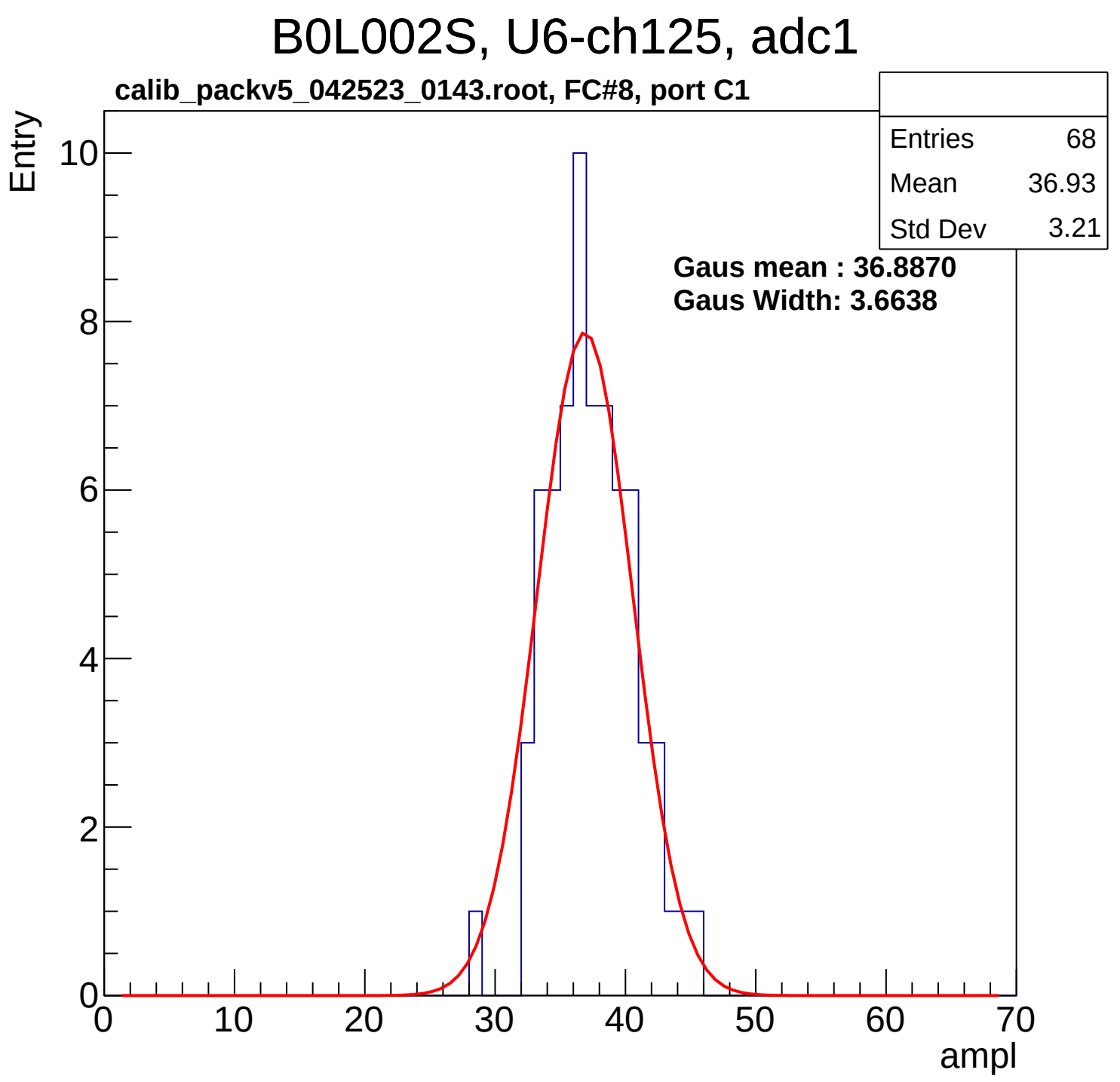
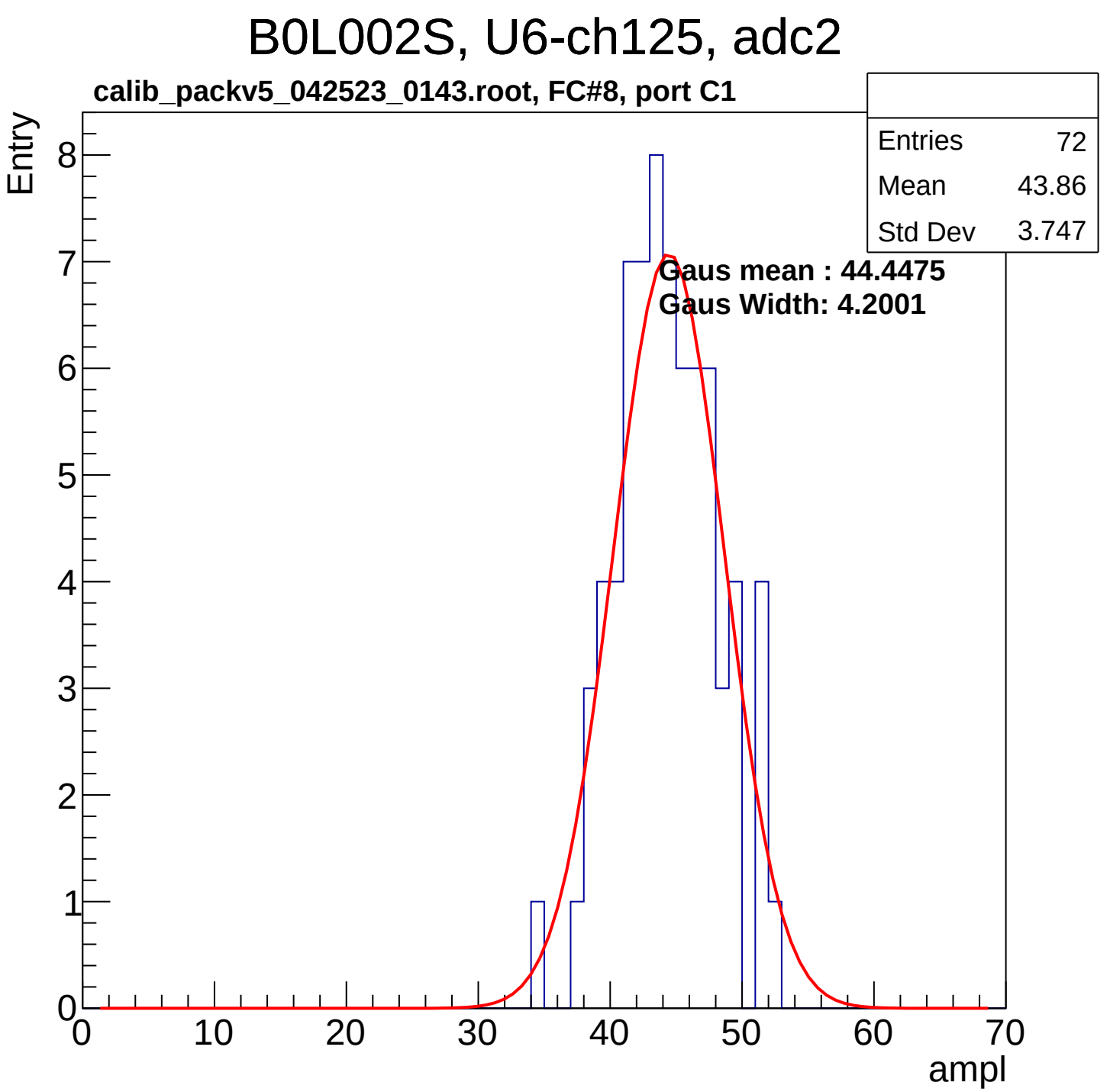
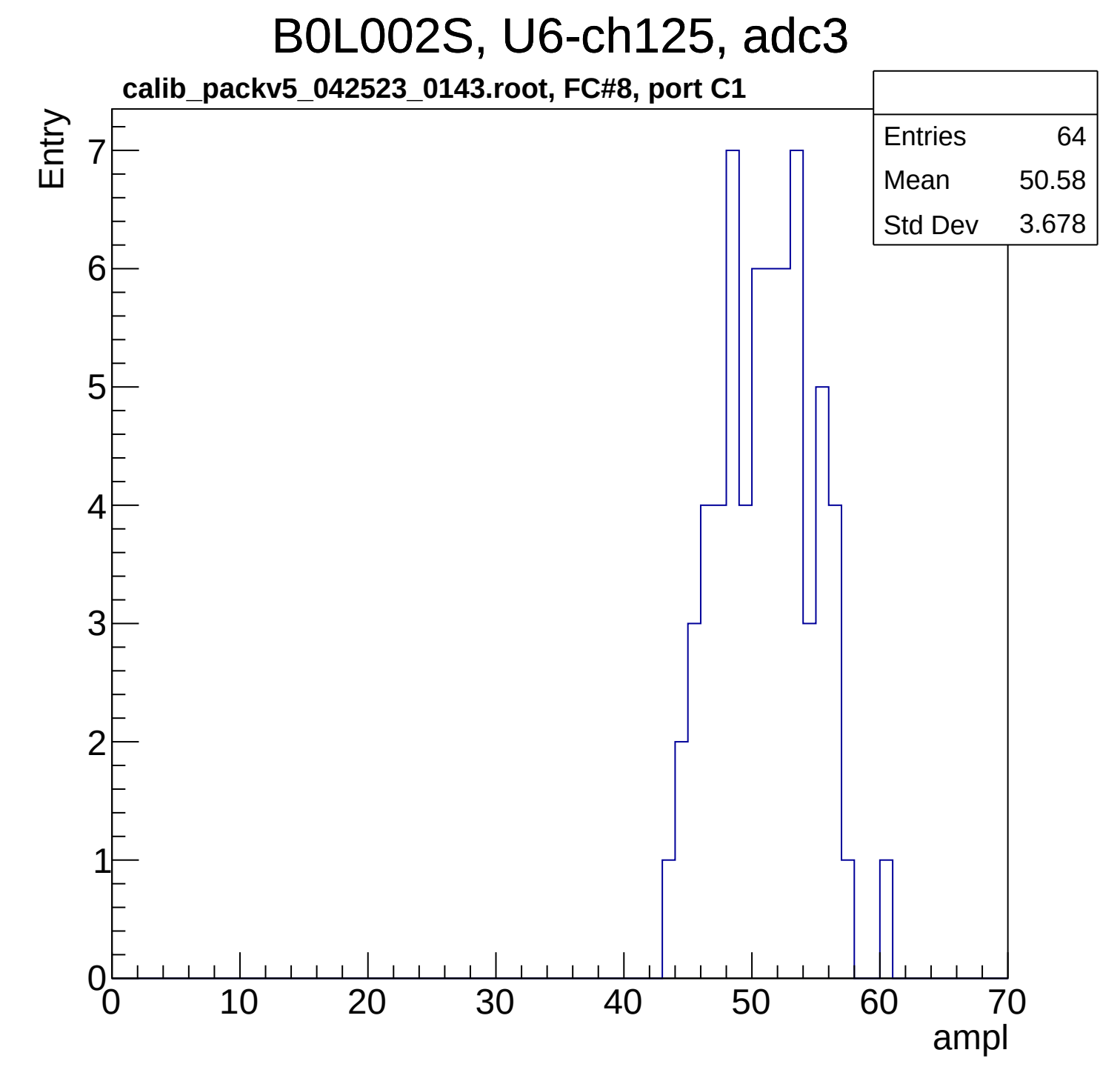
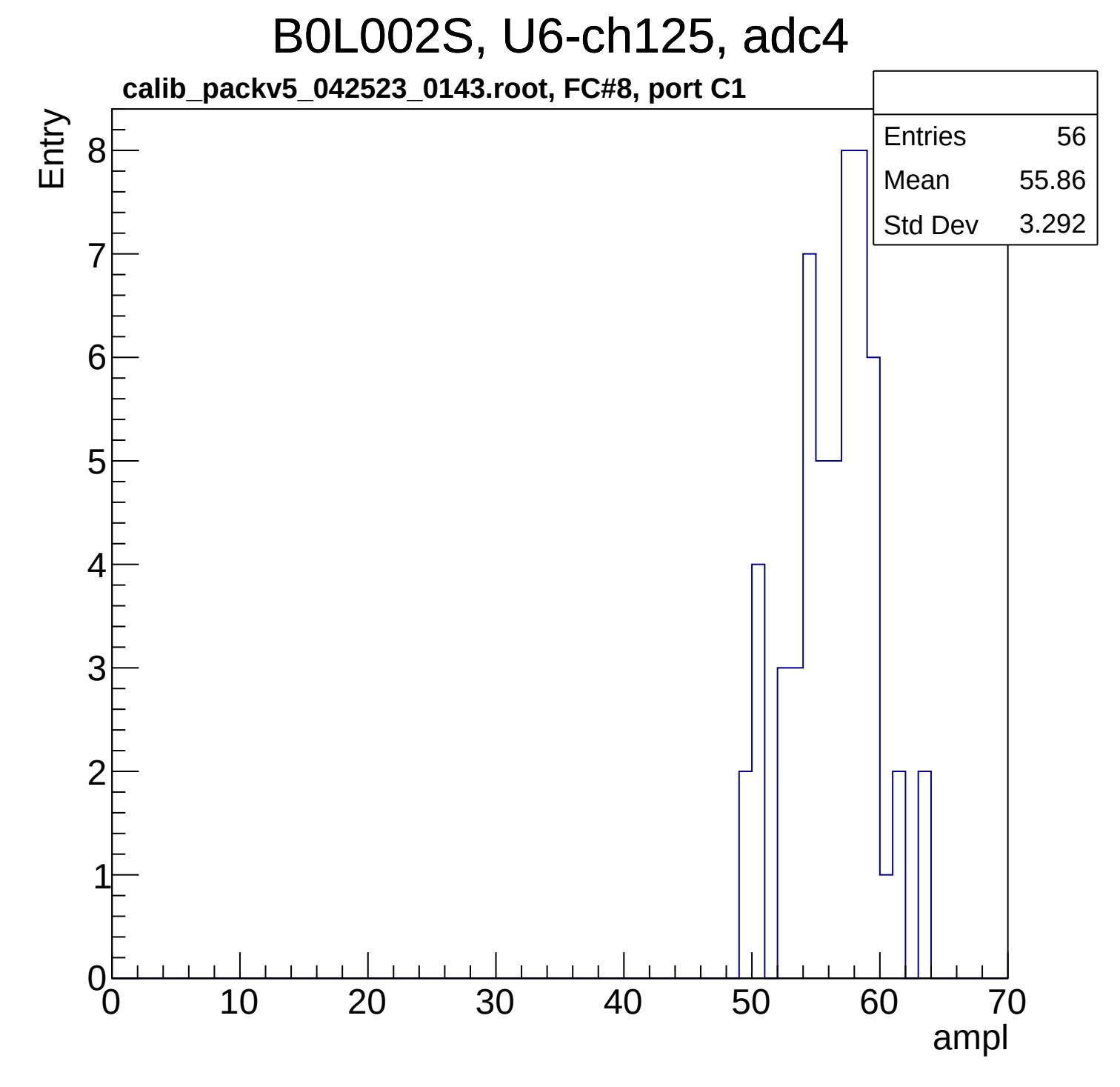
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

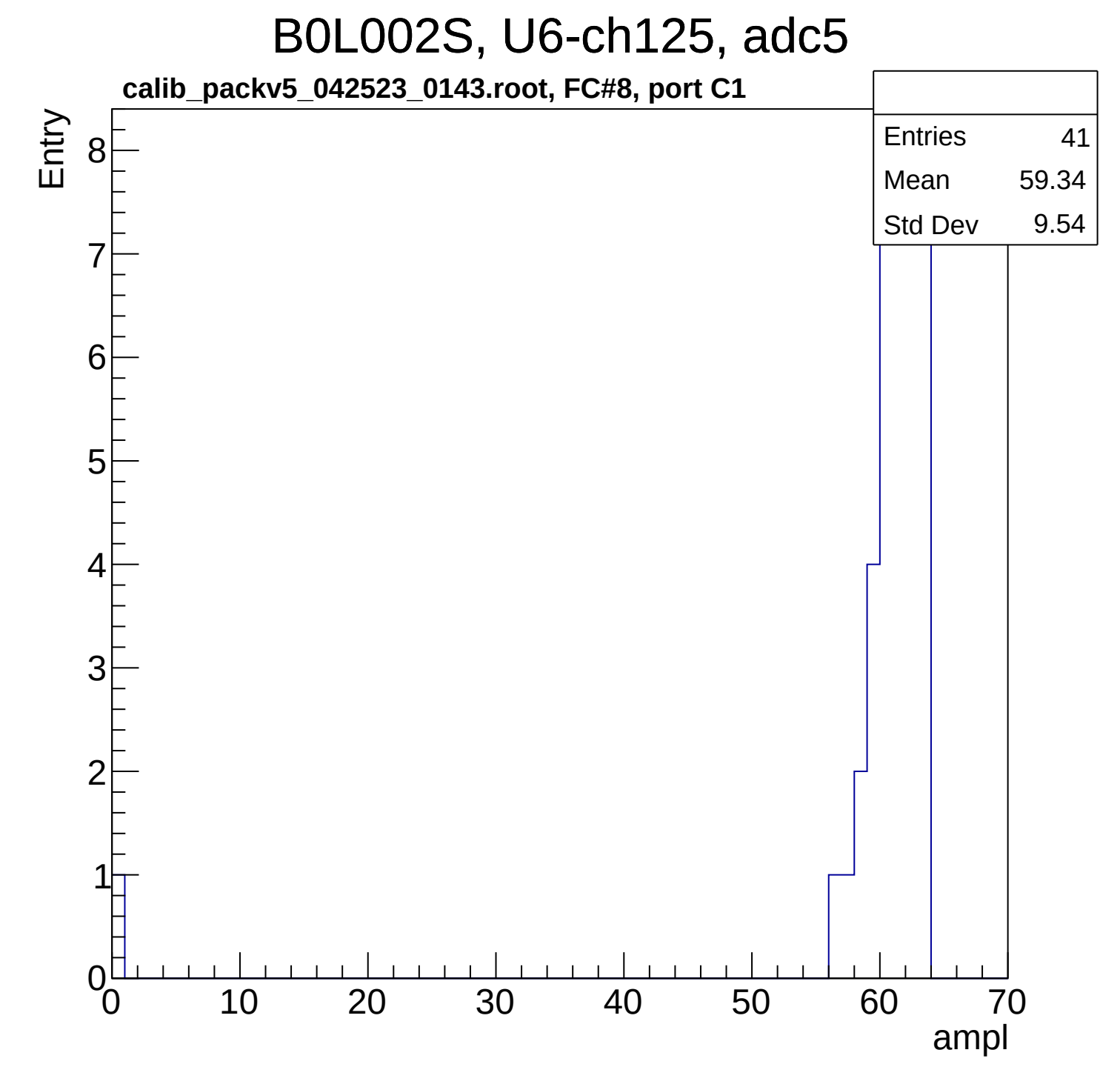

B0L002S, U6-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 30.44
Std Dev 2.92
Gaus mean : 30.9285
Gaus Width: 3.6958
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

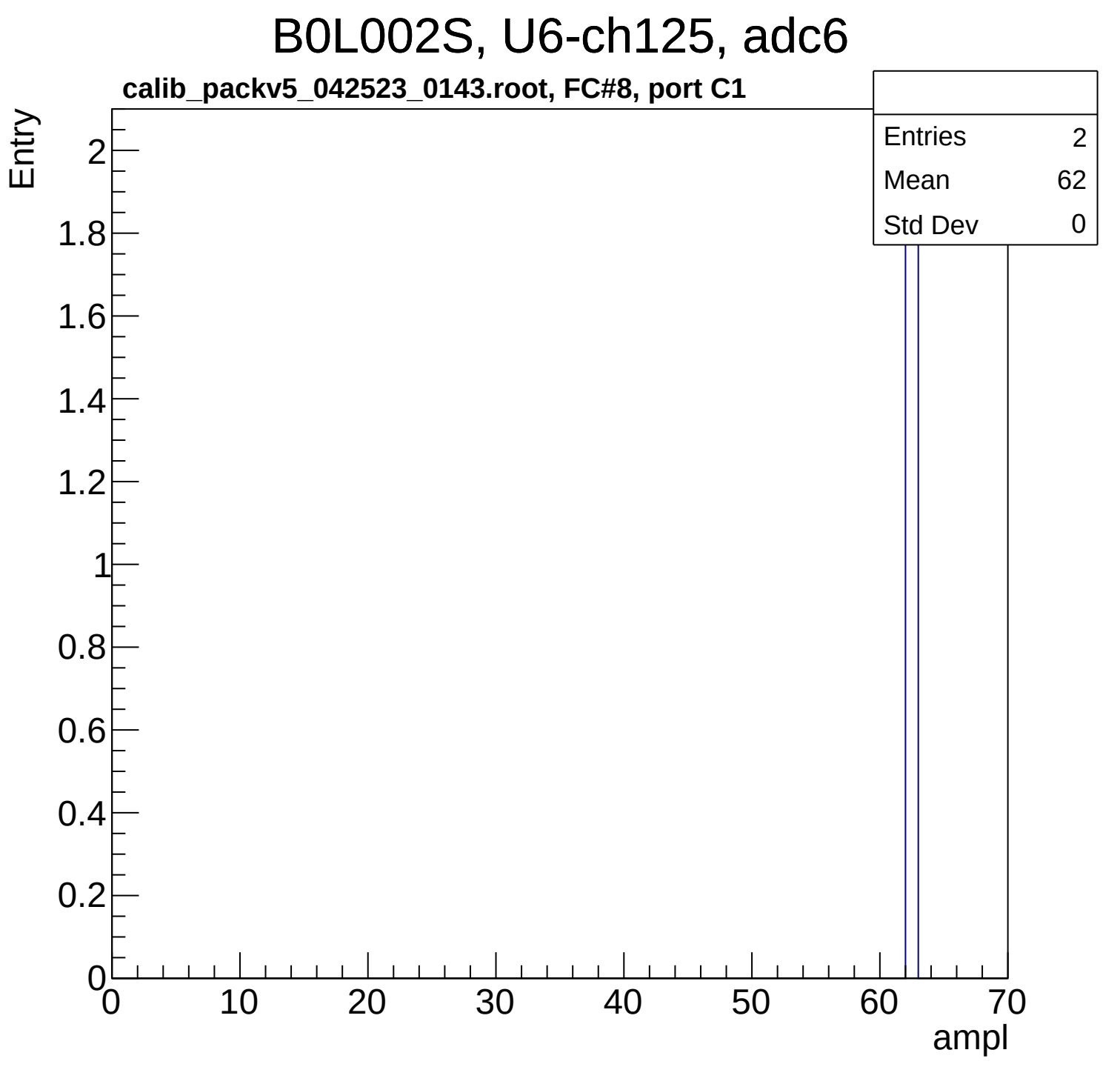

B0L002S, U6-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.93
Std Dev 3.21
Gaus mean : 36.8870
Gaus Width: 3.6638
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

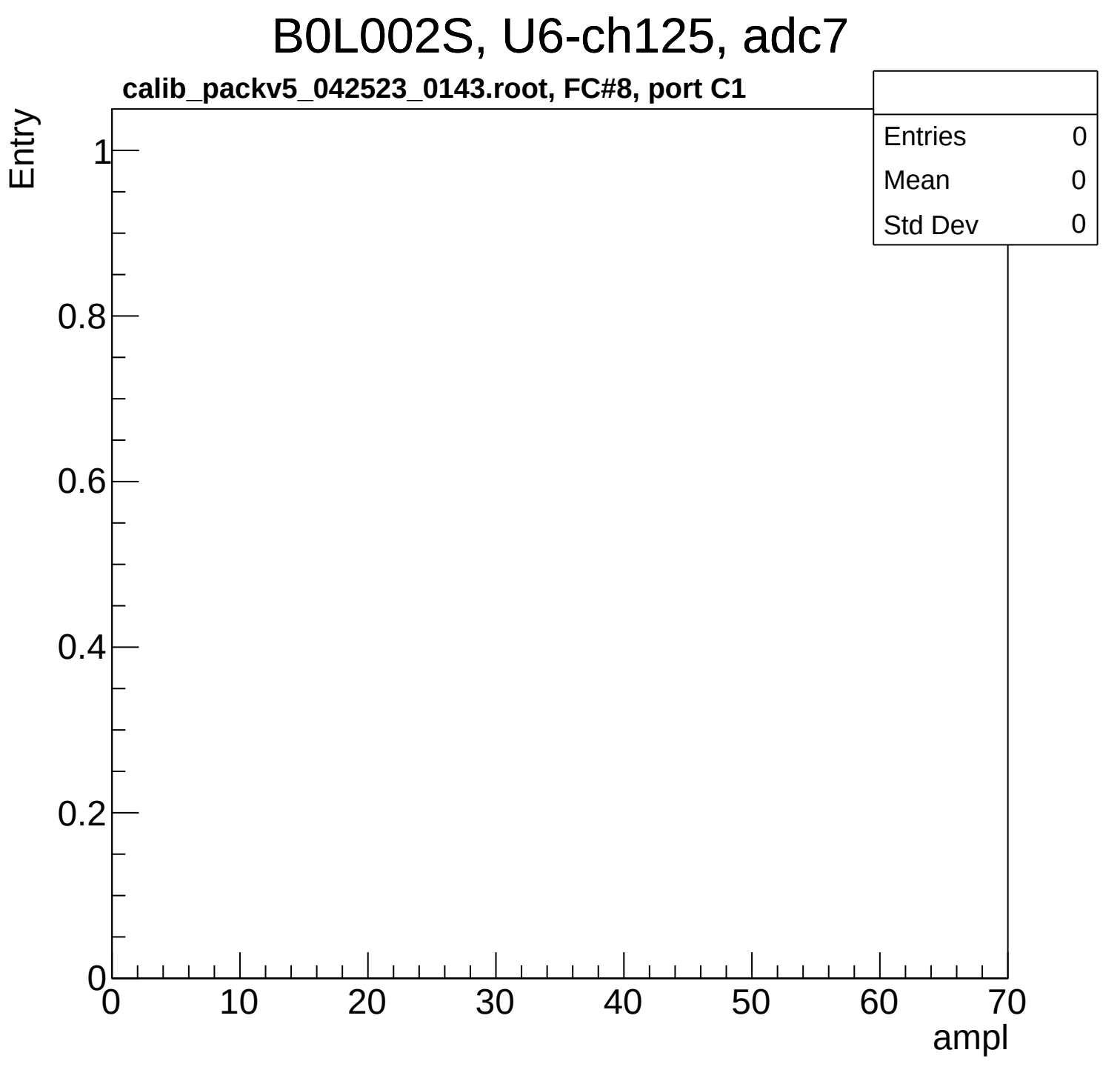

B0L002S, U6-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.86
Std Dev 3.747
Gaus mean : 44.4475
Gaus Width: 4.2001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.58
Std Dev 3.678
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.86
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.34
Std Dev 9.54
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U6-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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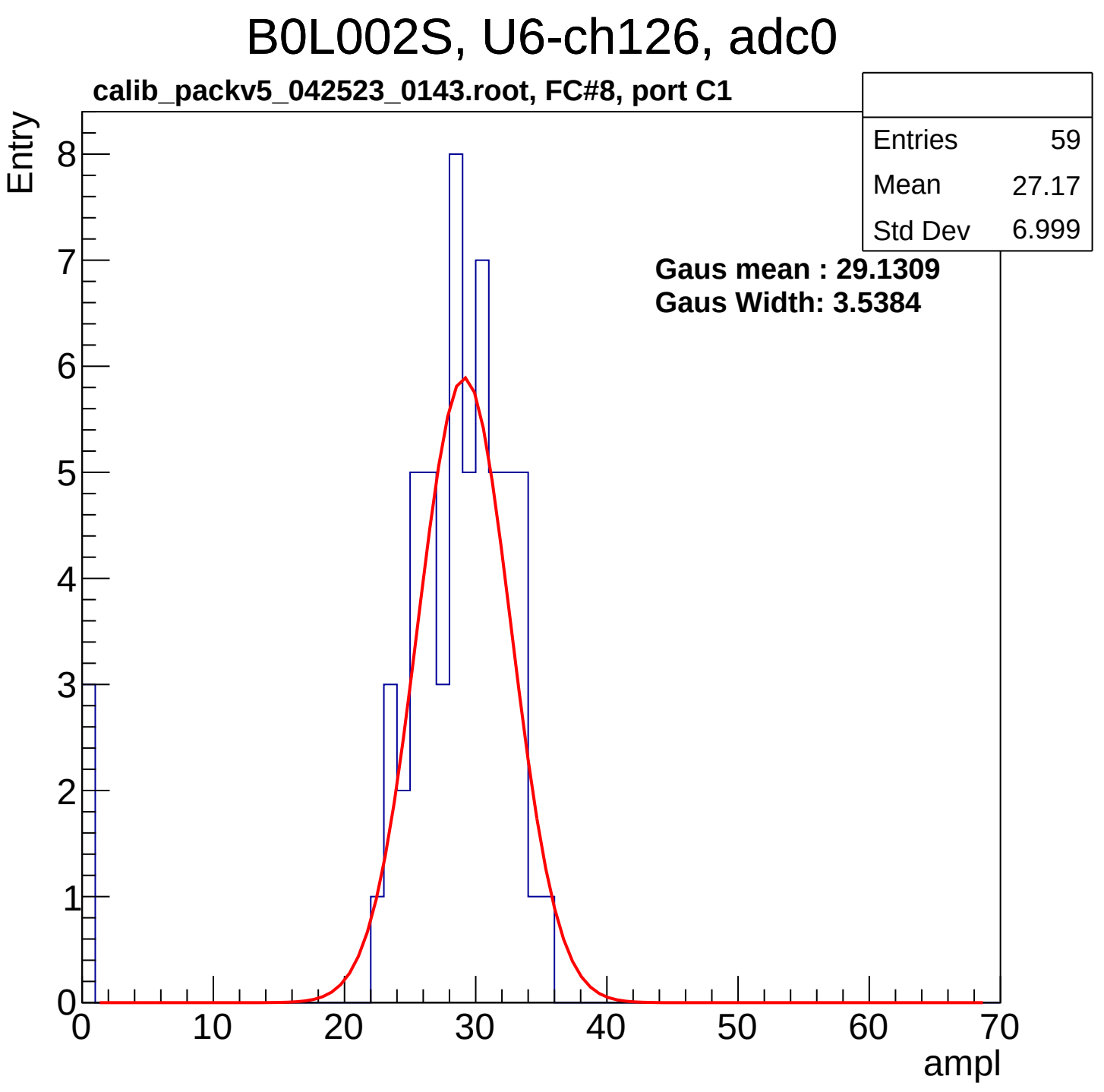
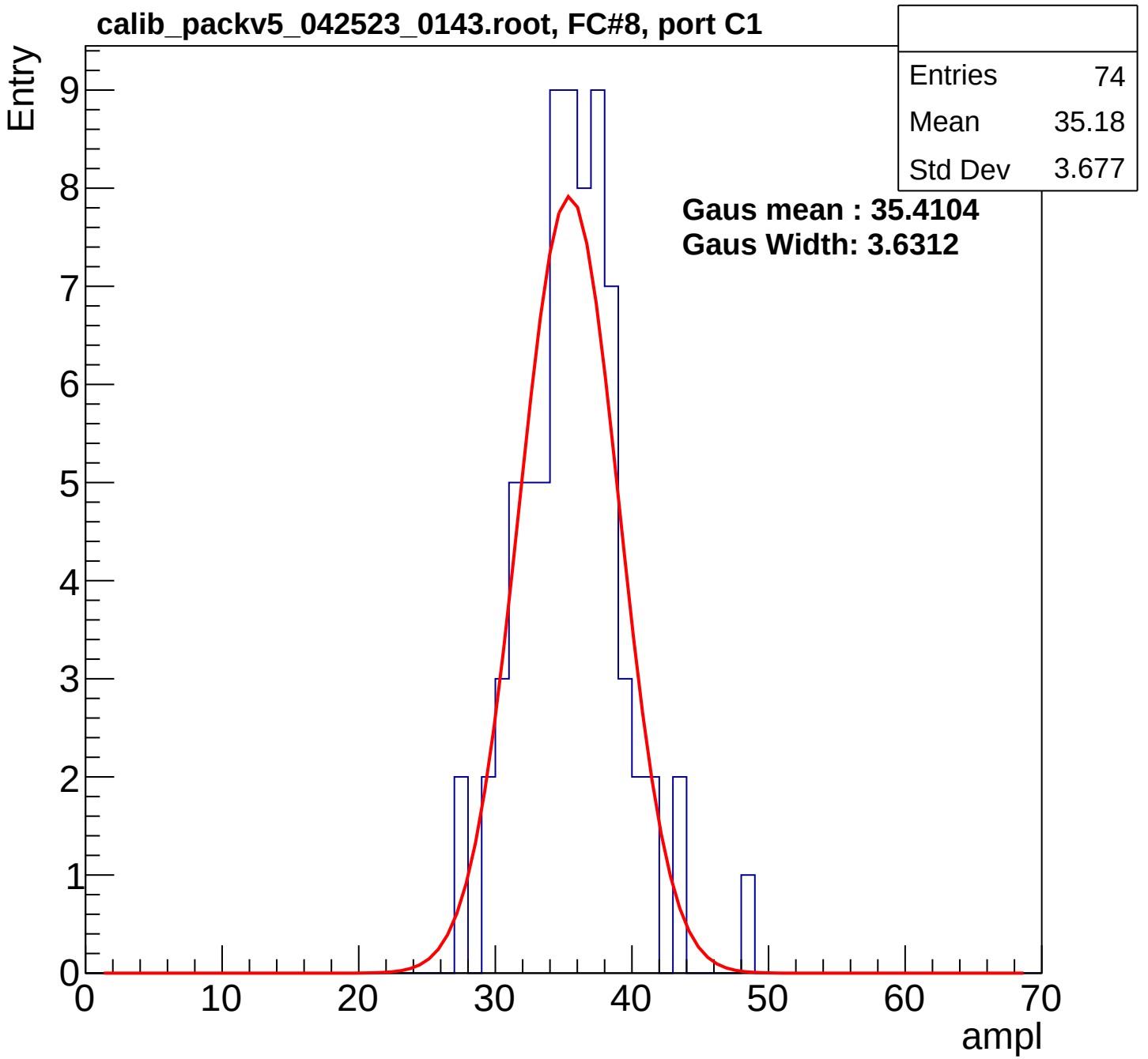
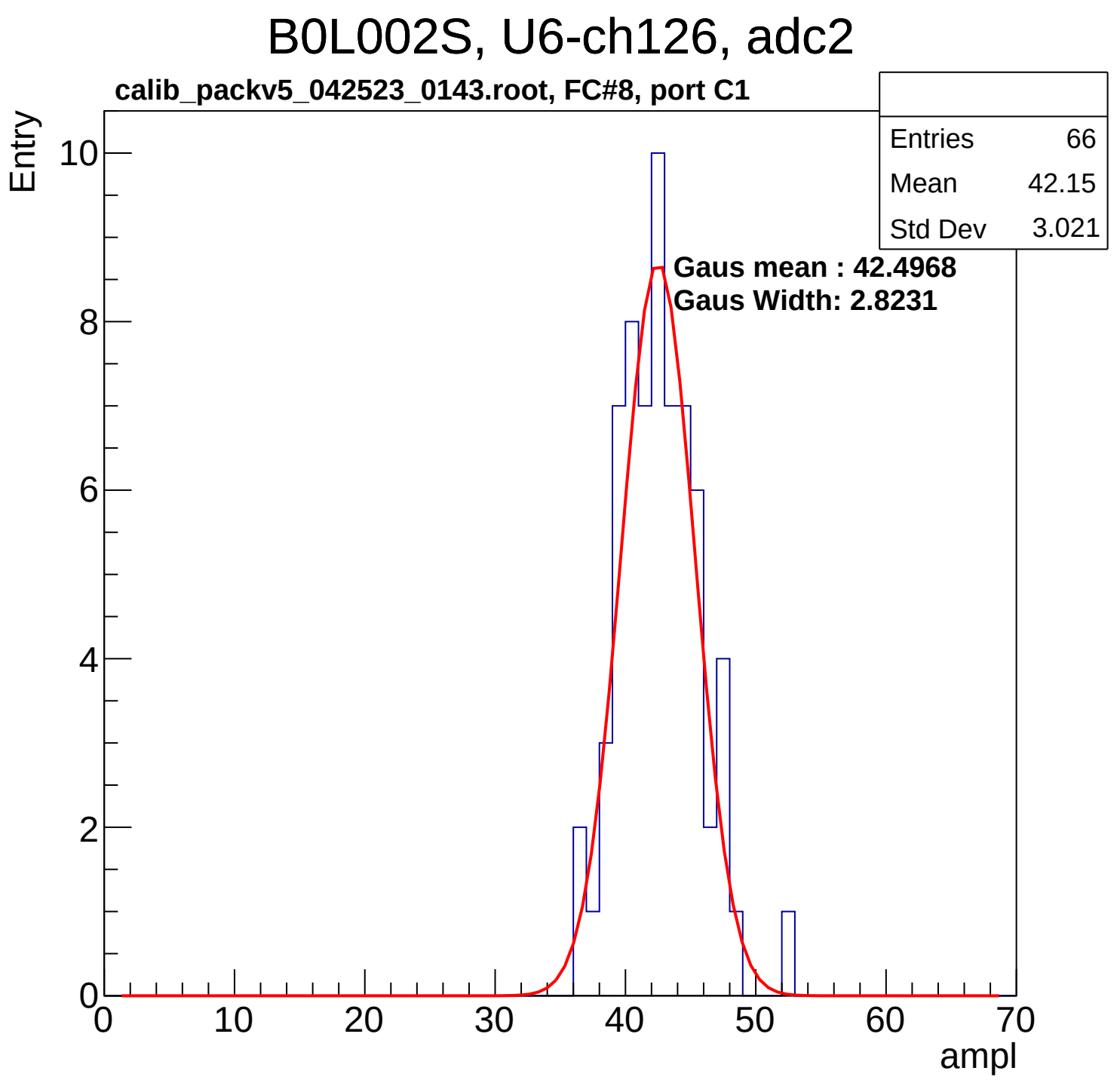
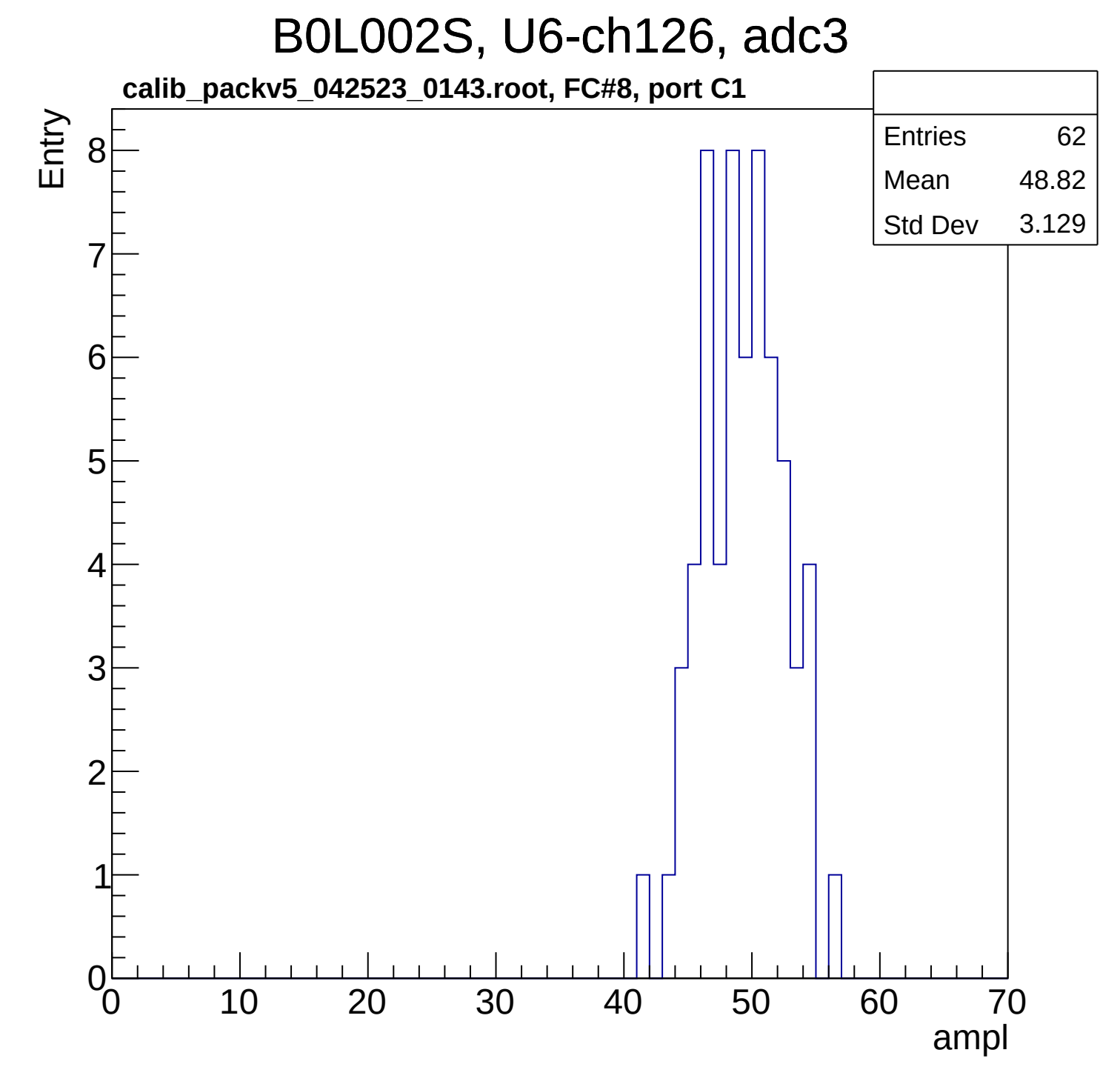
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 27.17
Std Dev 6.999
Gaus mean : 29.1309
Gaus Width: 3.5384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 35.18
Std Dev 3.677
Gaus mean : 35.4104
Gaus Width: 3.6312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U6-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.15
Std Dev 3.021
Gaus mean : 42.4968
Gaus Width: 2.8231
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U6-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 48.82
Std Dev 3.129
Entry
ampl
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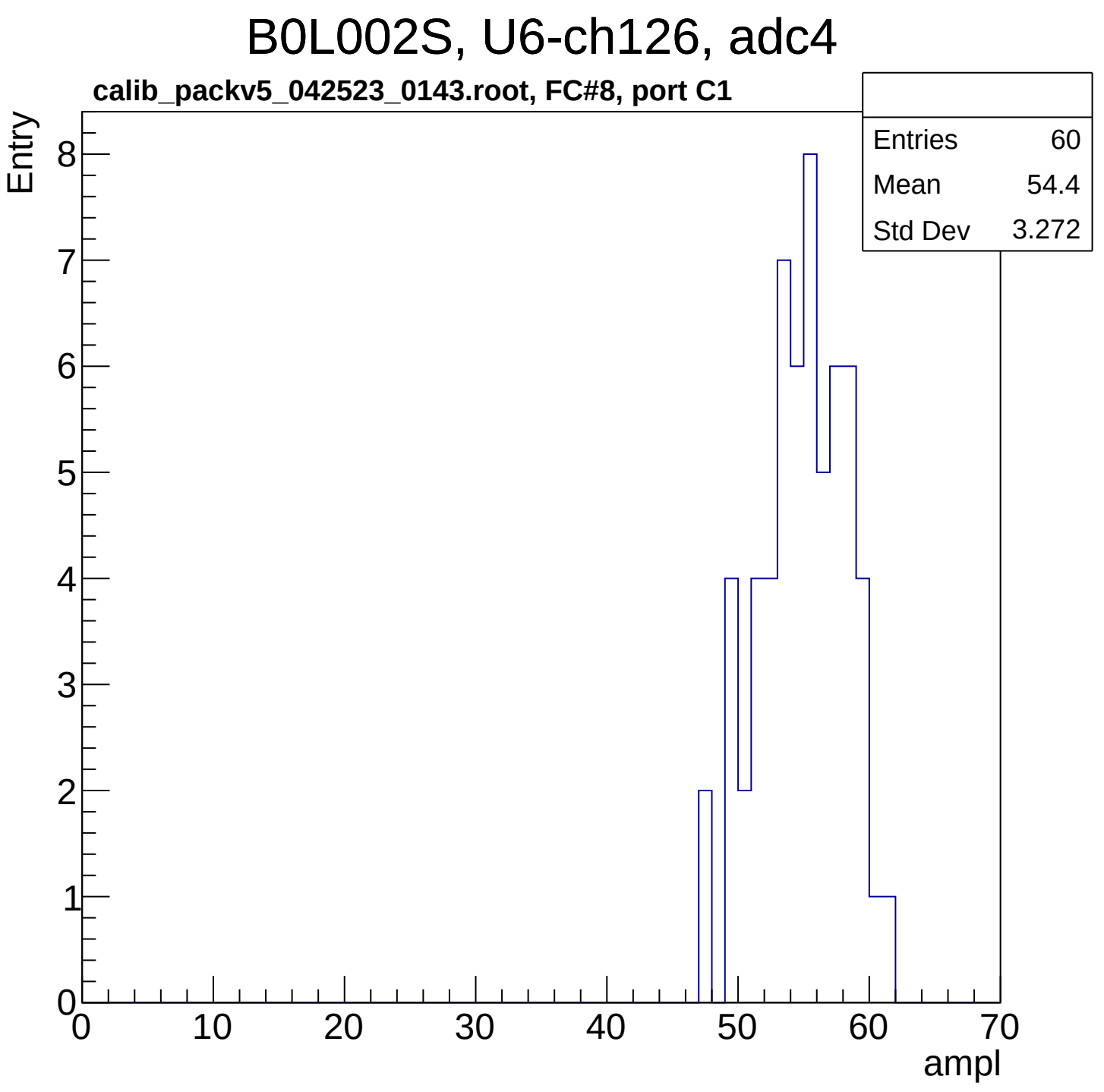
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 54.4
Std Dev 3.272
Entry
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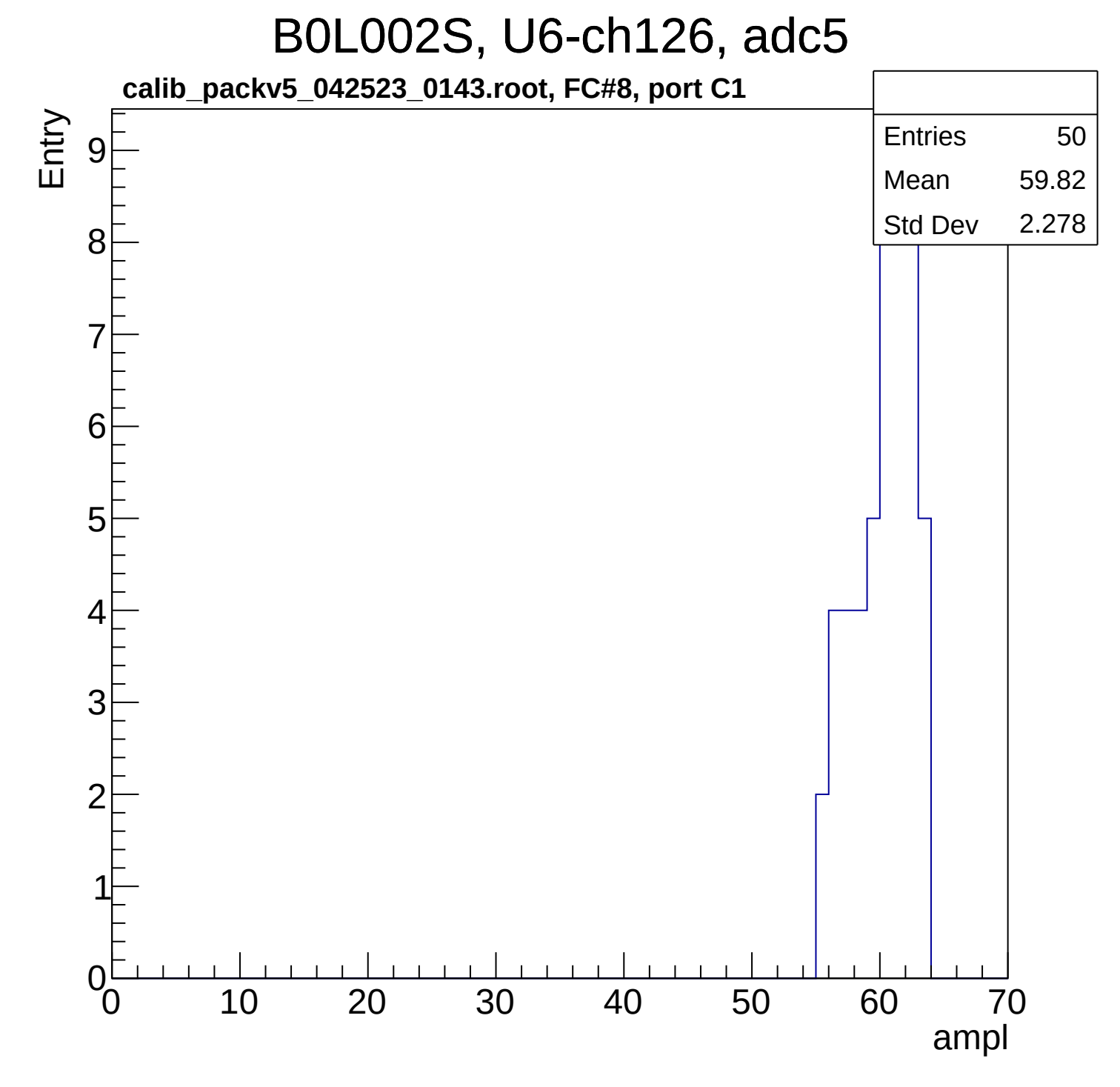
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 59.82
Std Dev 2.278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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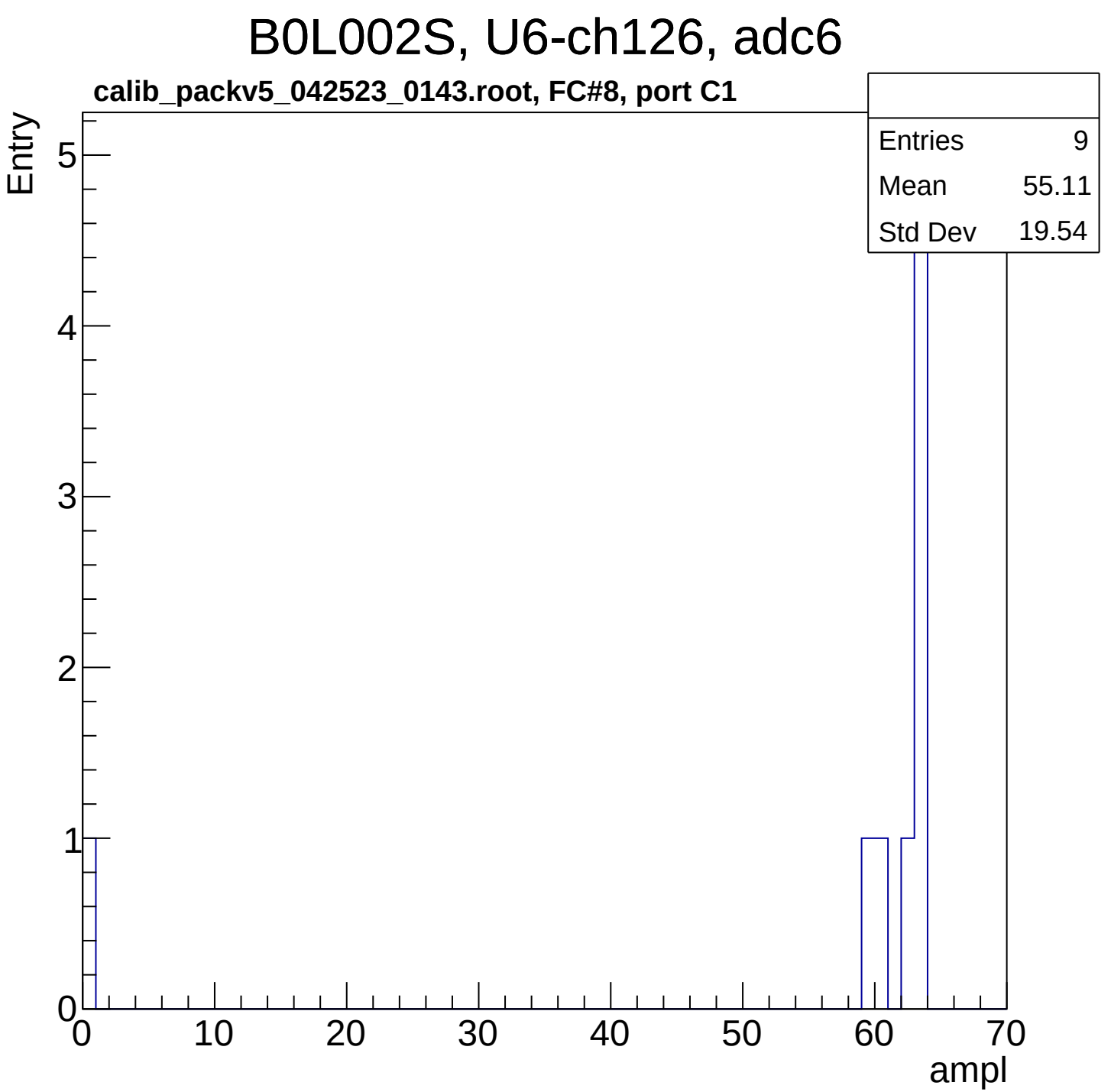
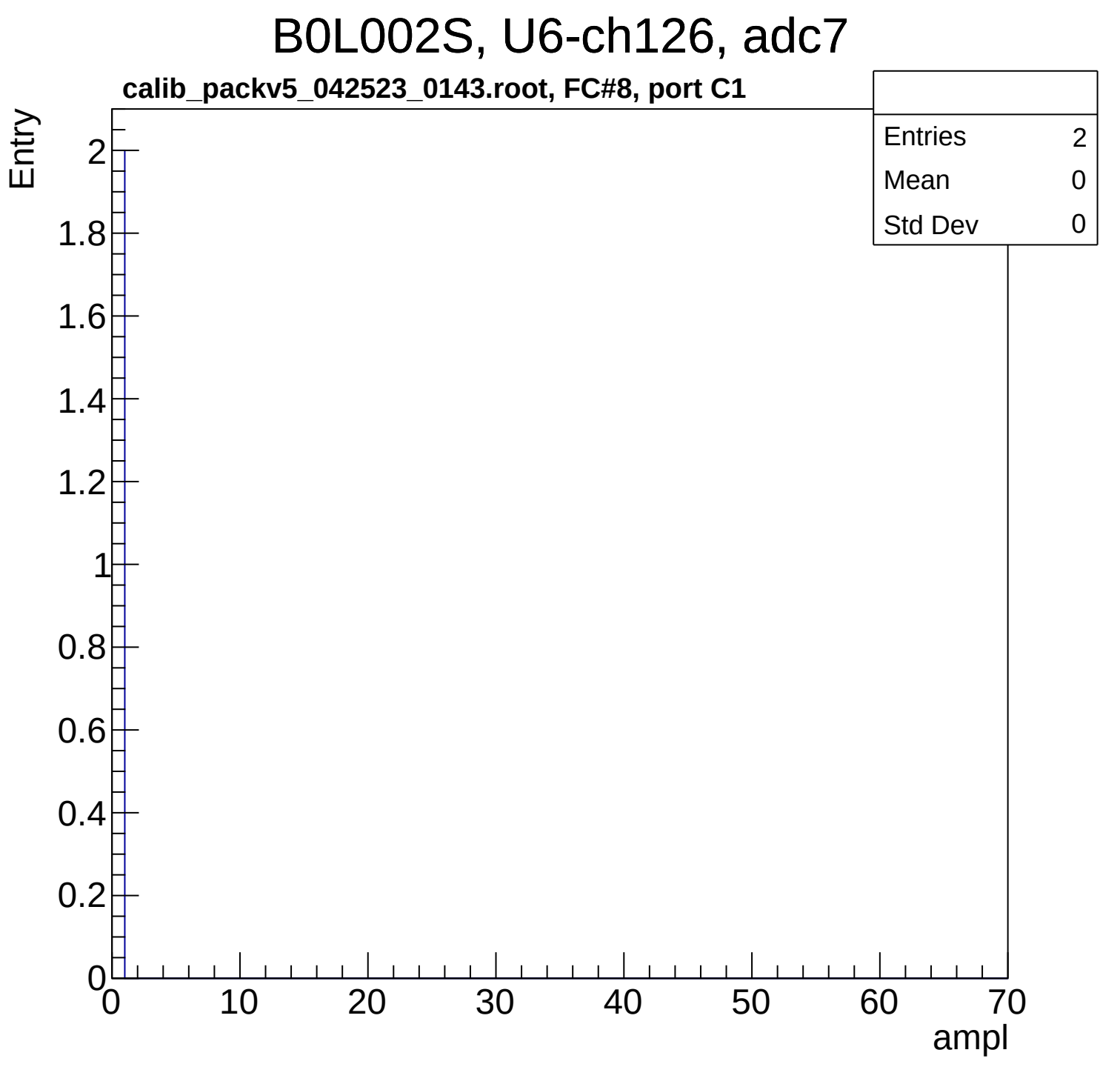

B0L002S, U6-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 55.11
Std Dev 19.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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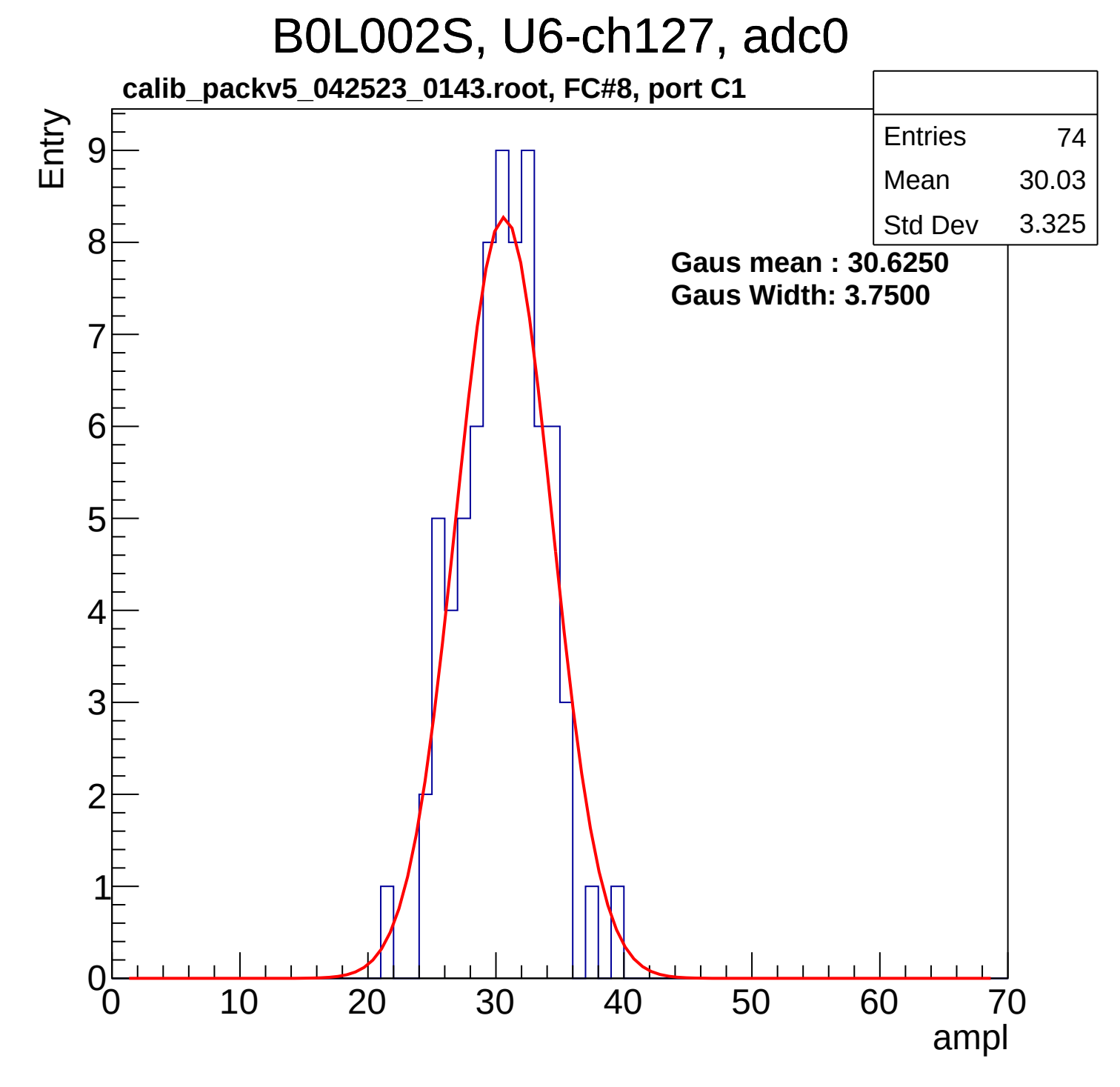
B0L002S, U6-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 30.03
Std Dev 3.325
Gaus mean : 30.6250
Gaus Width: 3.7500
Entry
ampl
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10
20
30
40
50
60
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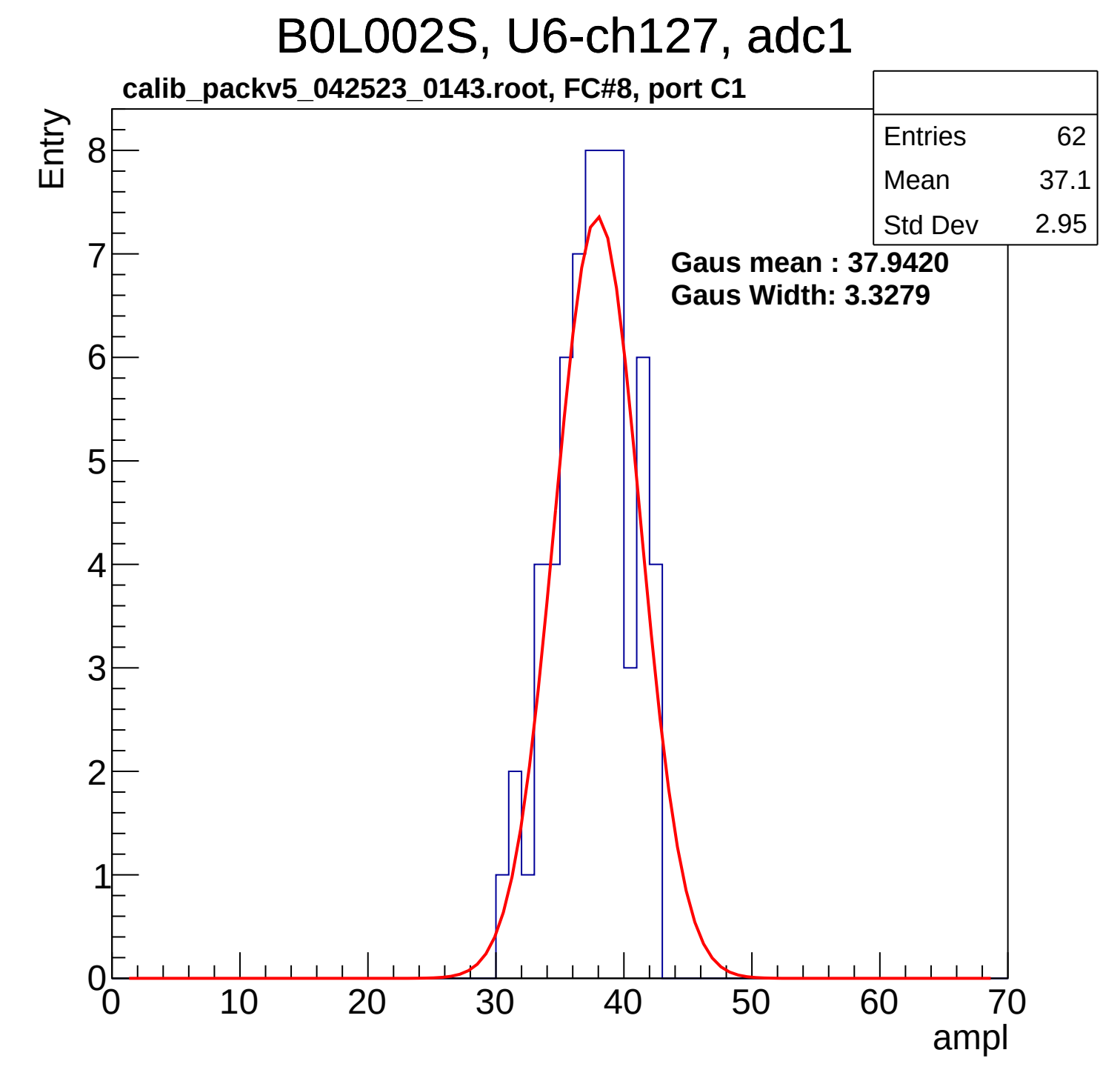
1
2
3
4
5
6
7
8
9


B0L002S, U6-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.1
Std Dev 2.95
Gaus mean : 37.9420
Gaus Width: 3.3279
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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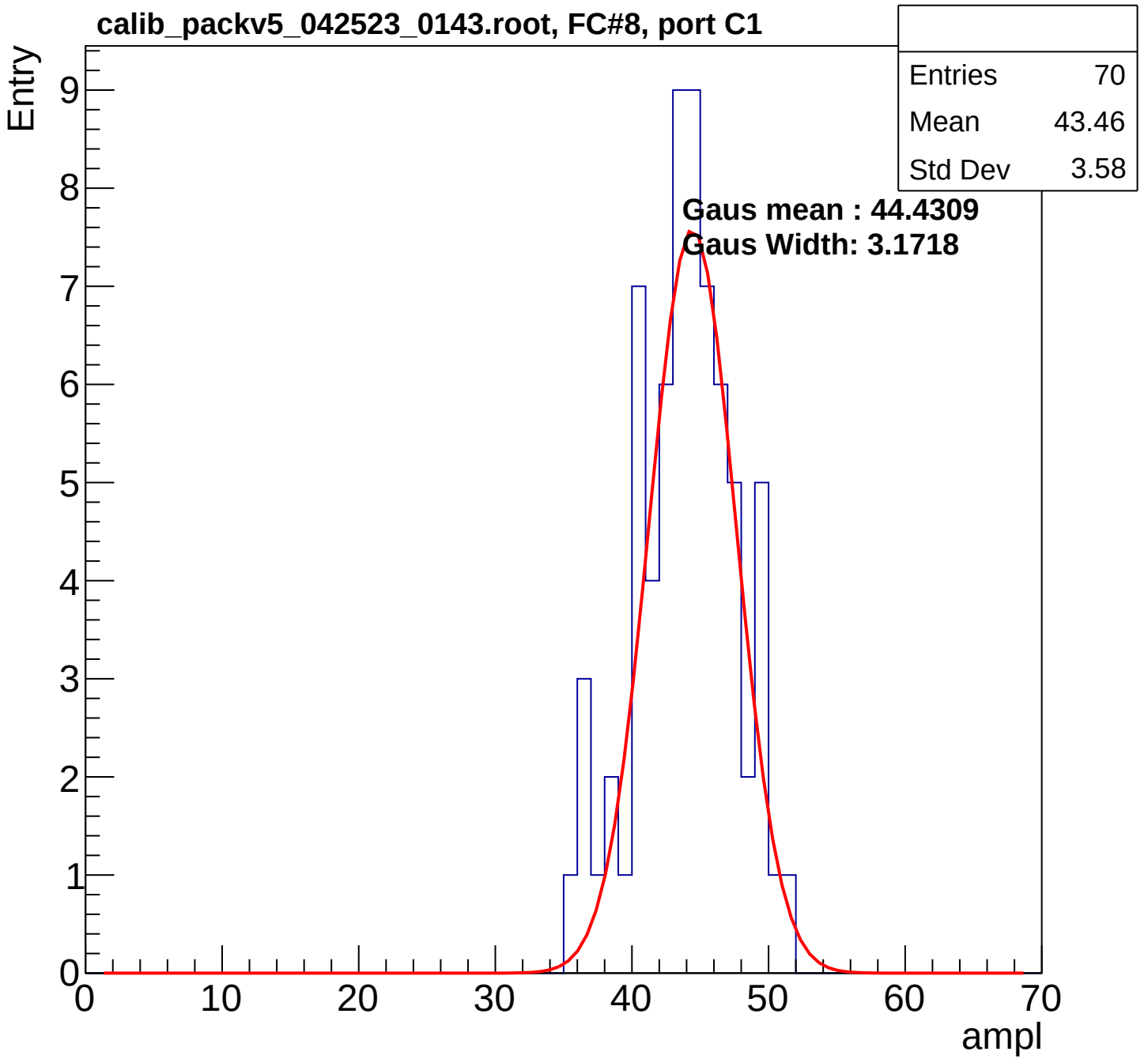
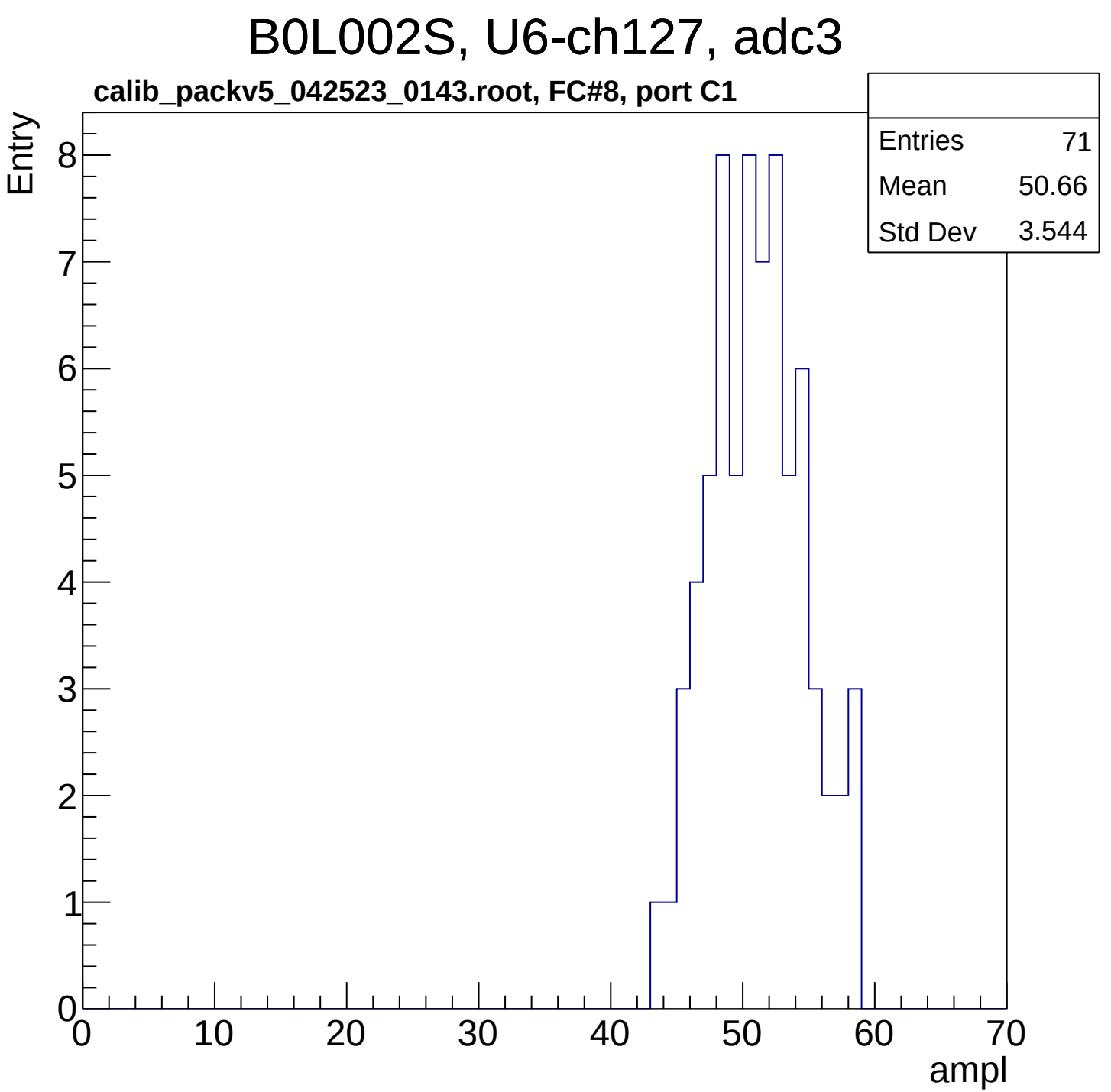
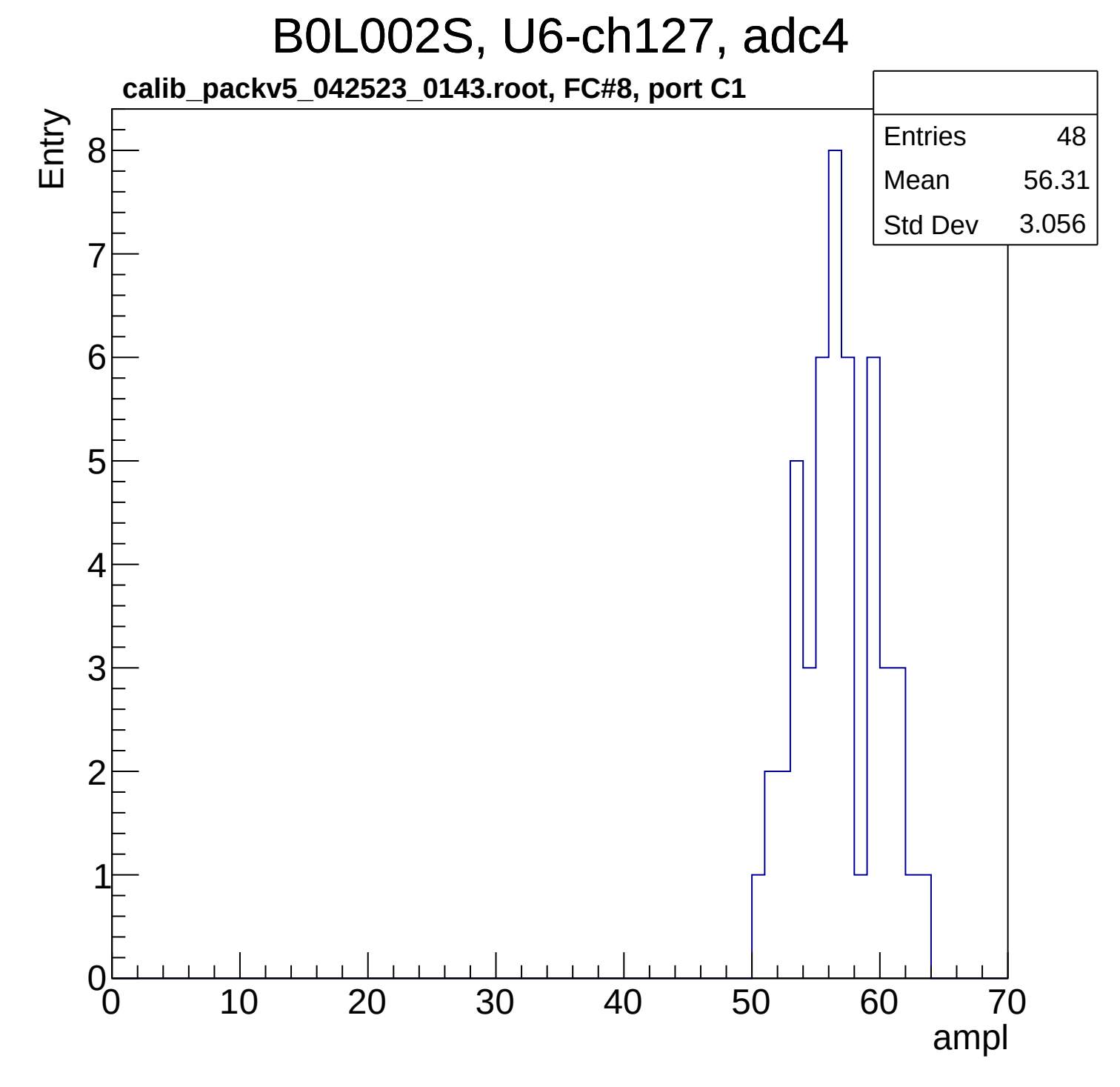
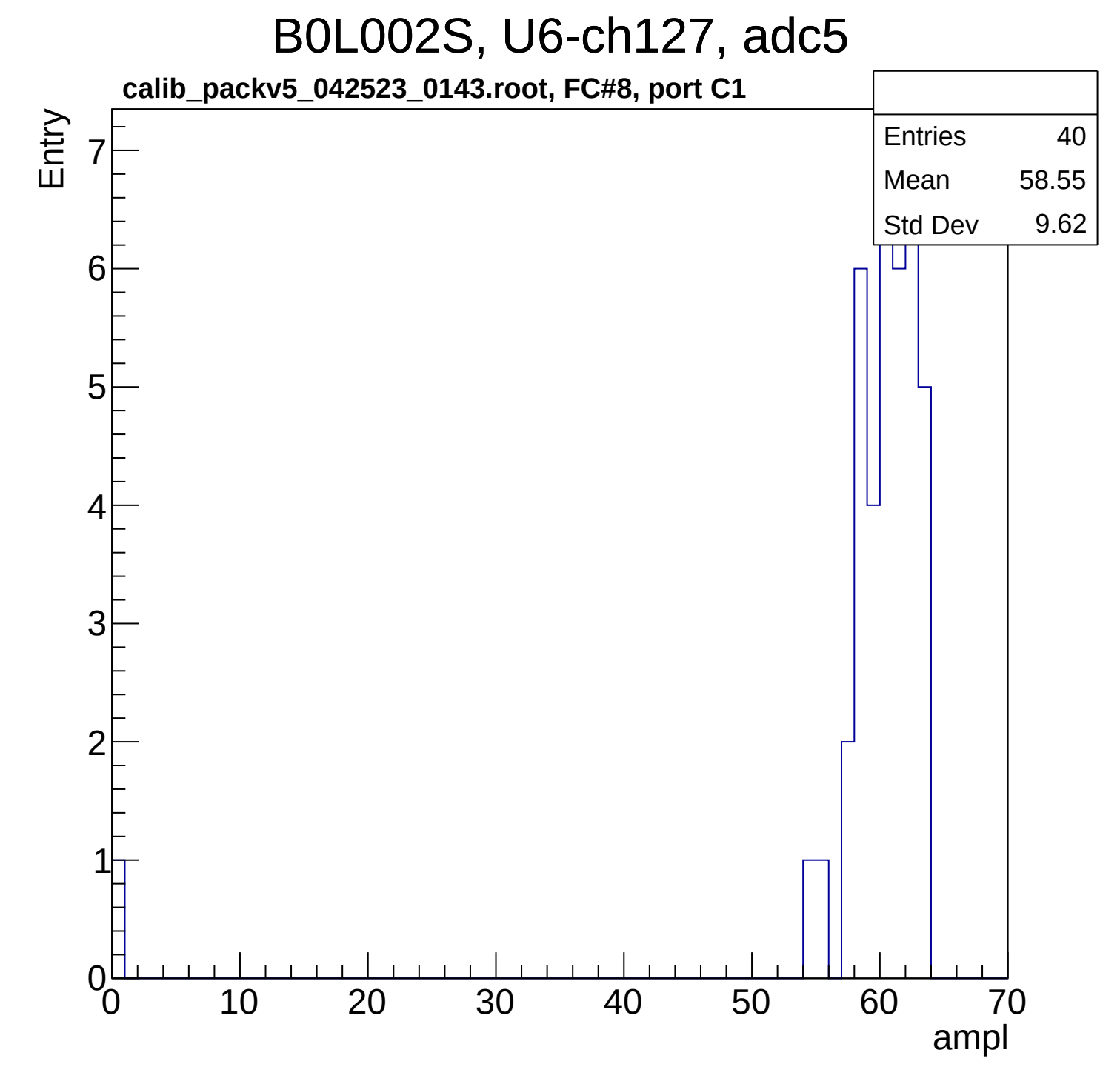
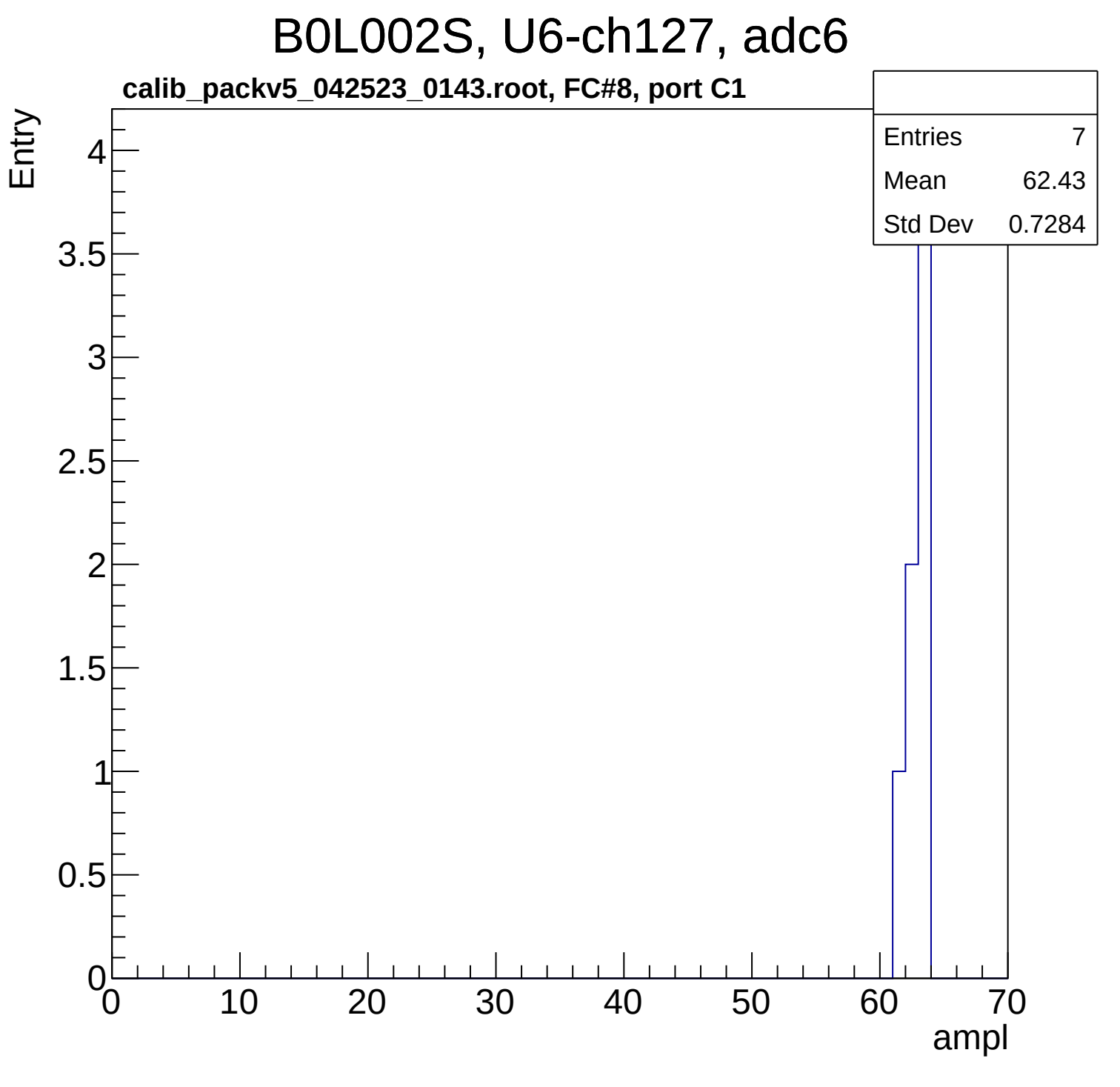
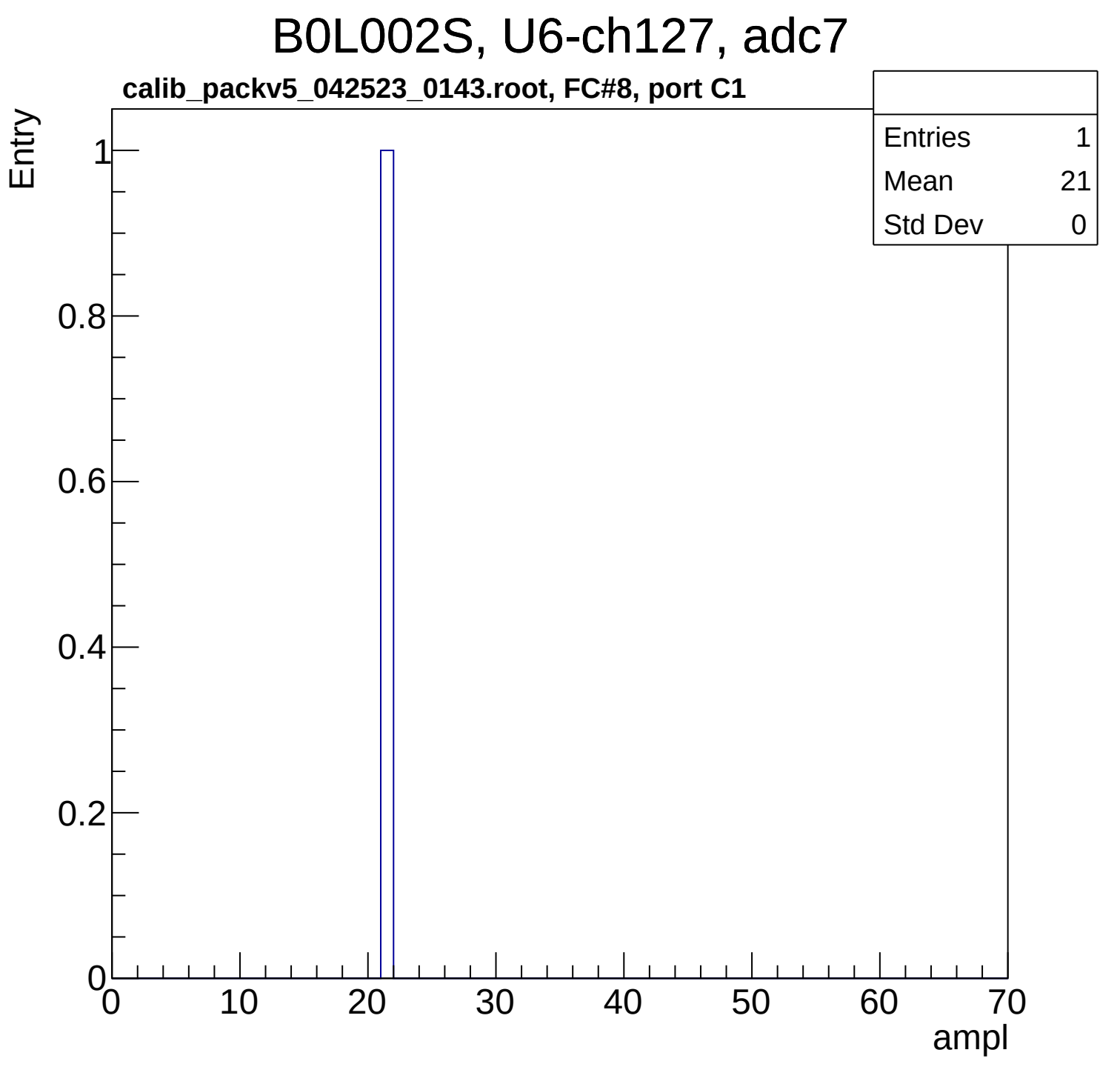

B0L002S, U6-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.46
Std Dev 3.58
Gaus mean : 44.4309
Gaus Width: 3.1718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

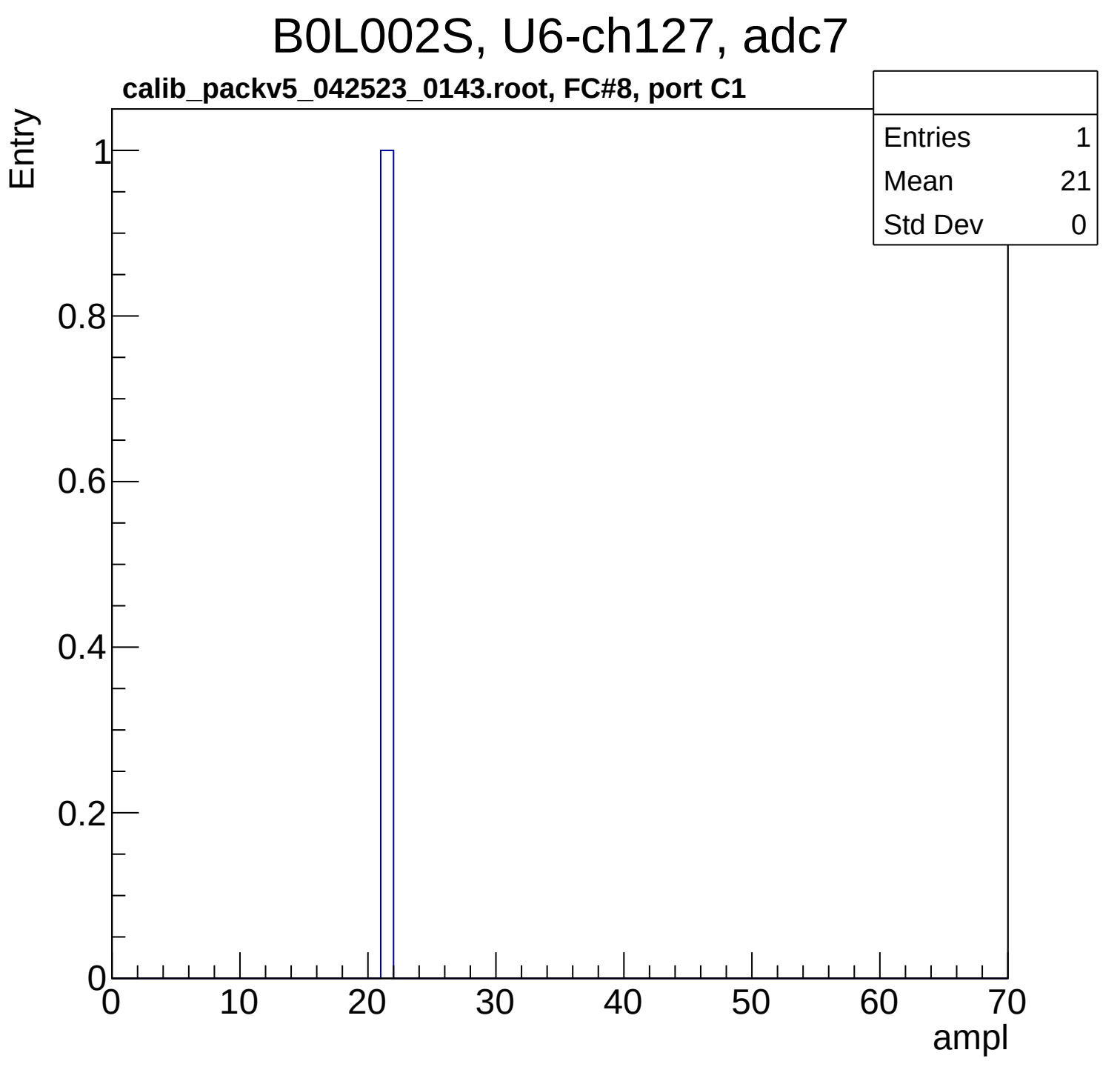

B0L002S, U6-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.66
Std Dev 3.544
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 56.31
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U6-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.55
Std Dev 9.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U6-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl