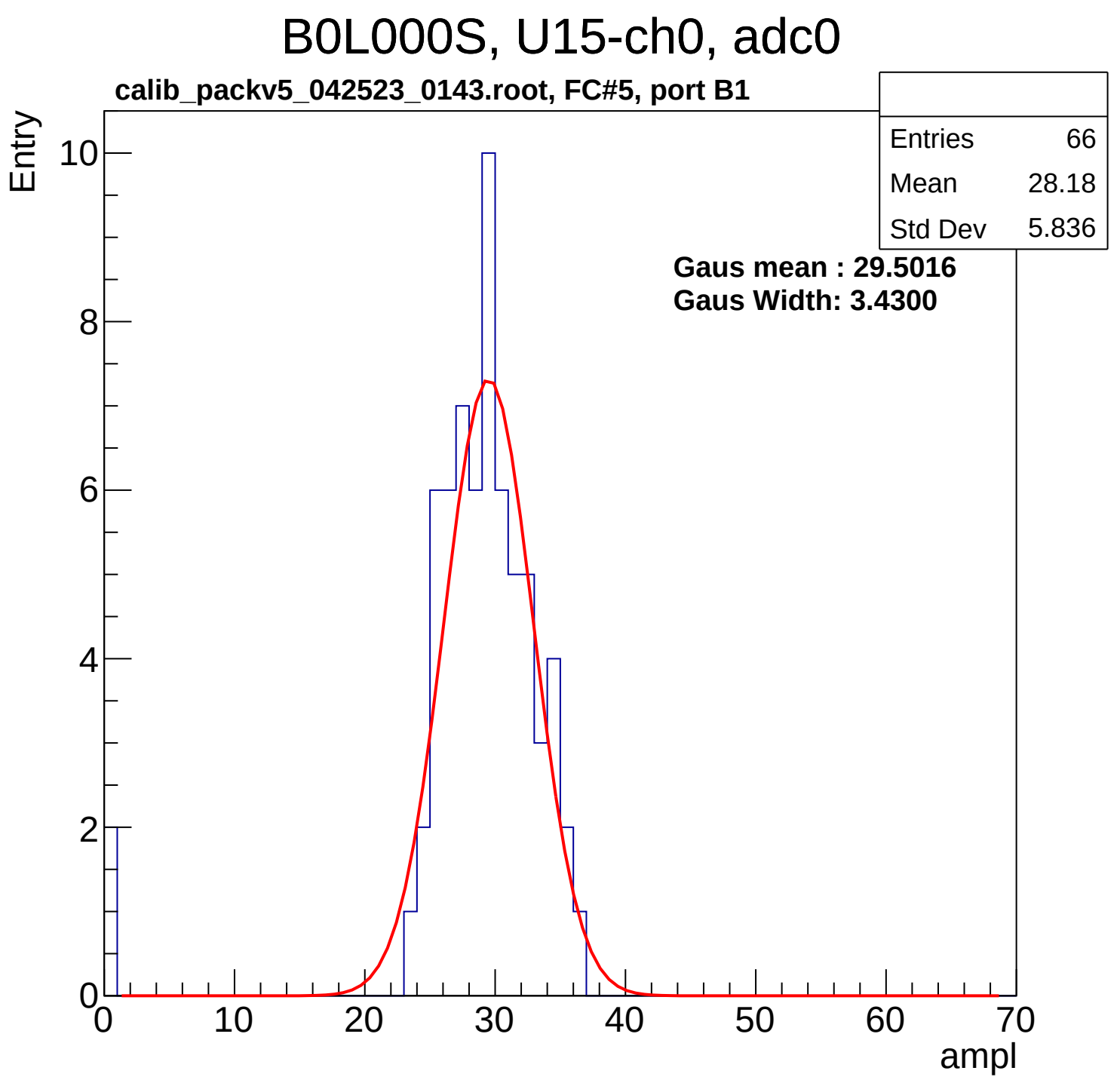
B0L000S, U15-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.18
Std Dev 5.836
Gaus mean : 29.5016
Gaus Width: 3.4300
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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60
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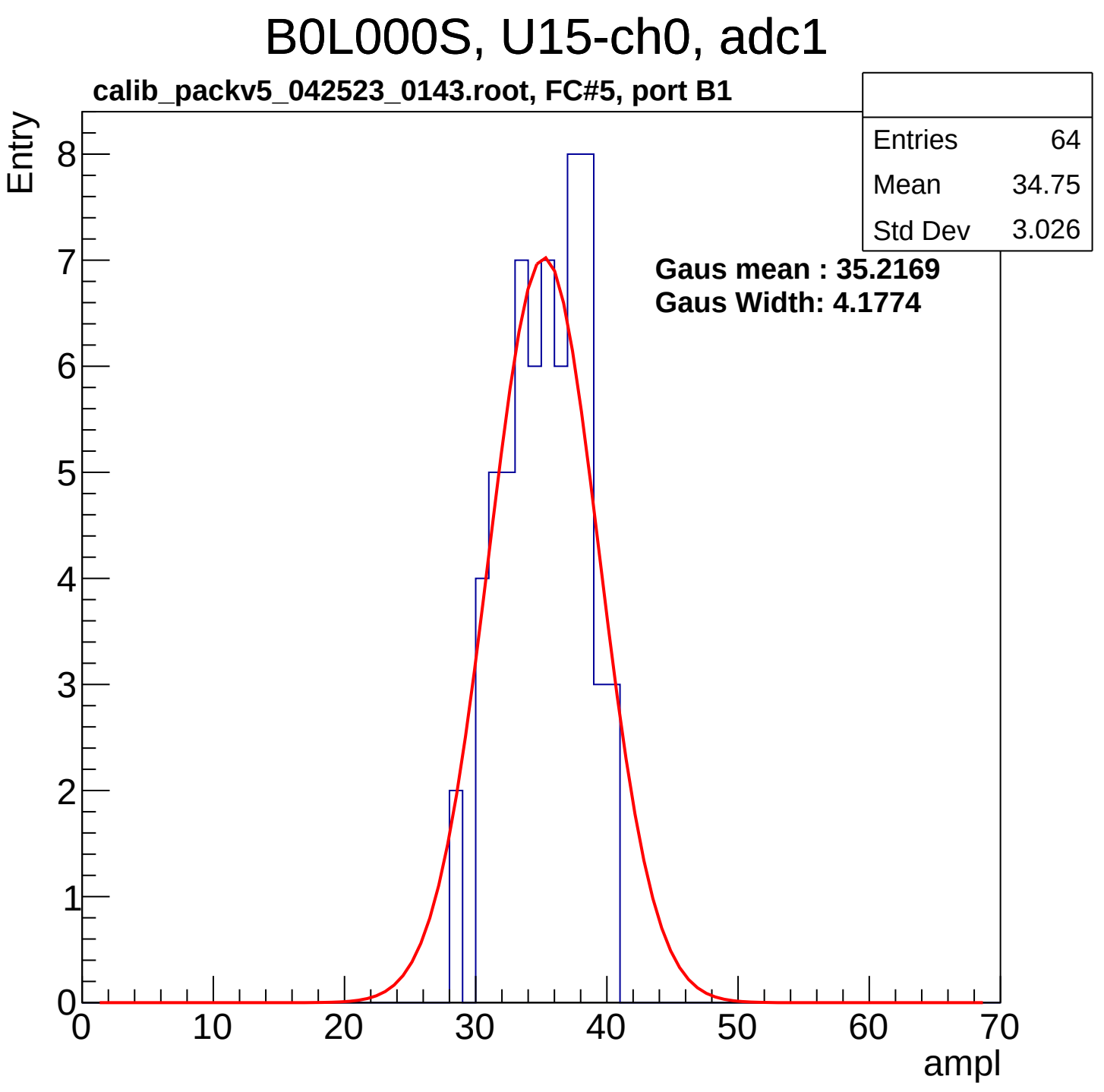
B0L000S, U15-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 34.75
Std Dev 3.026
Gaus mean : 35.2169
Gaus Width: 4.1774
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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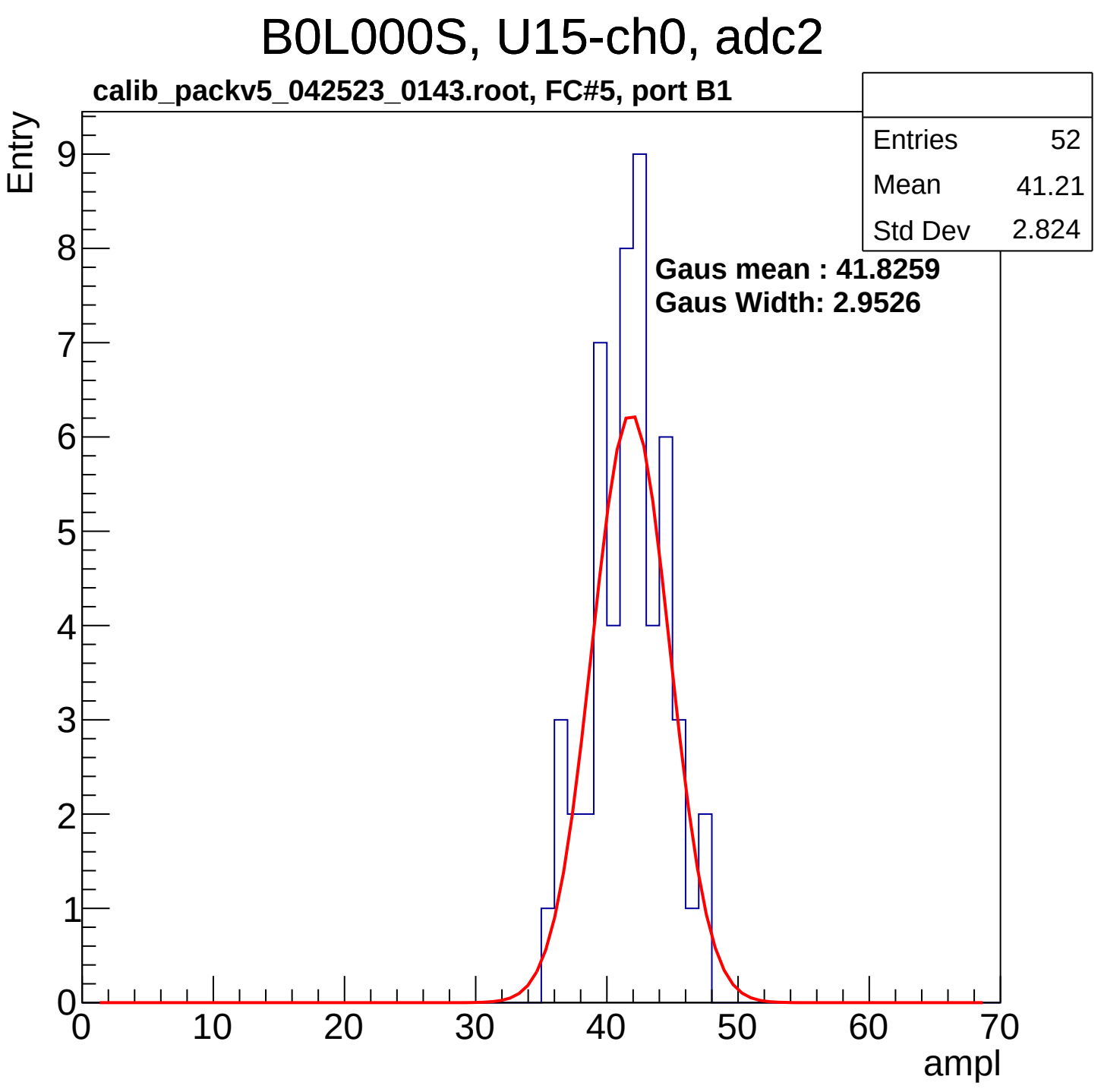
B0L000S, U15-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 41.21
Std Dev 2.824
Gaus mean : 41.8259
Gaus Width: 2.9526
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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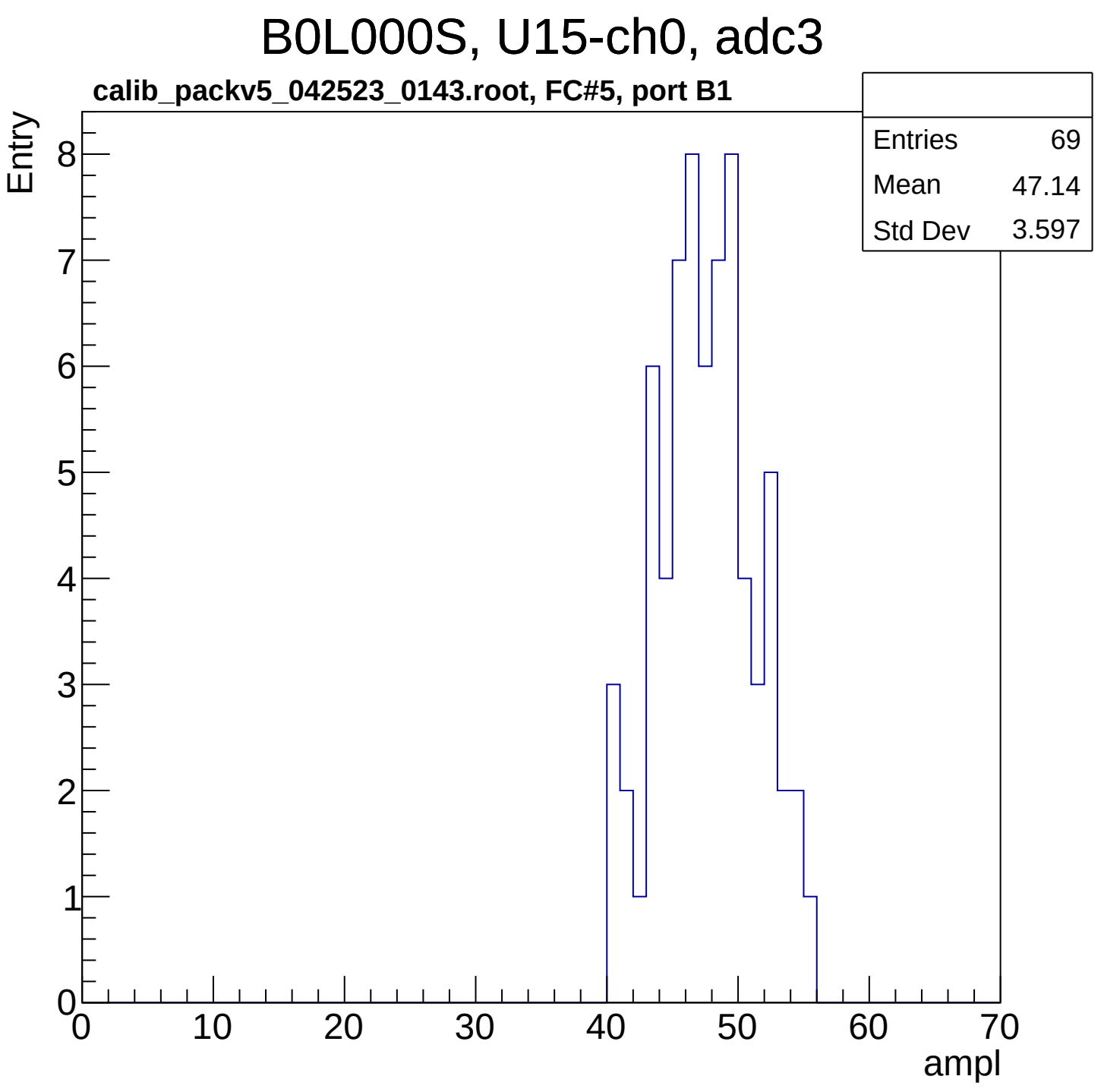
B0L000S, U15-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.14
Std Dev 3.597
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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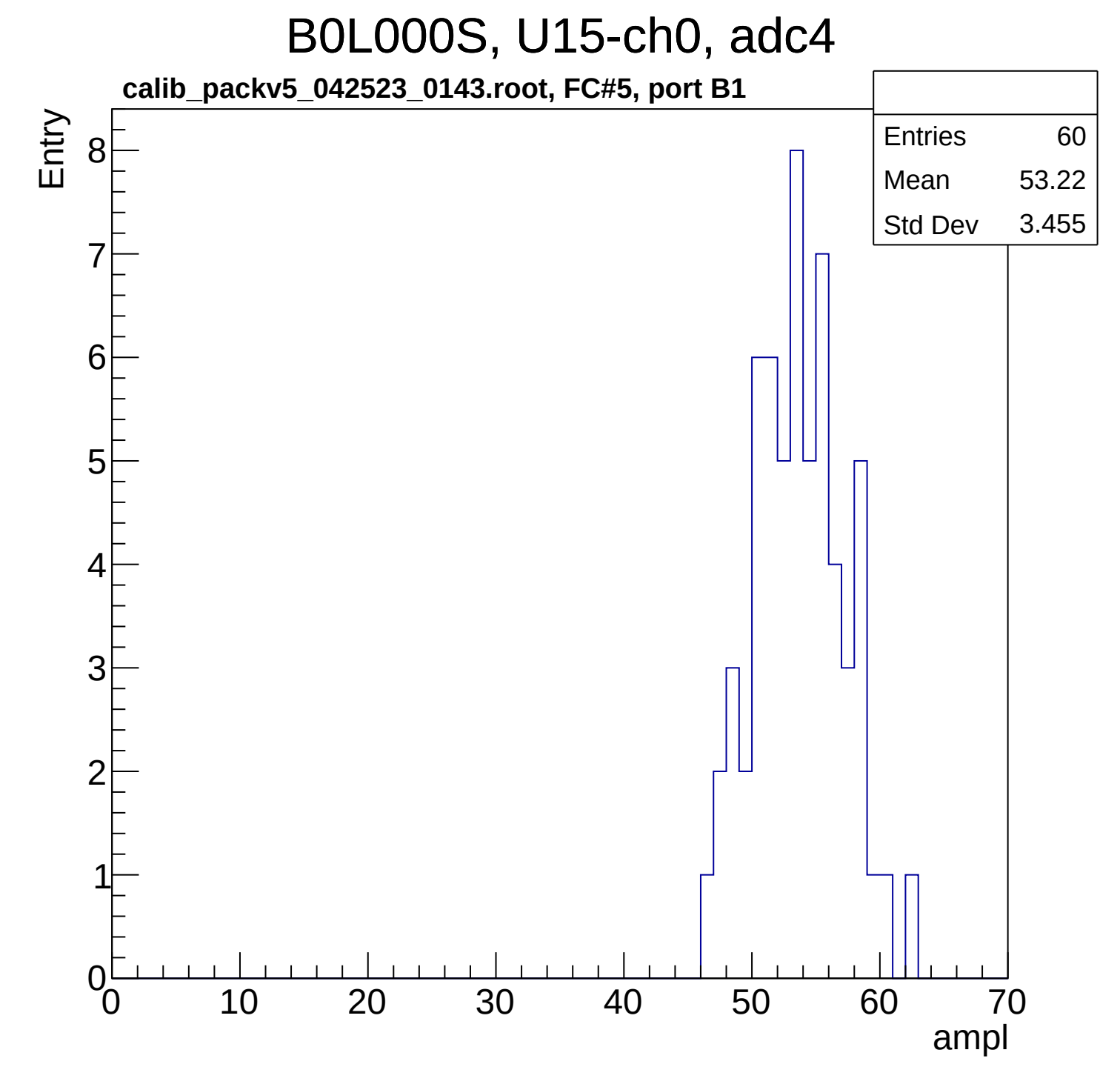
B0L000S, U15-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 53.22
Std Dev 3.455
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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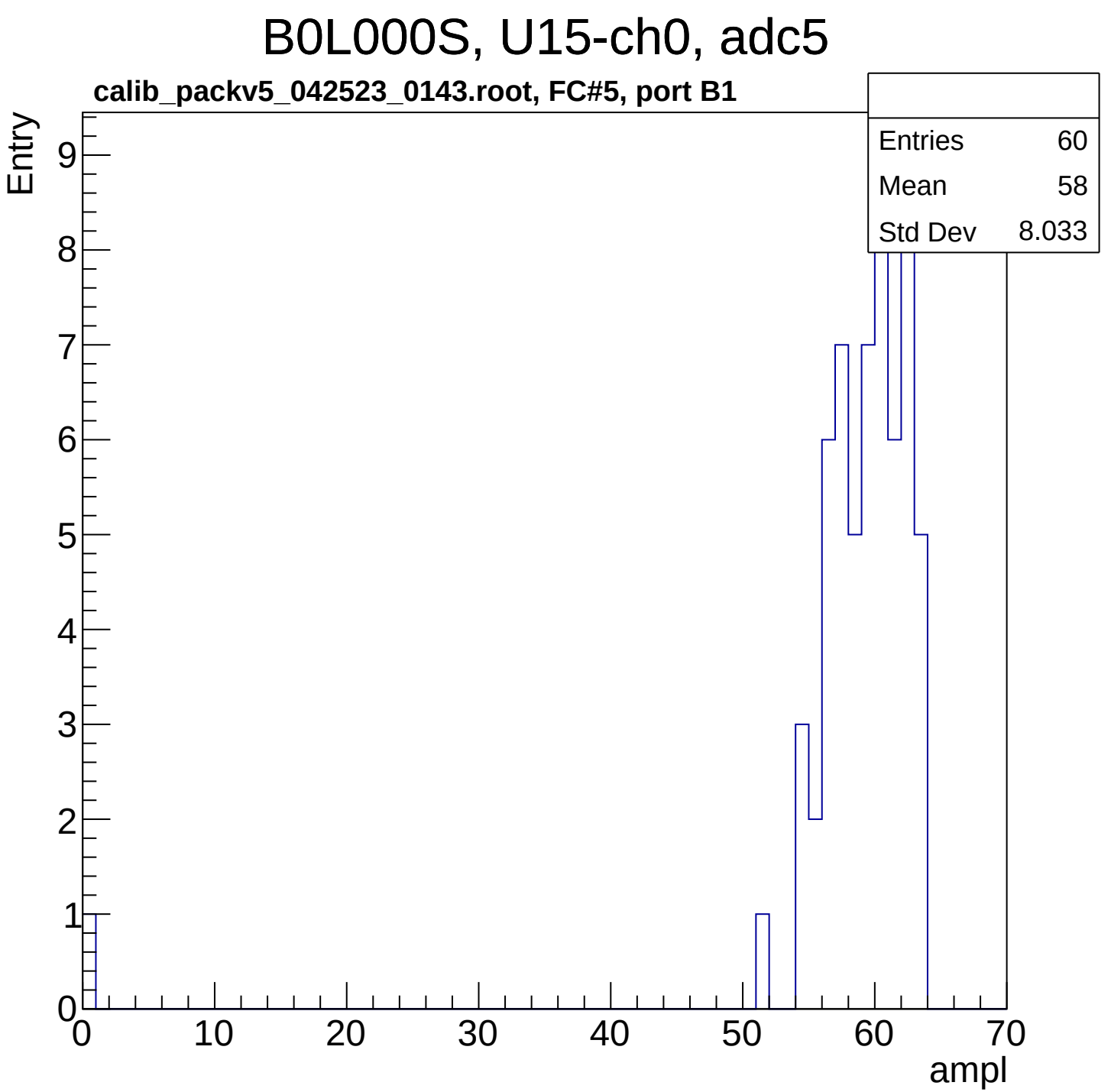
B0L000S, U15-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58
Std Dev 8.033
Entry
ampl
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3
4
5
6
7
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0
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60
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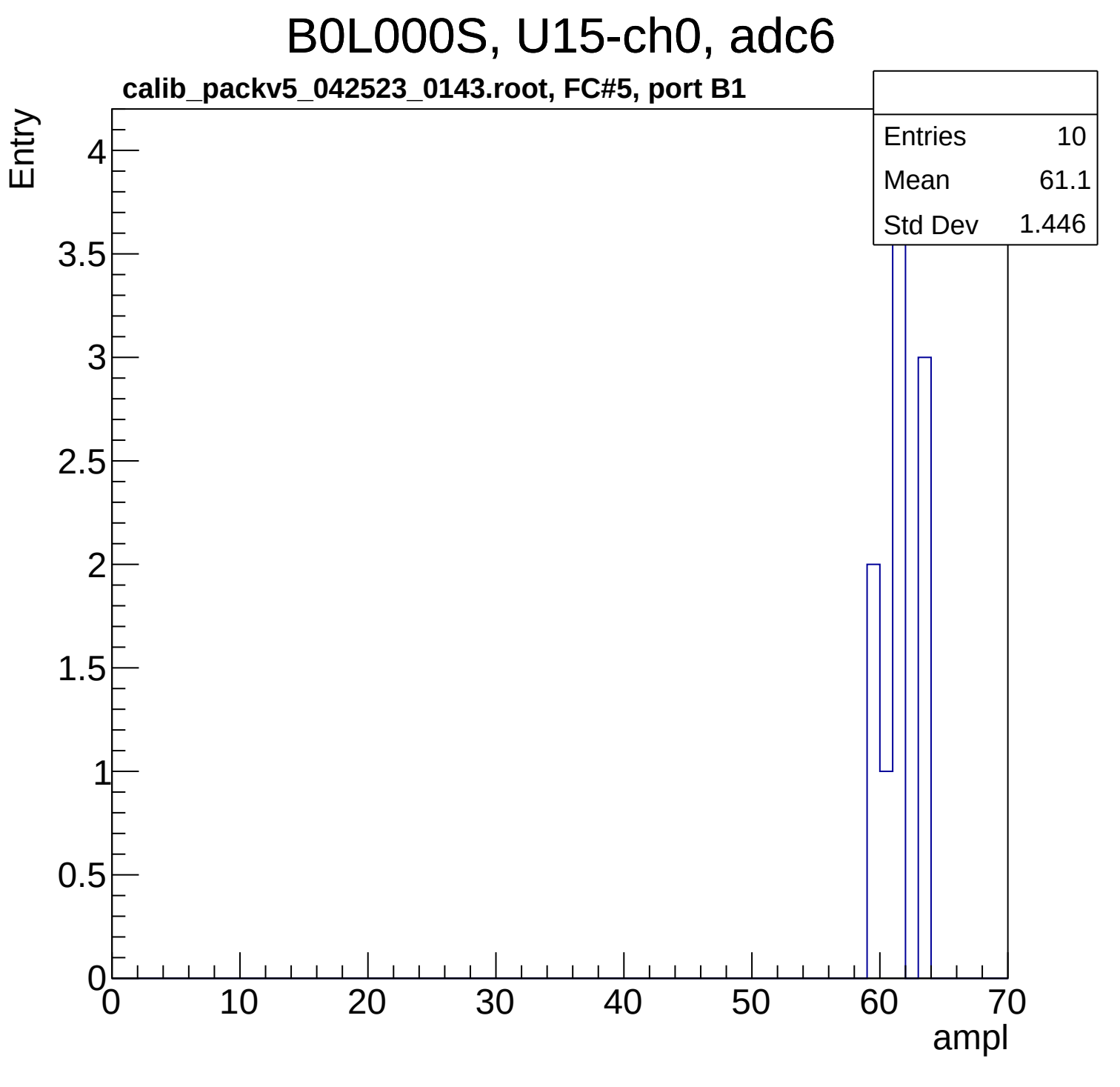
B0L000S, U15-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.1
Std Dev 1.446
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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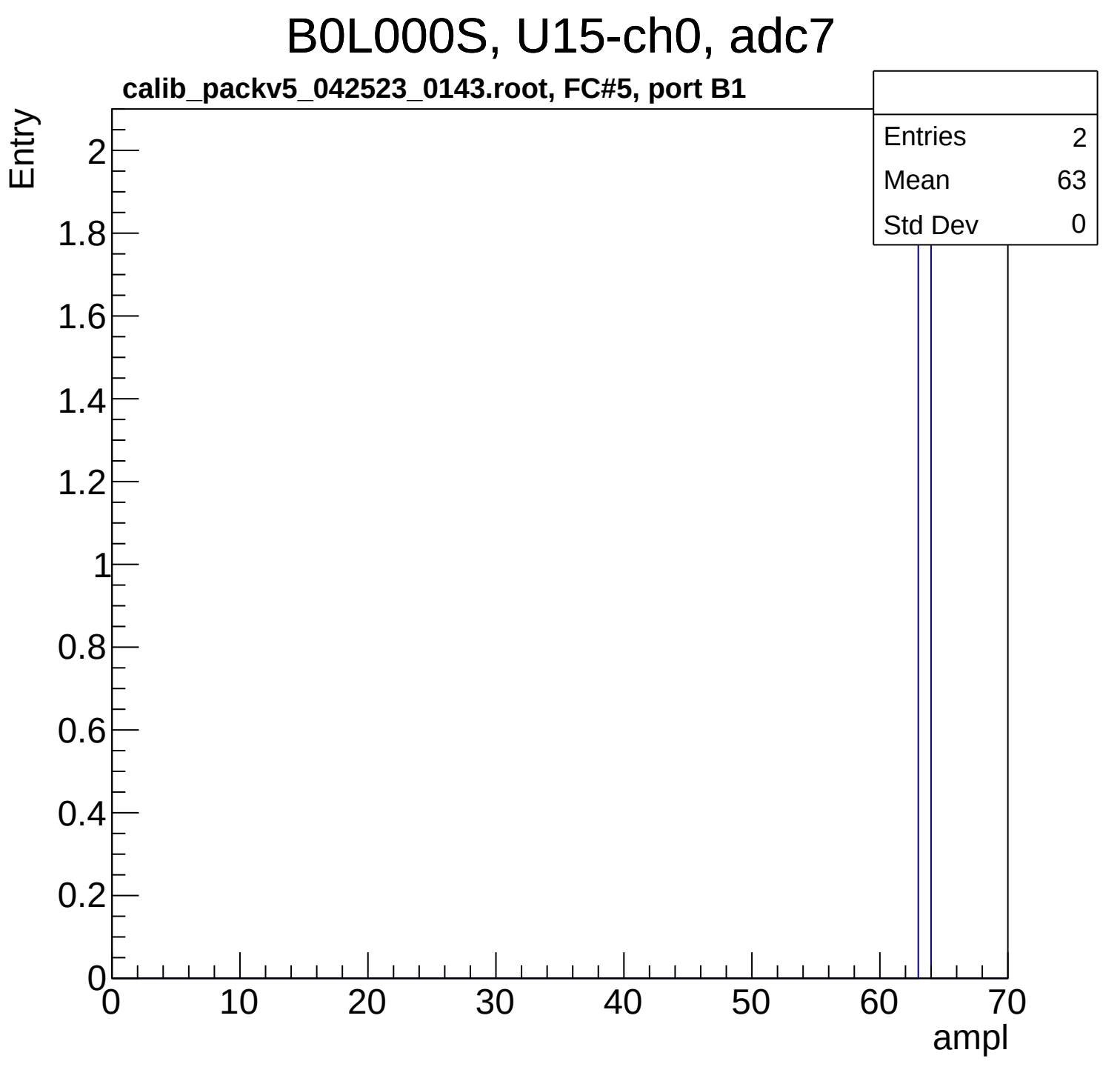
B0L000S, U15-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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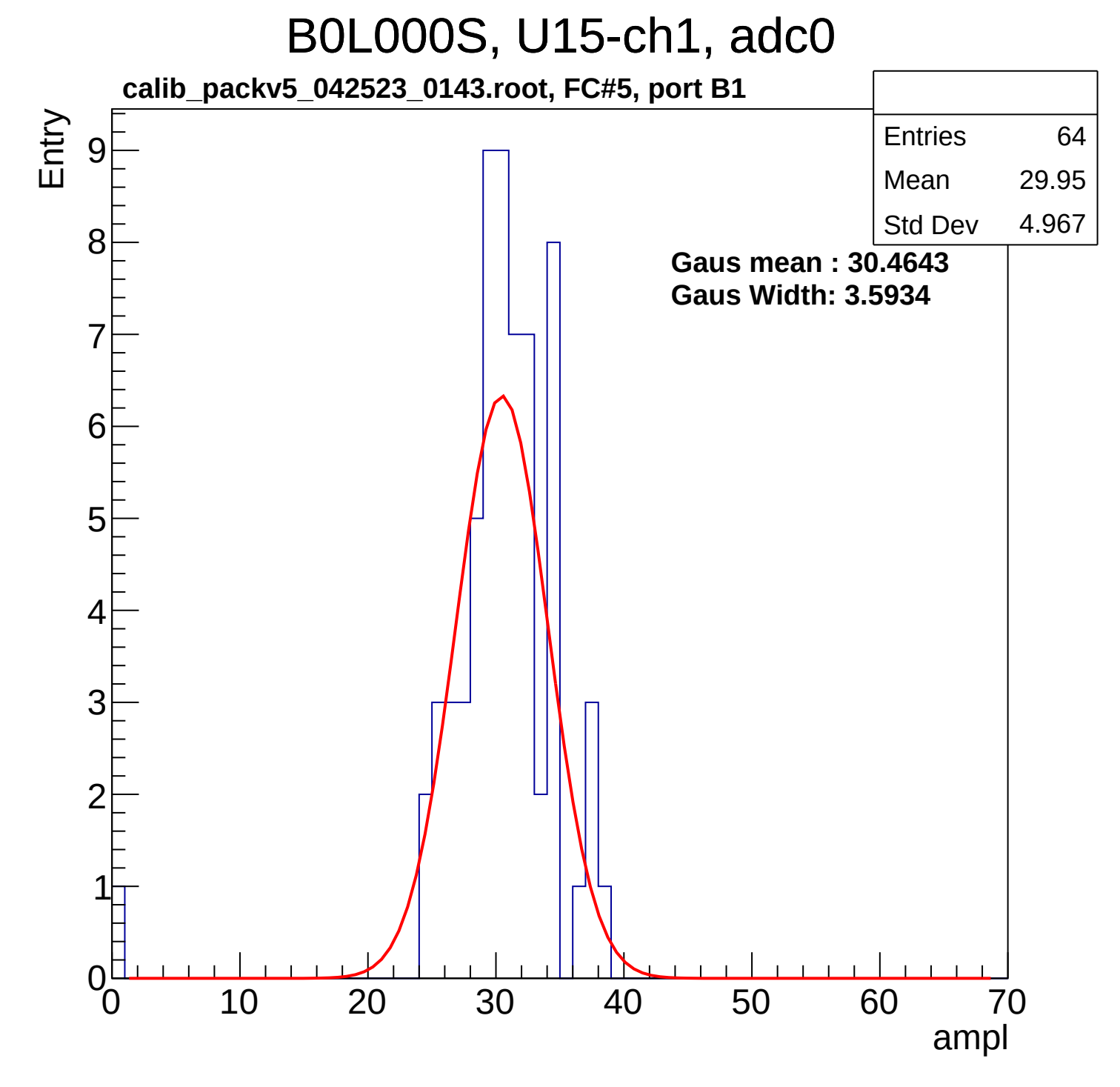
B0L000S, U15-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.95
Std Dev 4.967
Gaus mean : 30.4643
Gaus Width: 3.5934
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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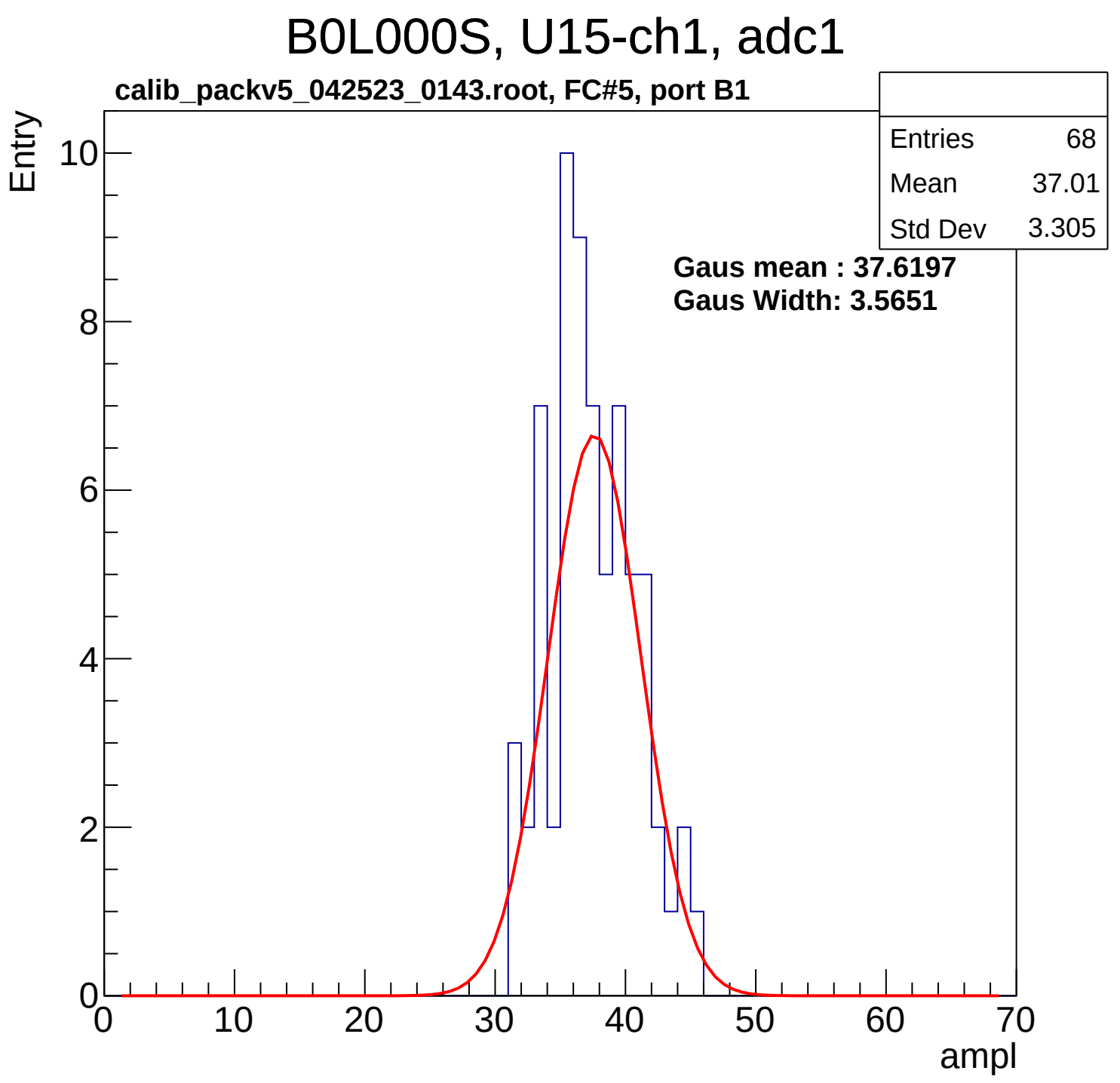
B0L000S, U15-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 37.01
Std Dev 3.305
Gaus mean : 37.6197
Gaus Width: 3.5651
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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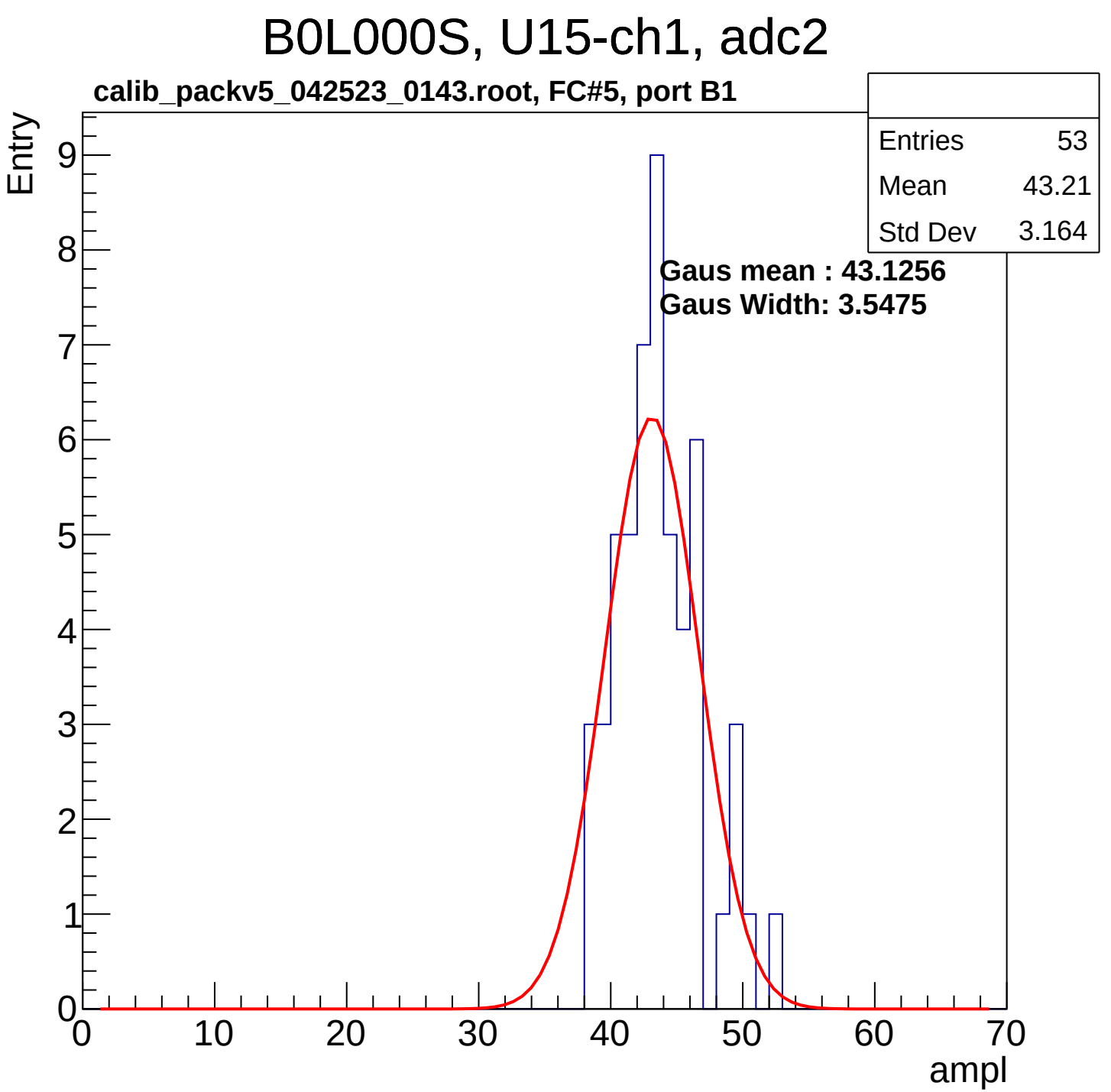
B0L000S, U15-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 43.21
Std Dev 3.164
Gaus mean : 43.1256
Gaus Width: 3.5475
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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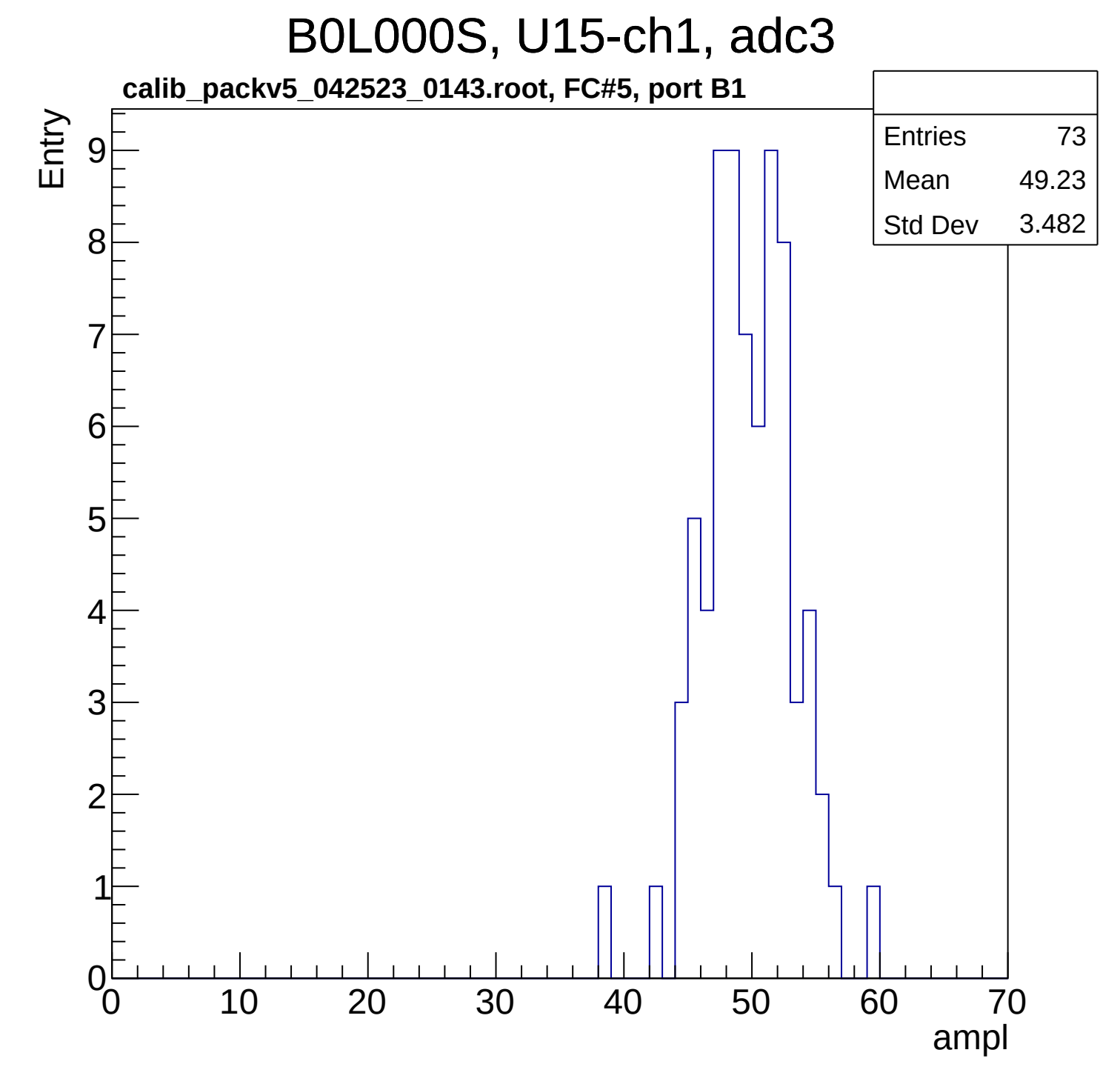
B0L000S, U15-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.23
Std Dev 3.482
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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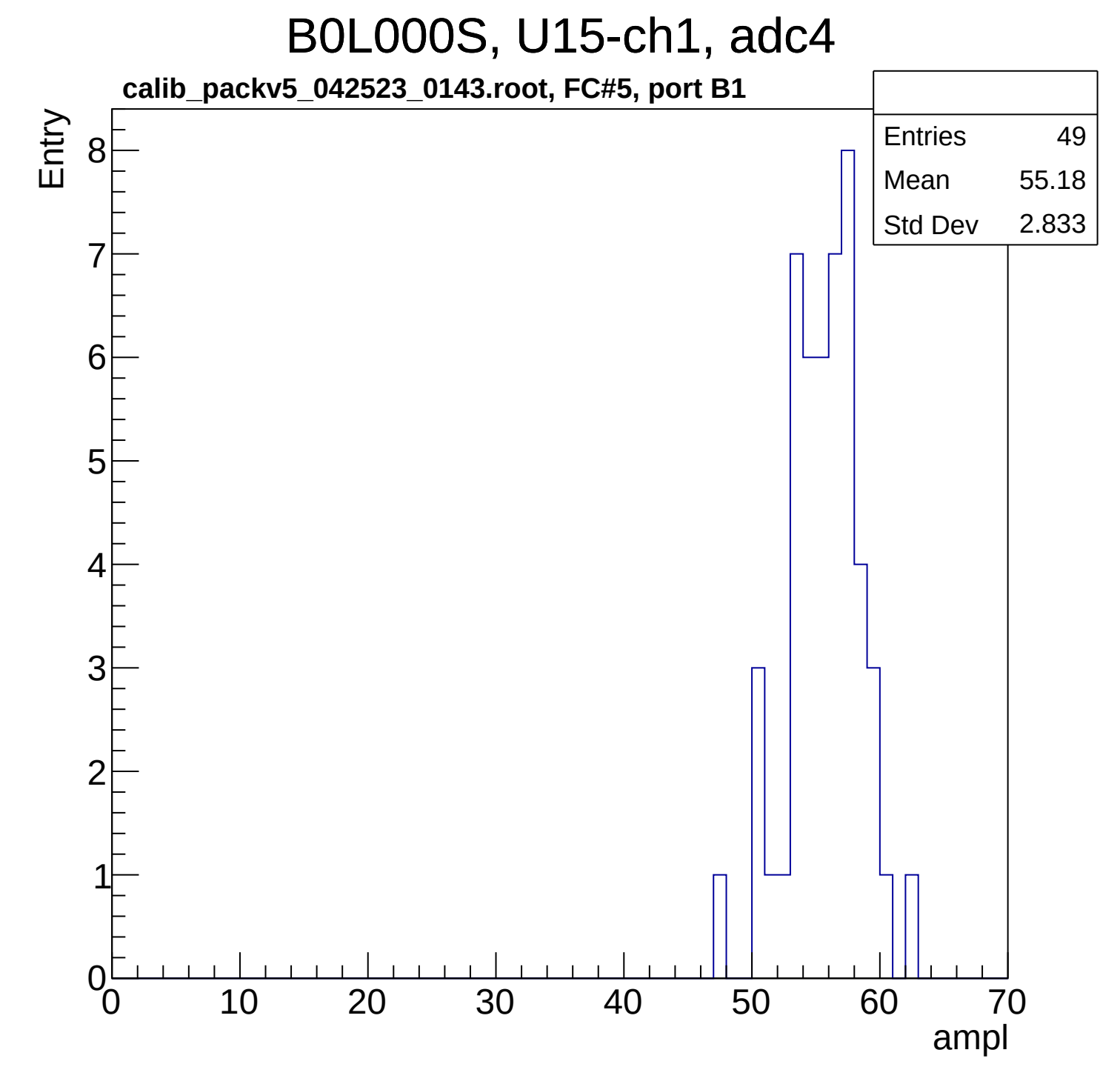
B0L000S, U15-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.18
Std Dev 2.833
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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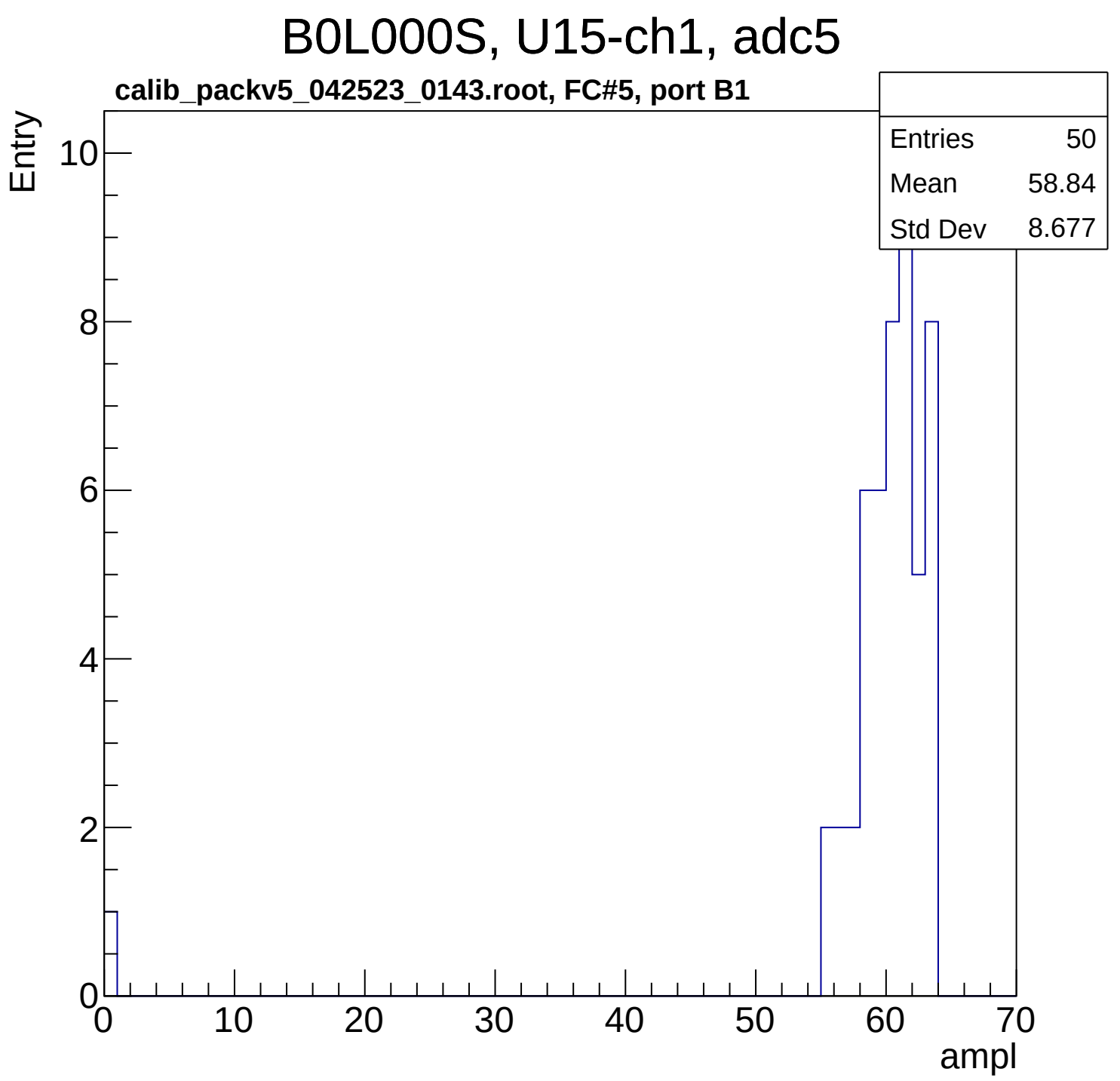
B0L000S, U15-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.84
Std Dev 8.677
Entry
ampl
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2
4
6
8
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0
10
20
30
40
50
60
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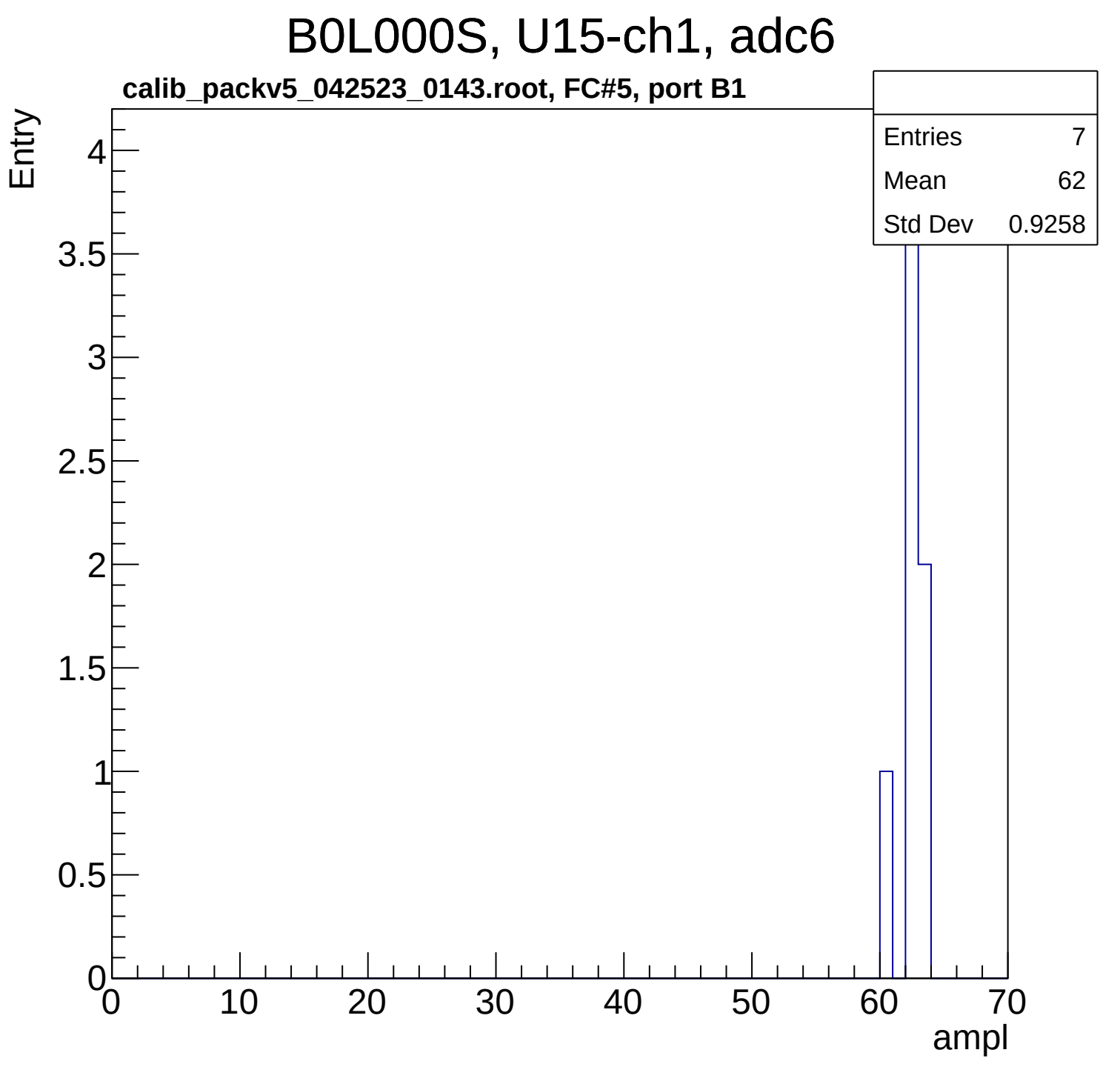
B0L000S, U15-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 0.9258
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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60
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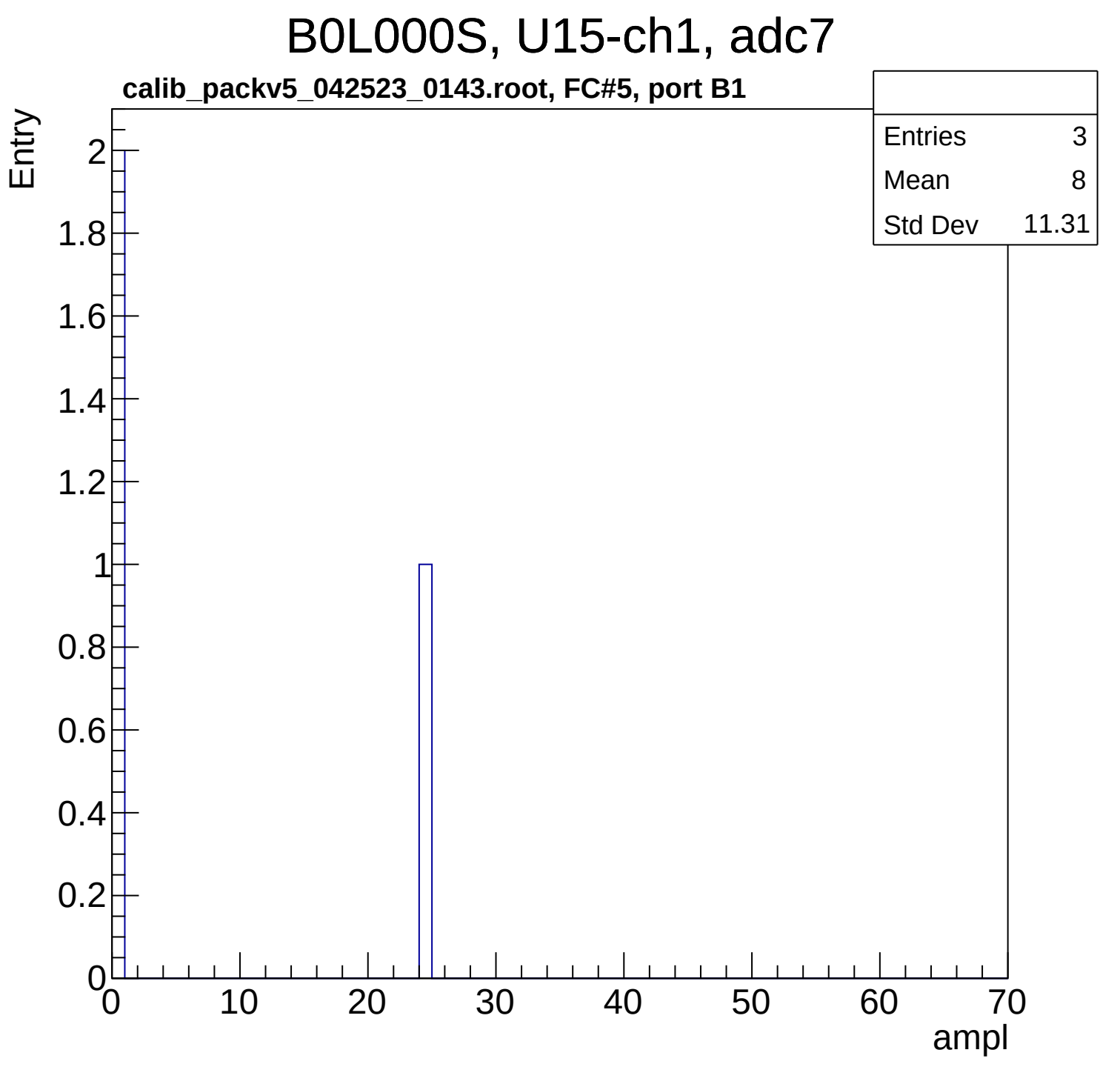
B0L000S, U15-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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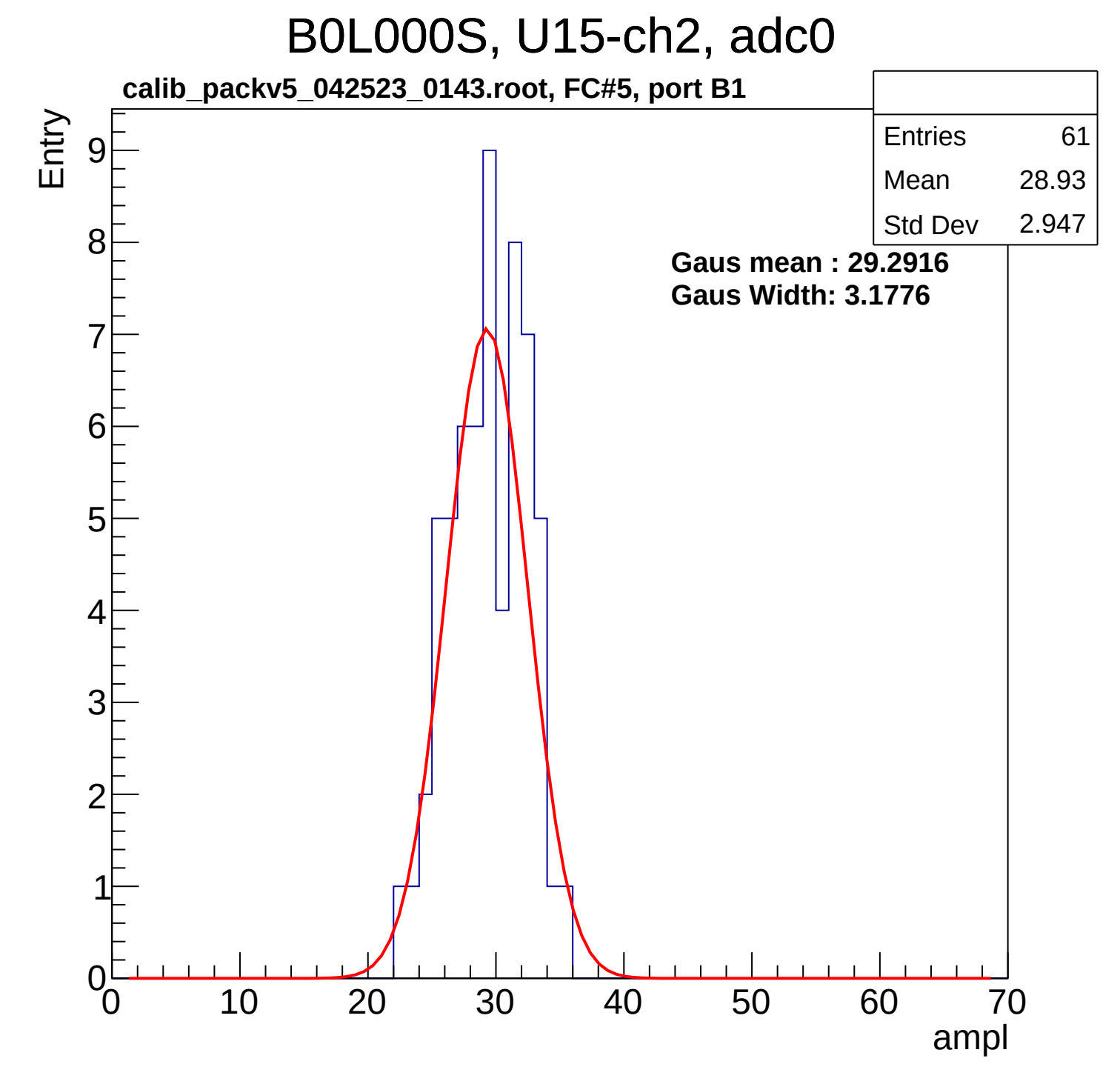
B0L000S, U15-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.93
Std Dev 2.947
Gaus mean : 29.2916
Gaus Width: 3.1776
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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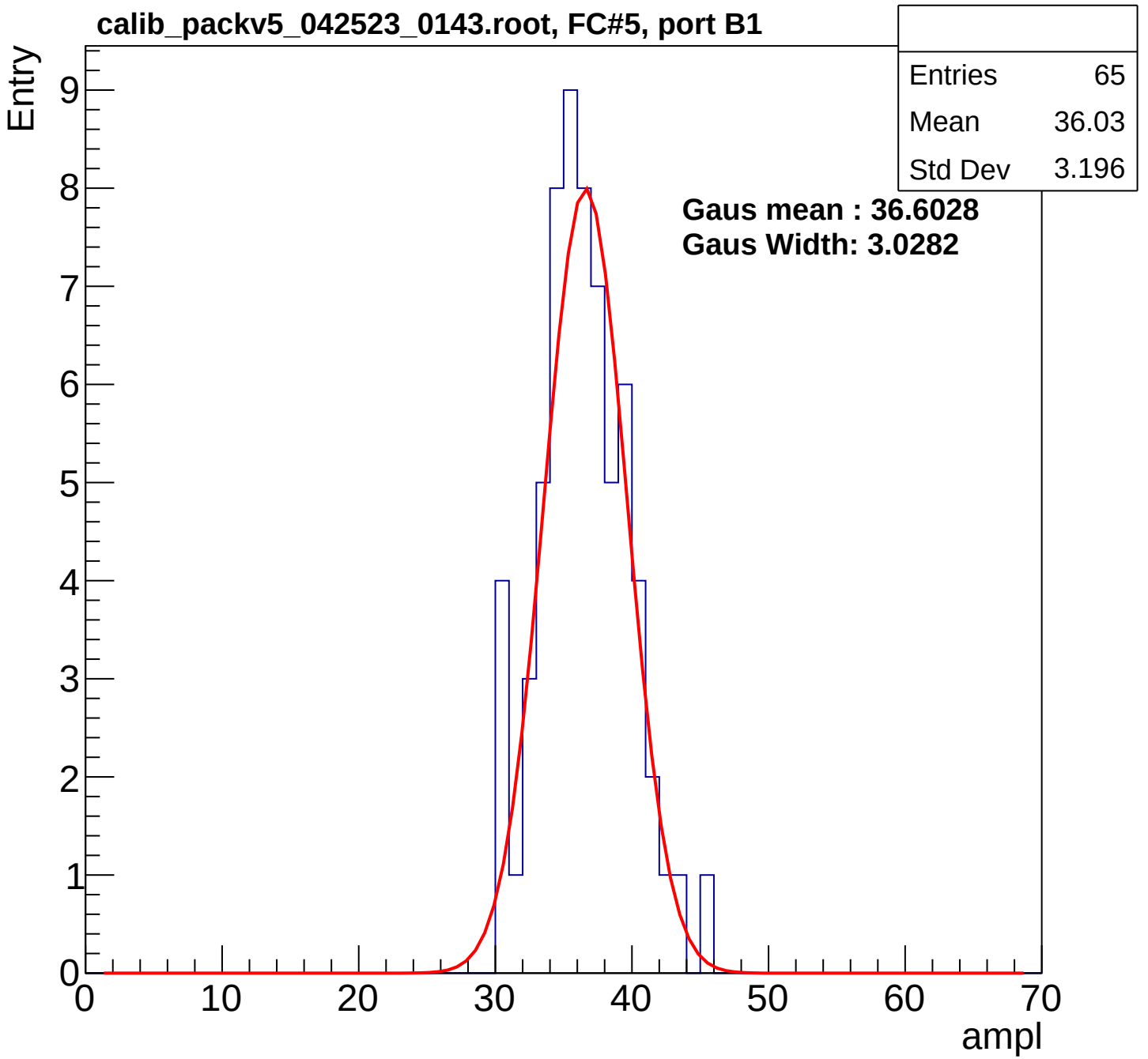
B0L000S, U15-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.03
Std Dev 3.196
Gaus mean : 36.6028
Gaus Width: 3.0282
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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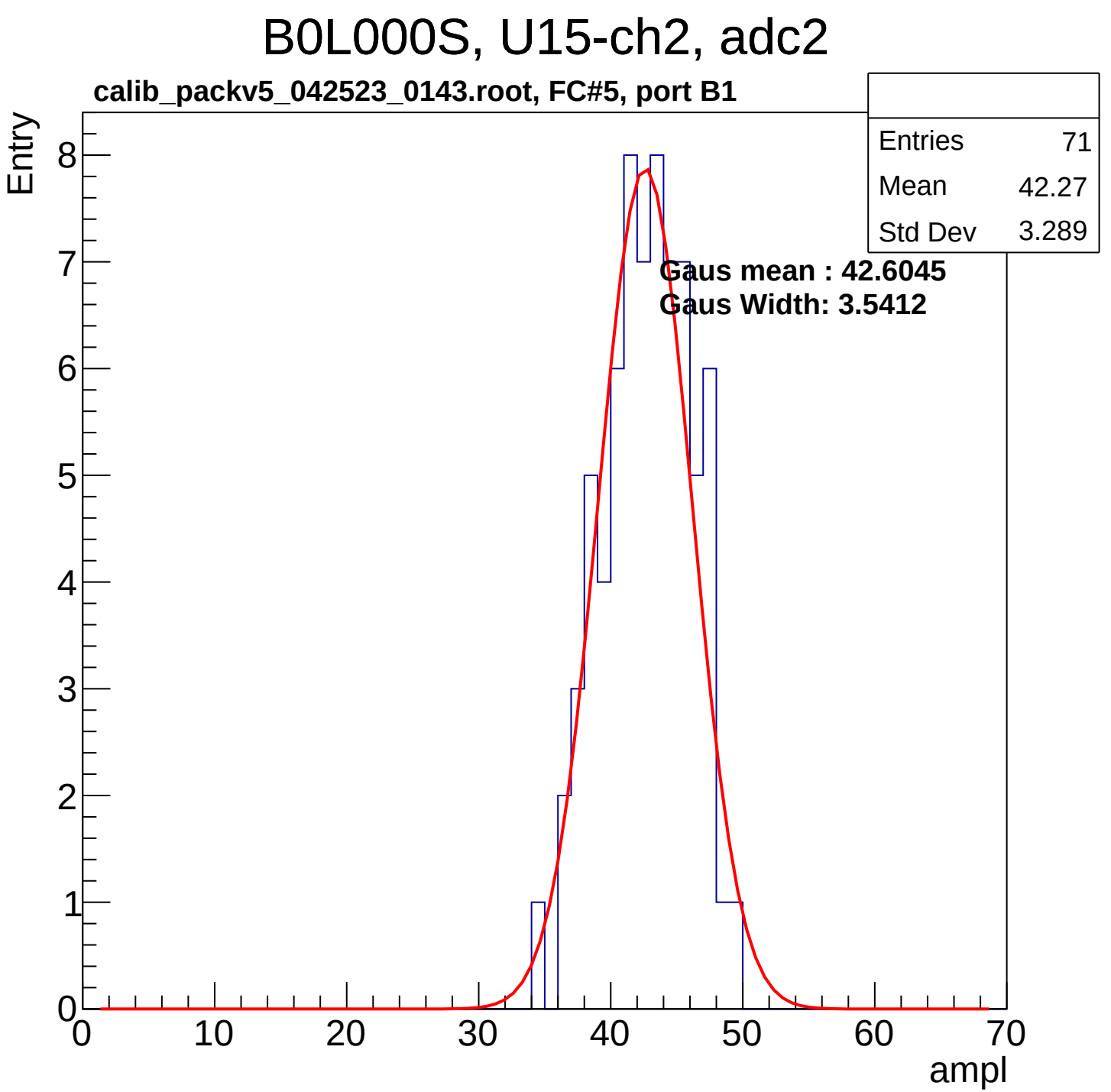
B0L000S, U15-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.27
Std Dev 3.289
Gaus mean : 42.6045
Gaus Width: 3.5412
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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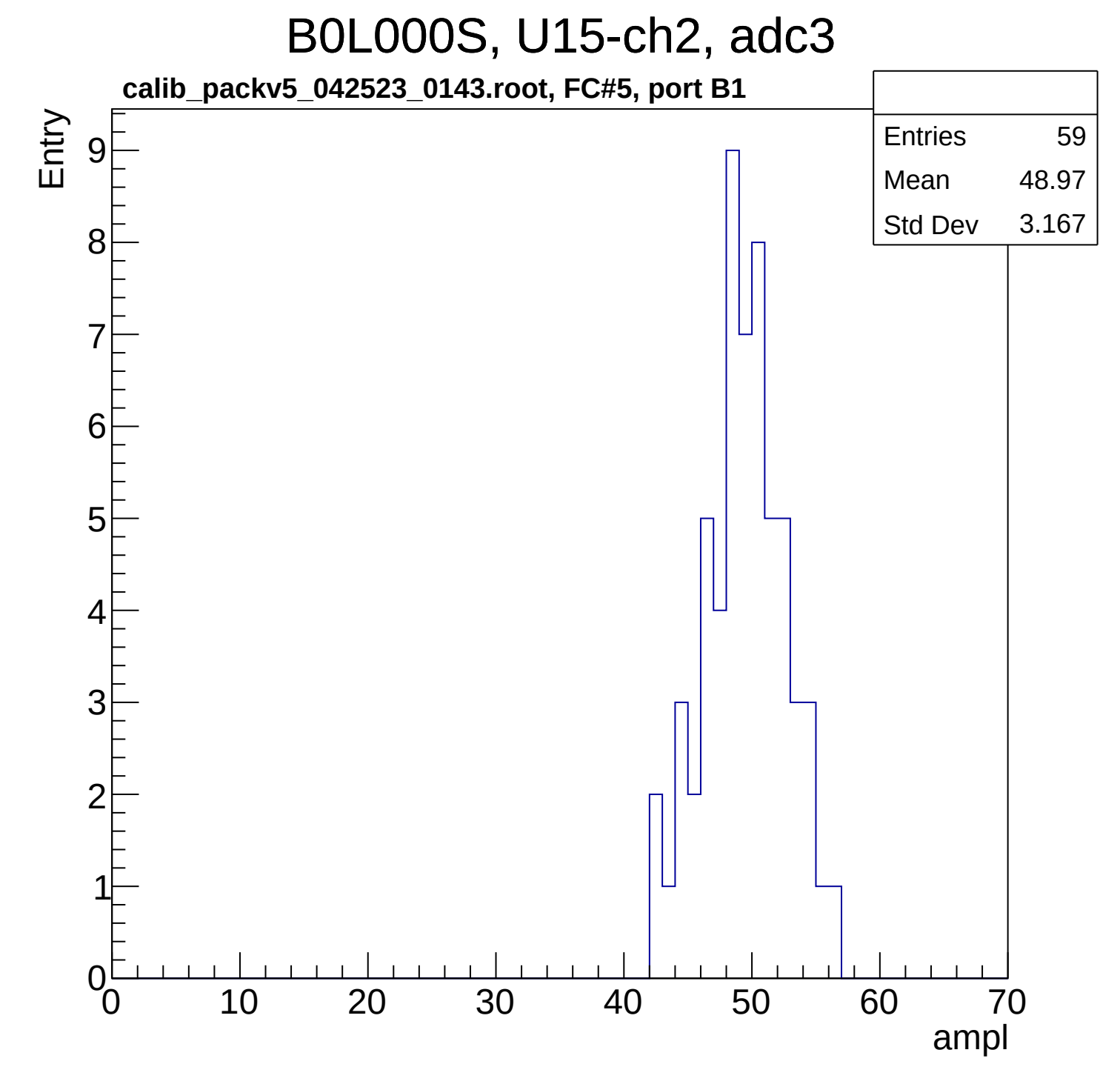
B0L000S, U15-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.97
Std Dev 3.167
Entry
ampl
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2
3
4
5
6
7
8
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0
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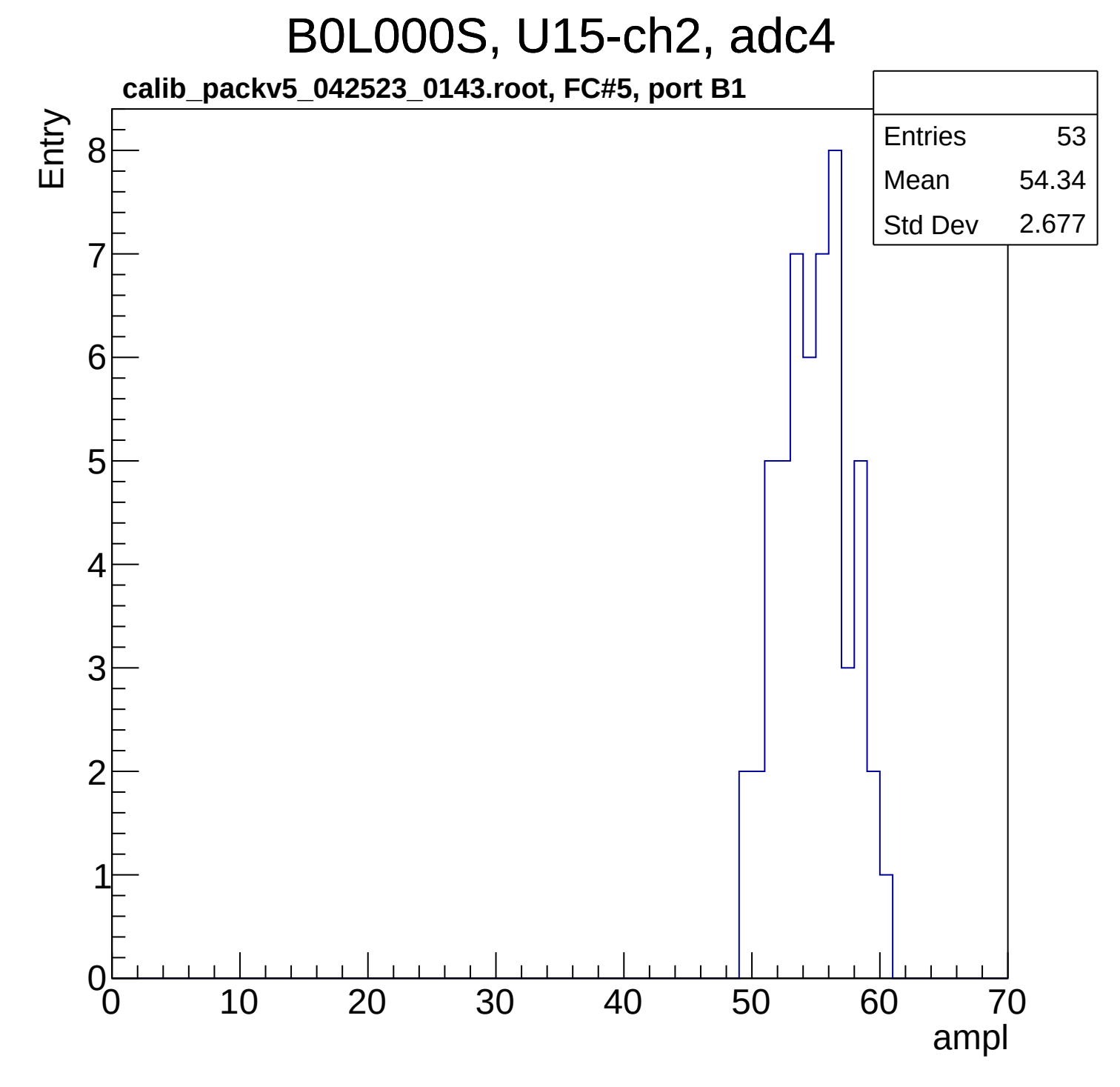
B0L000S, U15-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.34
Std Dev 2.677
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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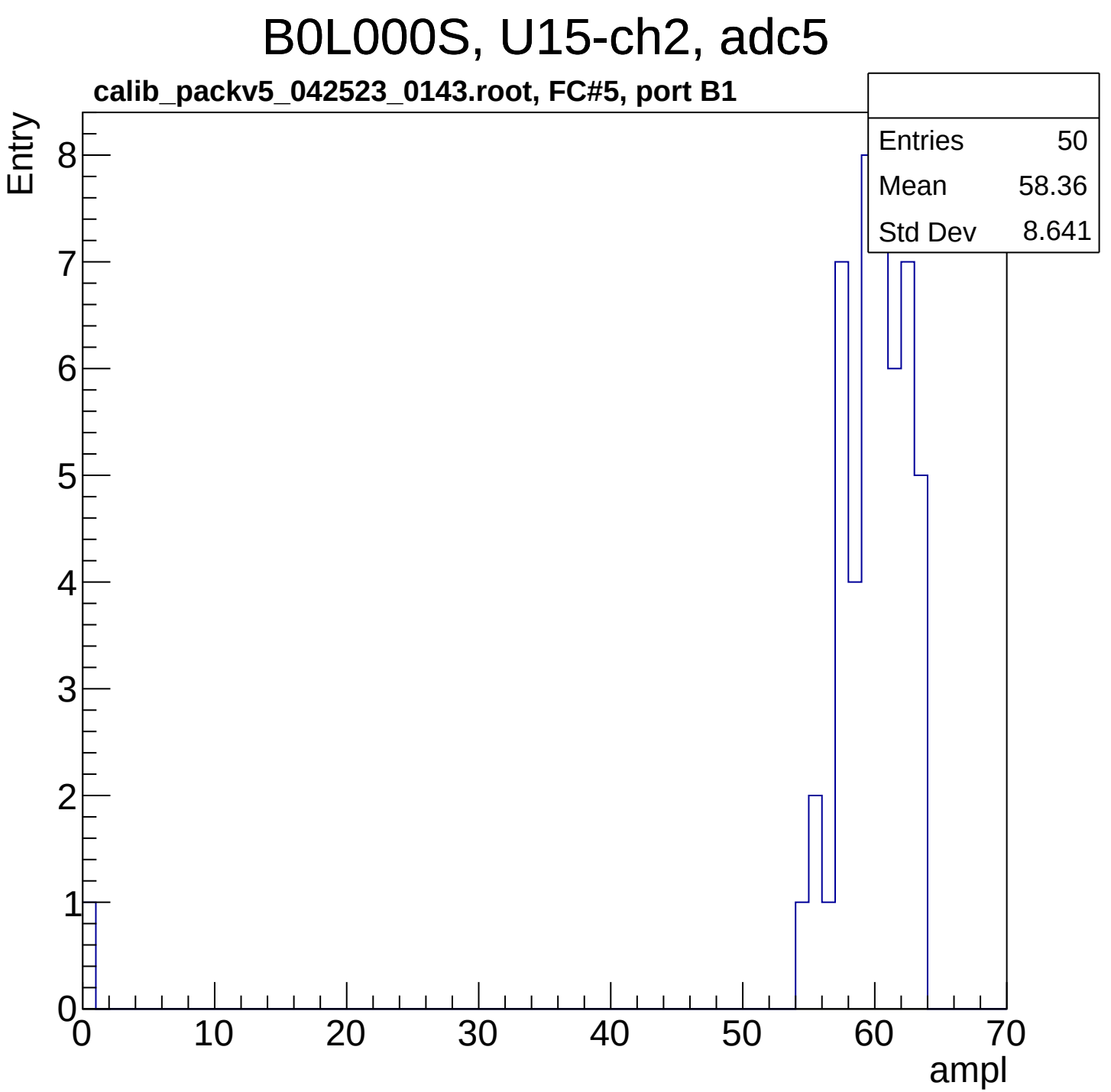
B0L000S, U15-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.36
Std Dev 8.641
Entry
ampl
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2
3
4
5
6
7
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0
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60
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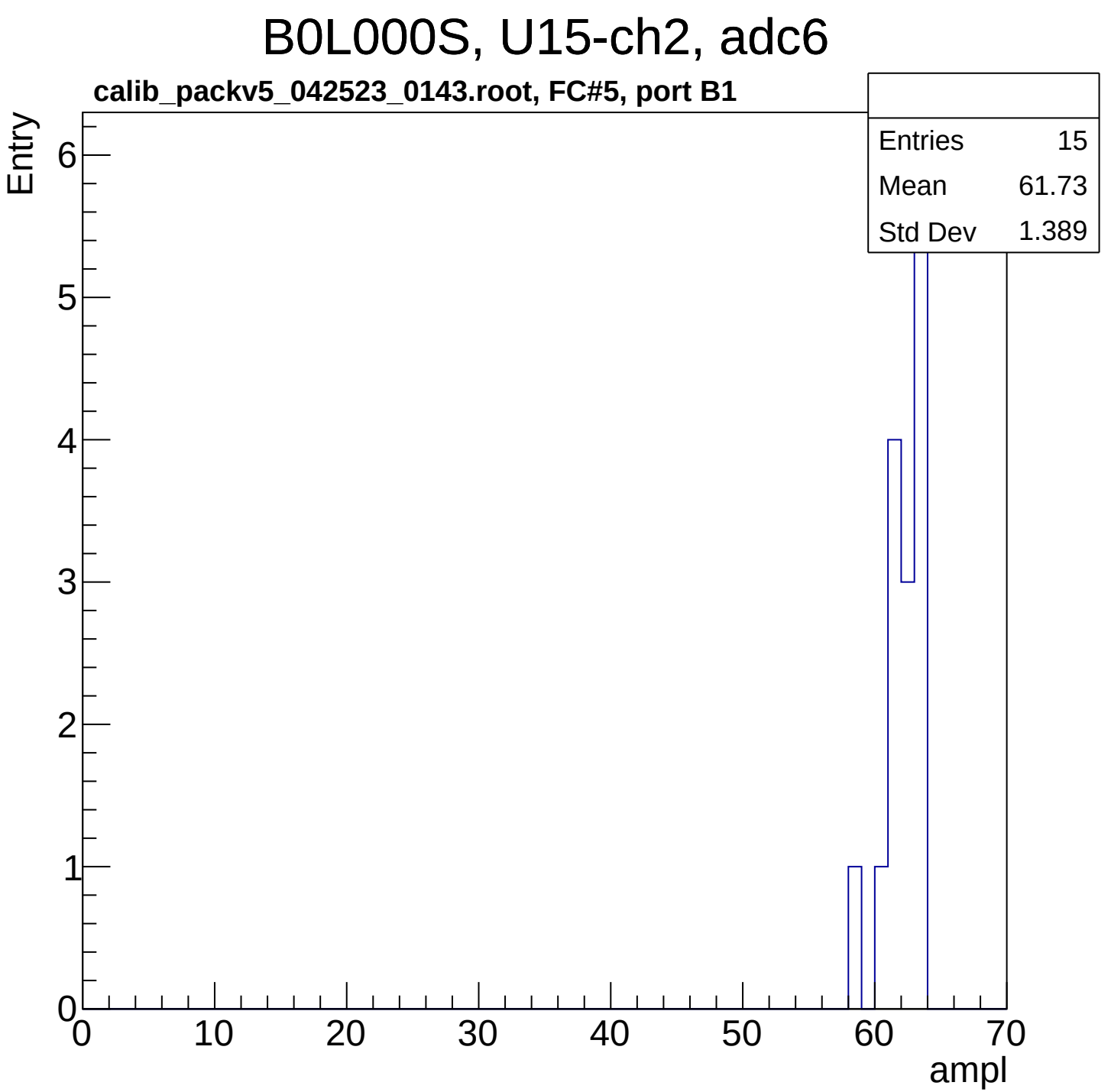
B0L000S, U15-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.73
Std Dev 1.389
Entry
ampl
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3
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0
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20
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50
60
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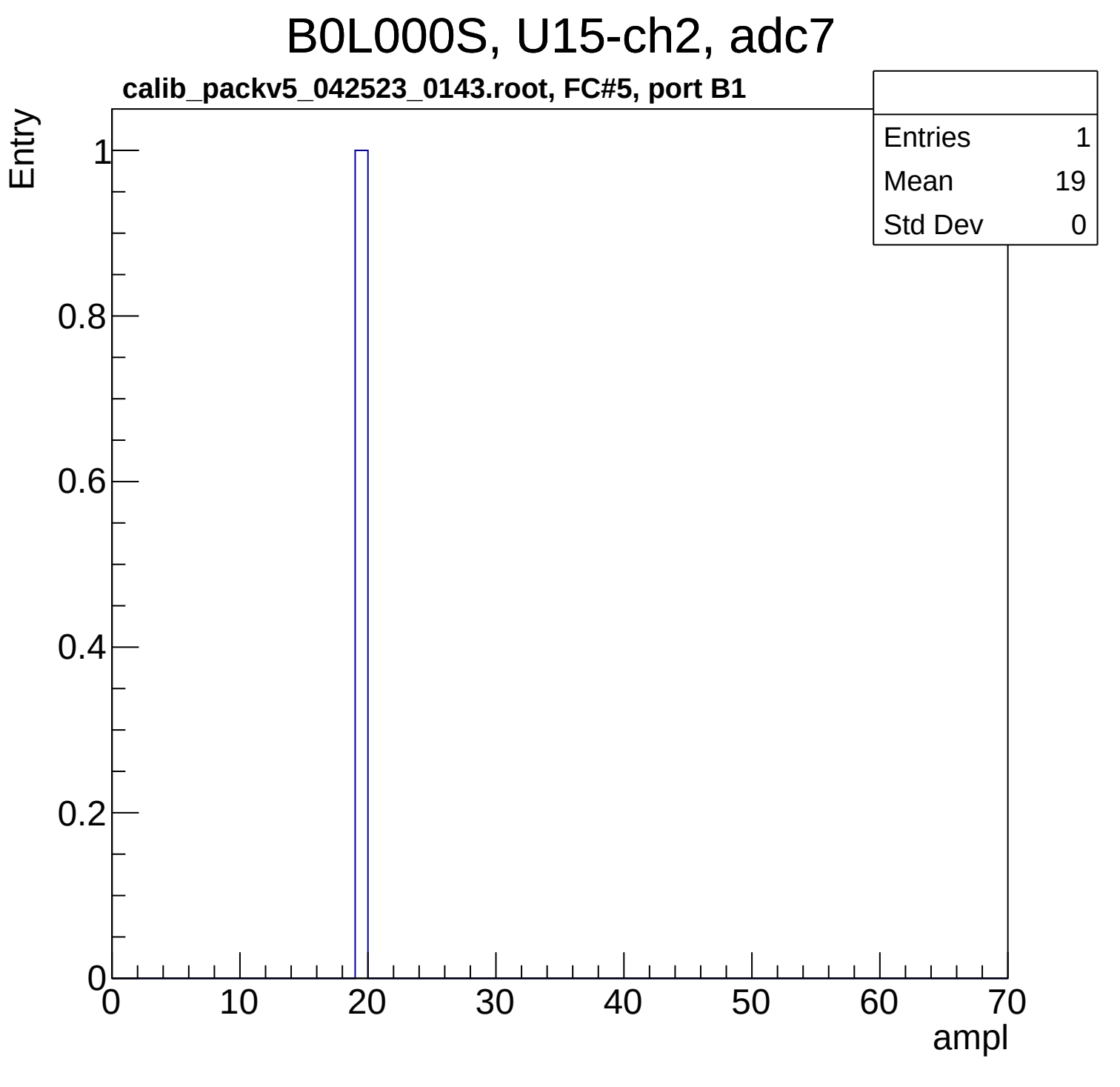
B0L000S, U15-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
ampl
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0.4
0.6
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0
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60
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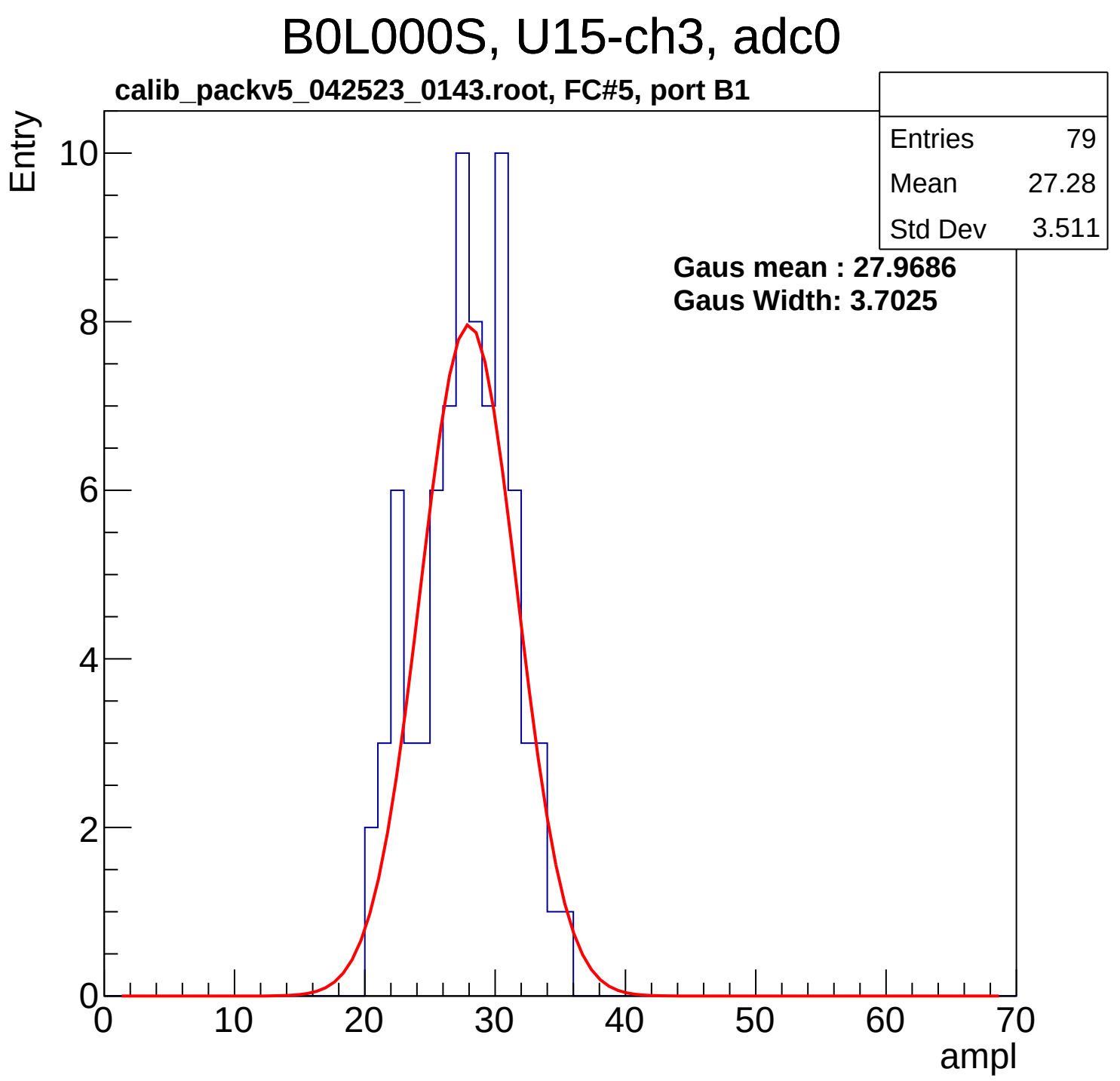
B0L000S, U15-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 27.28
Std Dev 3.511
Gaus mean : 27.9686
Gaus Width: 3.7025
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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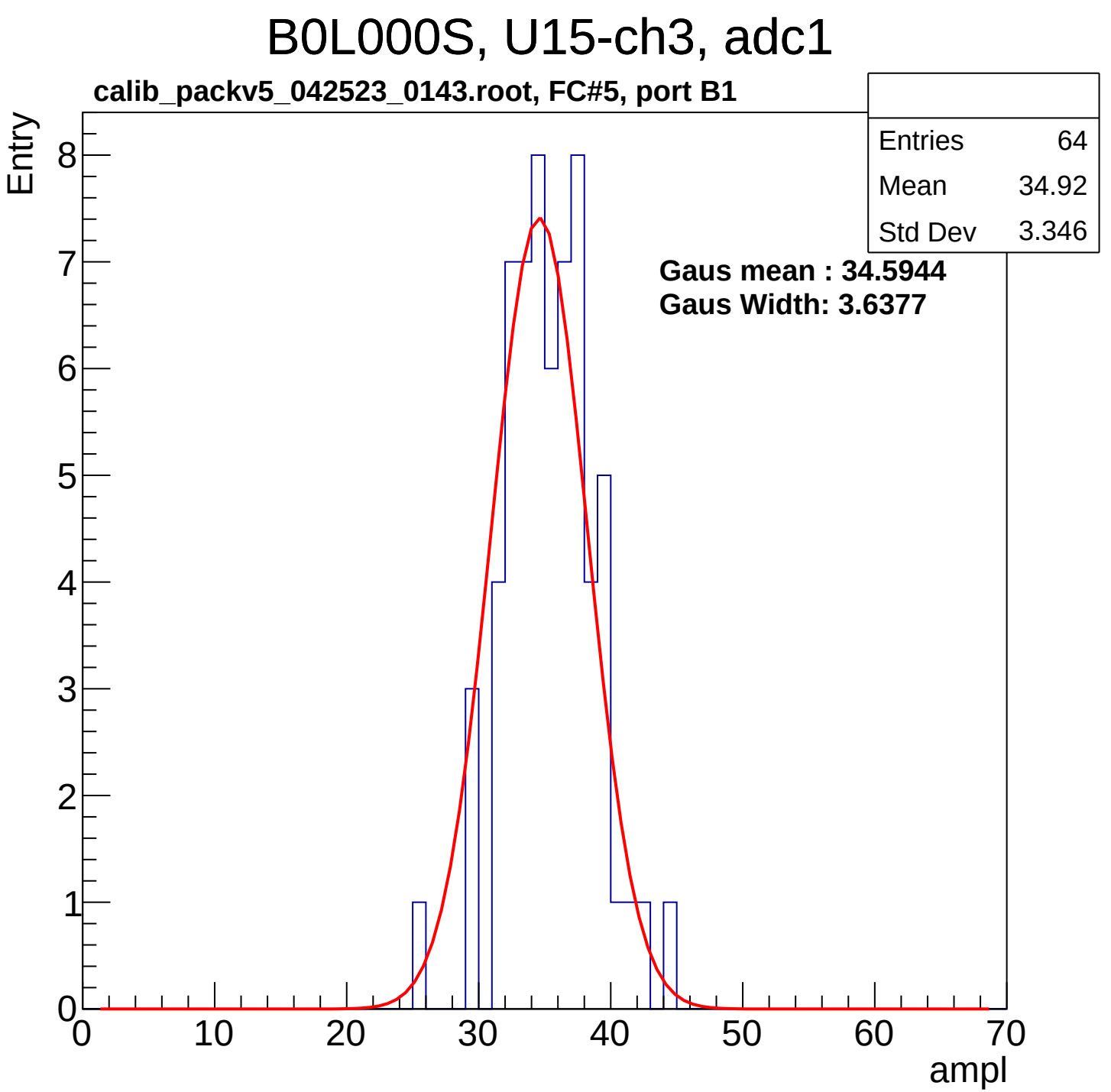
B0L000S, U15-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 34.92
Std Dev 3.346
Gaus mean : 34.5944
Gaus Width: 3.6377
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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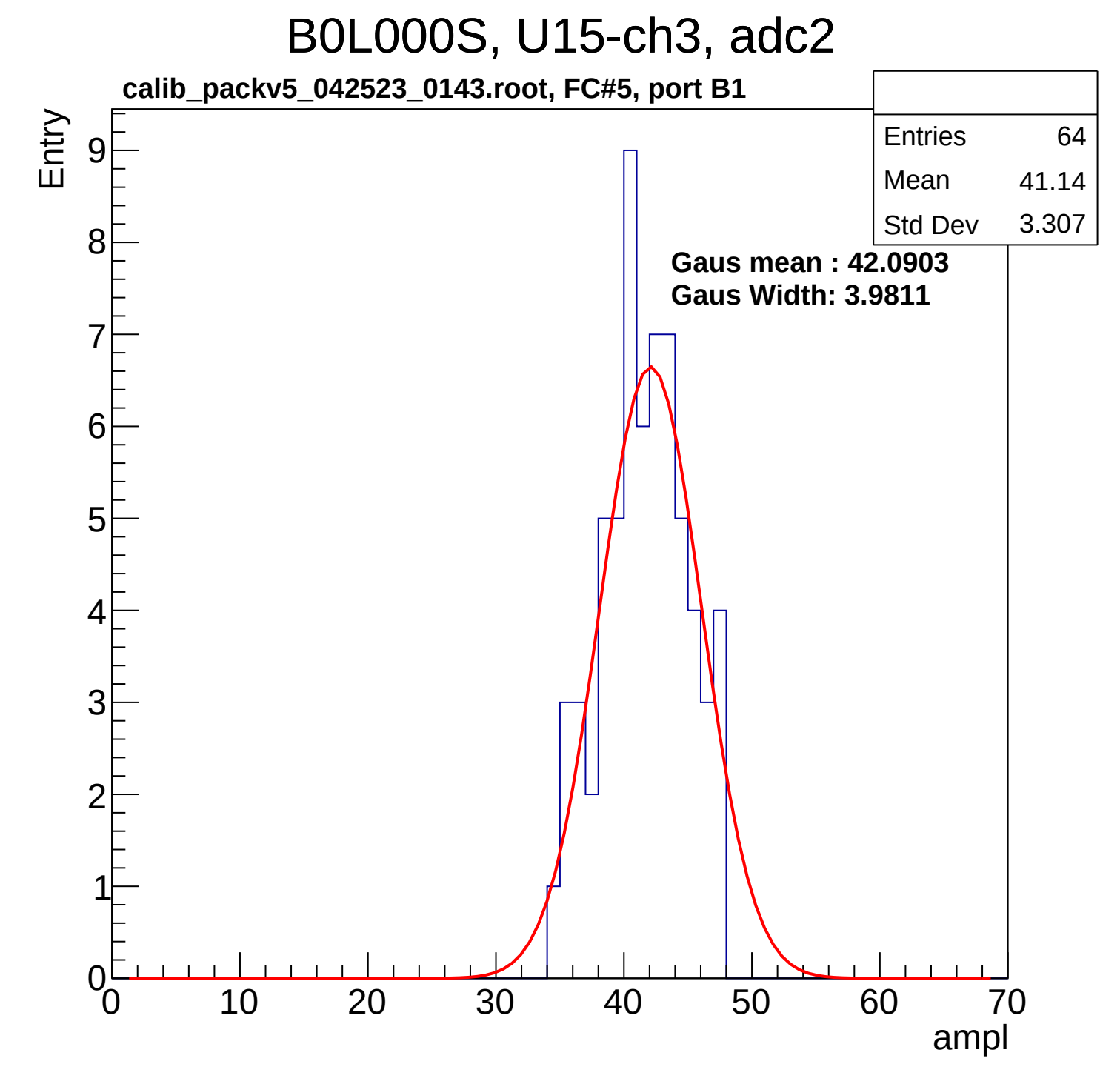
B0L000S, U15-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 41.14
Std Dev 3.307
Gaus mean : 42.0903
Gaus Width: 3.9811
Entry
ampl
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3
4
5
6
7
8
9
0
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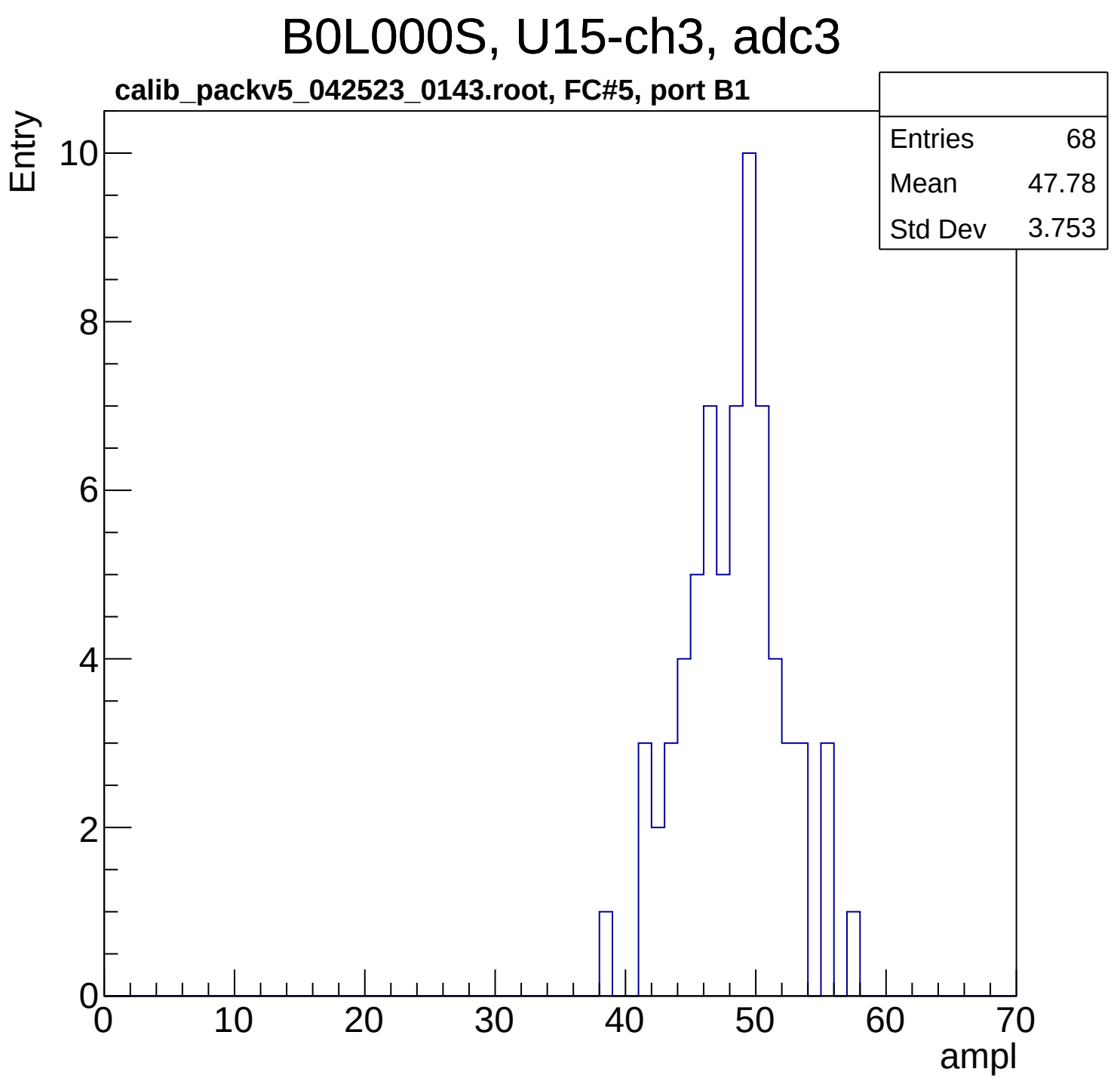
B0L000S, U15-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 47.78
Std Dev 3.753
Entry
ampl
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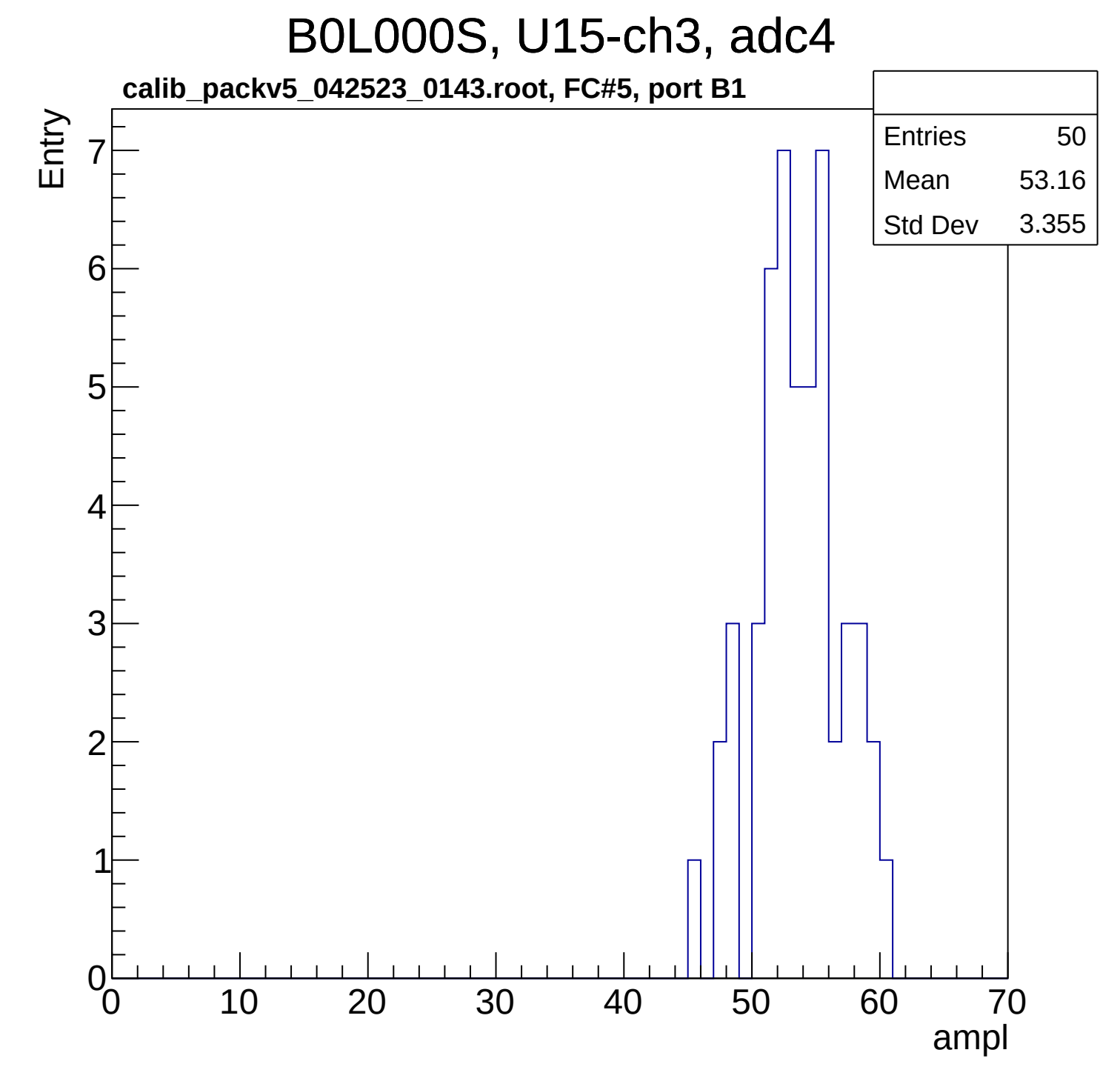
B0L000S, U15-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 53.16
Std Dev 3.355
Entry
ampl
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4
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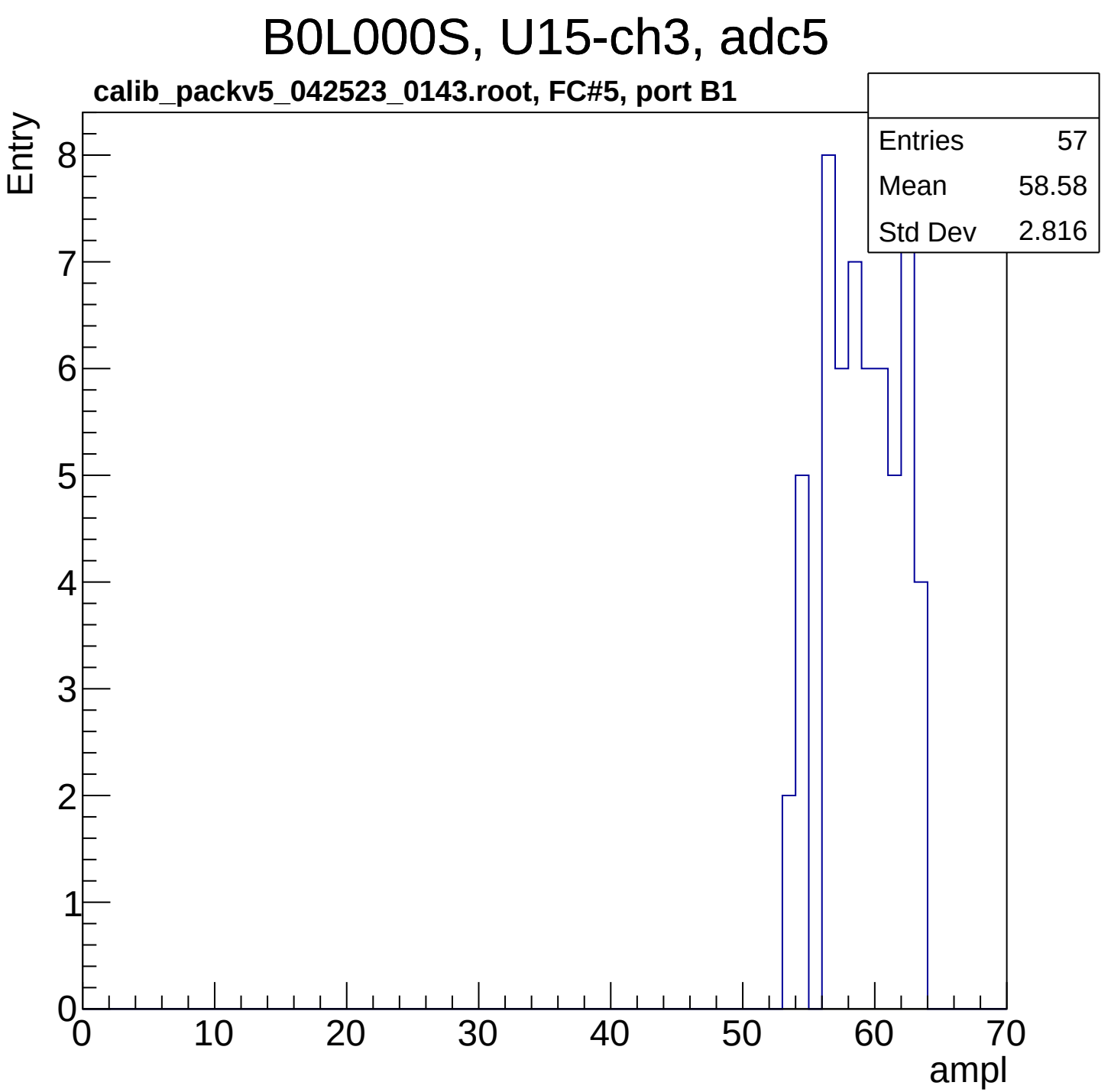
B0L000S, U15-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.58
Std Dev 2.816
Entry
ampl
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4
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7
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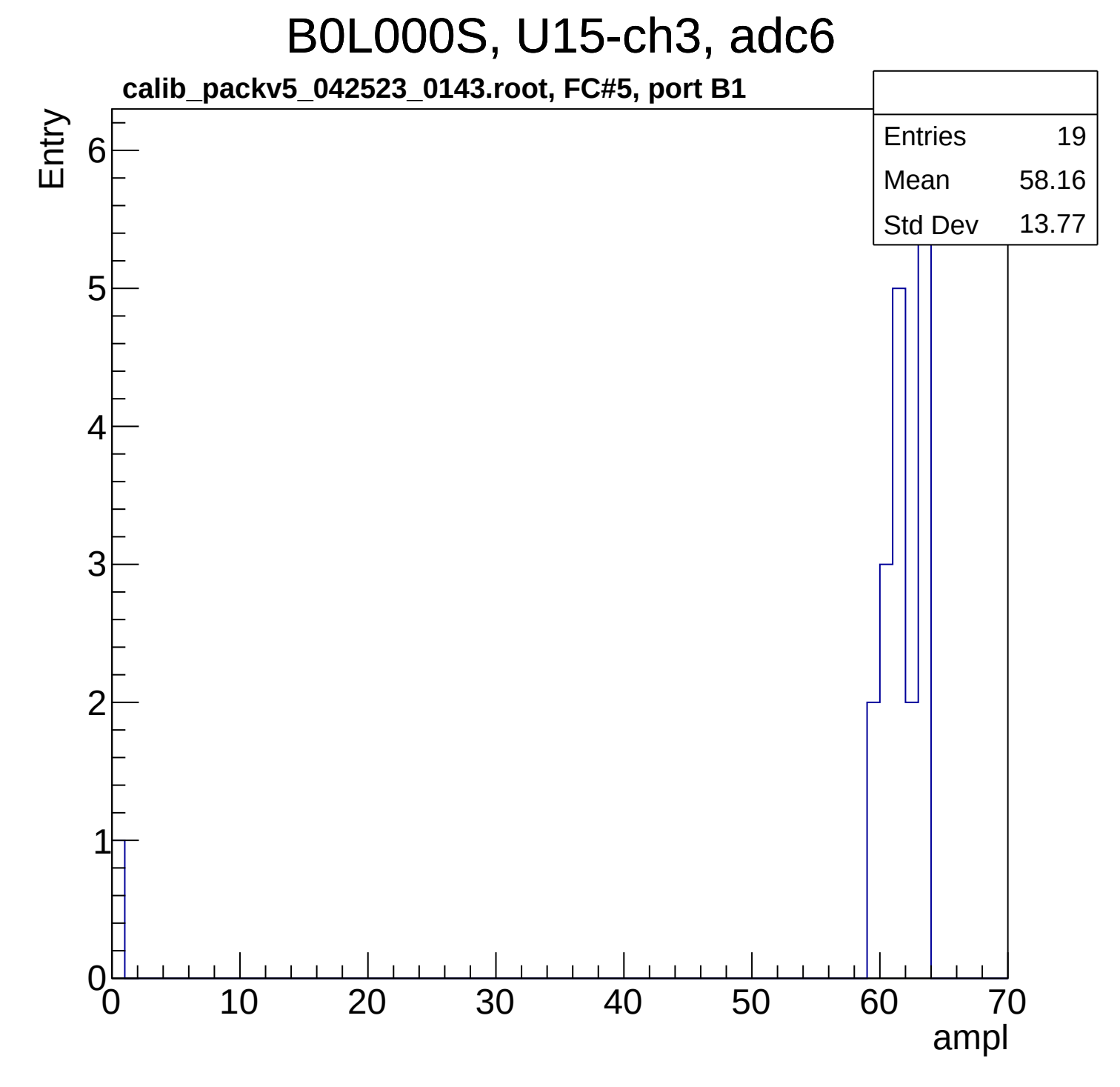
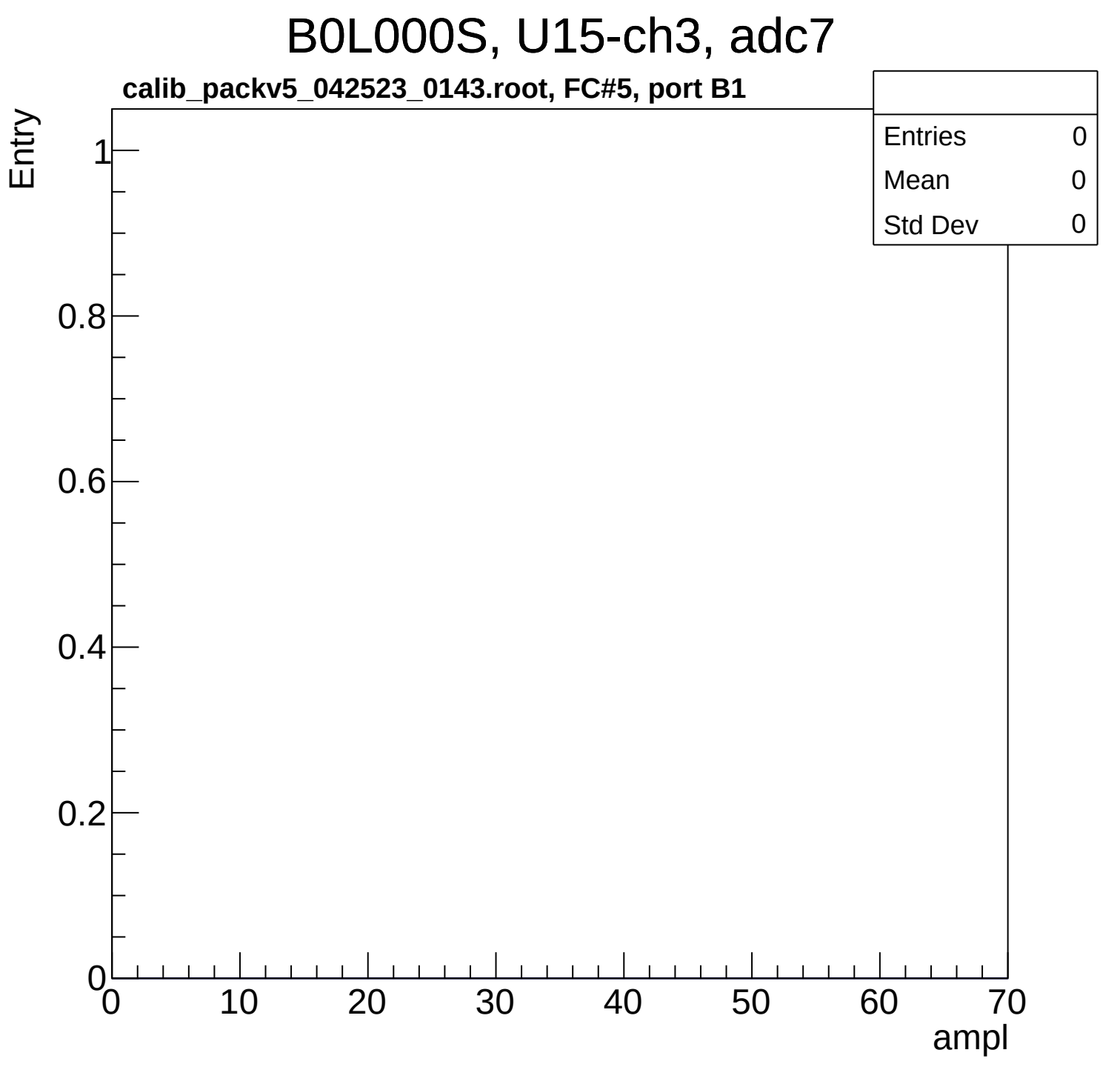

B0L000S, U15-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.16
Std Dev 13.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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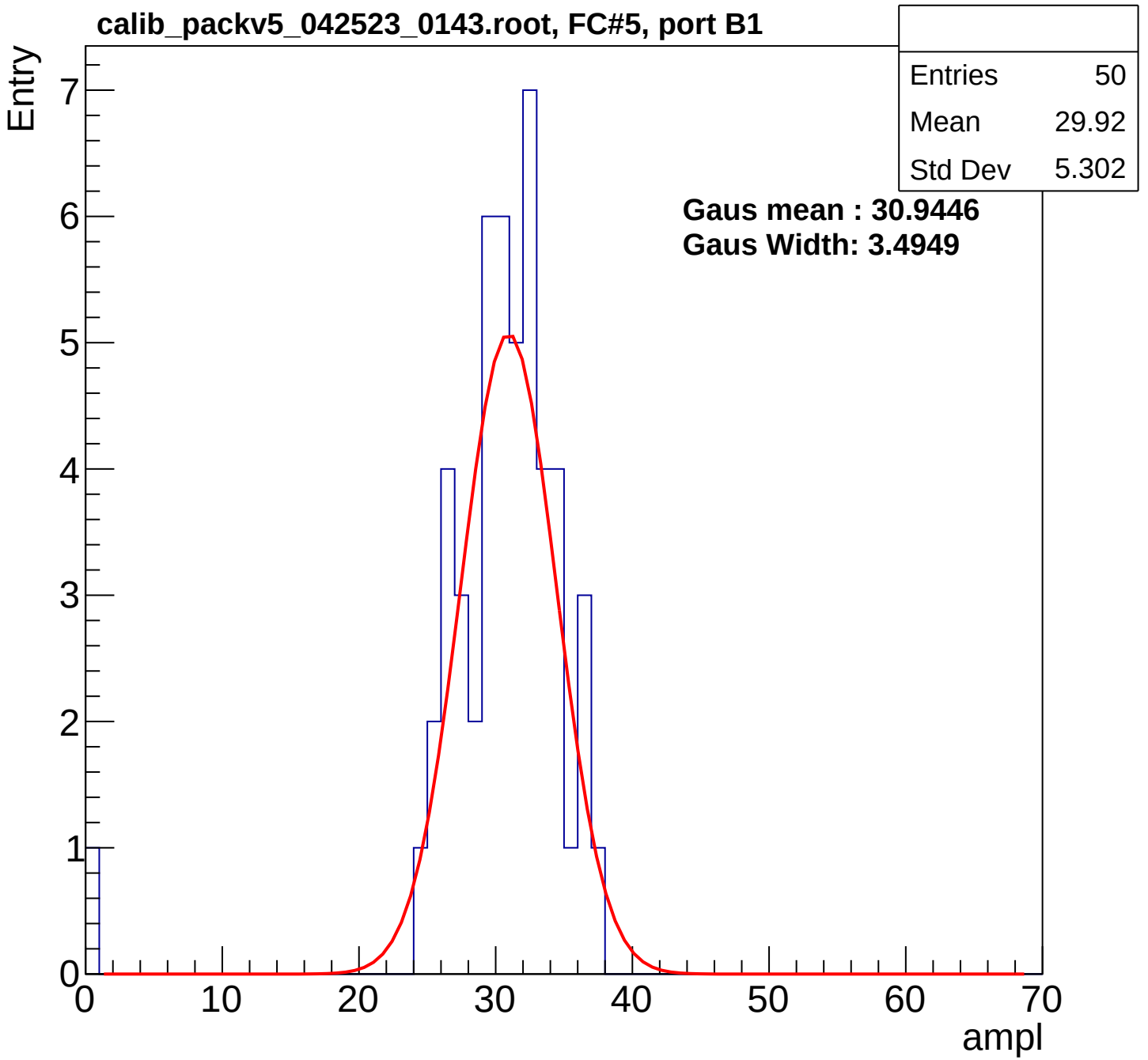
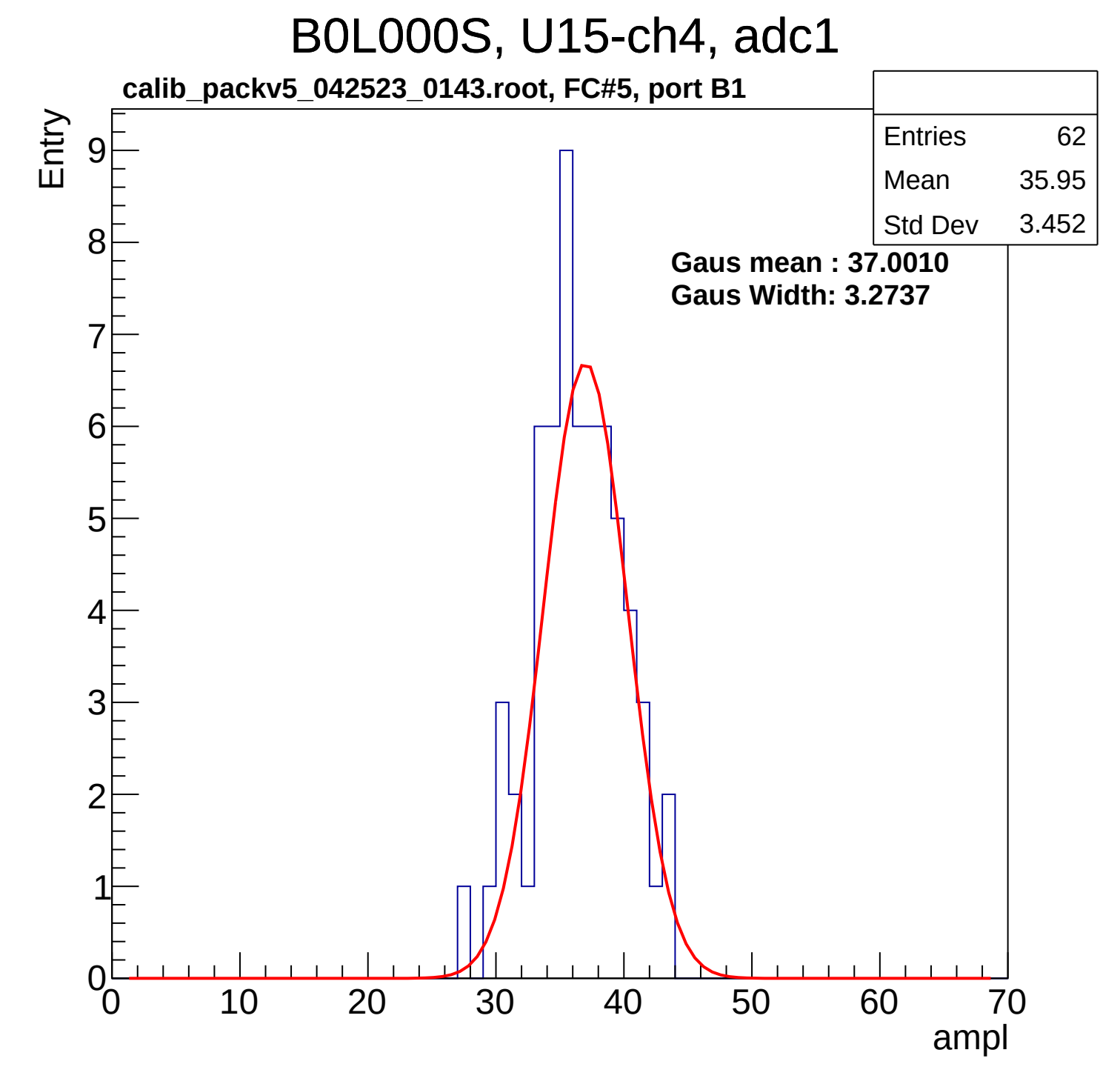
50
60
70
ampl


B0L000S, U15-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 29.92
Std Dev 5.302
Gaus mean : 30.9446
Gaus Width: 3.4949
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.95
Std Dev 3.452
Gaus mean : 37.0010
Gaus Width: 3.2737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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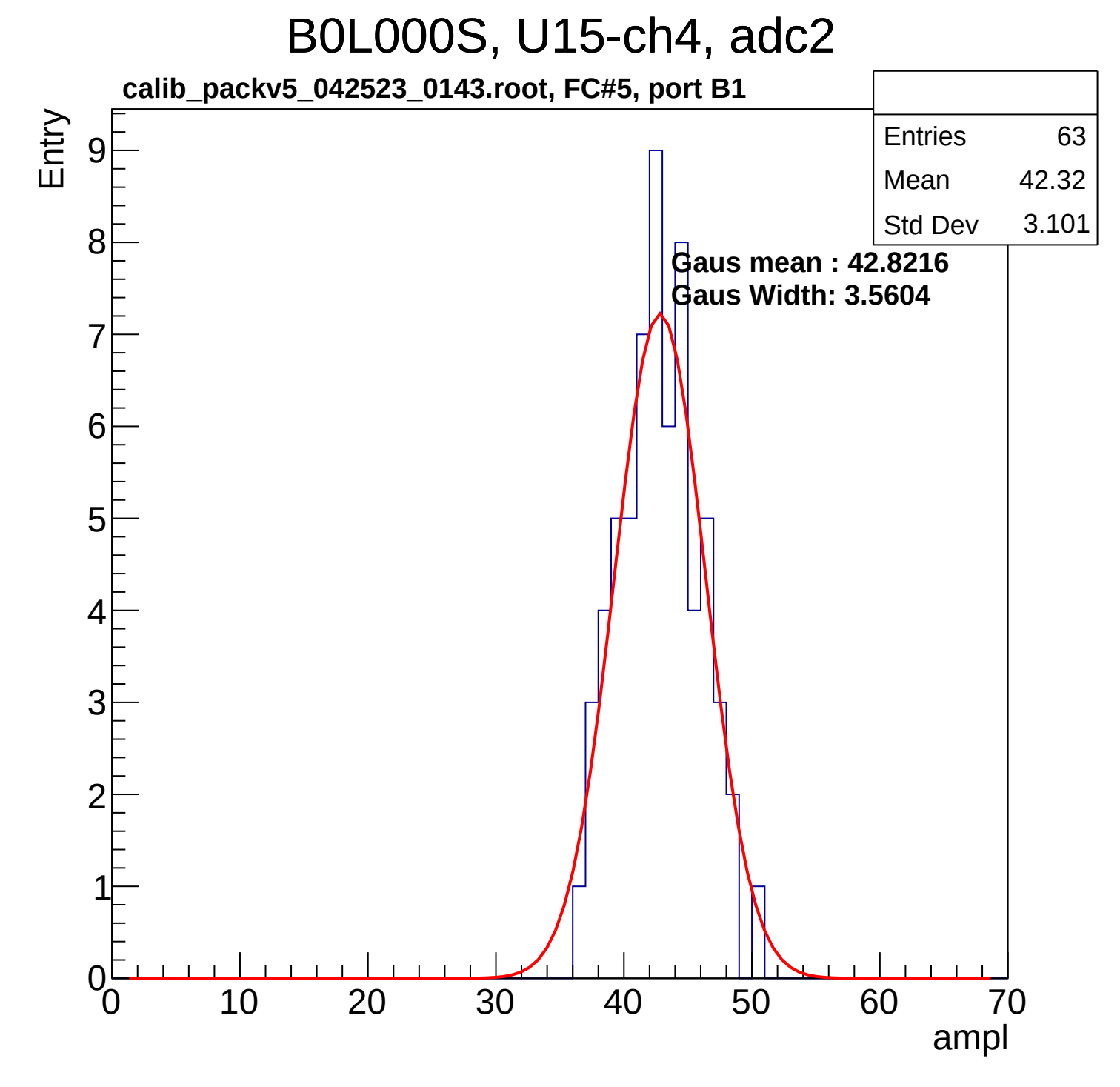
B0L000S, U15-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.32
Std Dev 3.101
Gaus mean : 42.8216
Gaus Width: 3.5604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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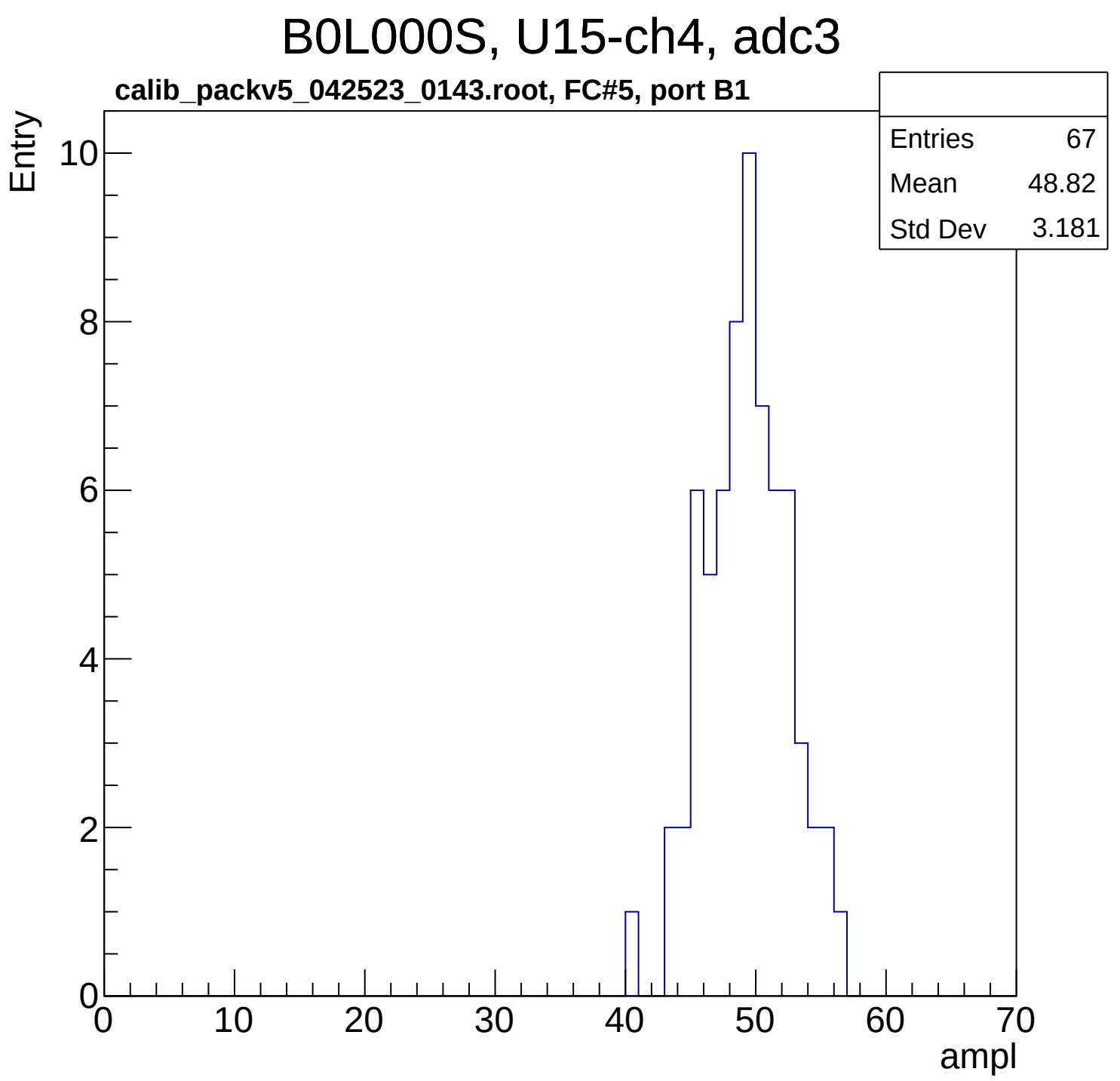
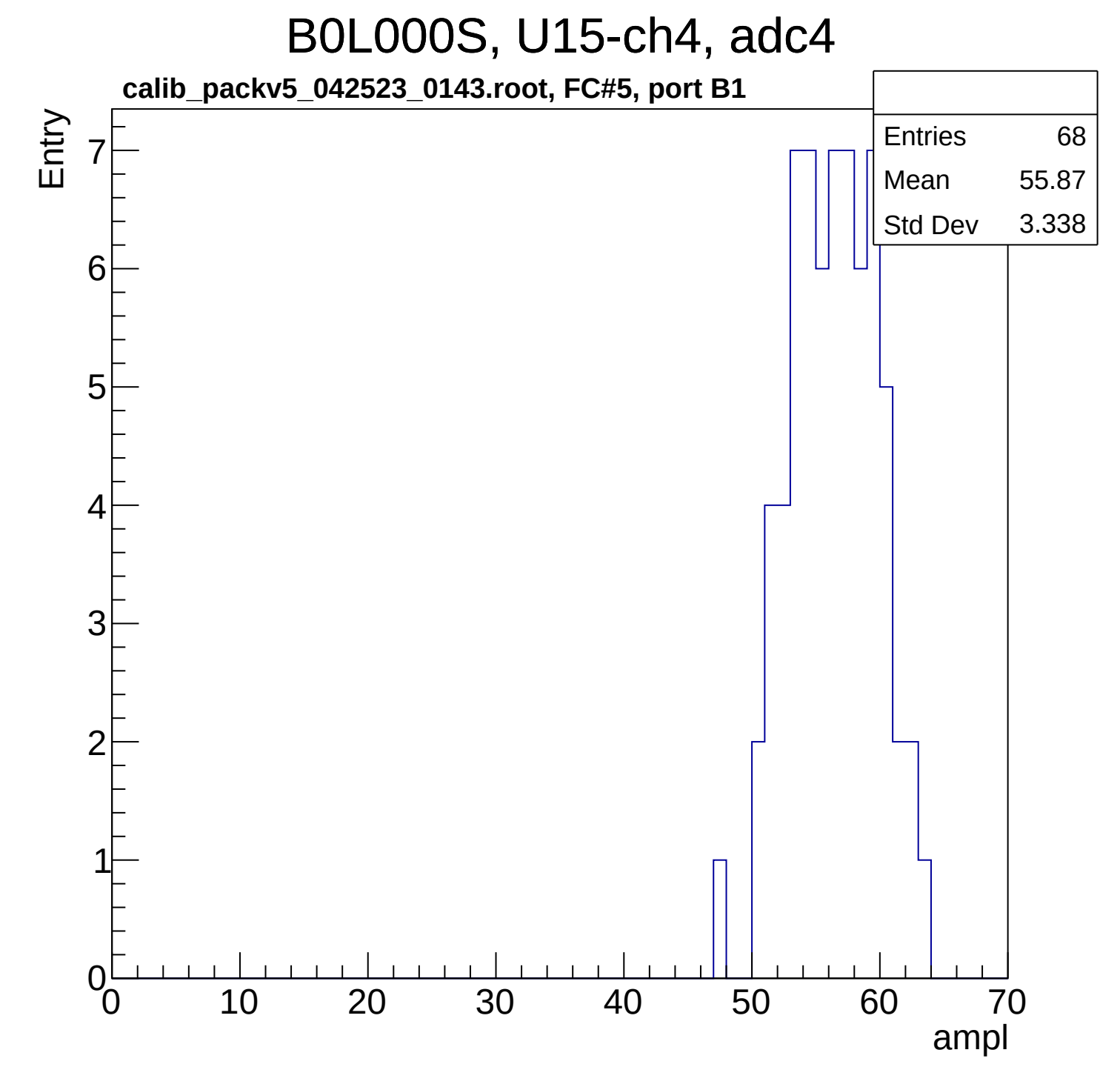
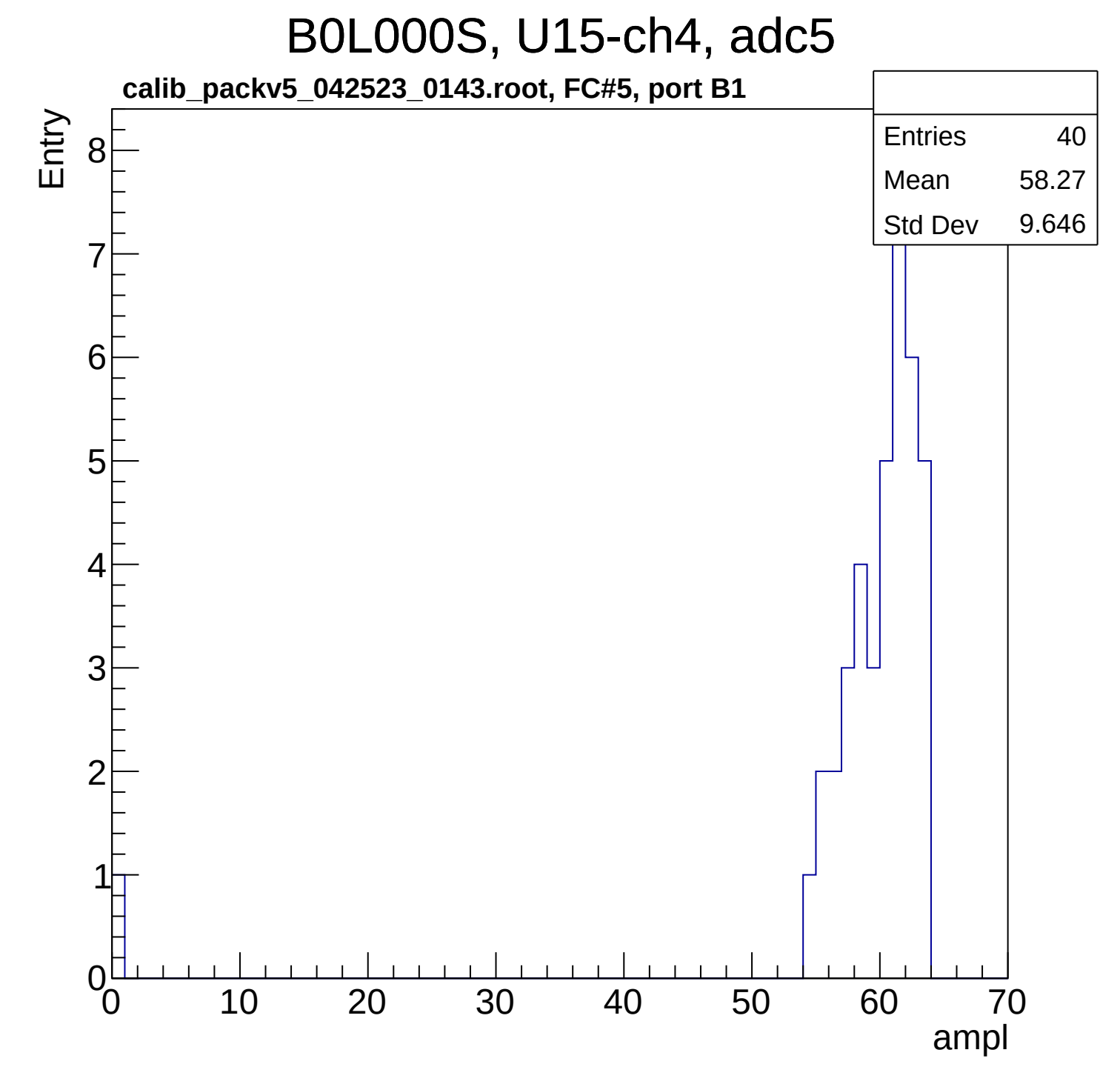
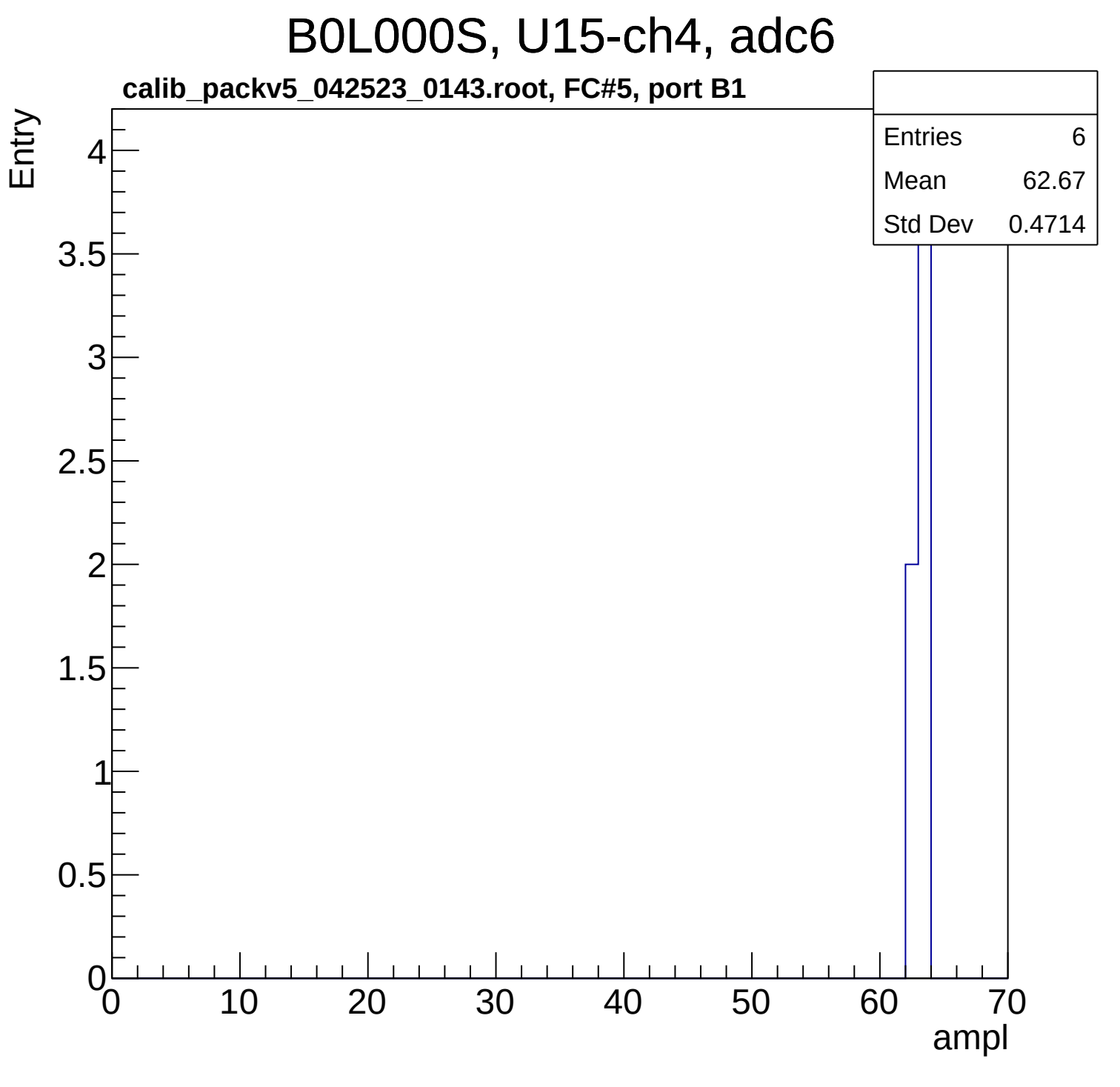

B0L000S, U15-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.82
Std Dev 3.181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.87
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.27
Std Dev 9.646
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
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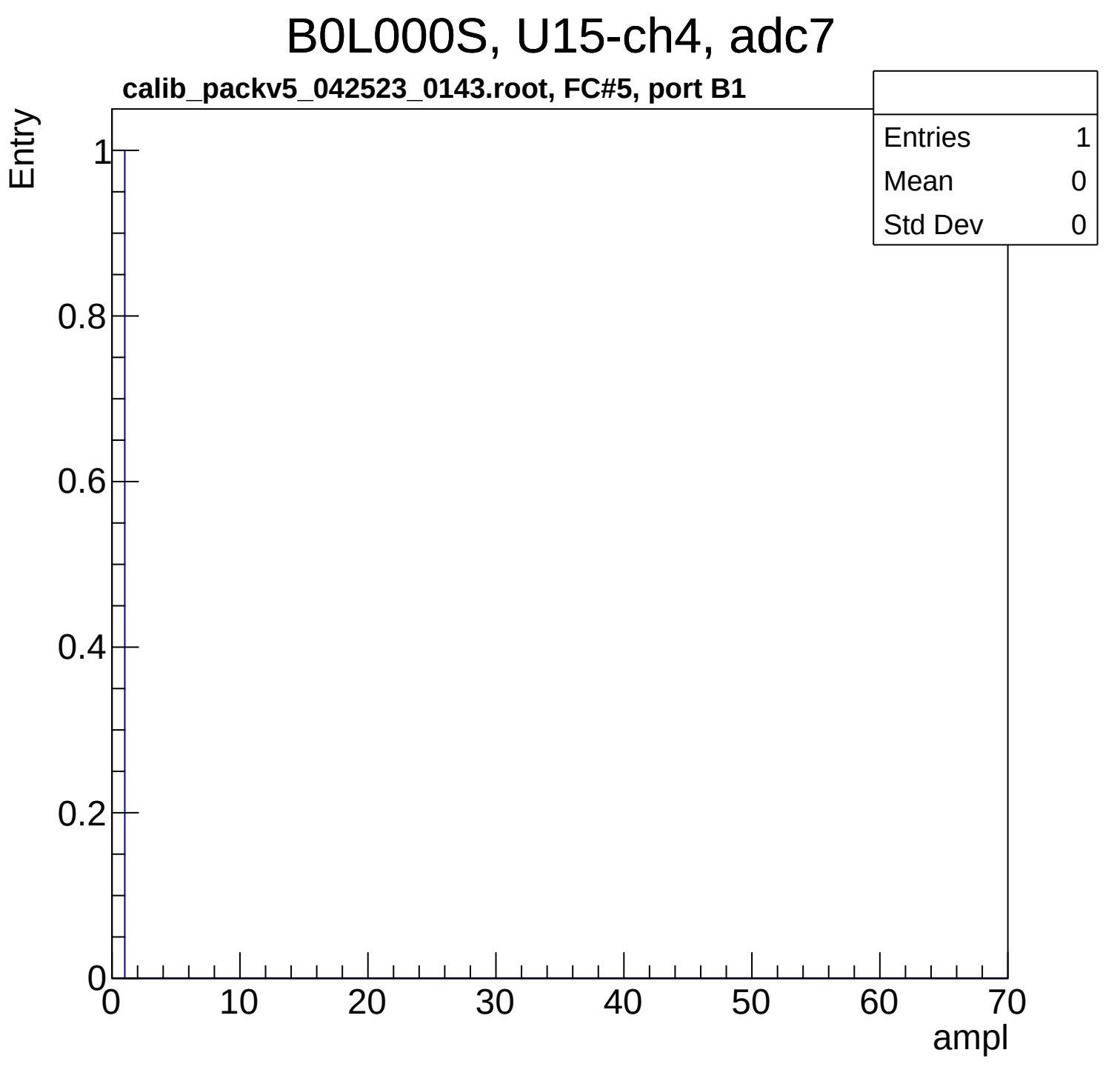
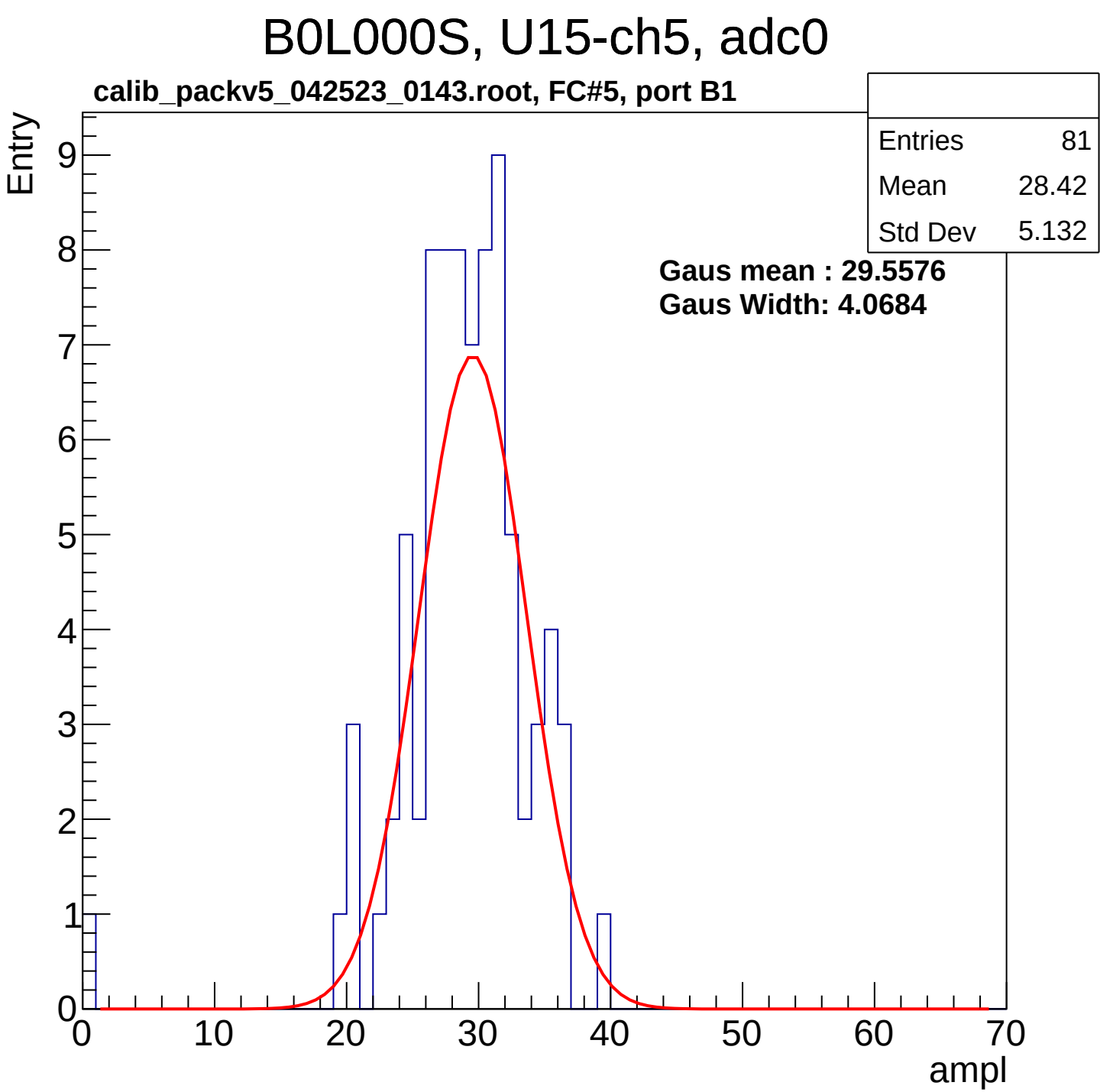
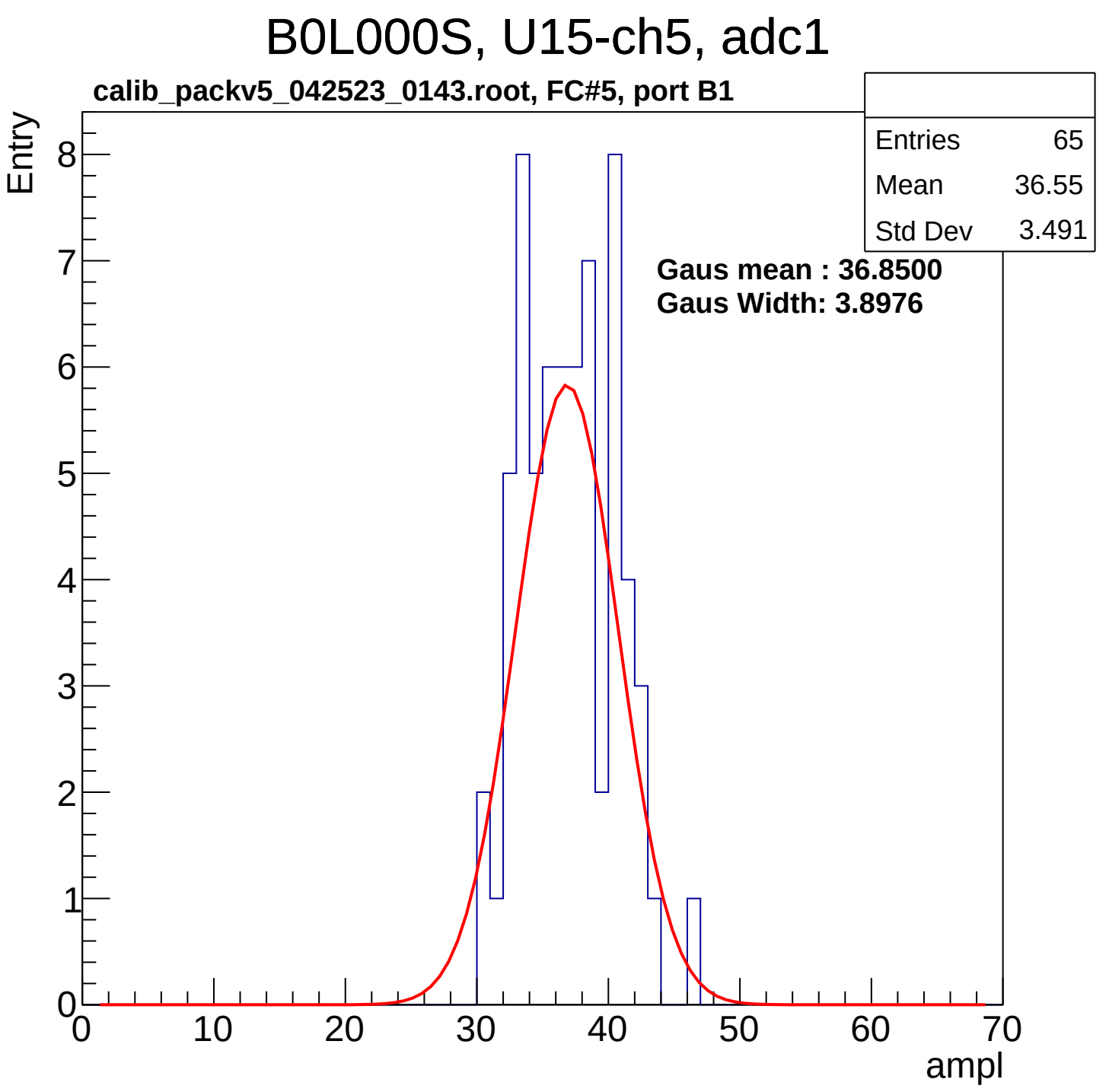
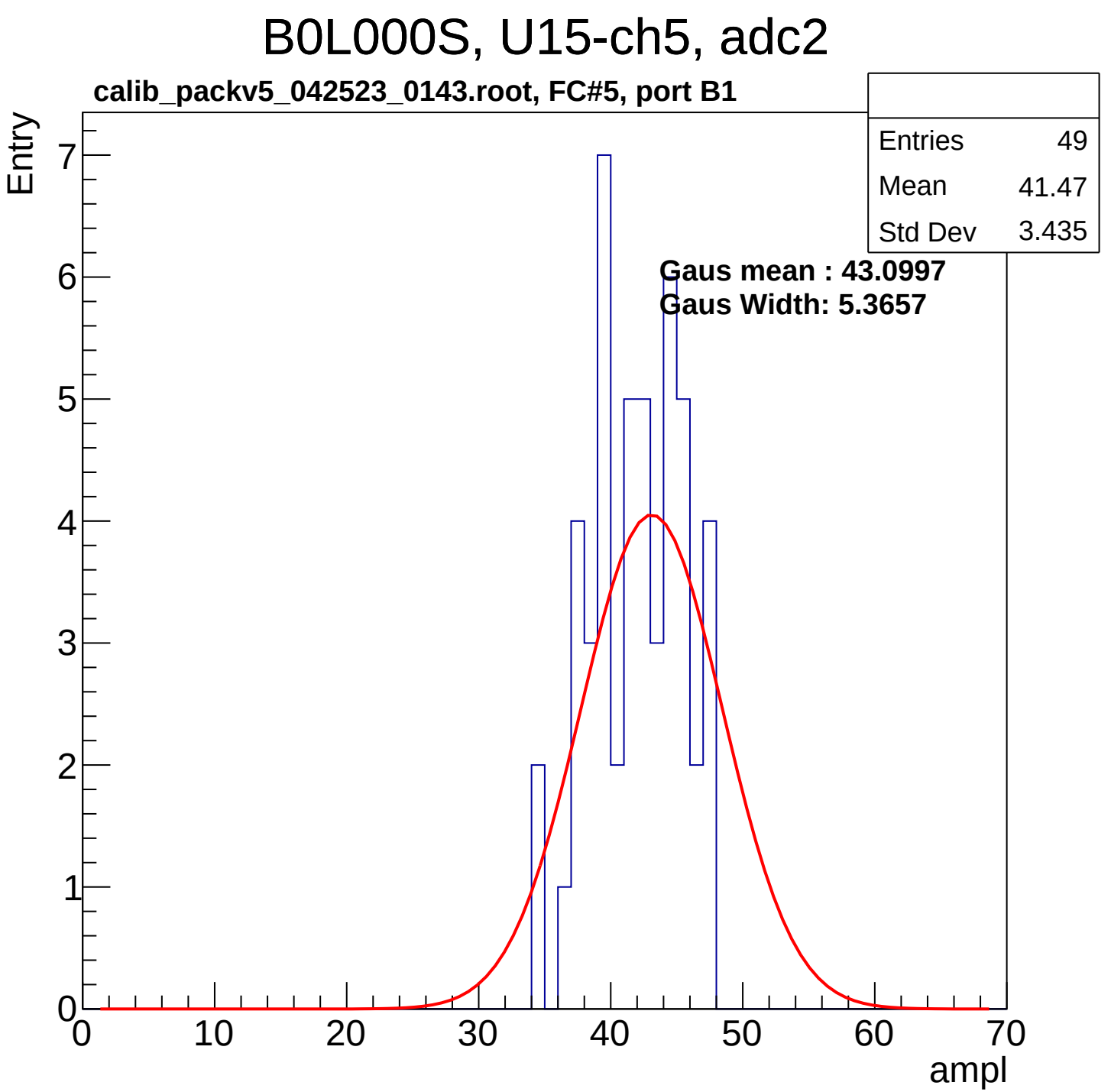
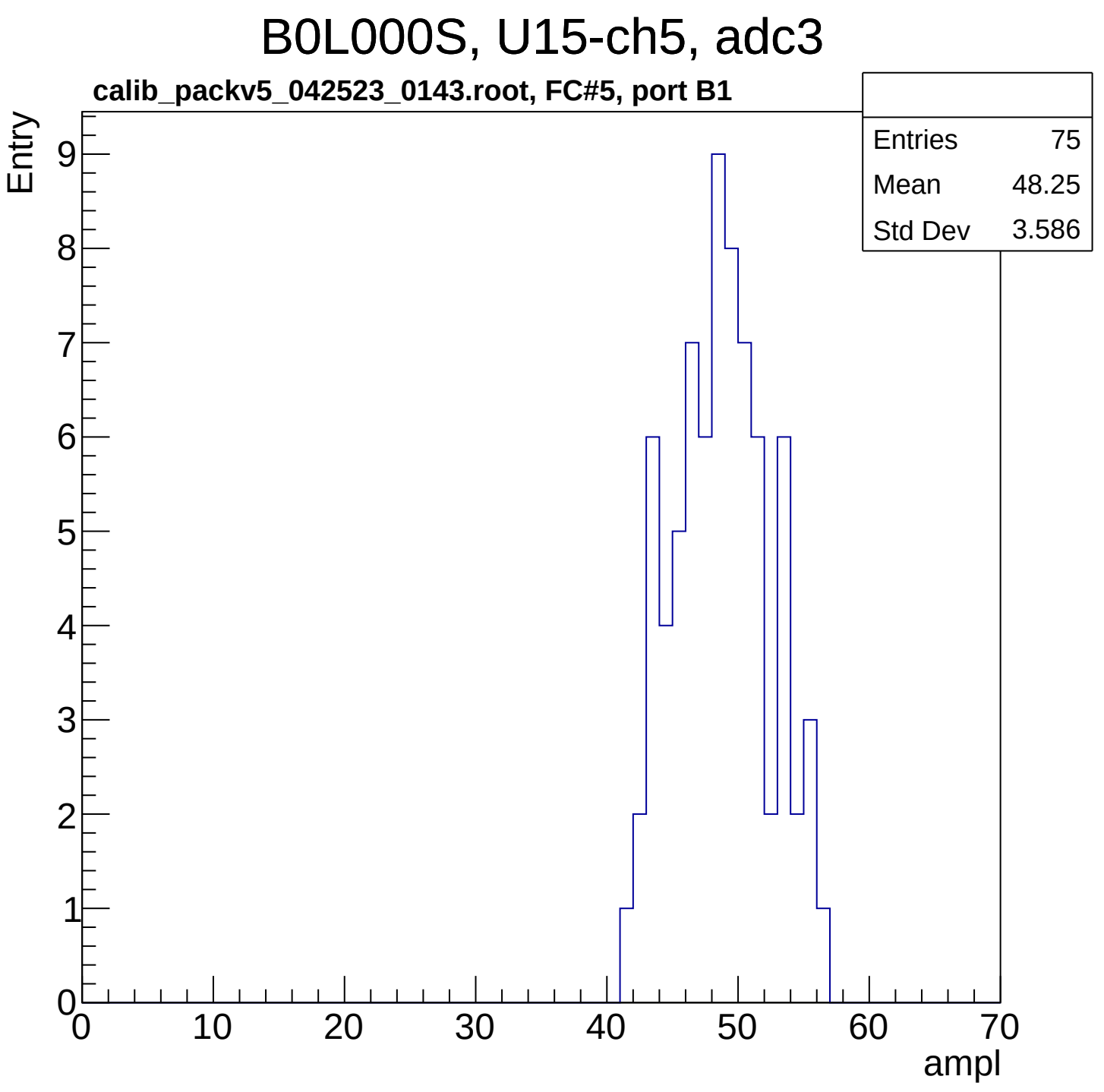
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

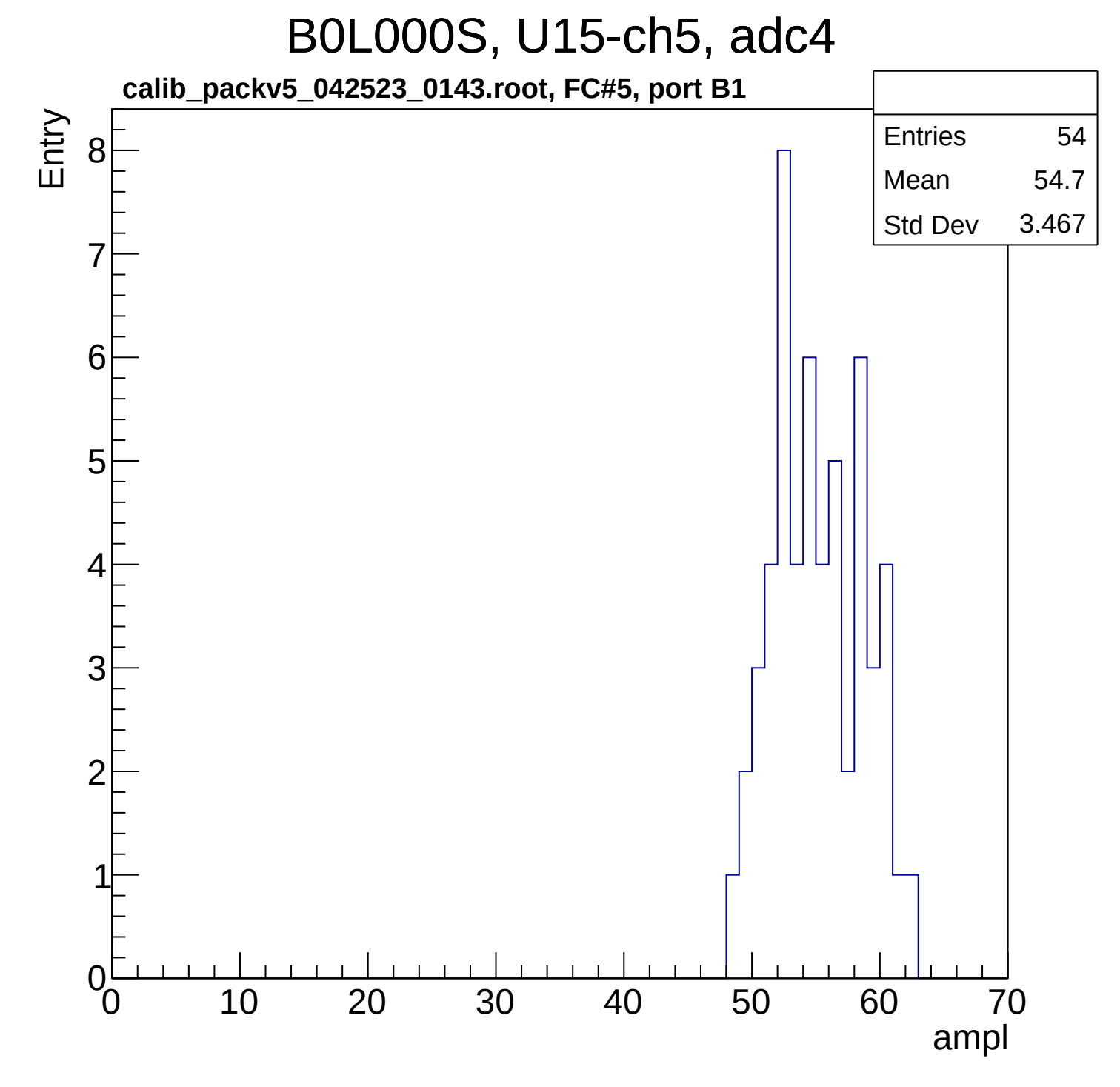

B0L000S, U15-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

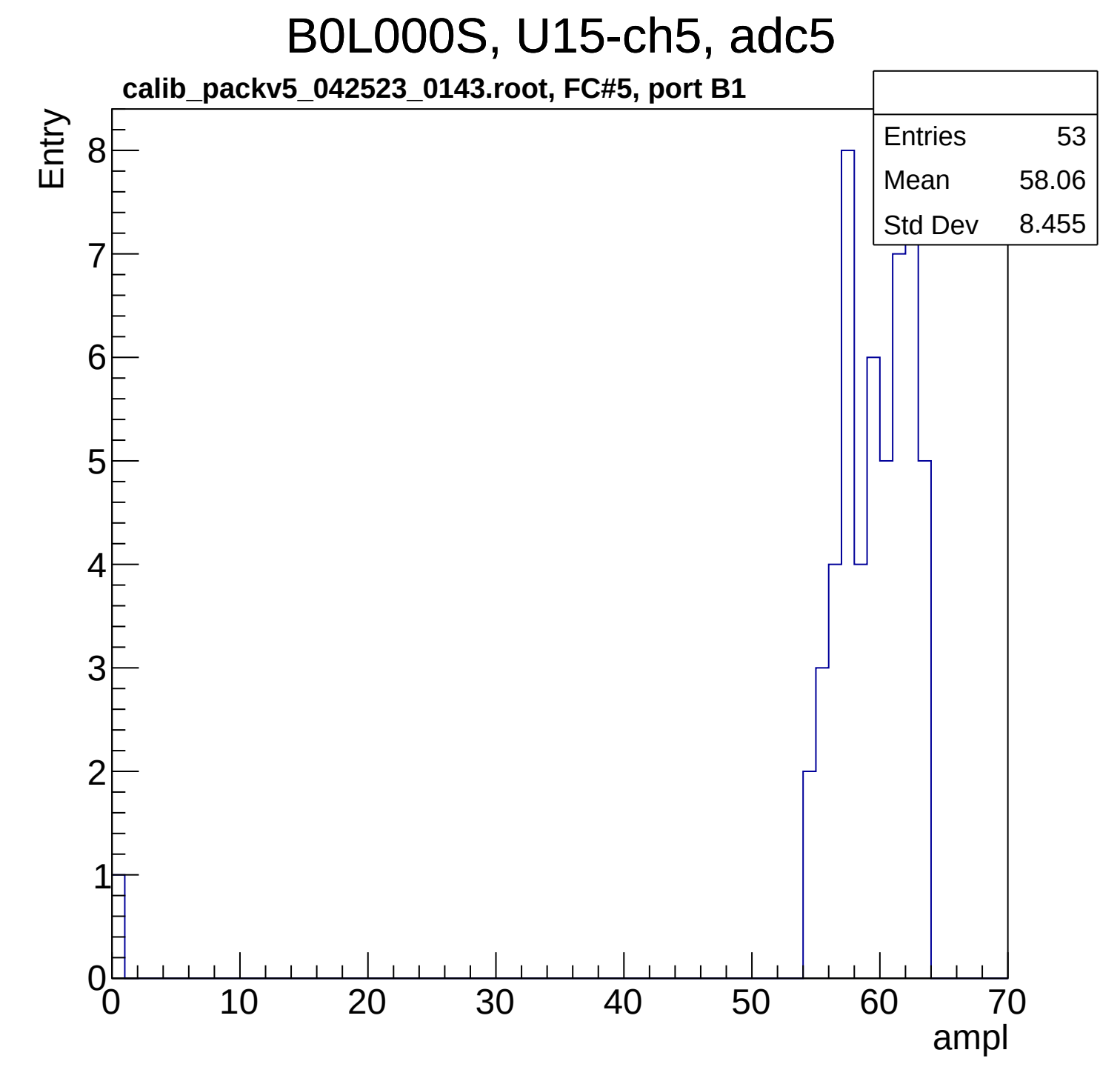

B0L000S, U15-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 28.42
Std Dev 5.132
Gaus mean : 29.5576
Gaus Width: 4.0684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

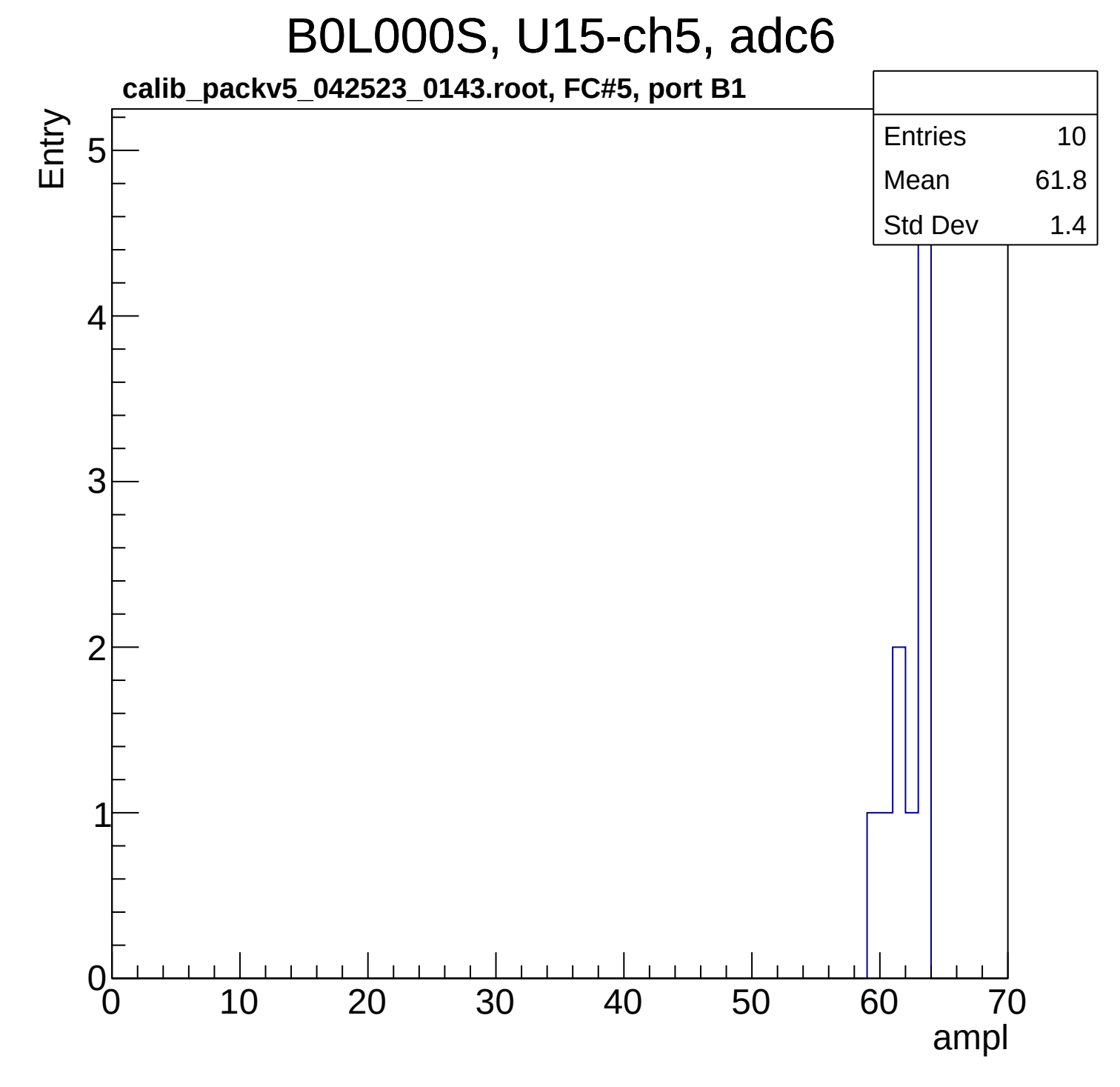

B0L000S, U15-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.55
Std Dev 3.491
Gaus mean : 36.8500
Gaus Width: 3.8976
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

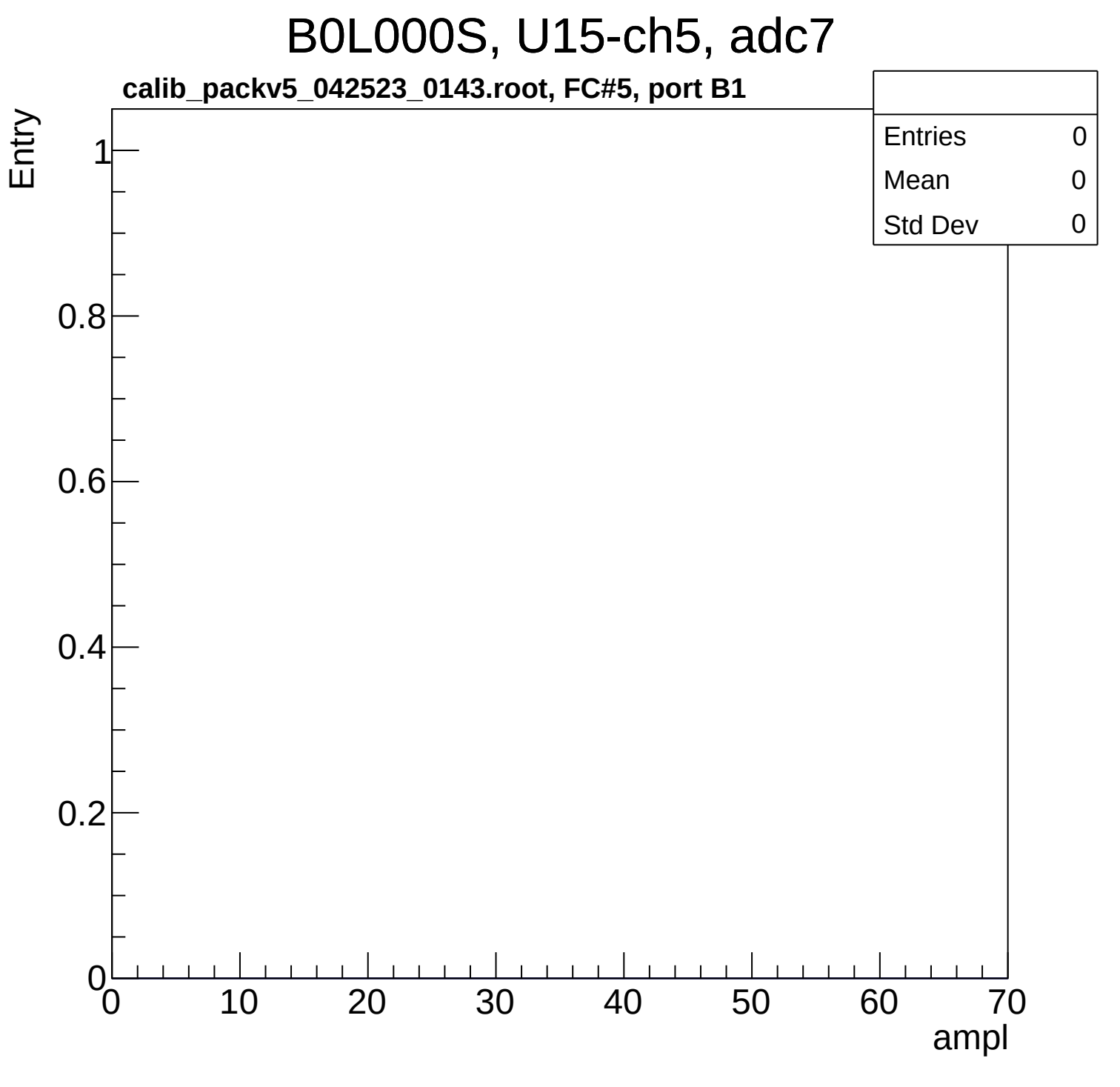

B0L000S, U15-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 41.47
Std Dev 3.435
Gaus mean : 43.0997
Gaus Width: 5.3657
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

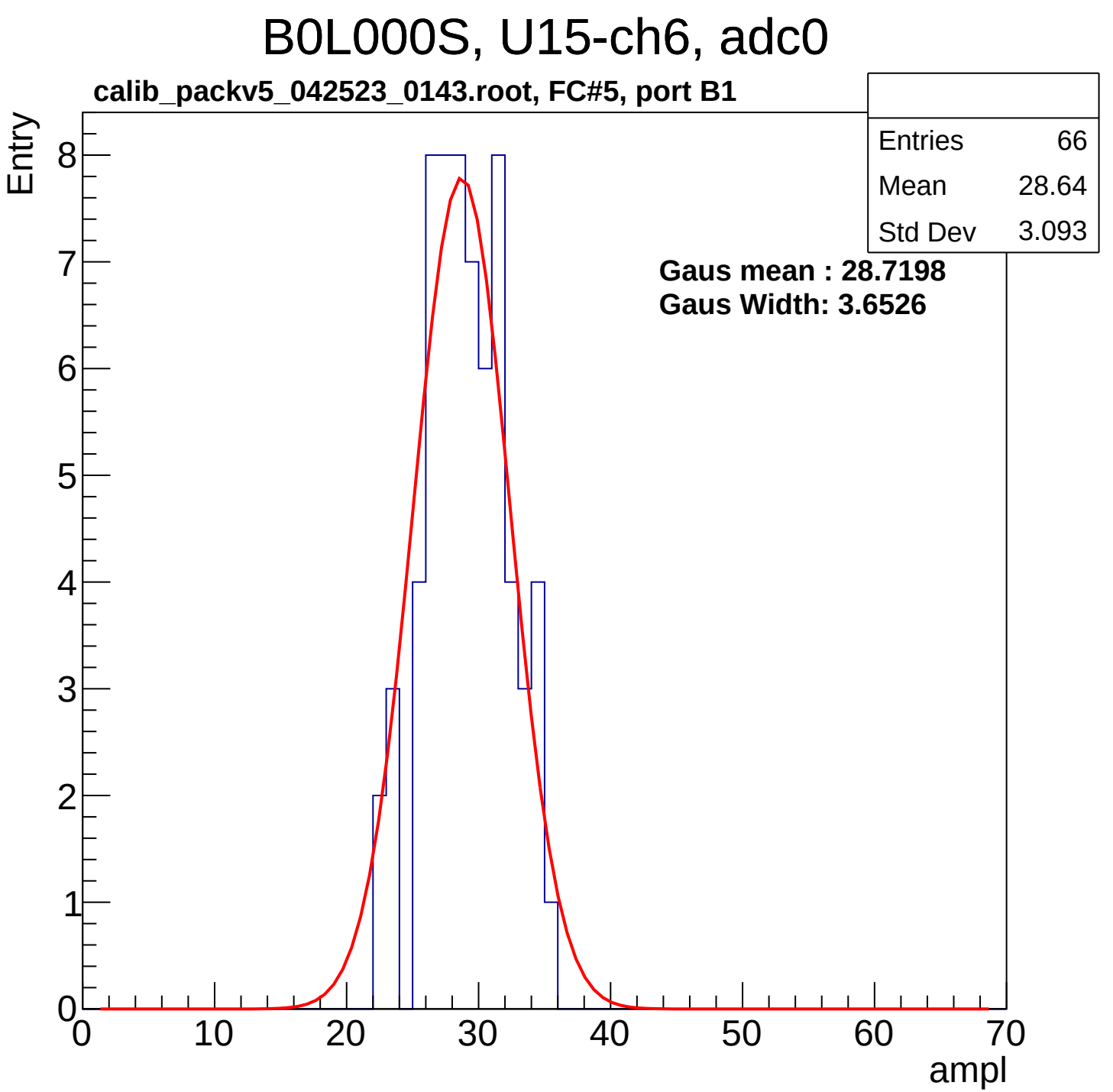

B0L000S, U15-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.25
Std Dev 3.586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.7
Std Dev 3.467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.06
Std Dev 8.455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.8
Std Dev 1.4
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.64
Std Dev 3.093
Gaus mean : 28.7198
Gaus Width: 3.6526
Entry
ampl
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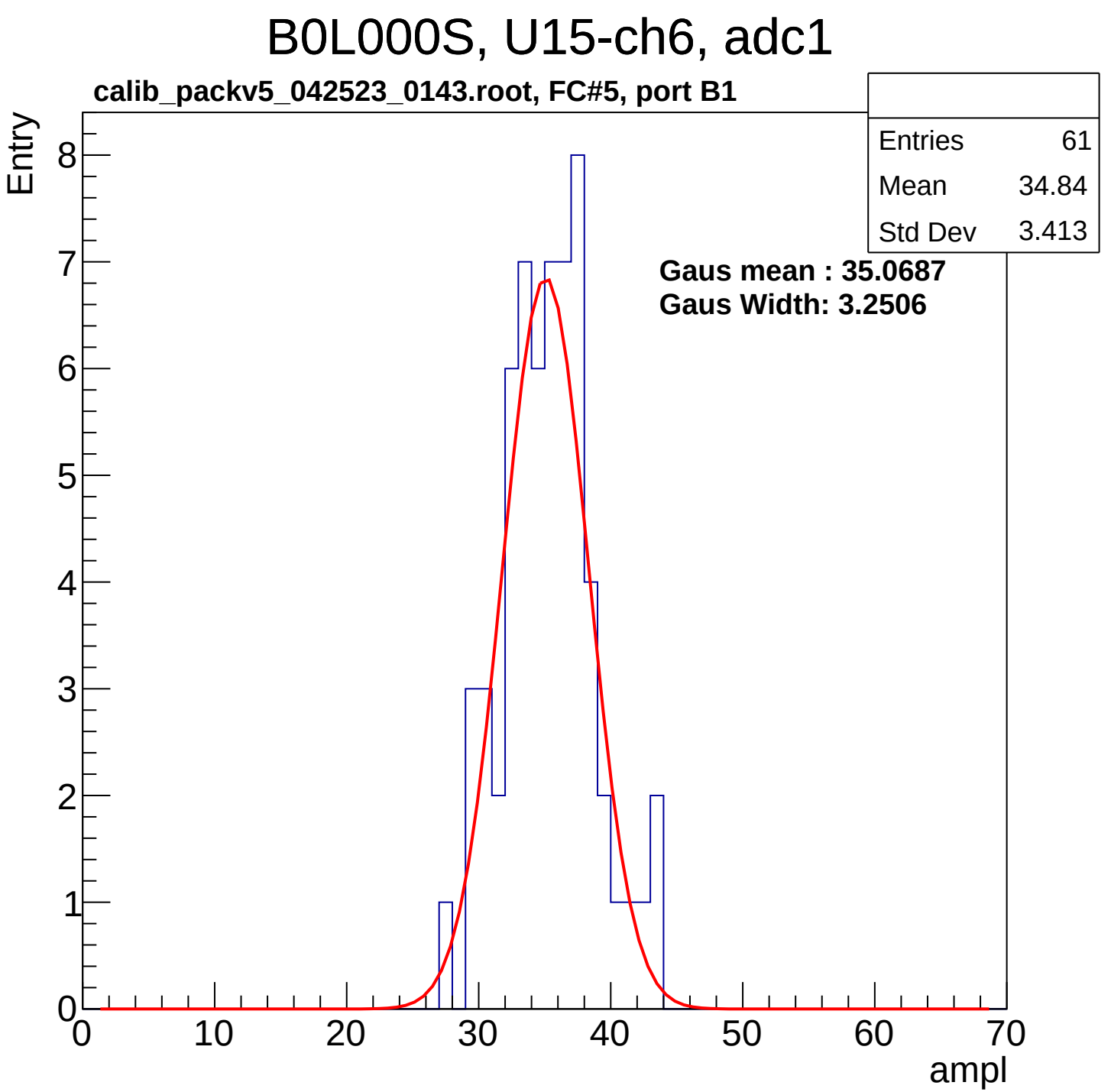
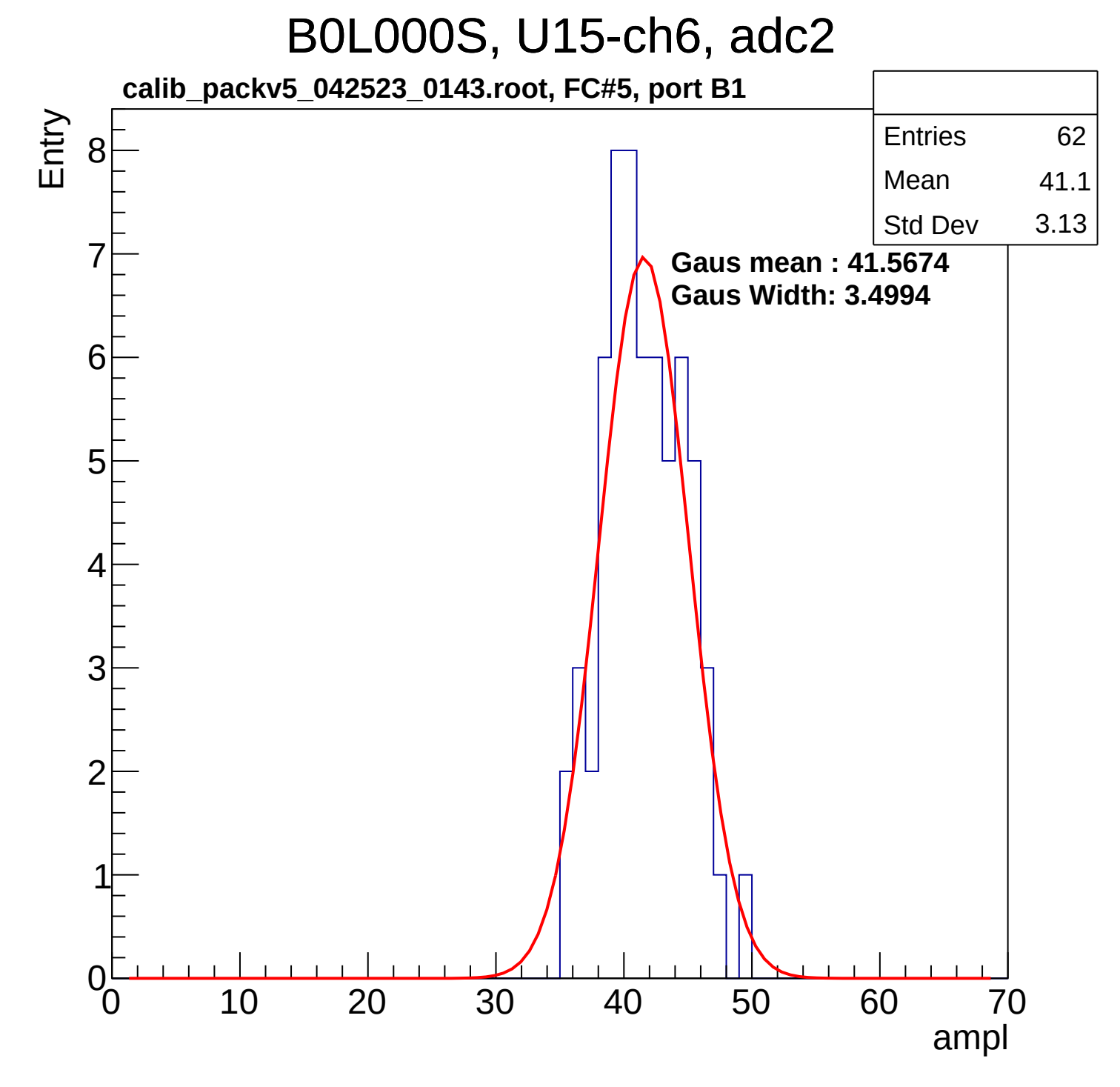
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.84
Std Dev 3.413
Gaus mean : 35.0687
Gaus Width: 3.2506
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.1
Std Dev 3.13
Gaus mean : 41.5674
Gaus Width: 3.4994
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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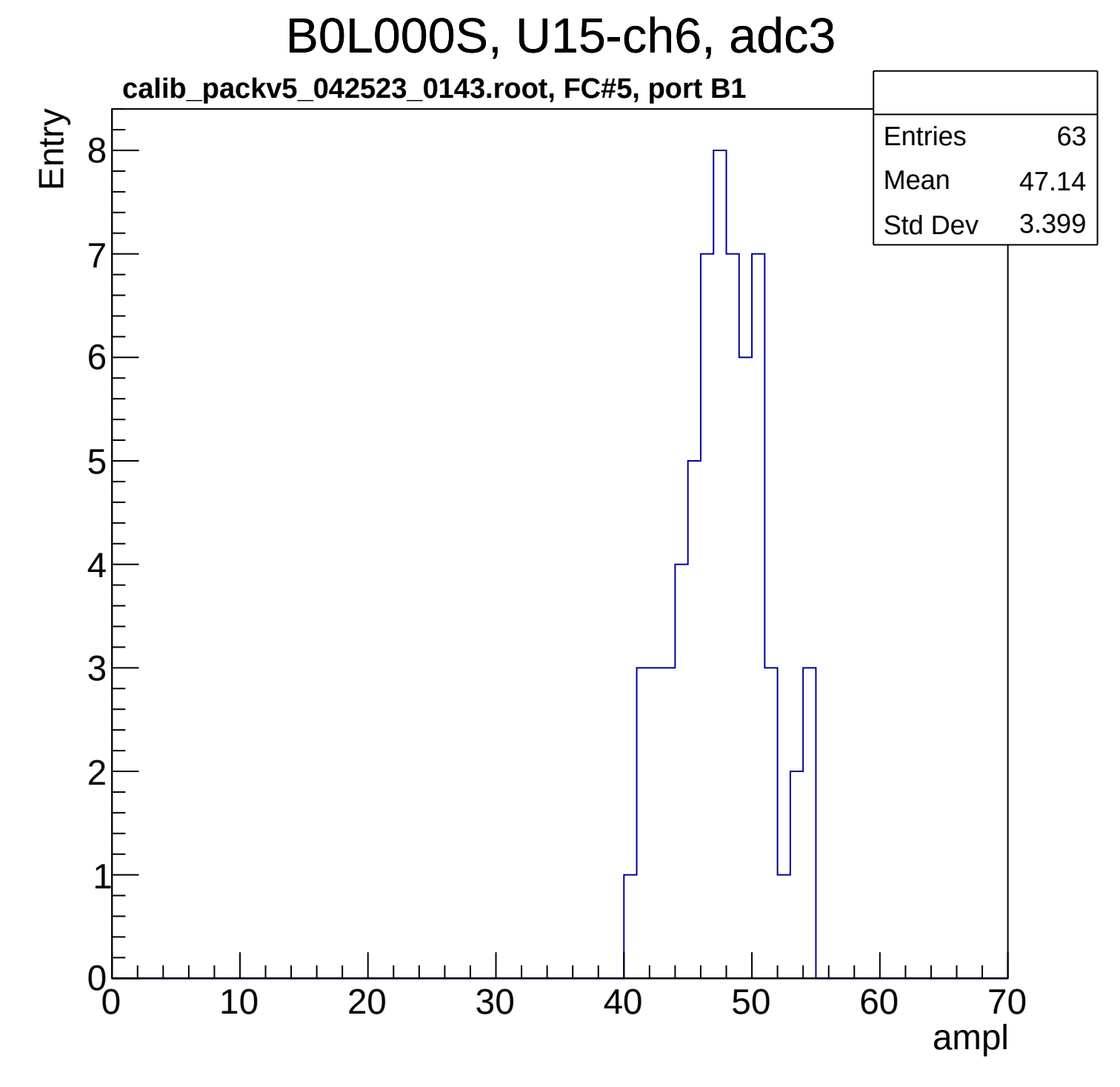
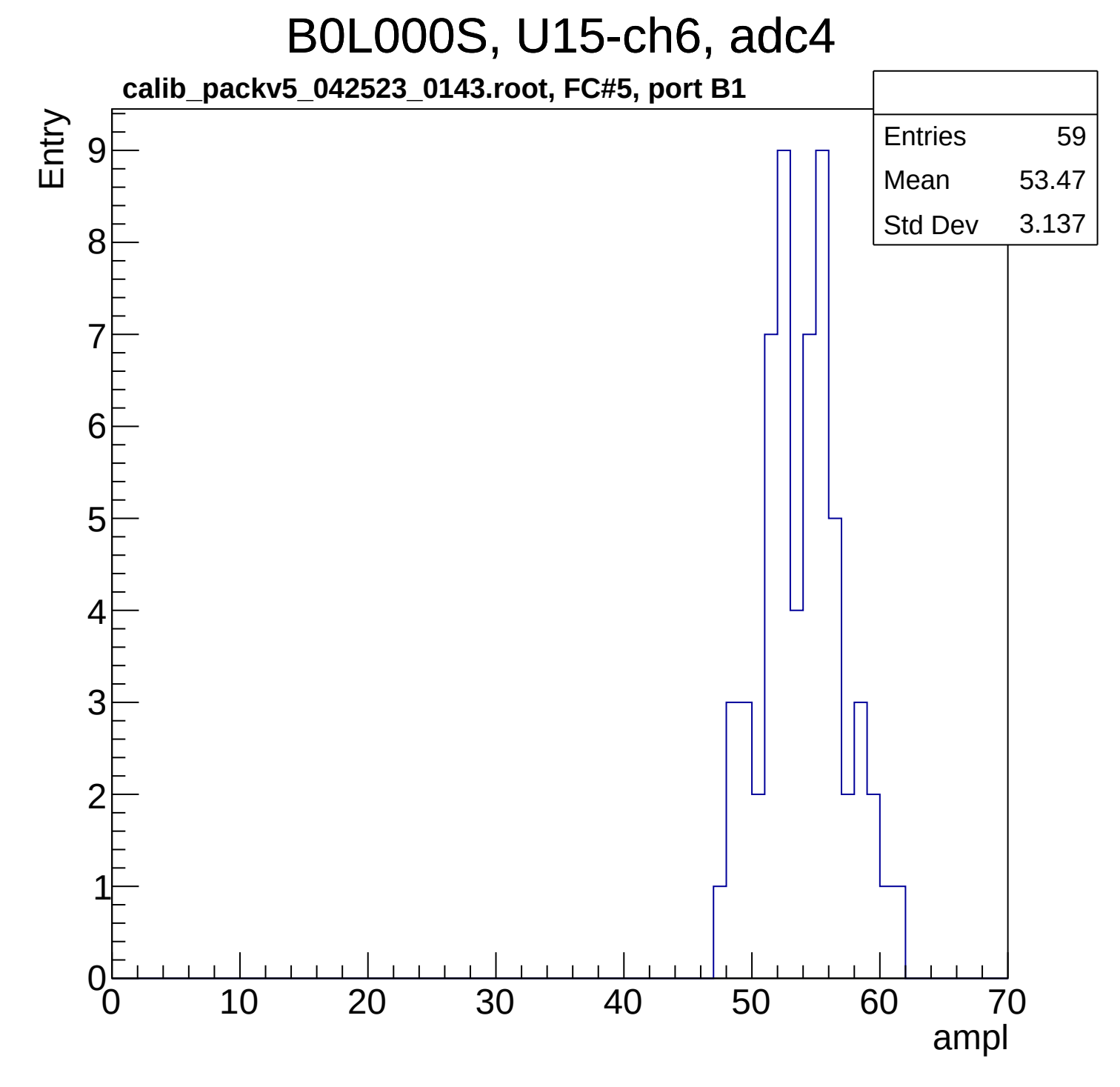
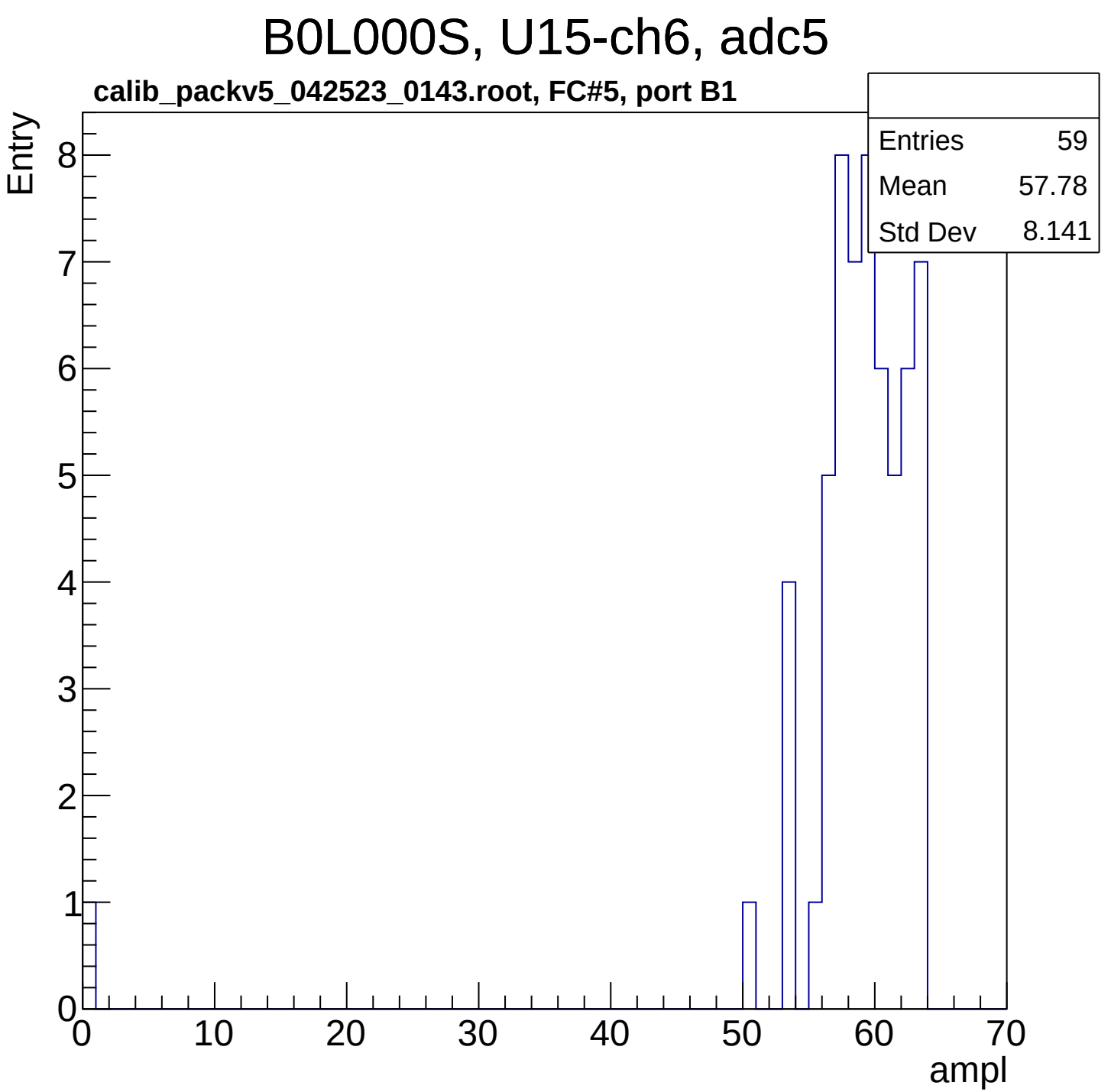
40
50
60
70


B0L000S, U15-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 47.14
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.47
Std Dev 3.137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.78
Std Dev 8.141
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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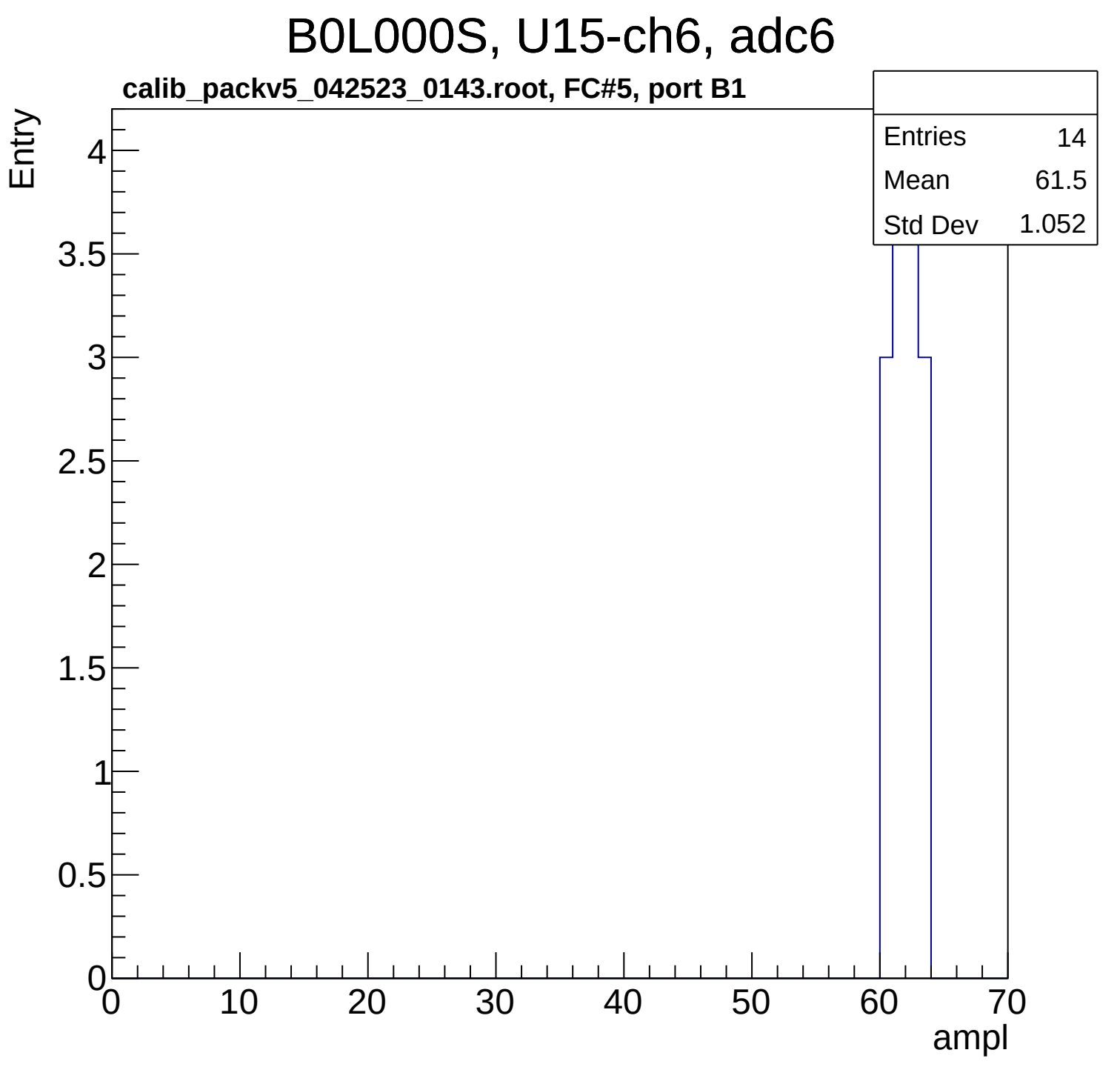
B0L000S, U15-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.5
Std Dev 1.052
Entry
ampl
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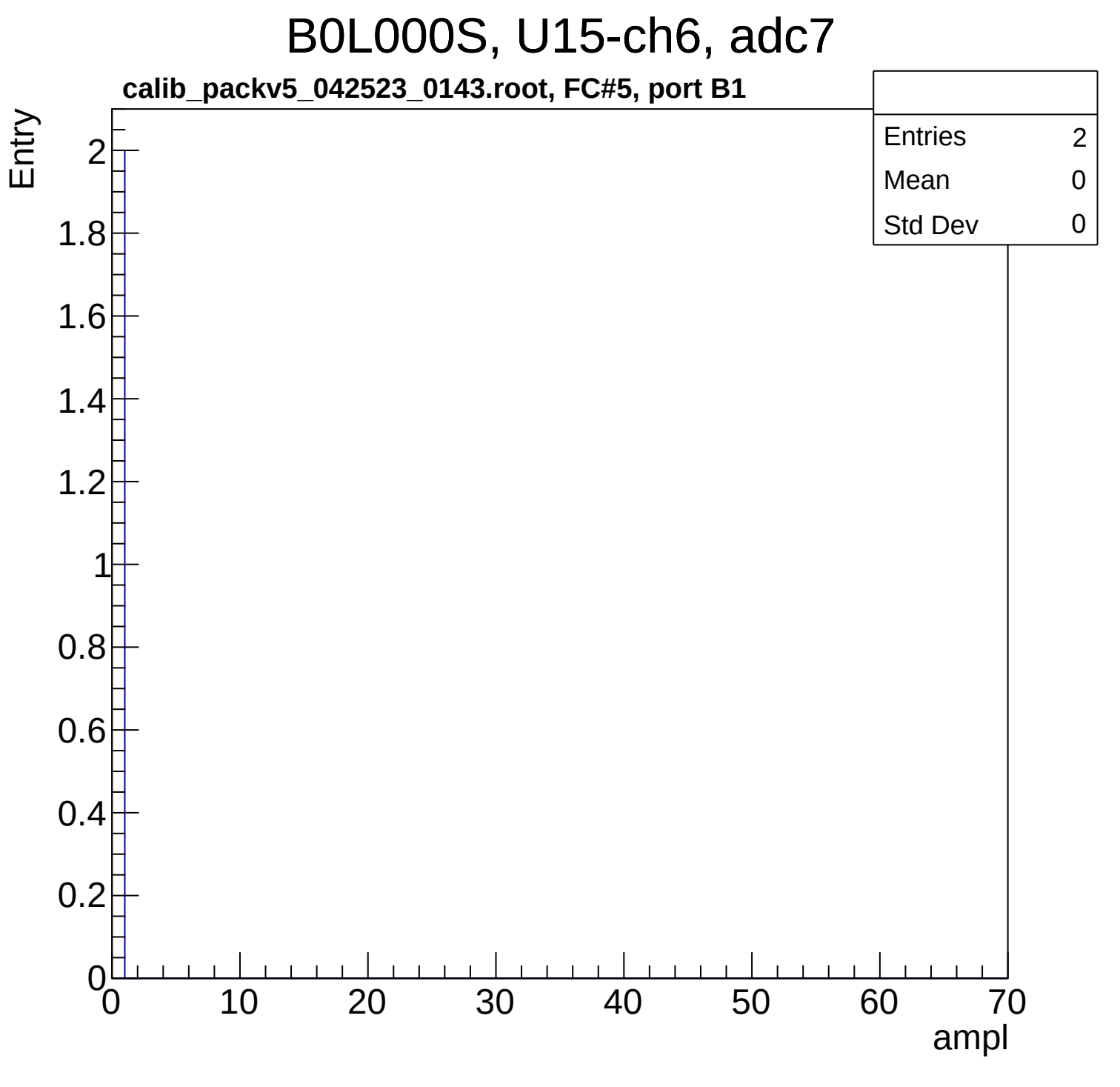
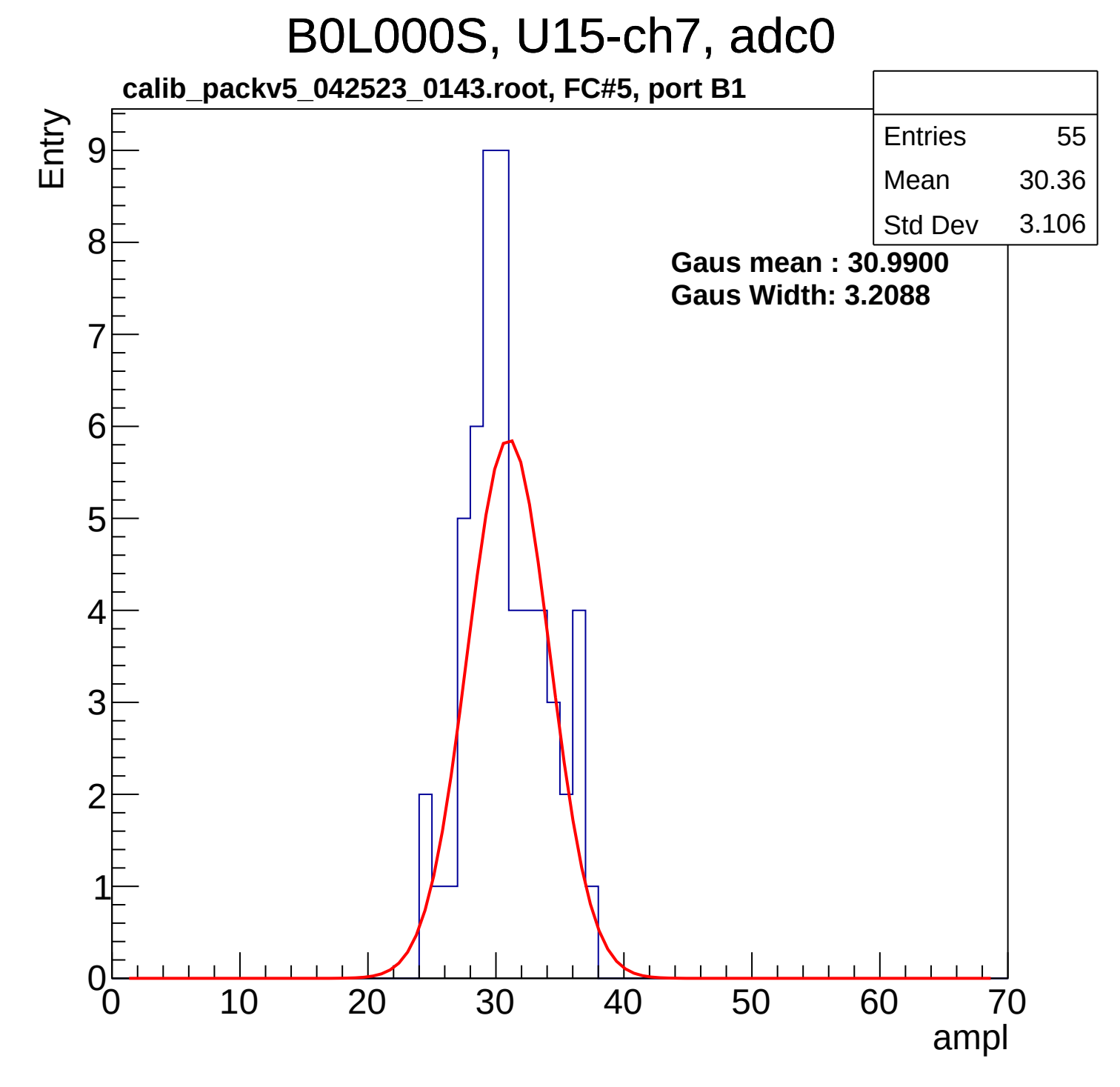
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.36
Std Dev 3.106
Gaus mean : 30.9900
Gaus Width: 3.2088
Entry
ampl
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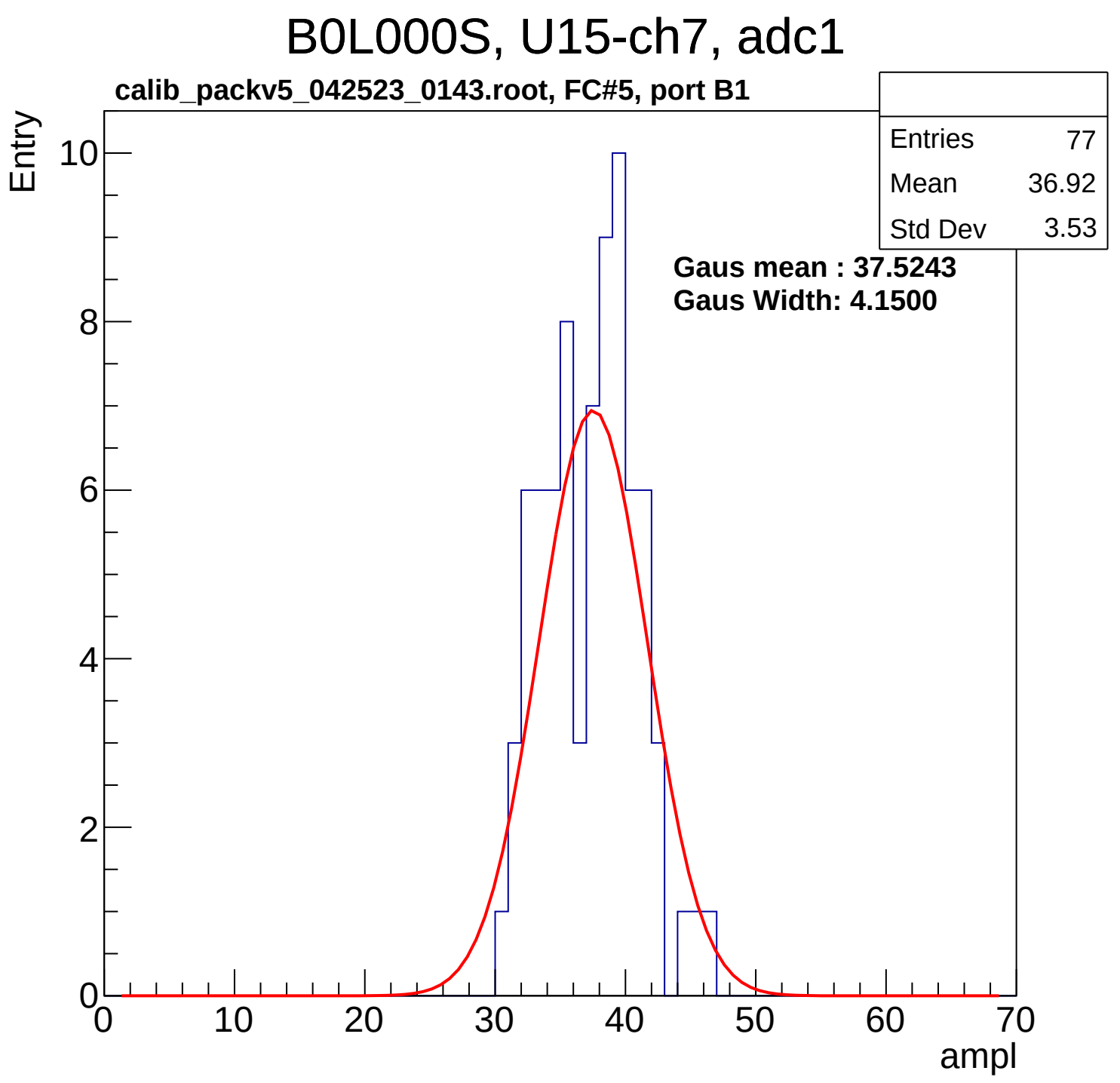
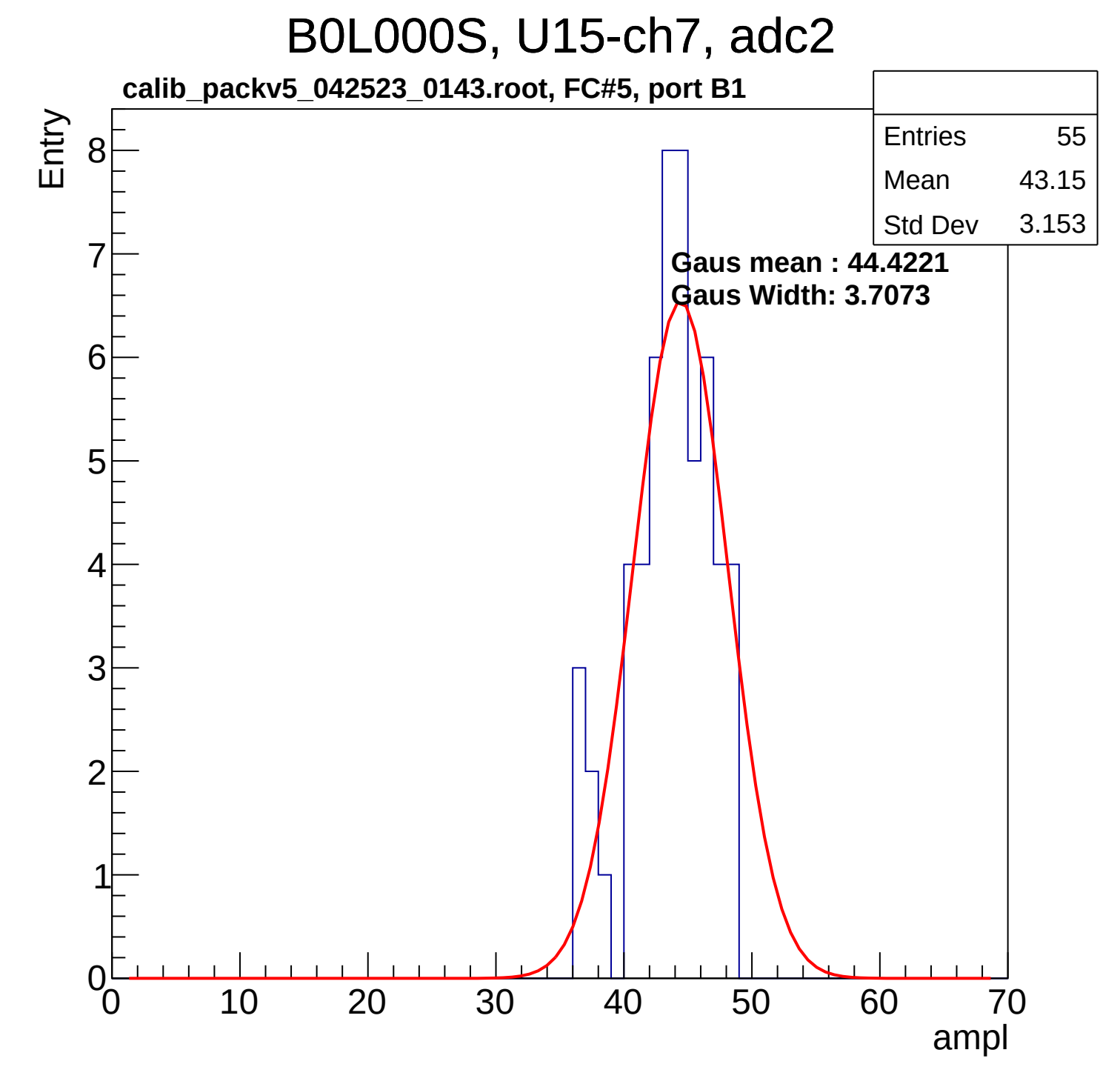
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.92
Std Dev 3.53
Gaus mean : 37.5243
Gaus Width: 4.1500
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.15
Std Dev 3.153
Gaus mean : 44.4221
Gaus Width: 3.7073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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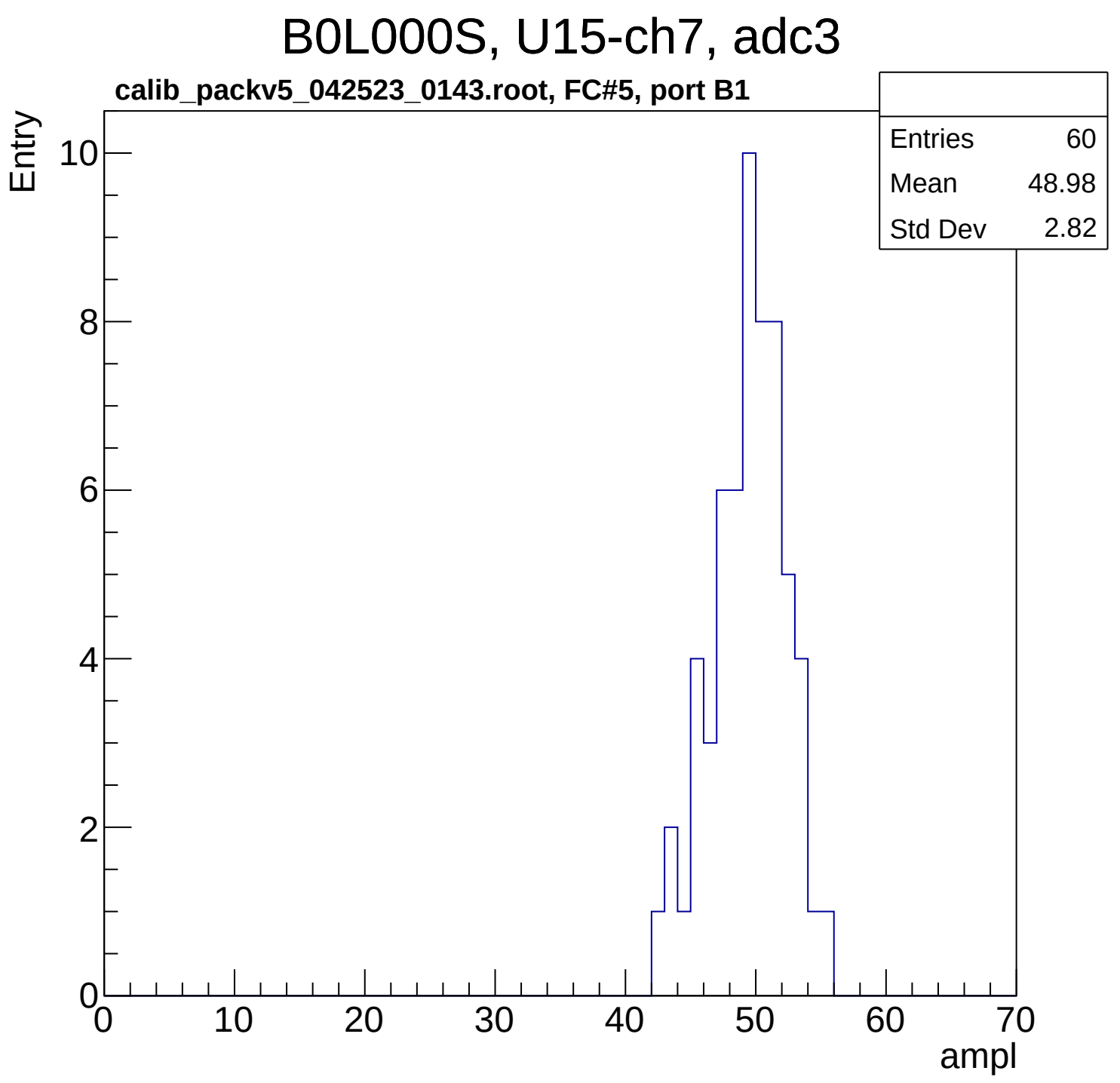
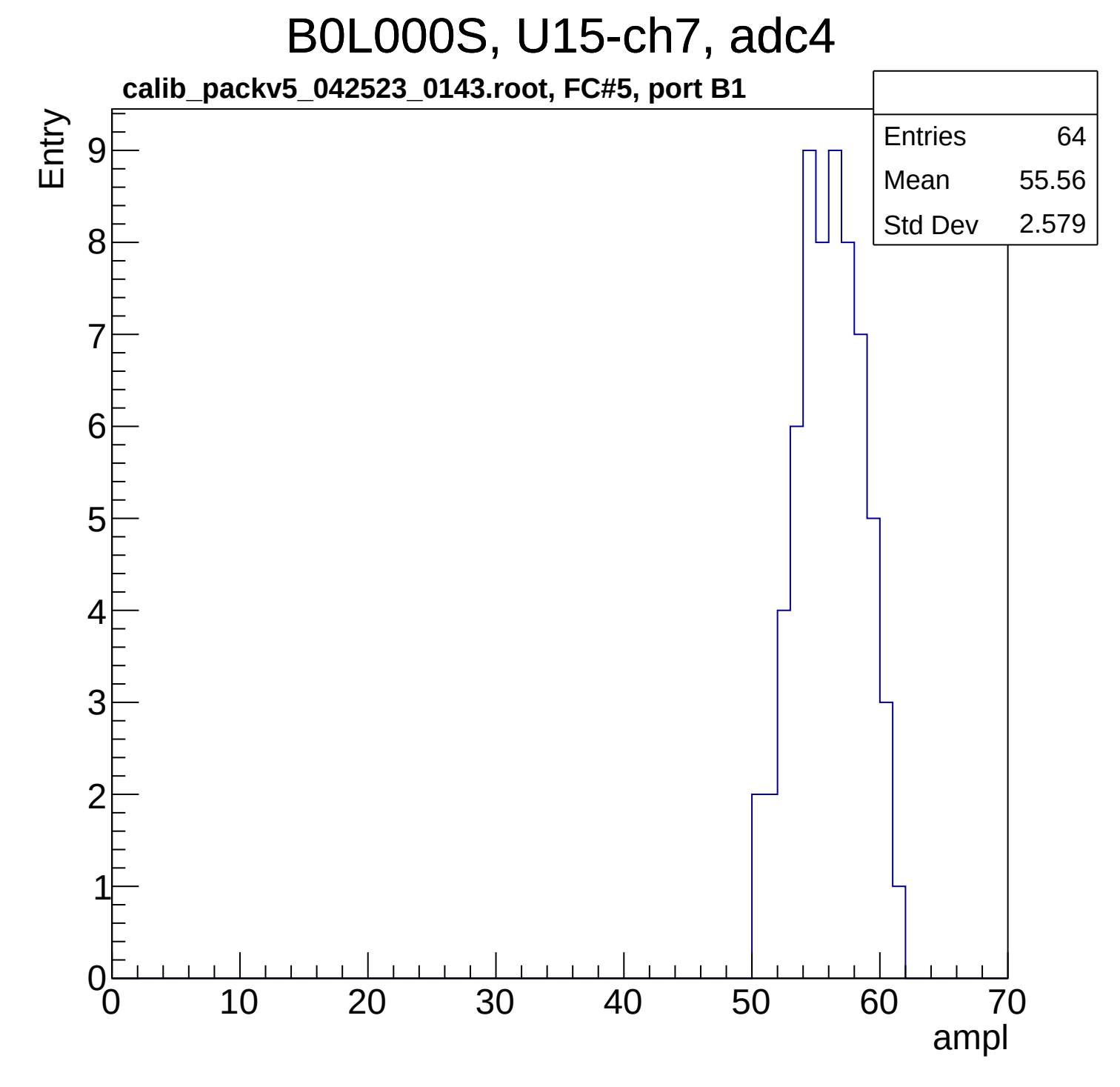

B0L000S, U15-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.98
Std Dev 2.82
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 55.56
Std Dev 2.579
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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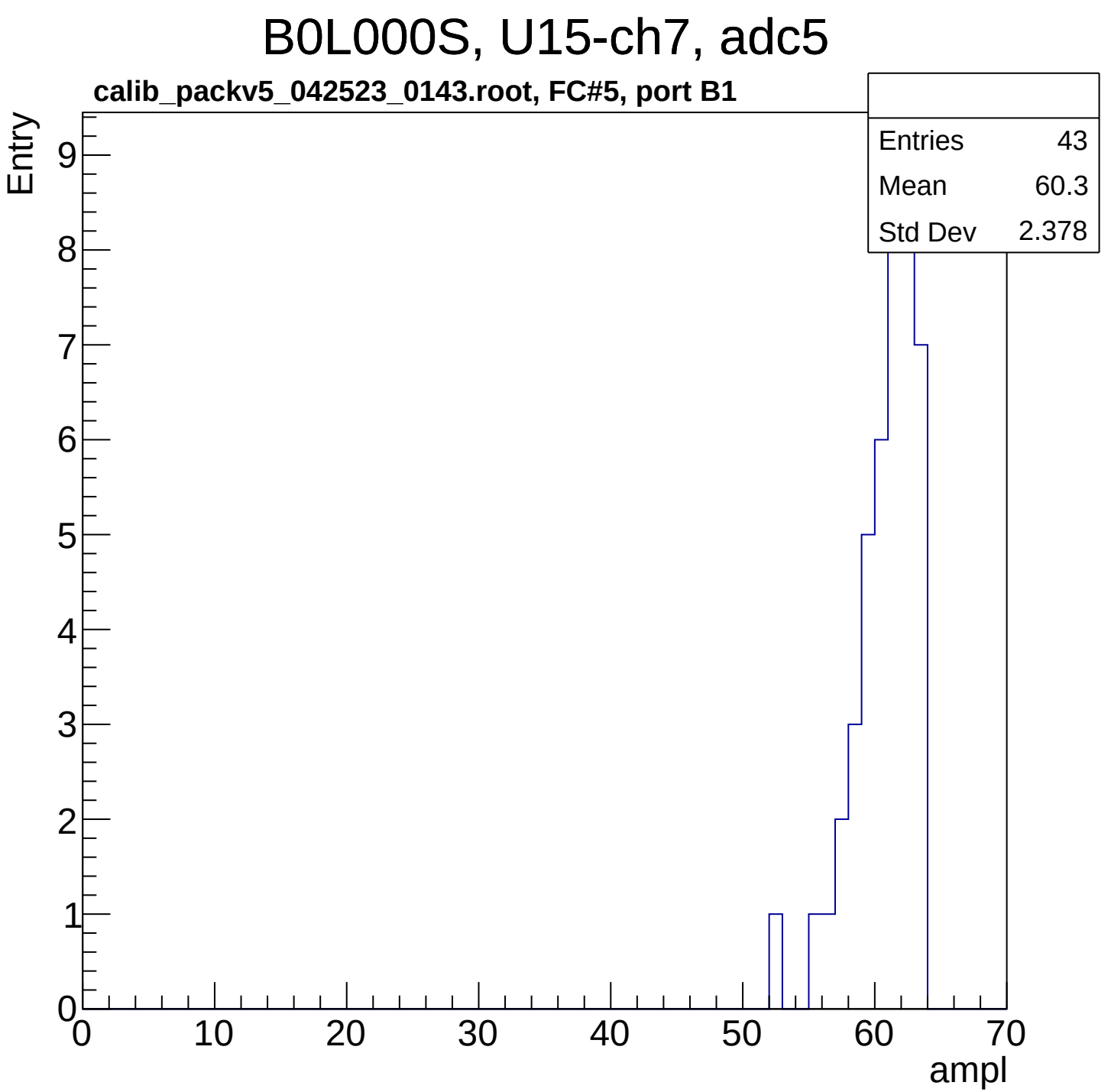
40
50
60
70


B0L000S, U15-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.3
Std Dev 2.378
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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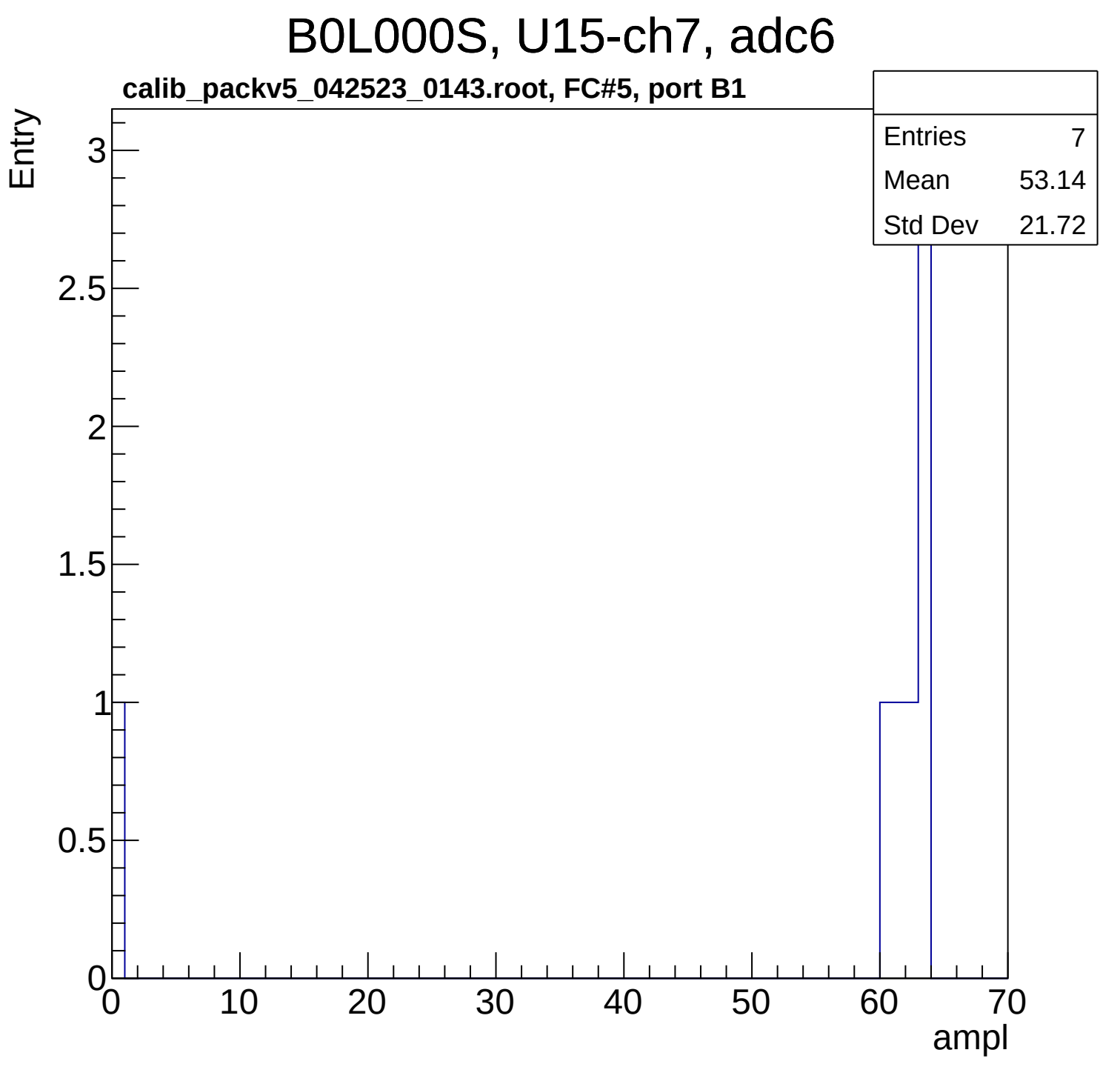
60
70


B0L000S, U15-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
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0.5
1
1.5
2
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0
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60
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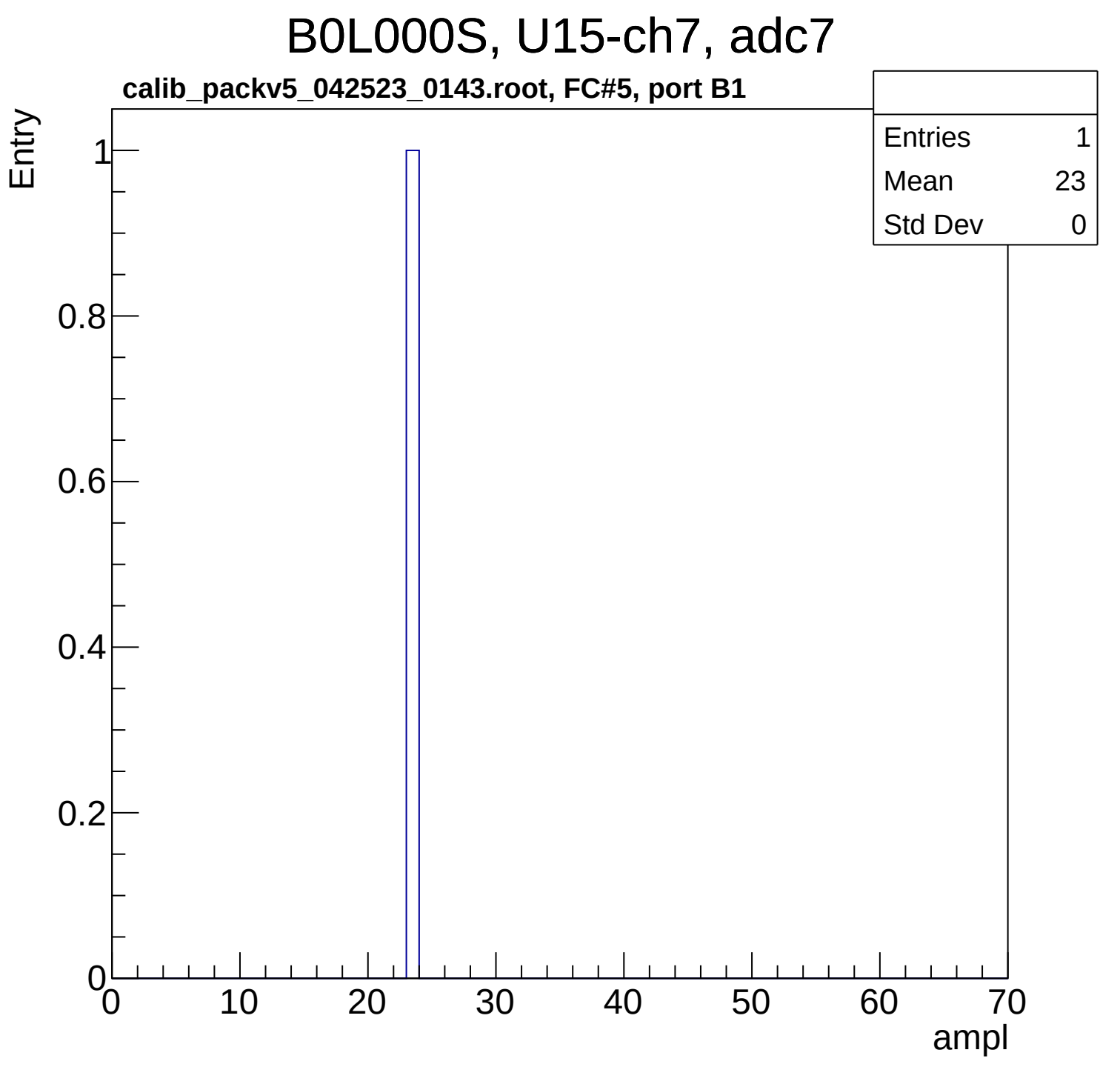
B0L000S, U15-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
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0
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10
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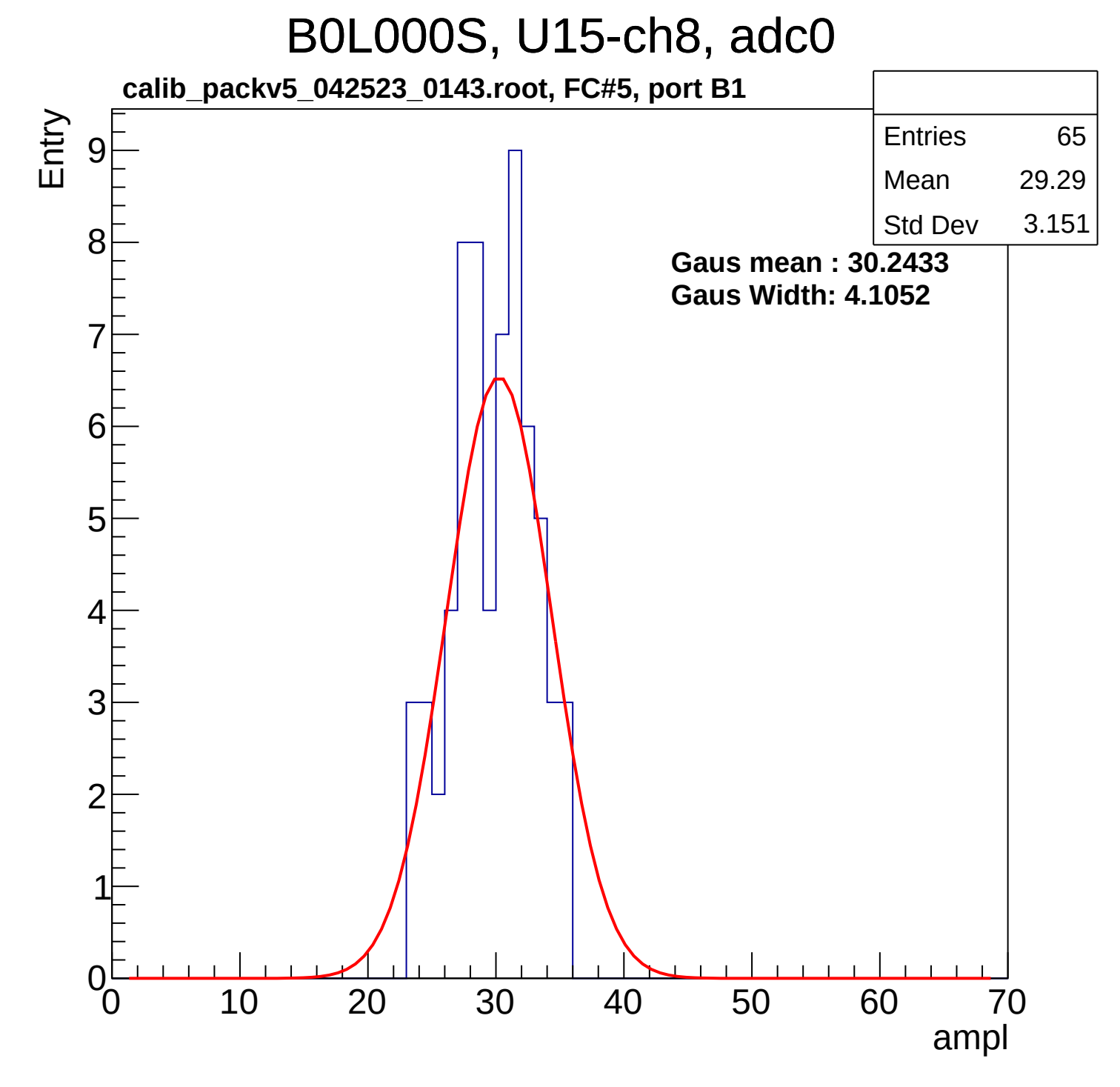
50
60
70
ampl


B0L000S, U15-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.29
Std Dev 3.151
Gaus mean : 30.2433
Gaus Width: 4.1052
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
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60
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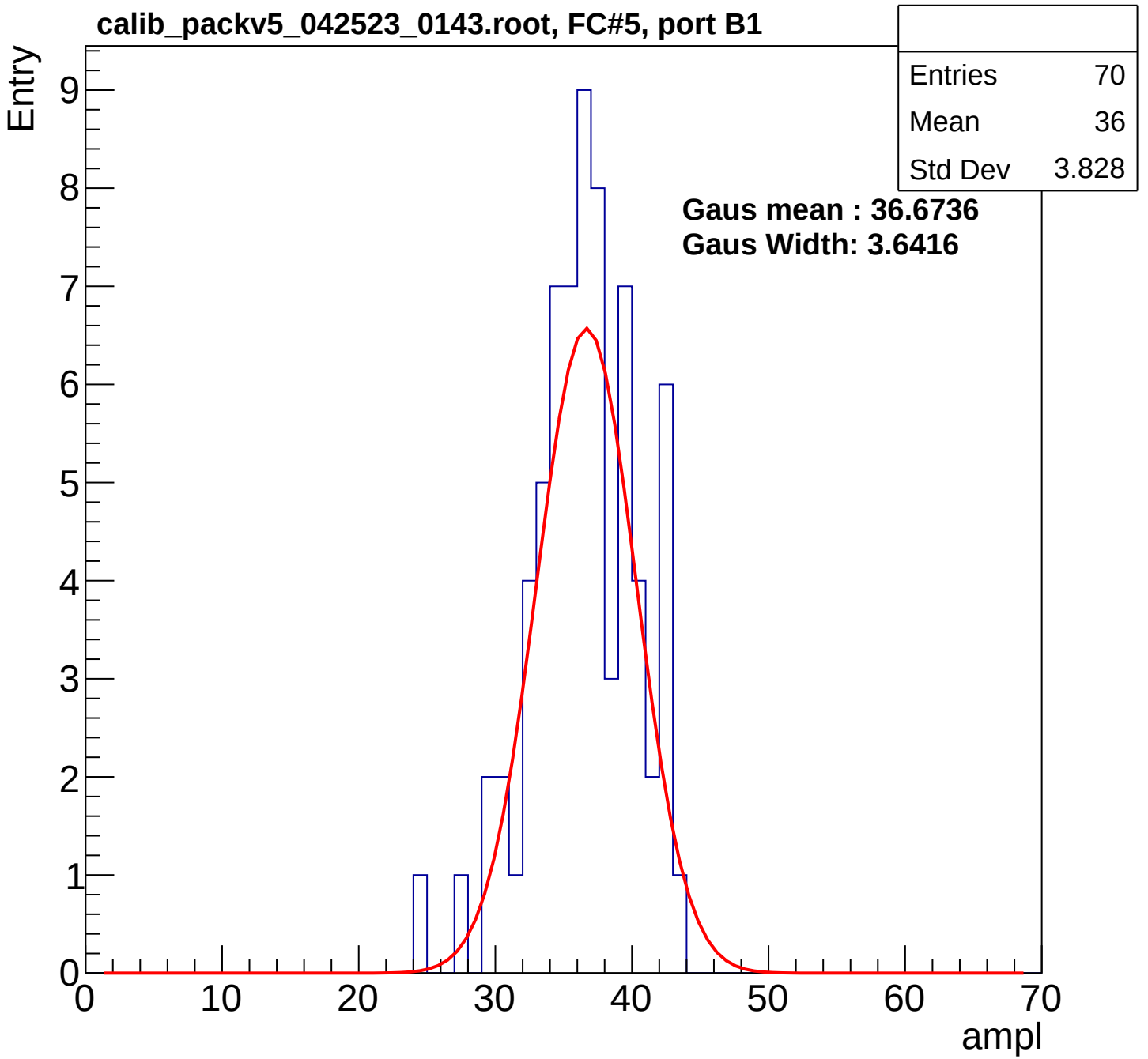
B0L000S, U15-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36
Std Dev 3.828
Gaus mean : 36.6736
Gaus Width: 3.6416
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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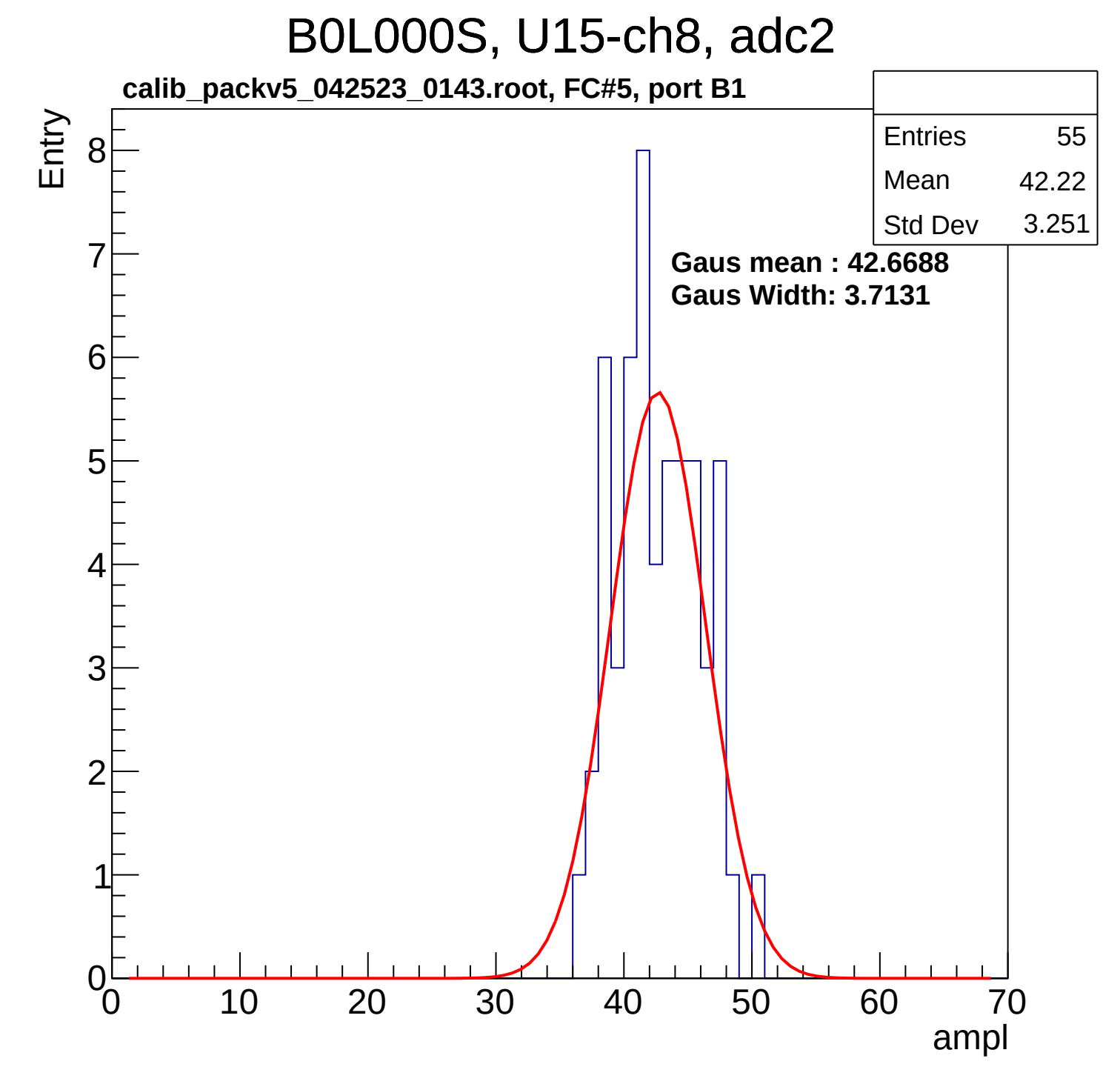
B0L000S, U15-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.22
Std Dev 3.251
Gaus mean : 42.6688
Gaus Width: 3.7131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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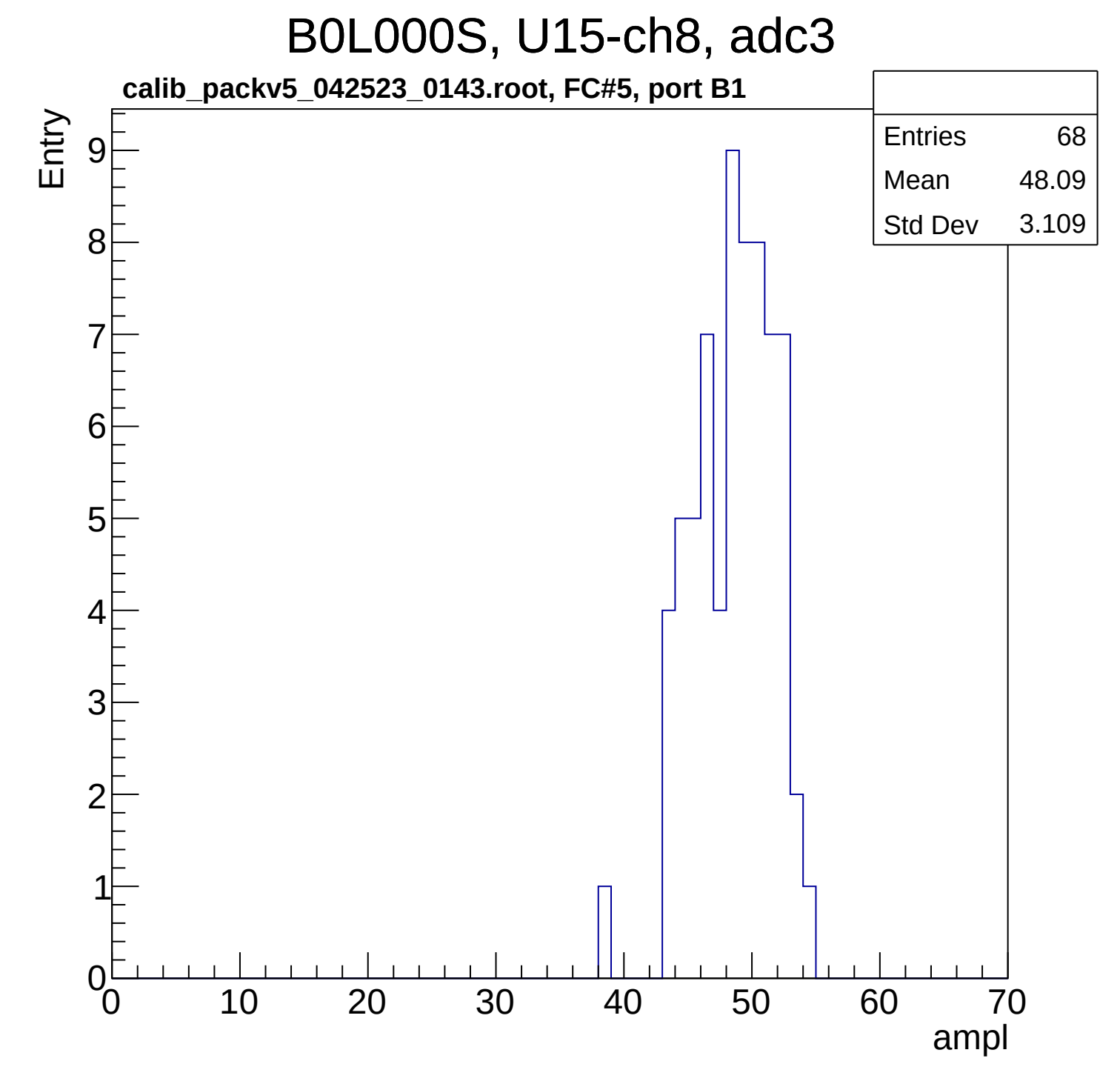
B0L000S, U15-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.09
Std Dev 3.109
Entry
ampl
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2
3
4
5
6
7
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0
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60
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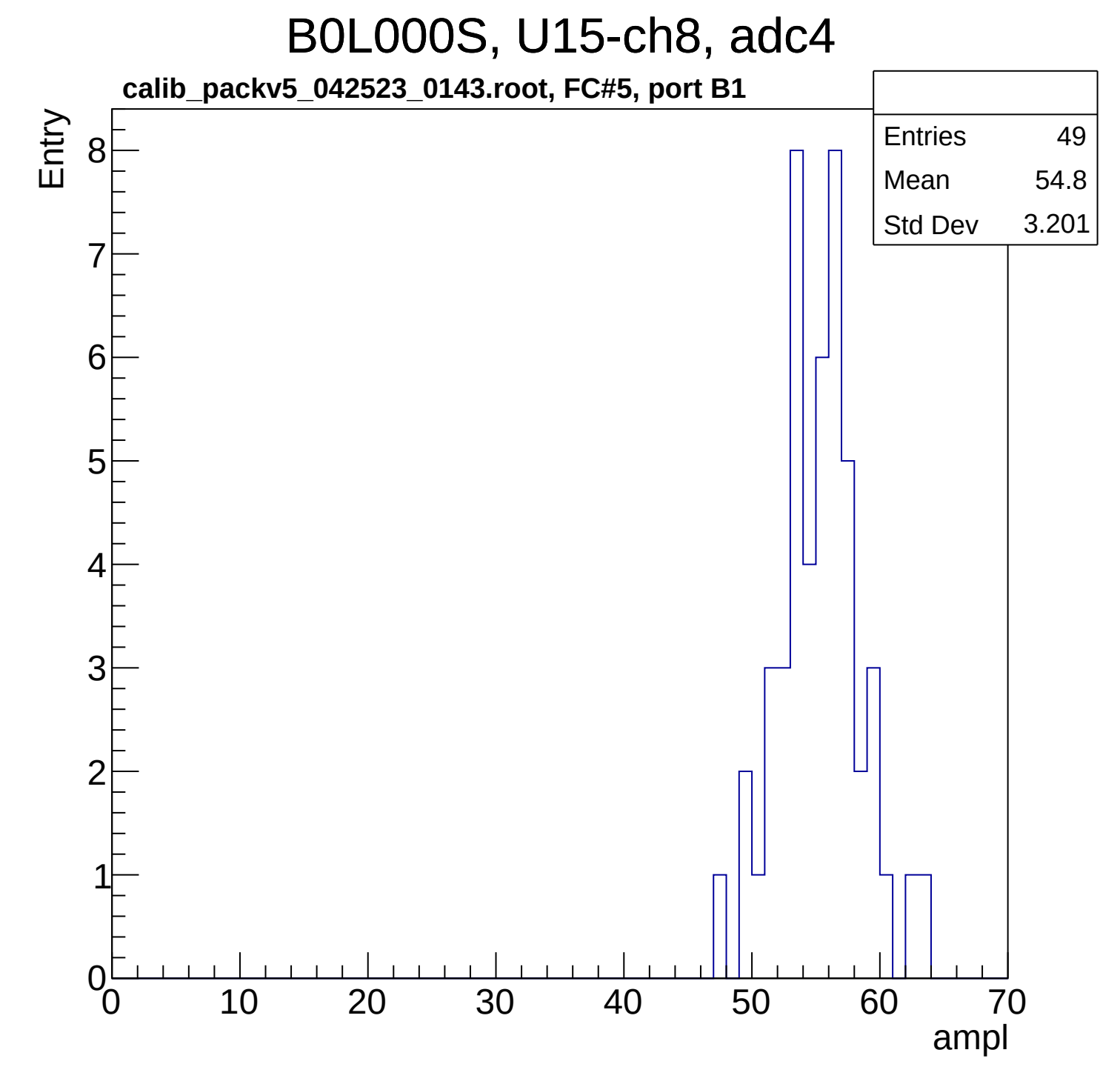
B0L000S, U15-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.8
Std Dev 3.201
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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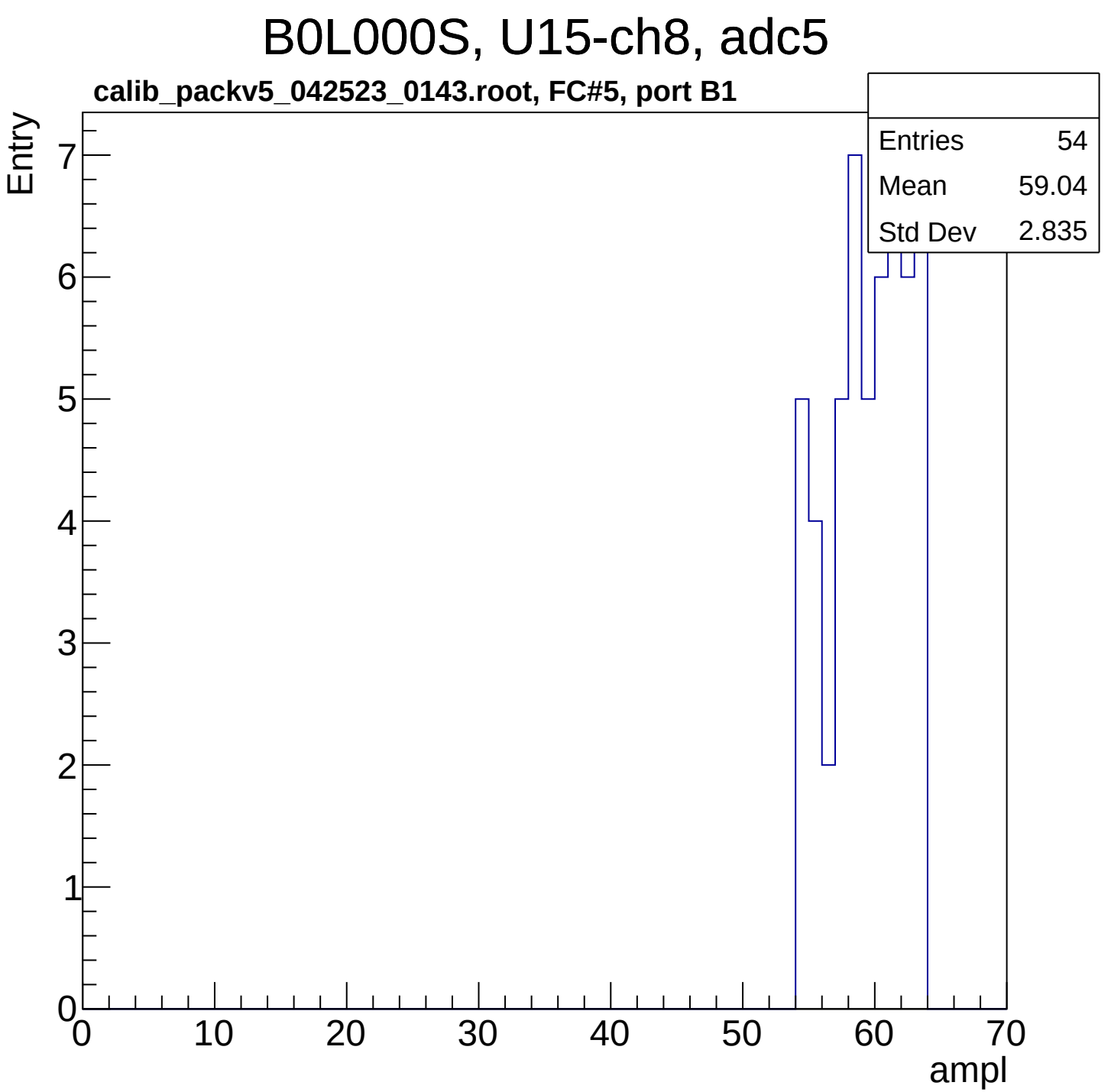
B0L000S, U15-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.04
Std Dev 2.835
Entry
ampl
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1
2
3
4
5
6
7
0
10
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40
50
60
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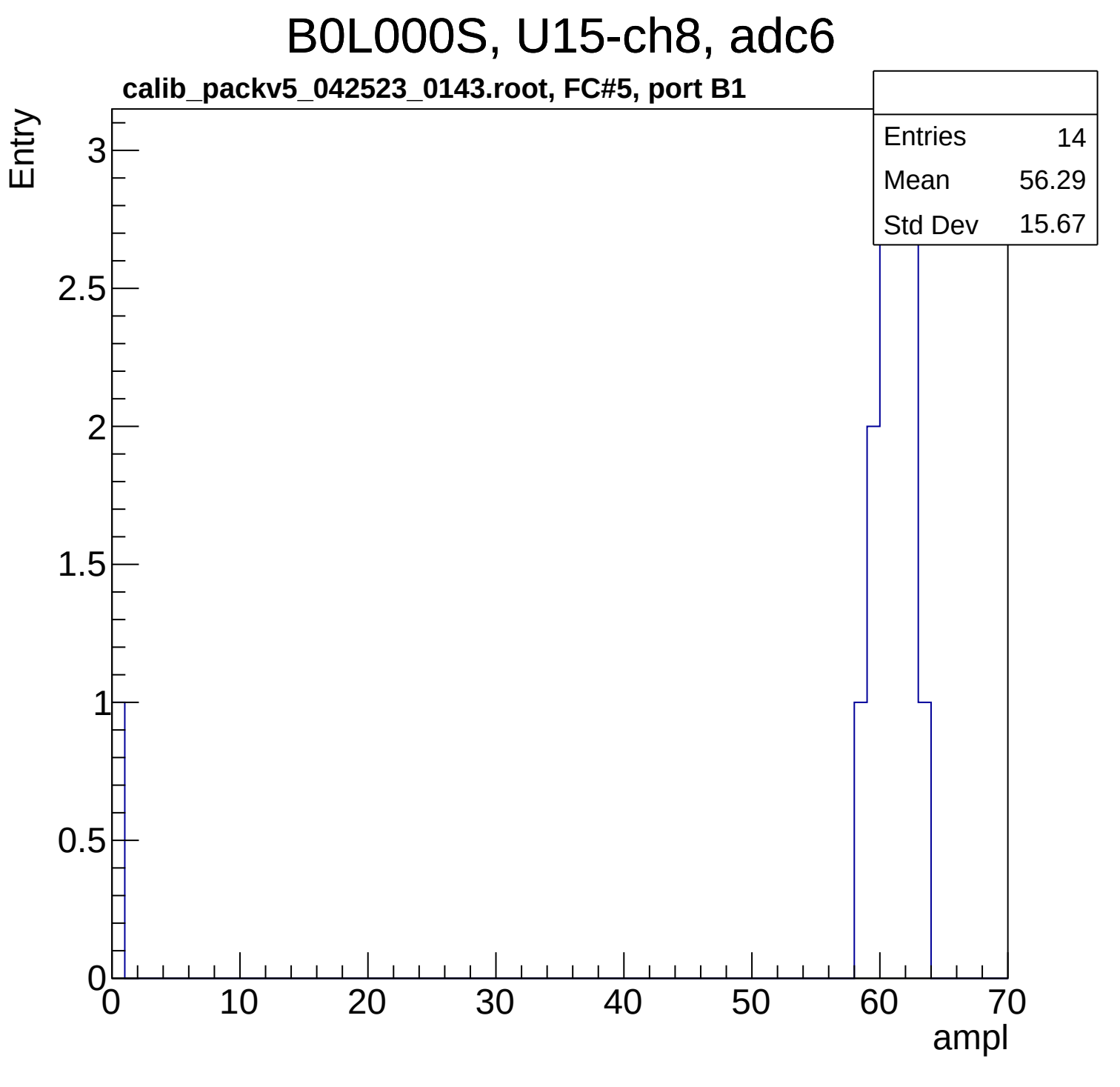
B0L000S, U15-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 56.29
Std Dev 15.67
Entry
ampl
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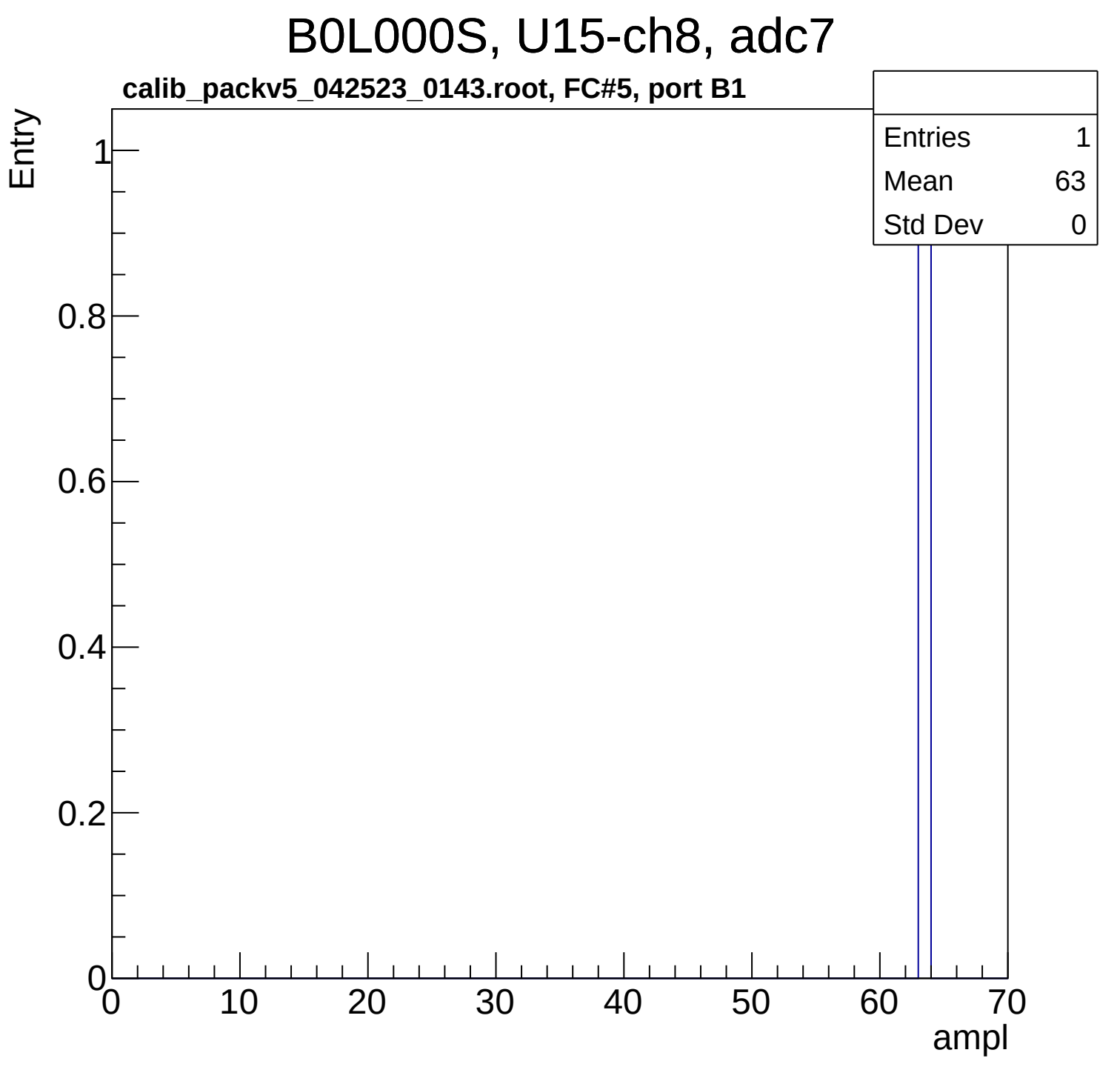
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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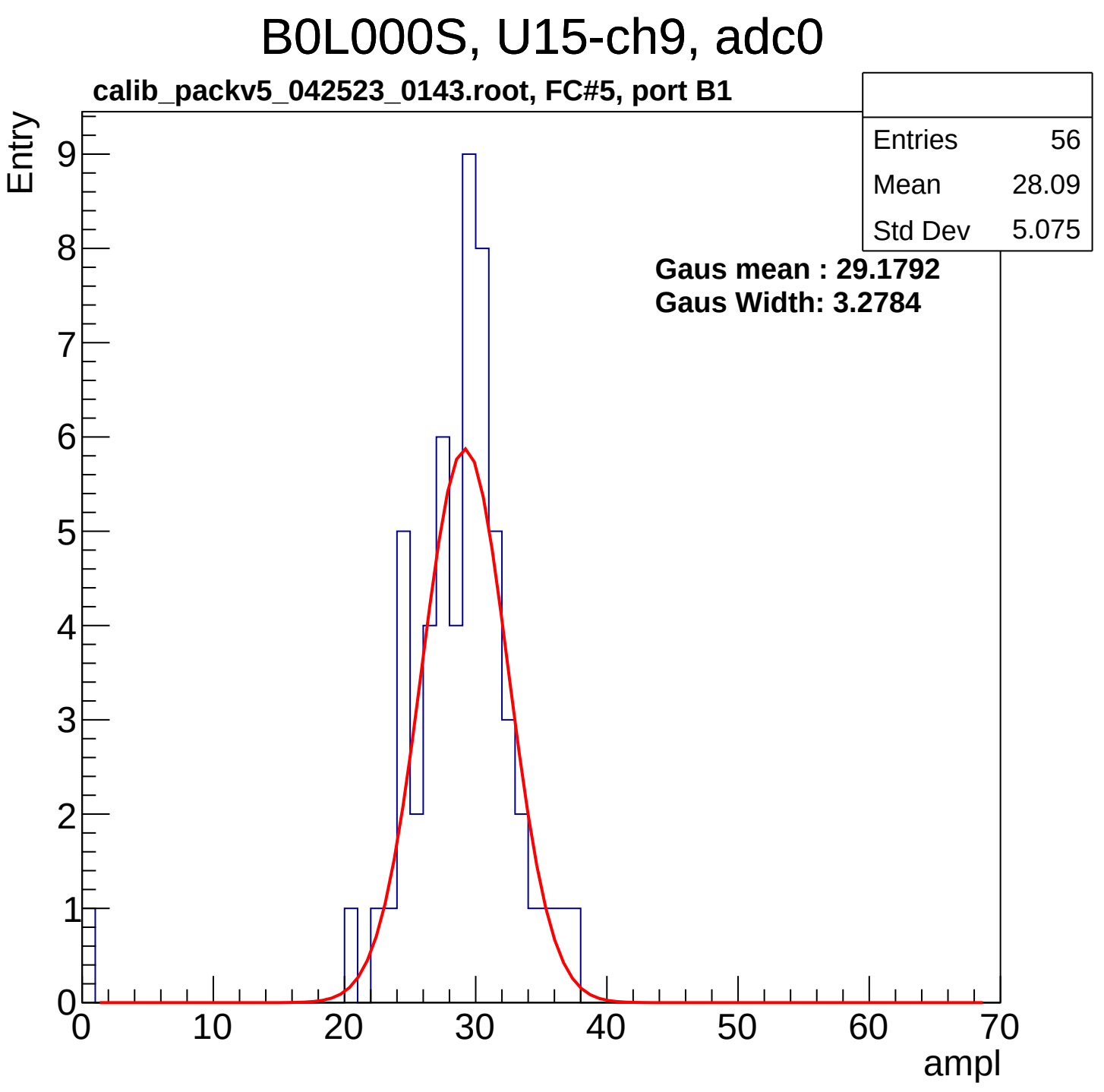
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 28.09
Std Dev 5.075
Gaus mean : 29.1792
Gaus Width: 3.2784
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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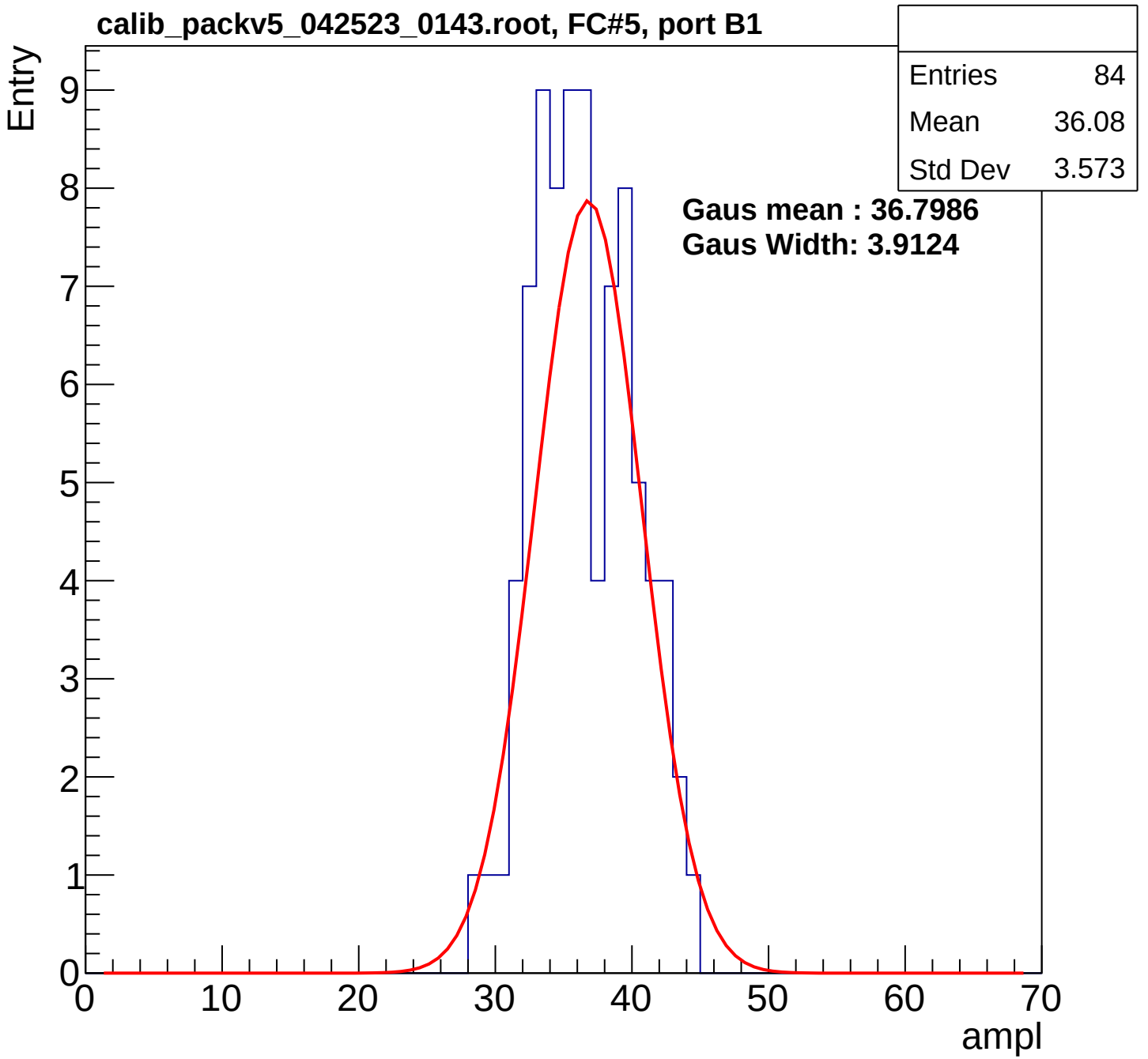
B0L000S, U15-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 36.08
Std Dev 3.573
Gaus mean : 36.7986
Gaus Width: 3.9124
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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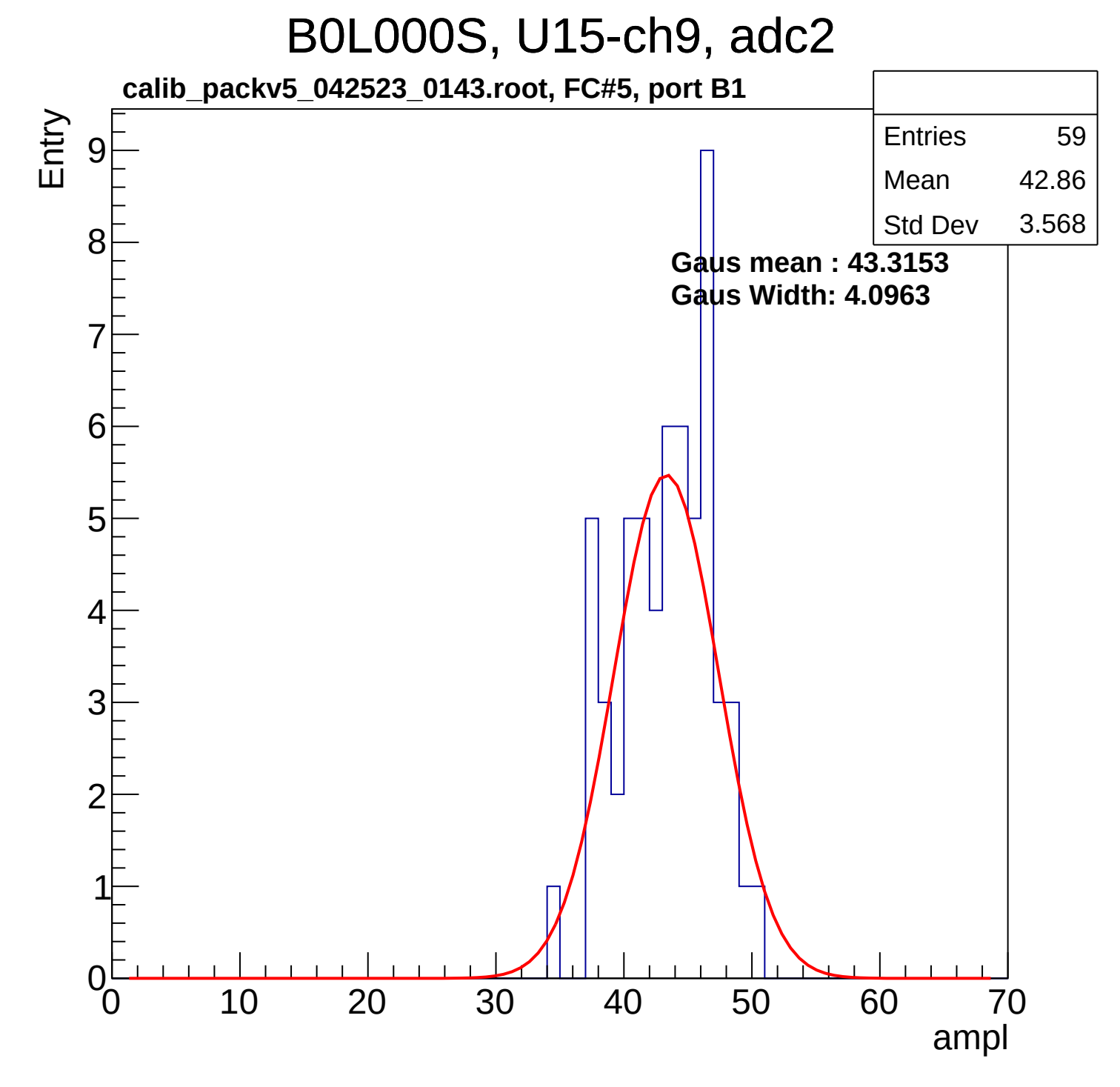
B0L000S, U15-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.86
Std Dev 3.568
Gaus mean : 43.3153
Gaus Width: 4.0963
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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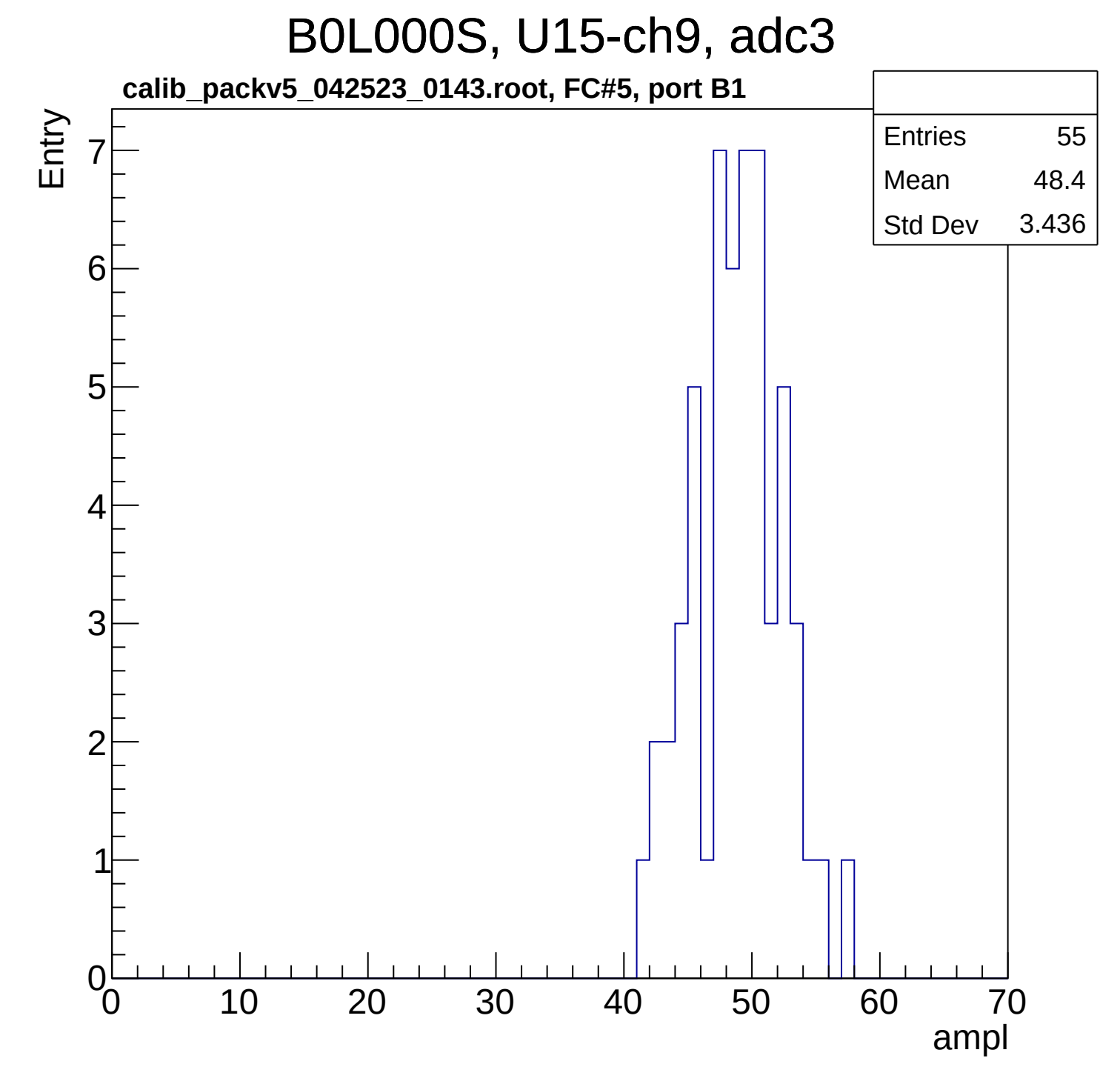
B0L000S, U15-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.4
Std Dev 3.436
Entry
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1
2
3
4
5
6
7
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10
20
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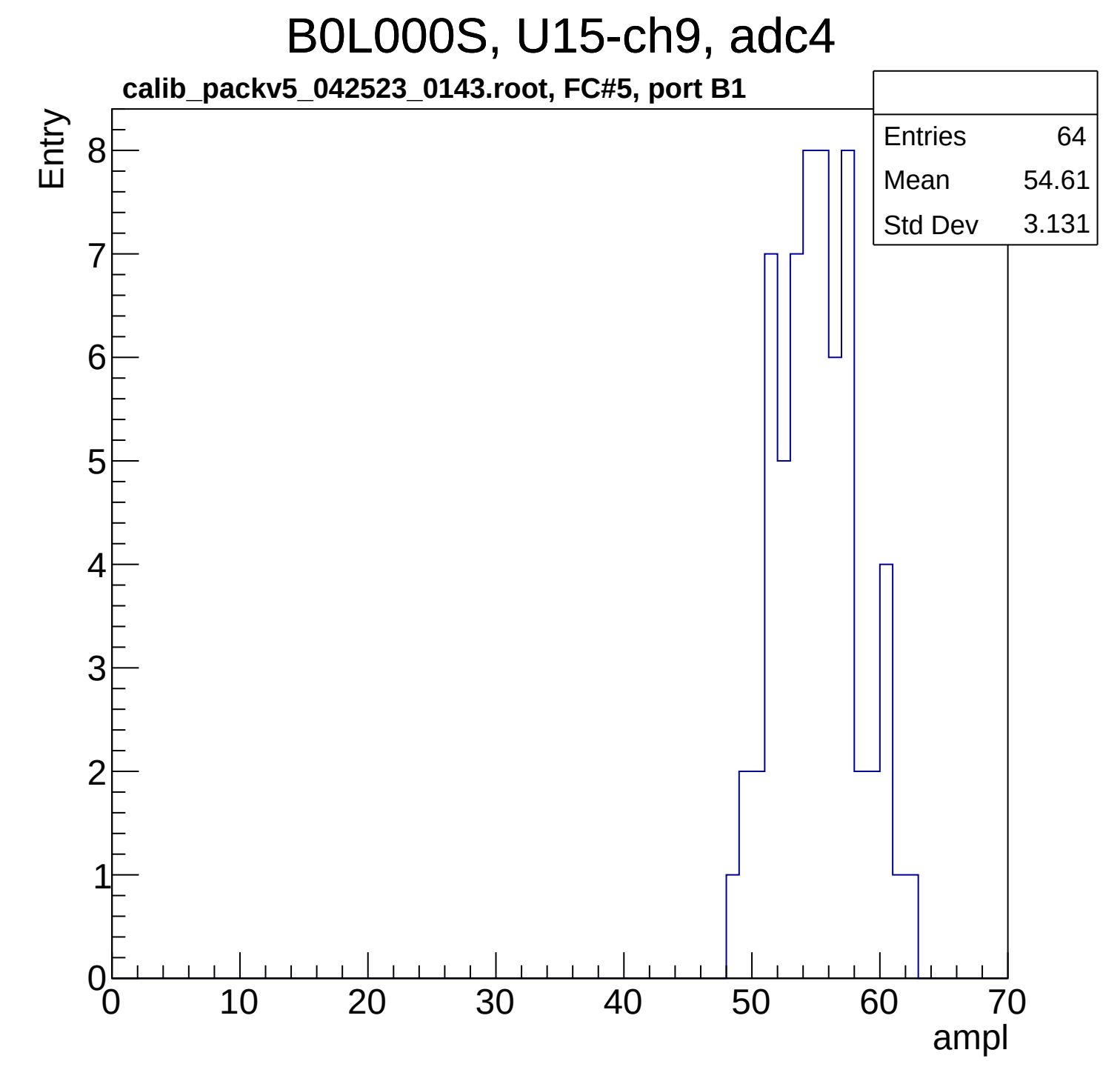
50
60
70
ampl


B0L000S, U15-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 54.61
Std Dev 3.131
Entry
ampl
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1
2
3
4
5
6
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0
10
20
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40
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60
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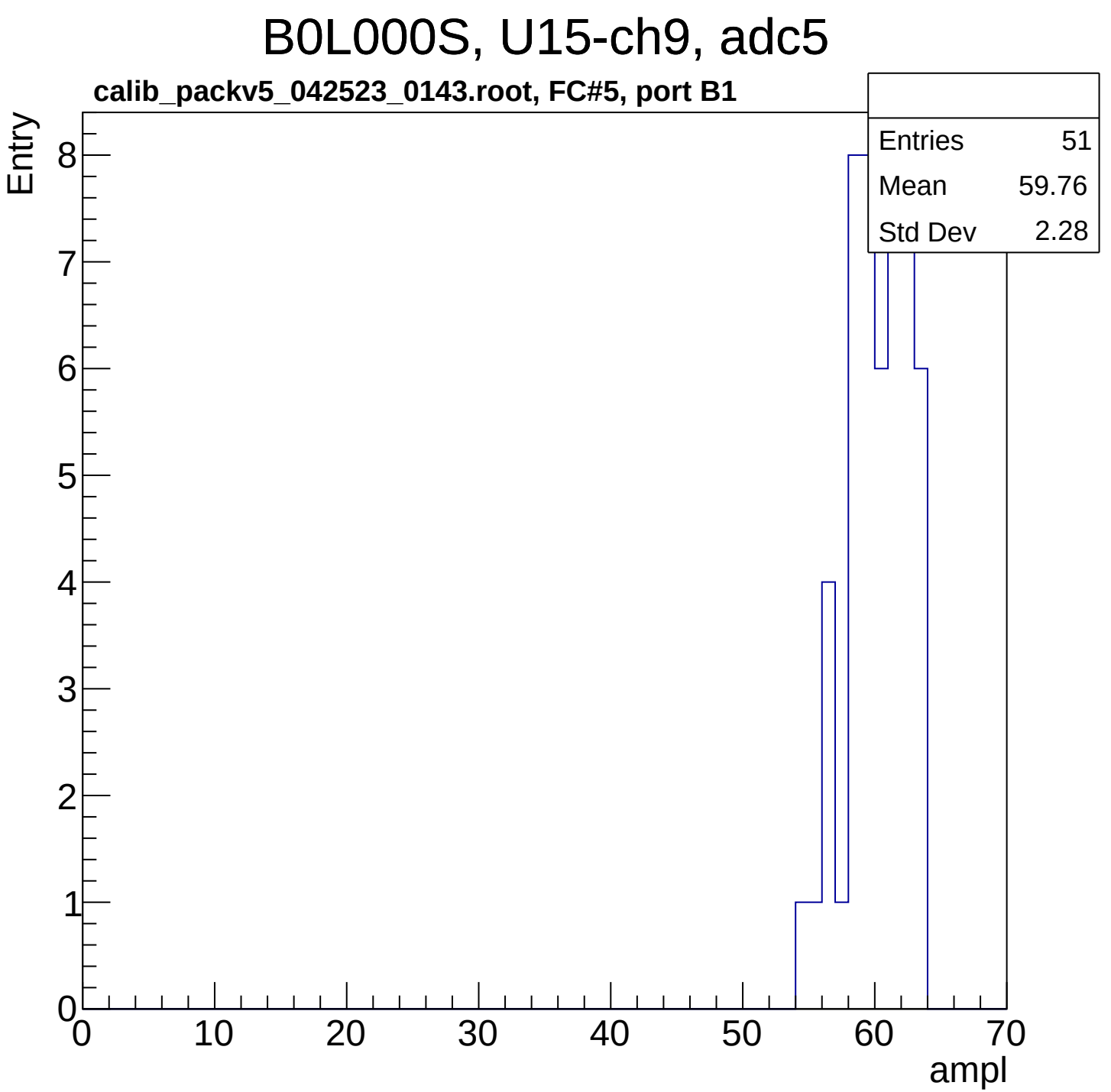
B0L000S, U15-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.76
Std Dev 2.28
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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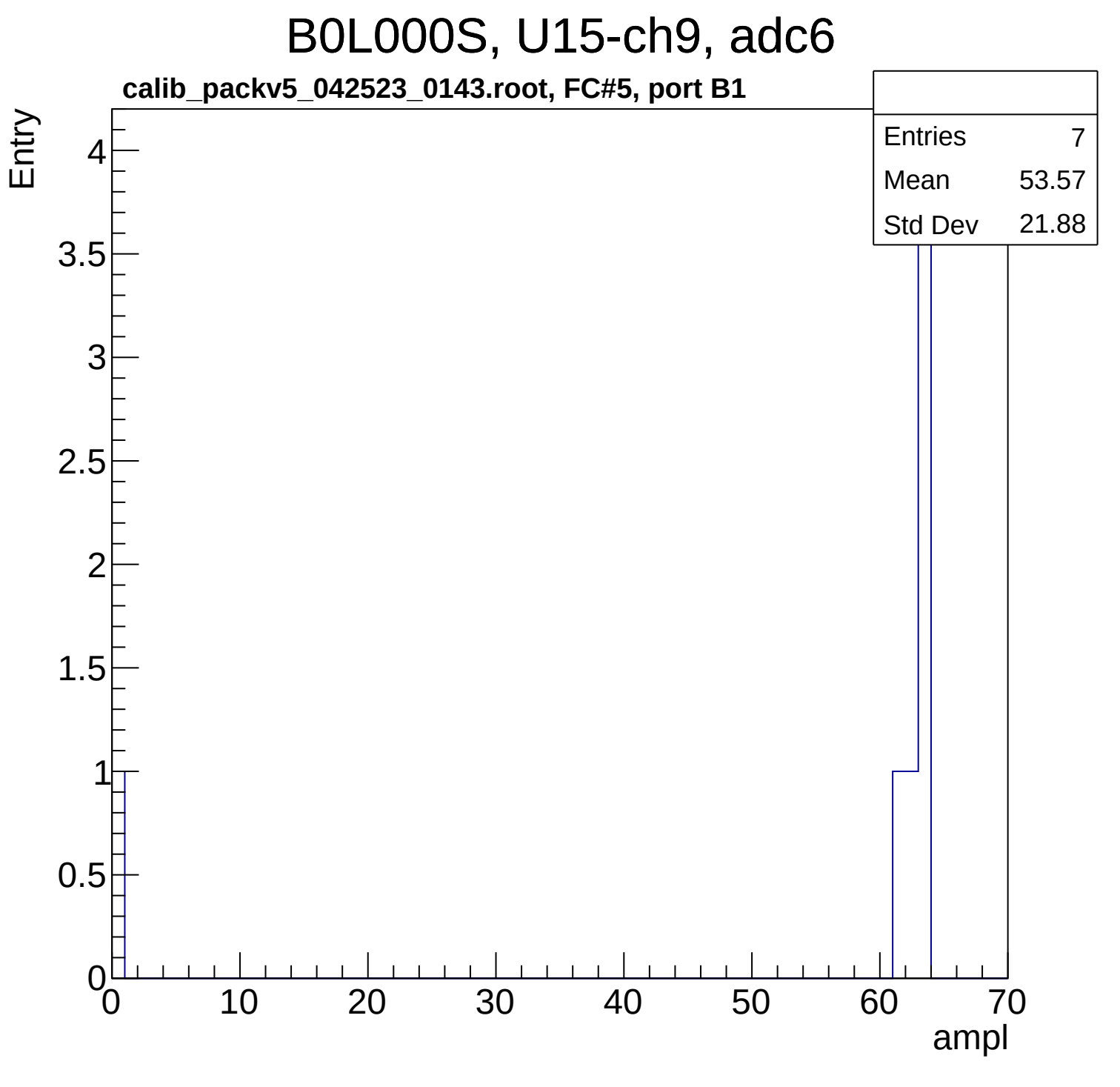
B0L000S, U15-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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60
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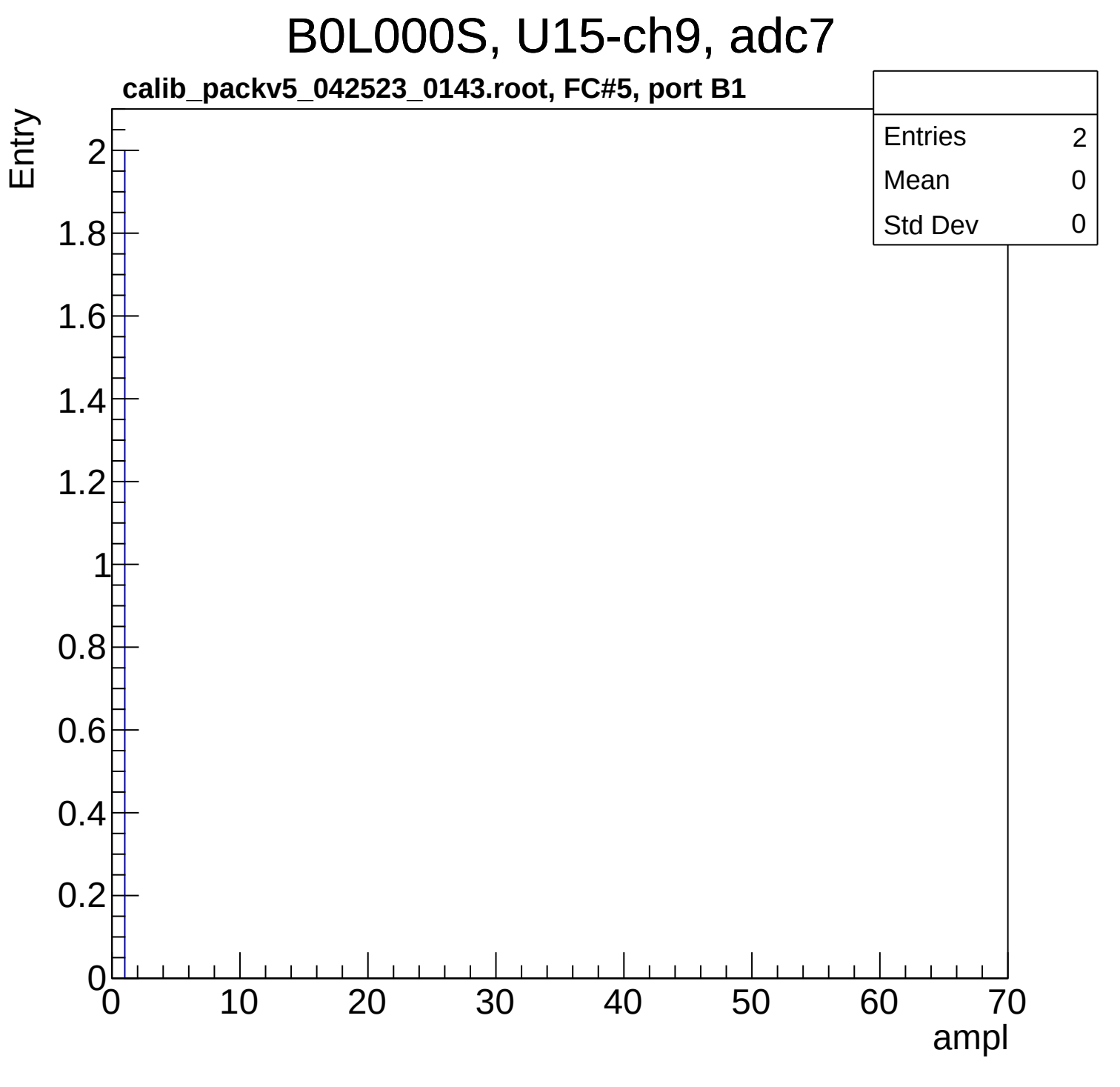
B0L000S, U15-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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40
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60
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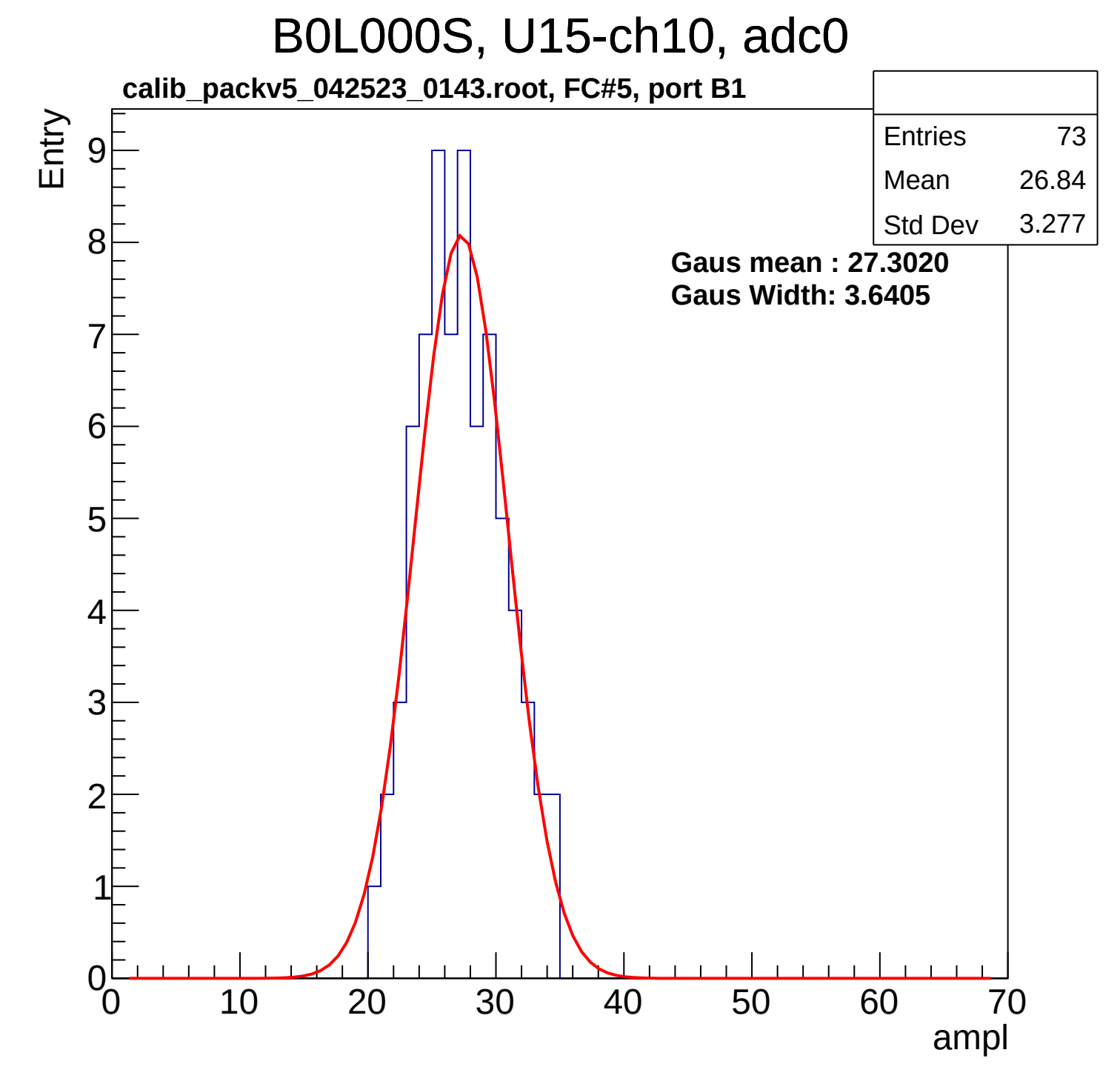
B0L000S, U15-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 26.84
Std Dev 3.277
Gaus mean : 27.3020
Gaus Width: 3.6405
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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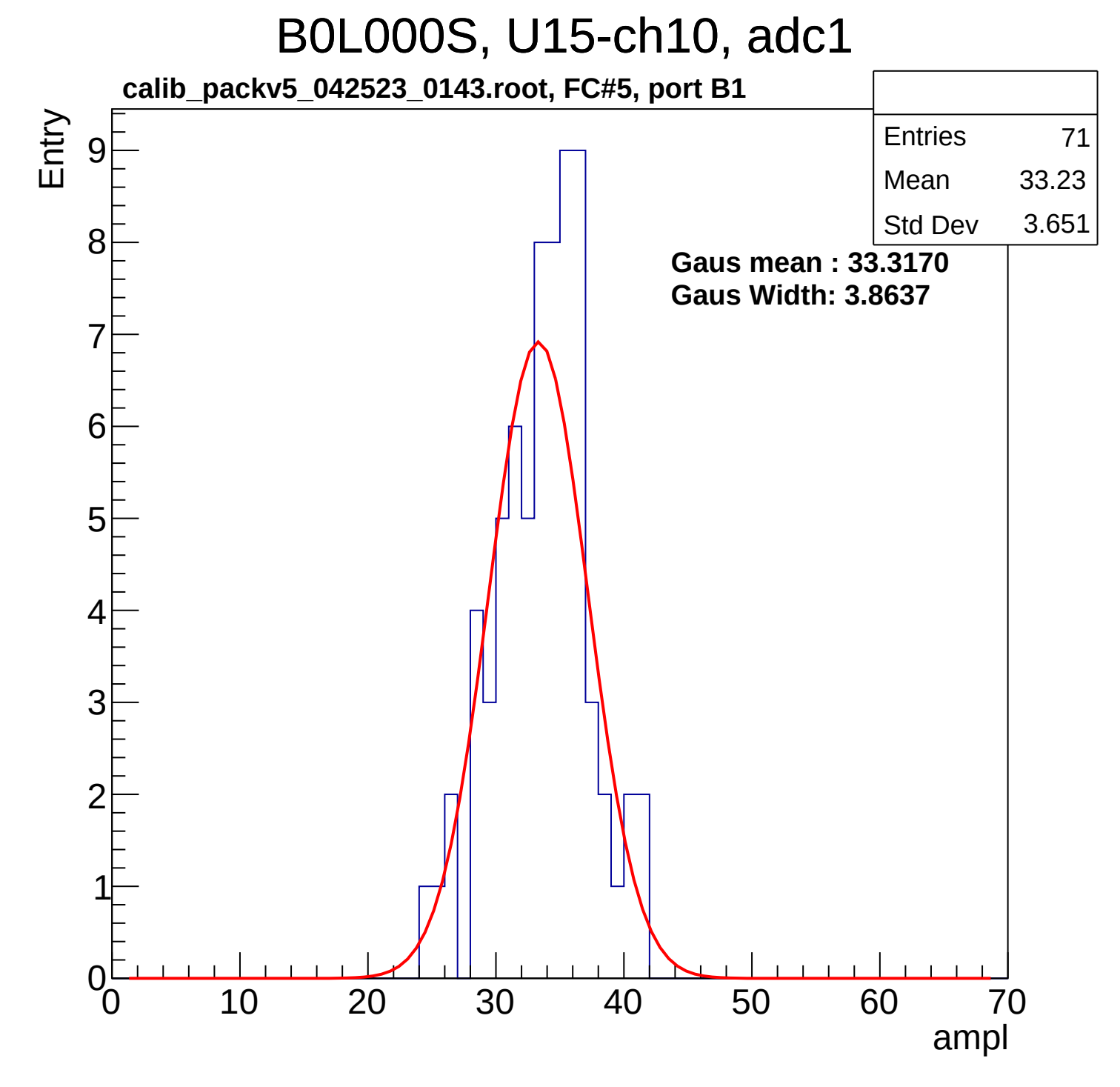
B0L000S, U15-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 33.23
Std Dev 3.651
Gaus mean : 33.3170
Gaus Width: 3.8637
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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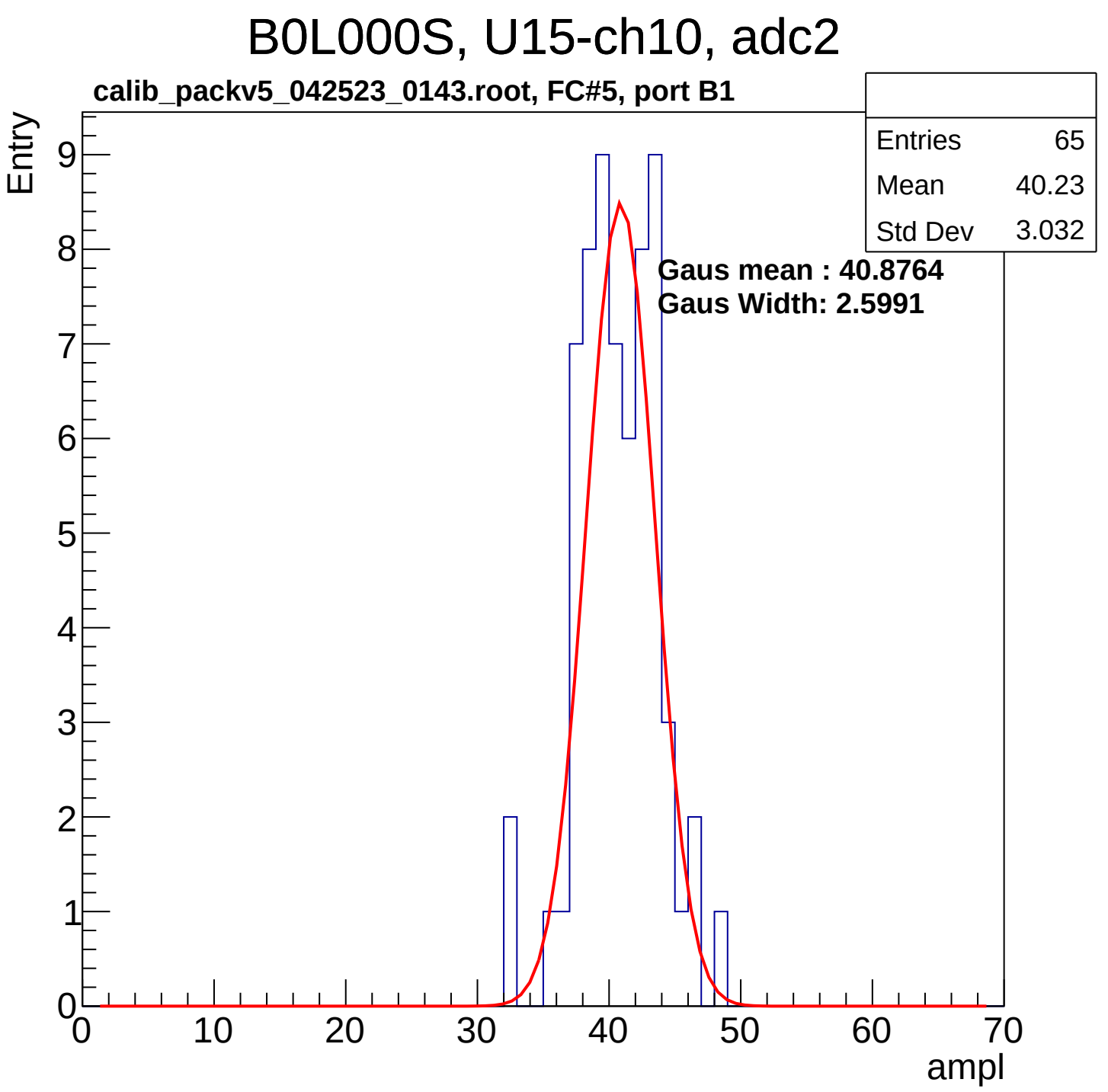
B0L000S, U15-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 40.23
Std Dev 3.032
Gaus mean : 40.8764
Gaus Width: 2.5991
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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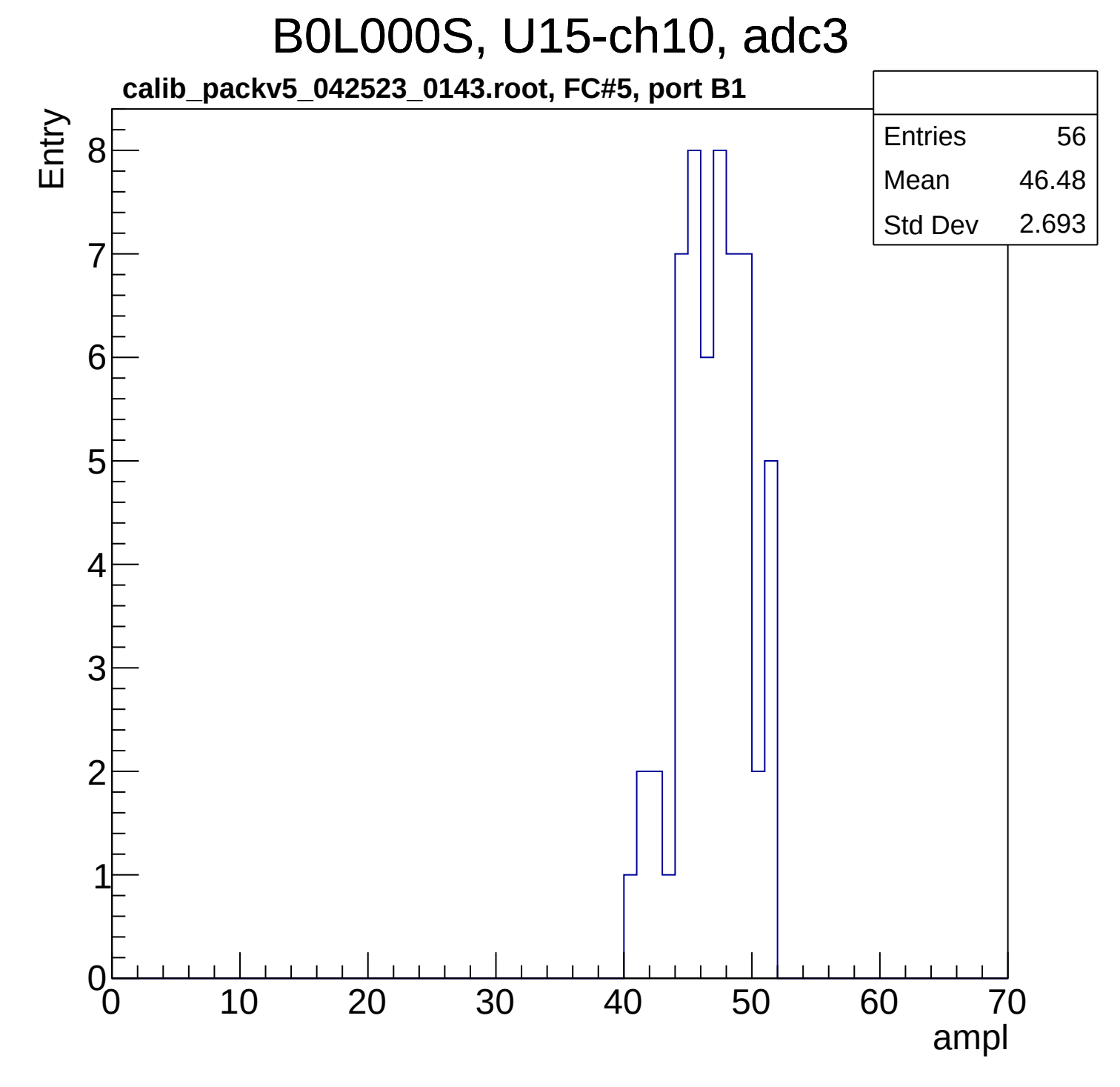
B0L000S, U15-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 46.48
Std Dev 2.693
Entry
ampl
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2
3
4
5
6
7
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0
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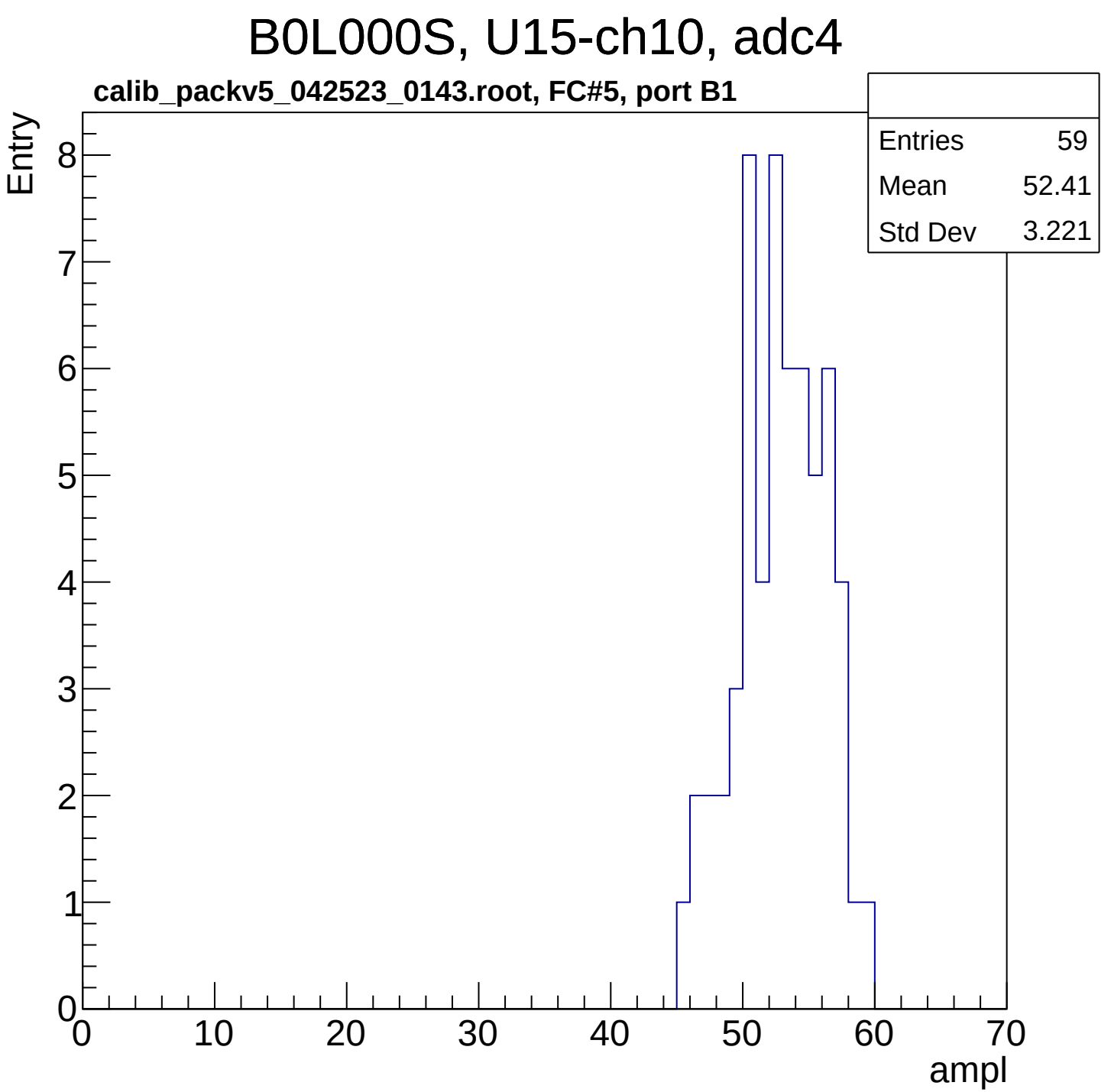
B0L000S, U15-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 52.41
Std Dev 3.221
Entry
ampl
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3
4
5
6
7
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0
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40
50
60
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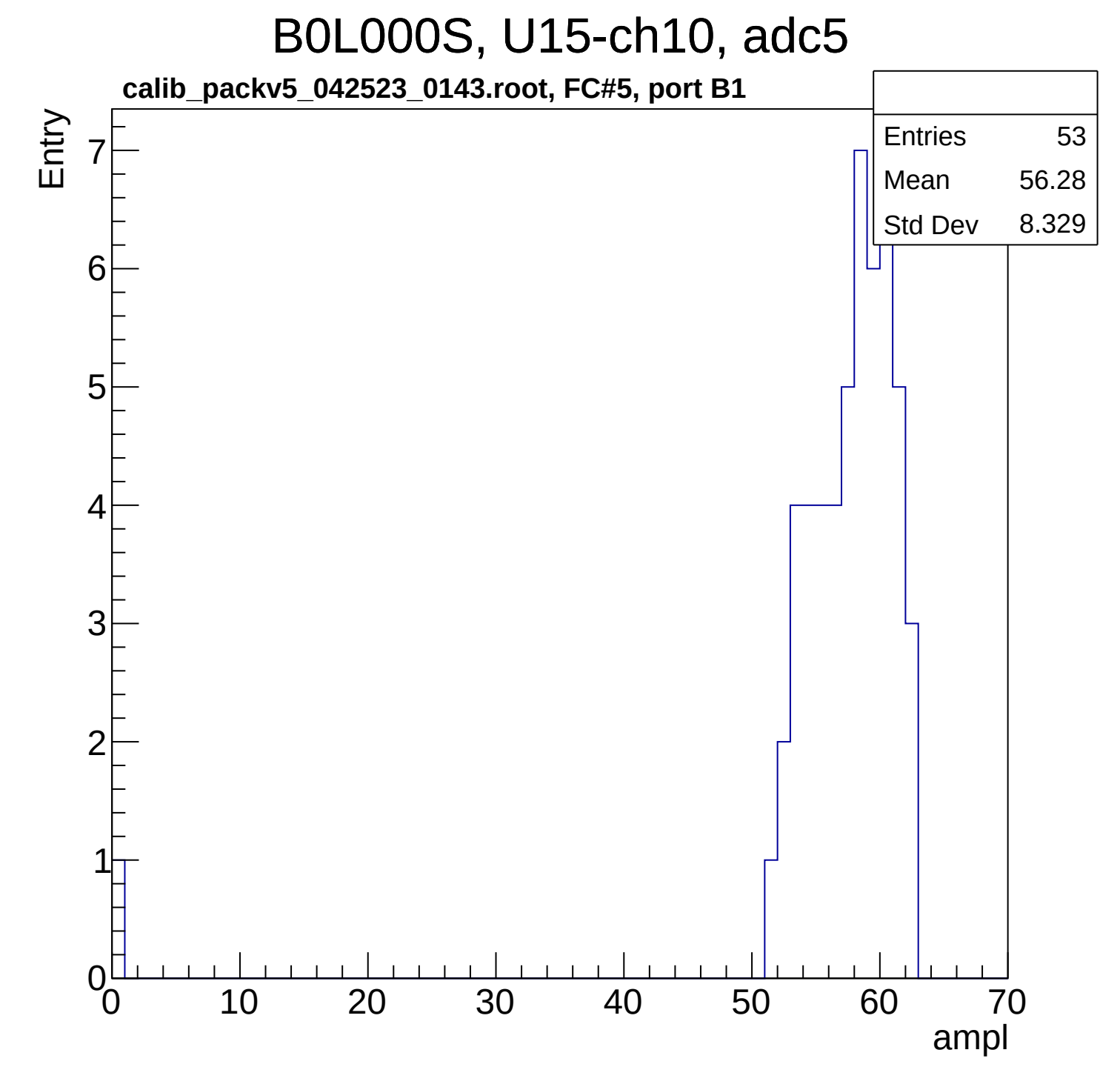
B0L000S, U15-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.28
Std Dev 8.329
Entry
ampl
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3
4
5
6
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0
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60
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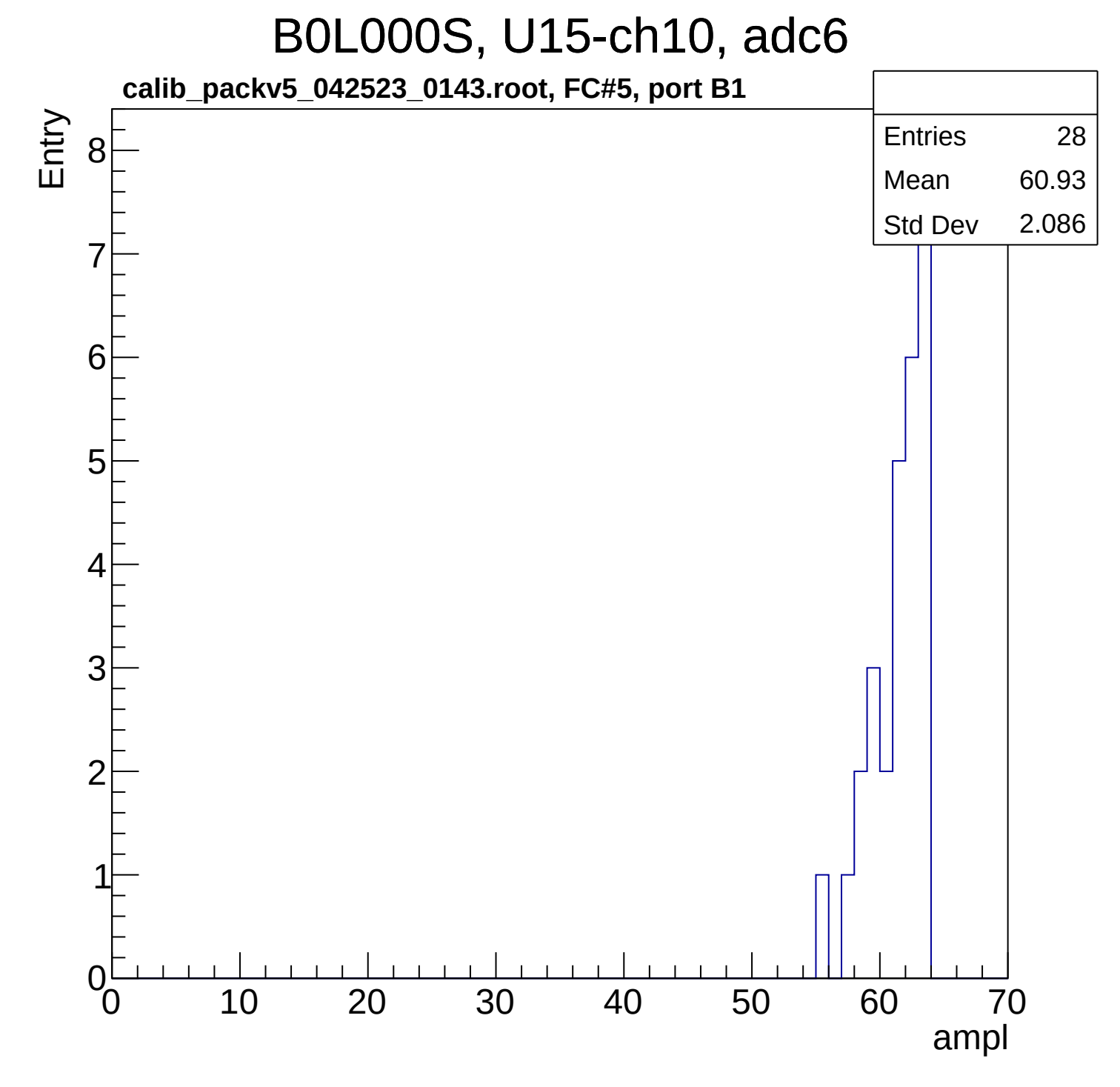
B0L000S, U15-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 60.93
Std Dev 2.086
Entry
ampl
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5
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60
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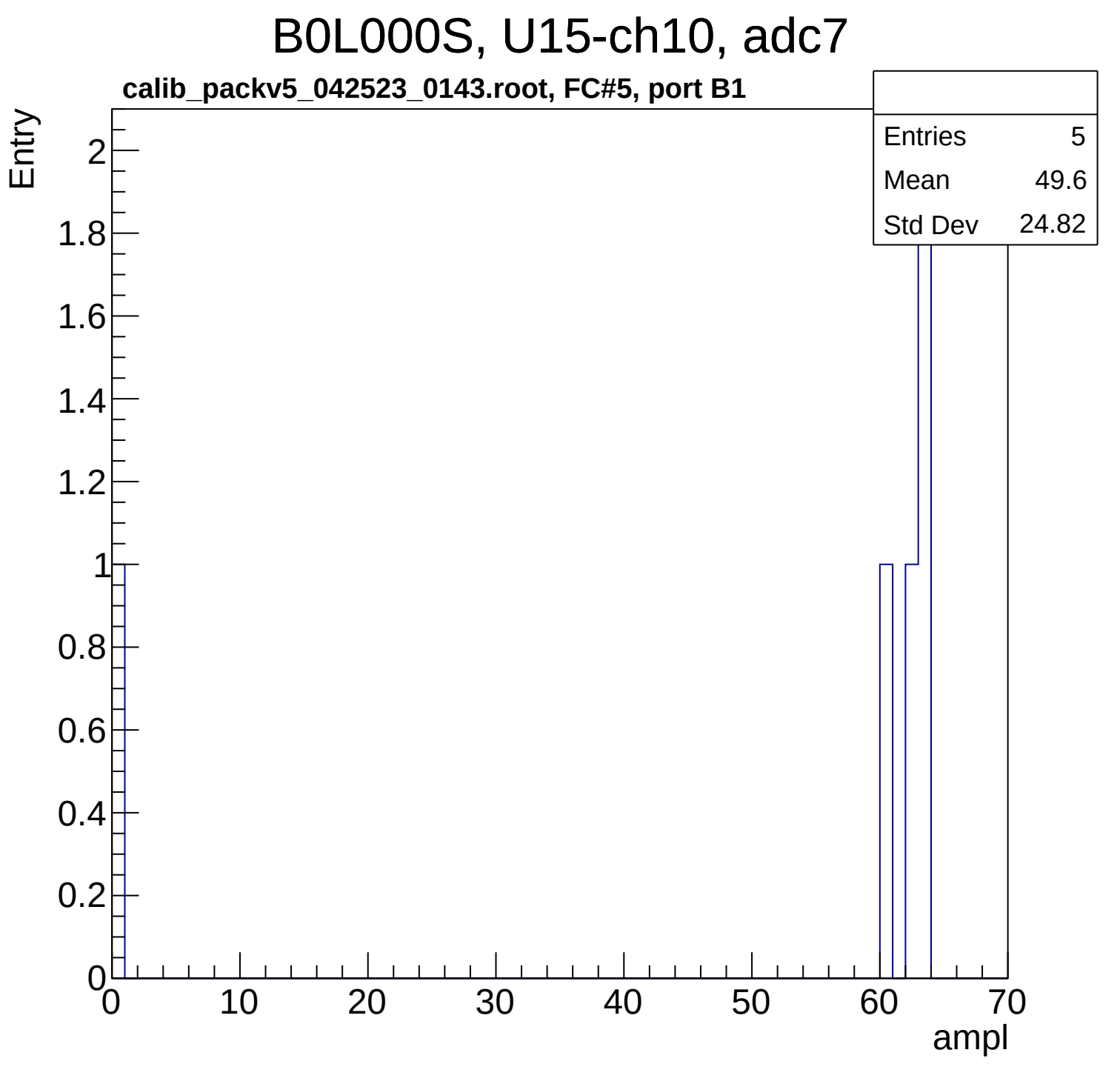
B0L000S, U15-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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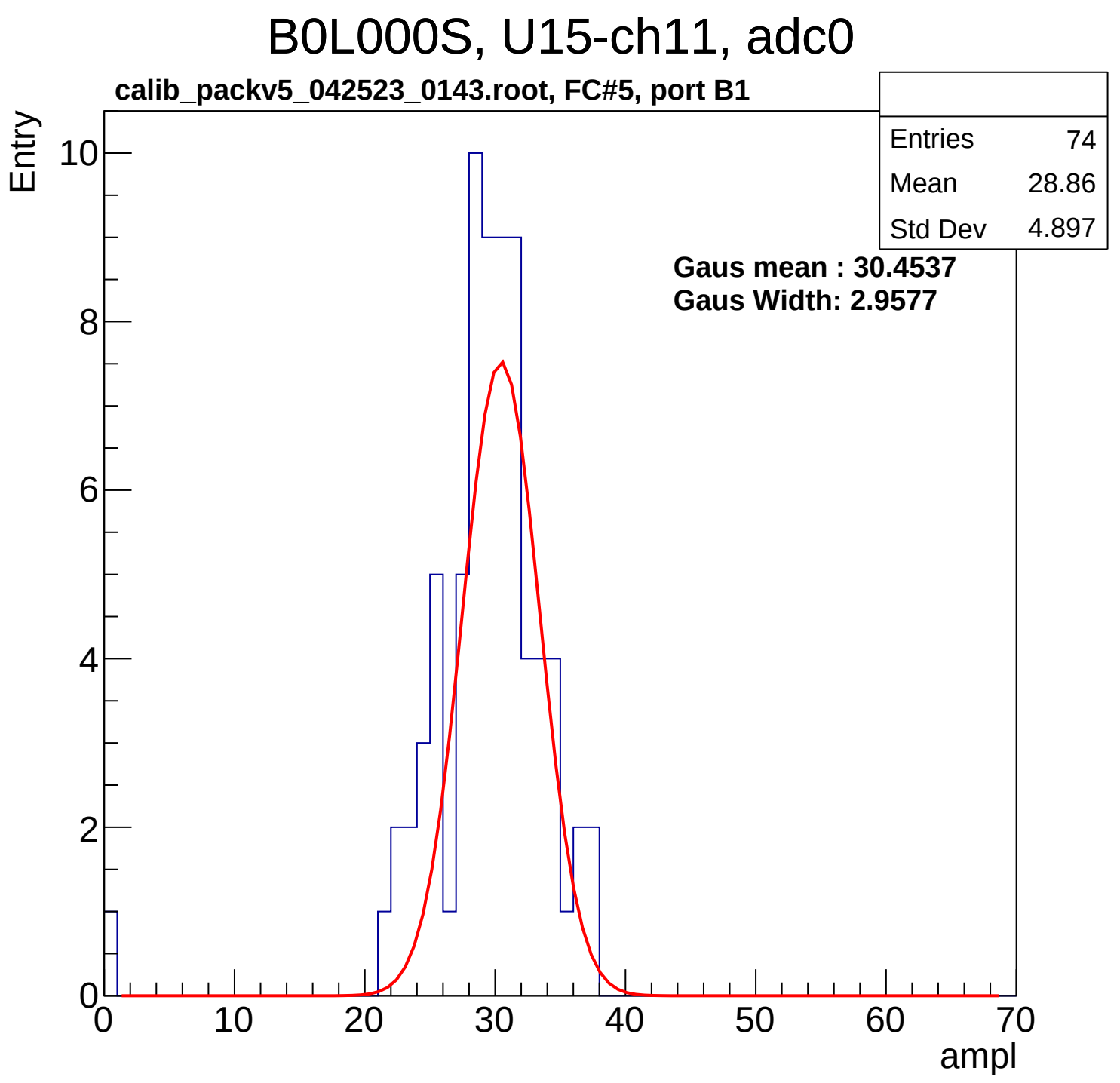
B0L000S, U15-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.86
Std Dev 4.897
Gaus mean : 30.4537
Gaus Width: 2.9577
Entry
ampl
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4
6
8
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0
10
20
30
40
50
60
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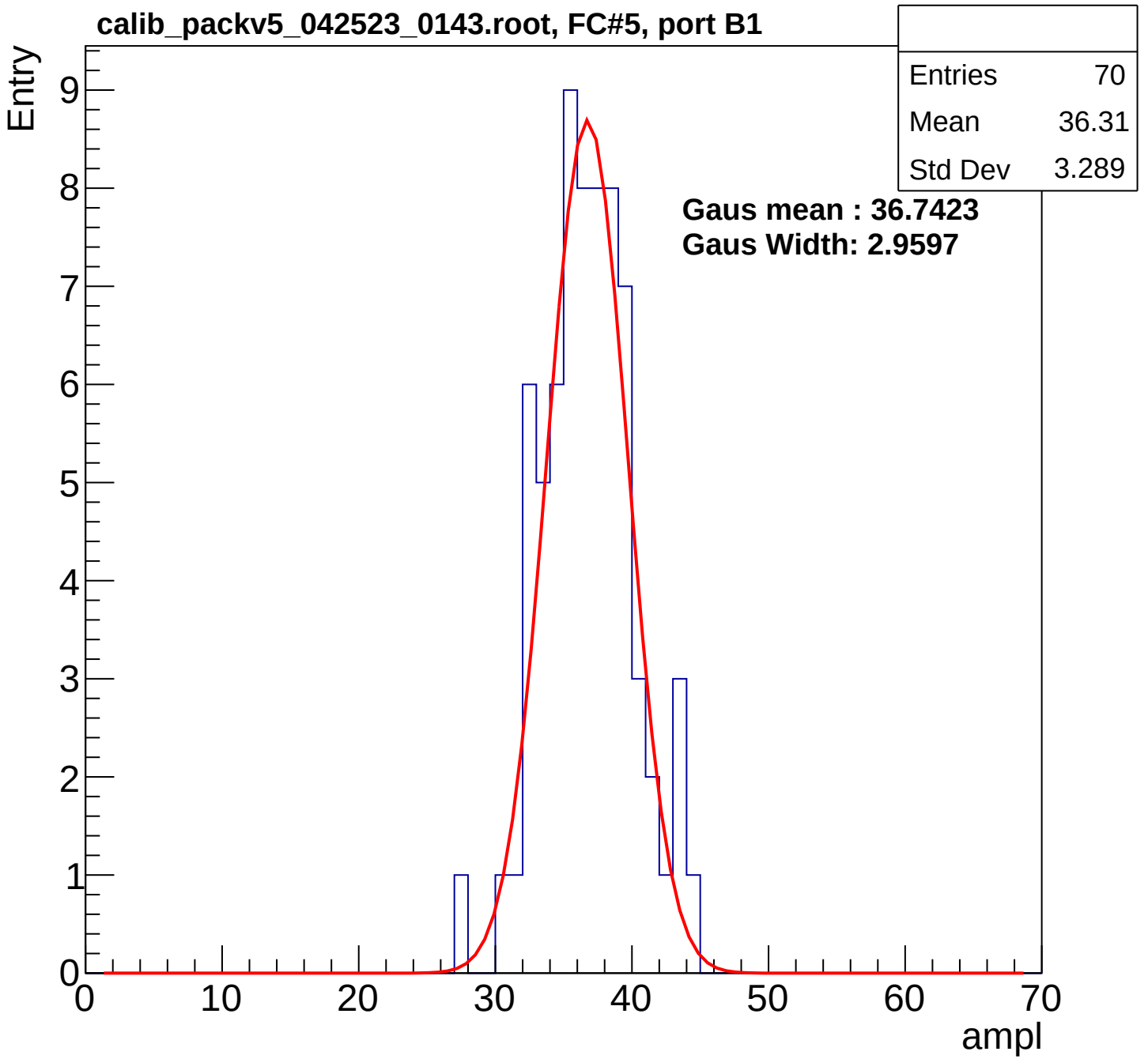
B0L000S, U15-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.31
Std Dev 3.289
Gaus mean : 36.7423
Gaus Width: 2.9597
Entry
ampl
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2
3
4
5
6
7
8
9
0
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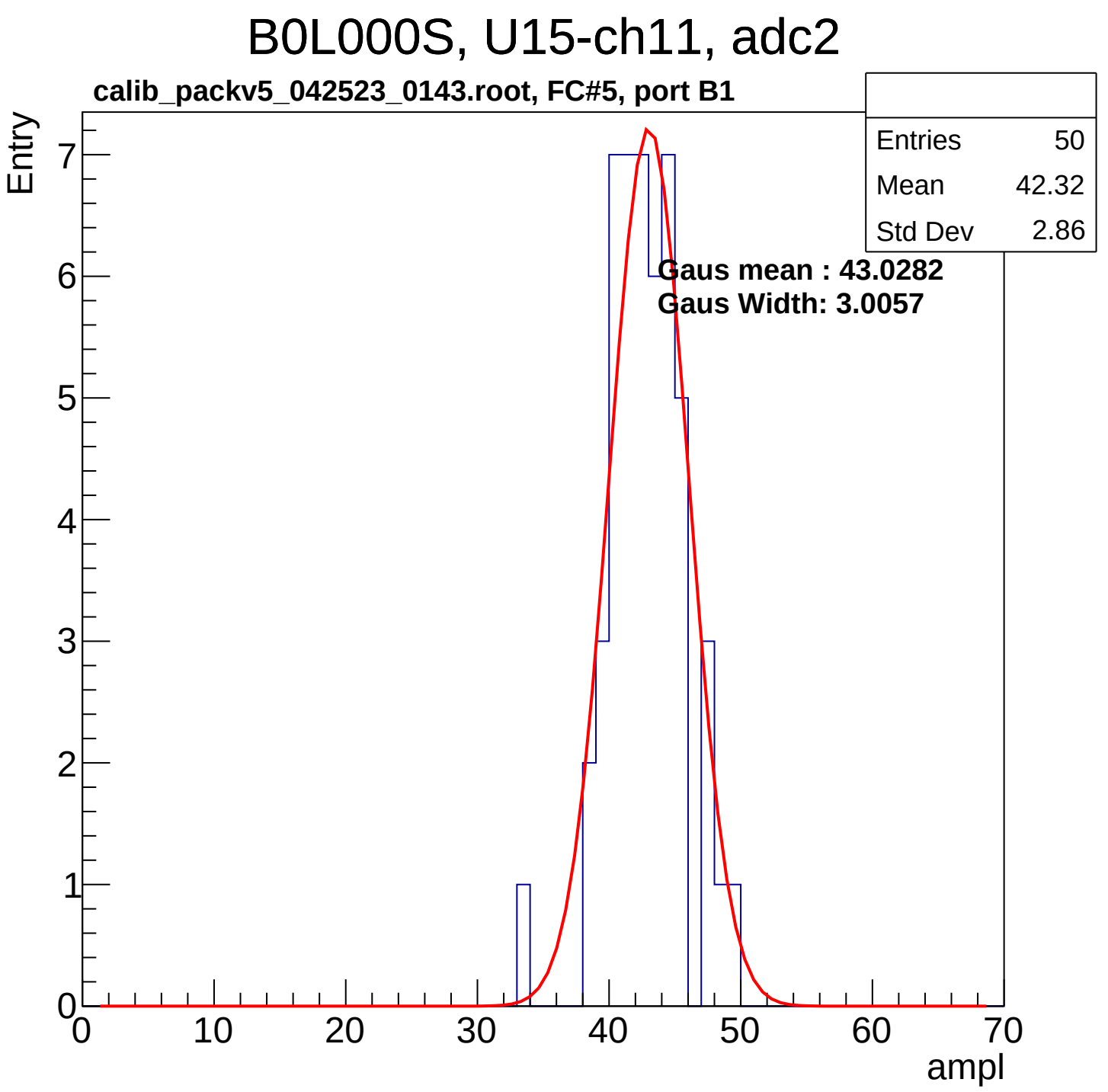
B0L000S, U15-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.32
Std Dev 2.86
Gaus mean : 43.0282
Gaus Width: 3.0057
Entry
ampl
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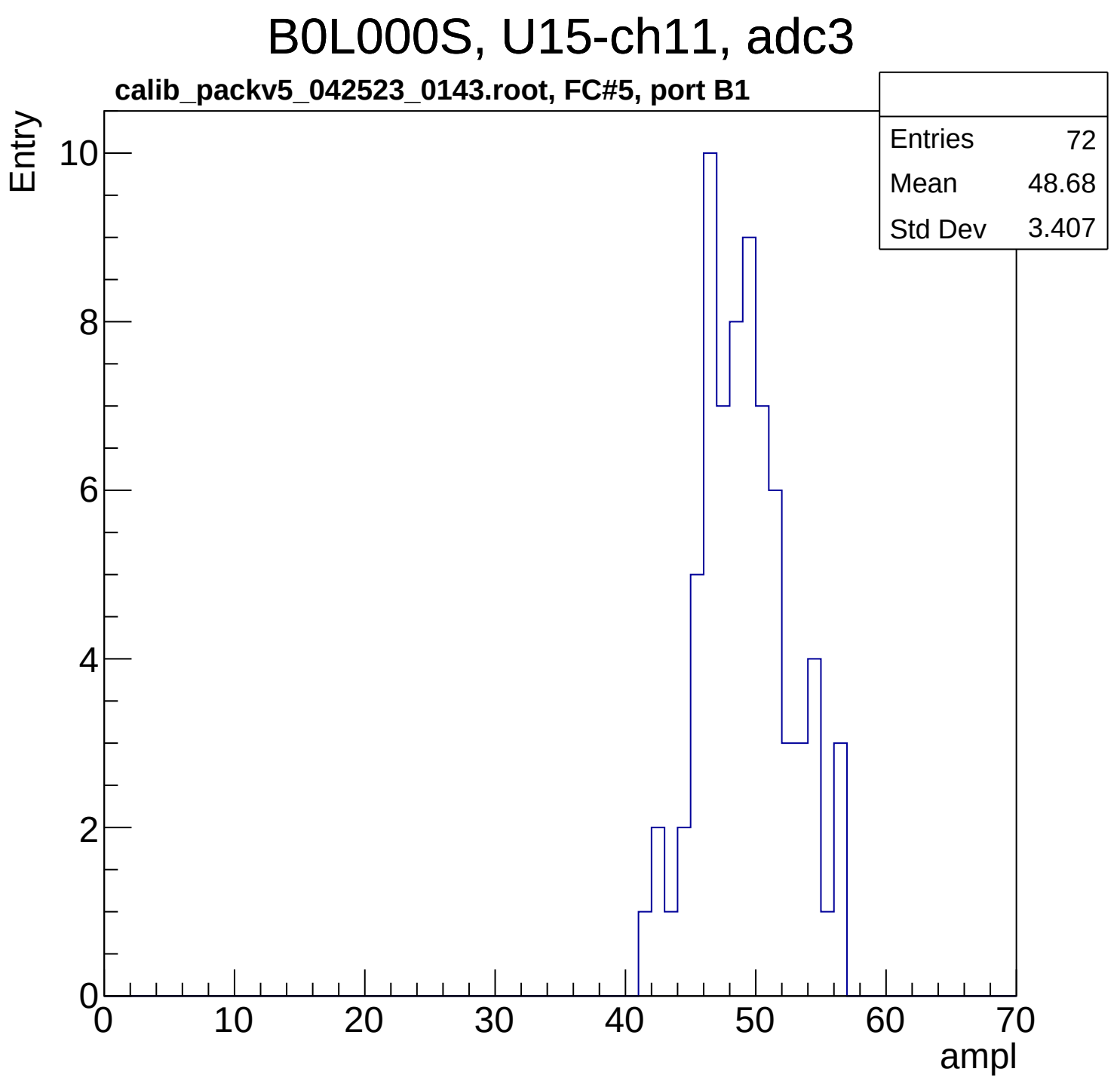
B0L000S, U15-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.68
Std Dev 3.407
Entry
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50
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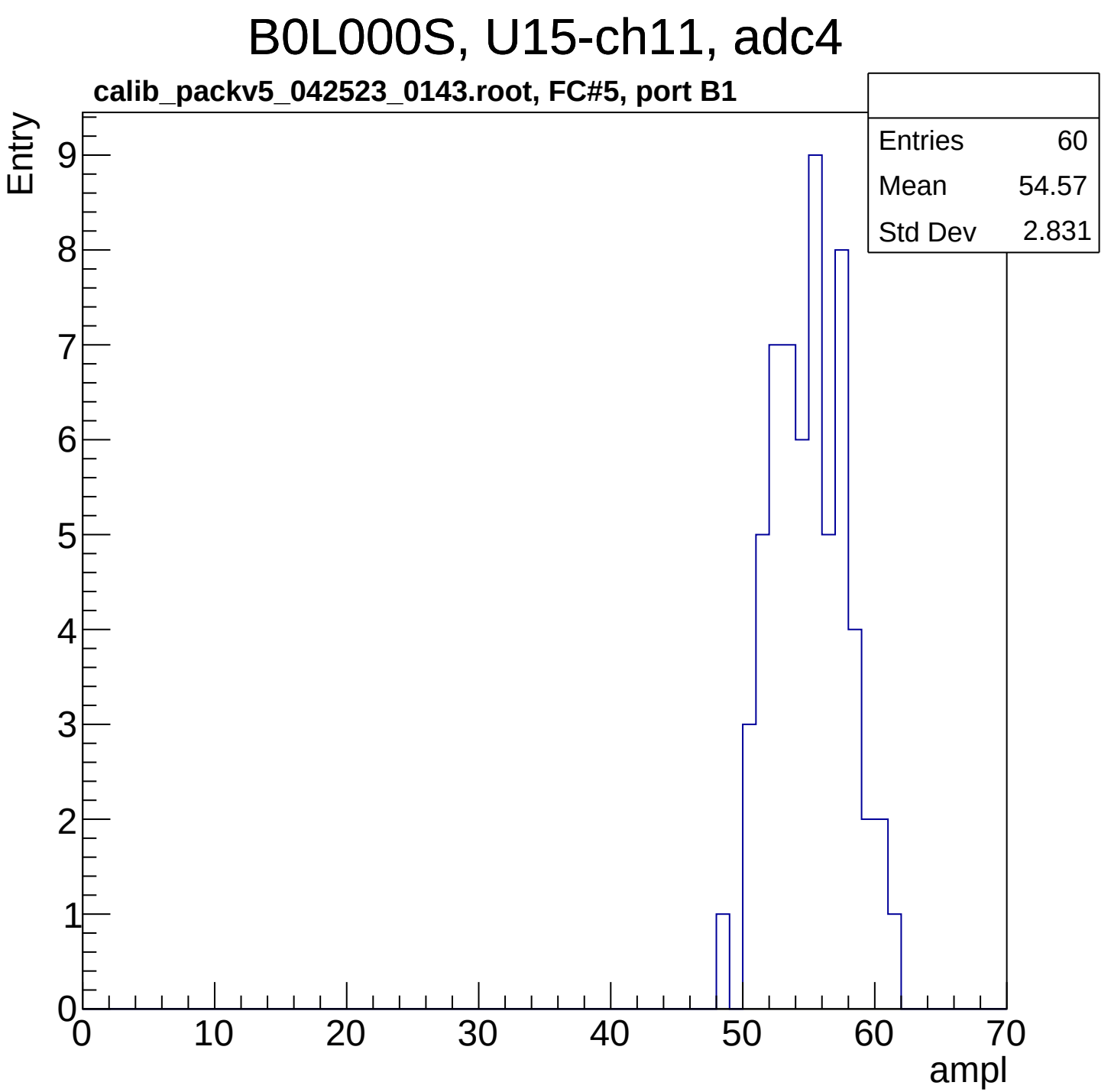
70
ampl


B0L000S, U15-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 54.57
Std Dev 2.831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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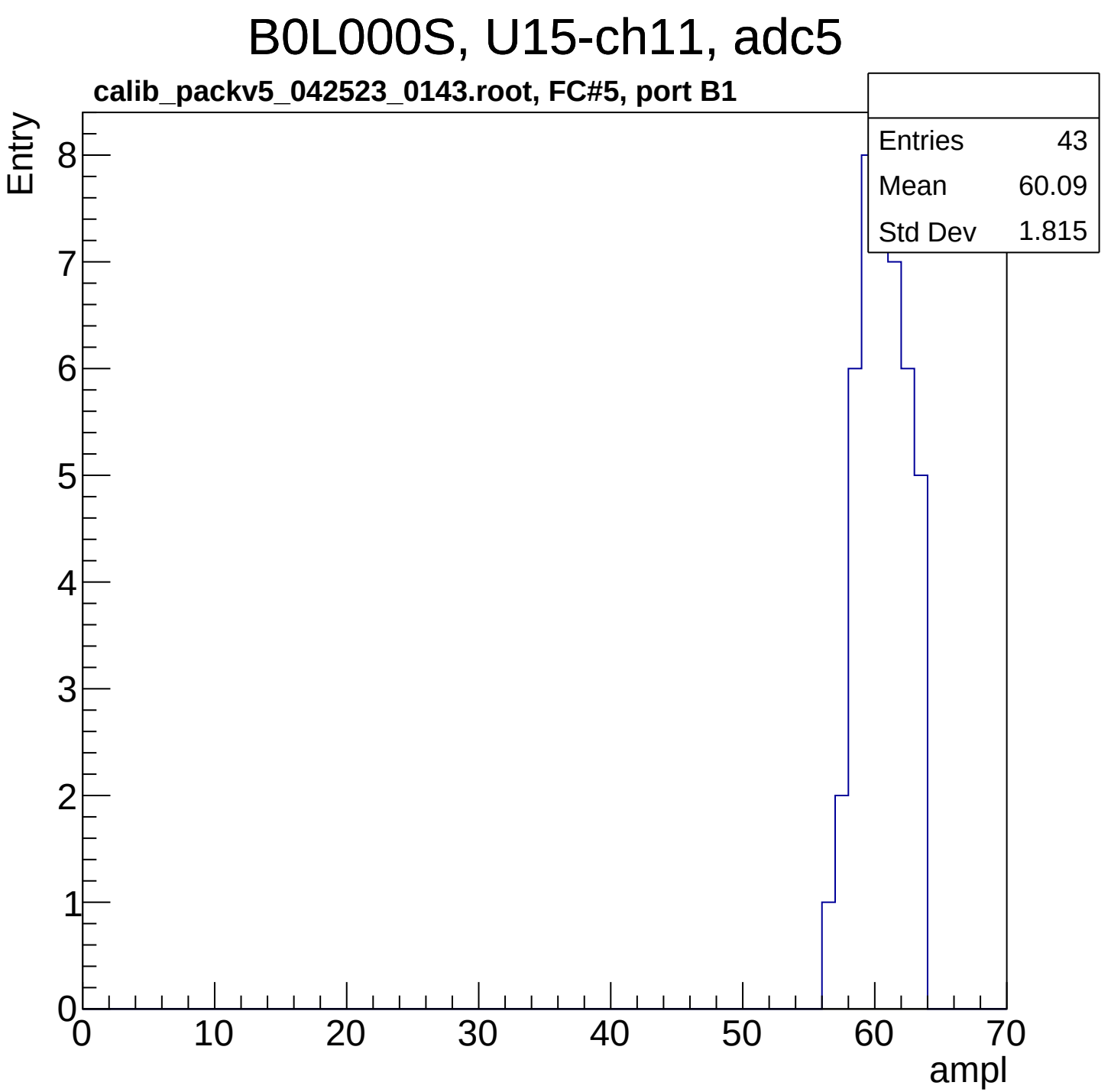
B0L000S, U15-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.09
Std Dev 1.815
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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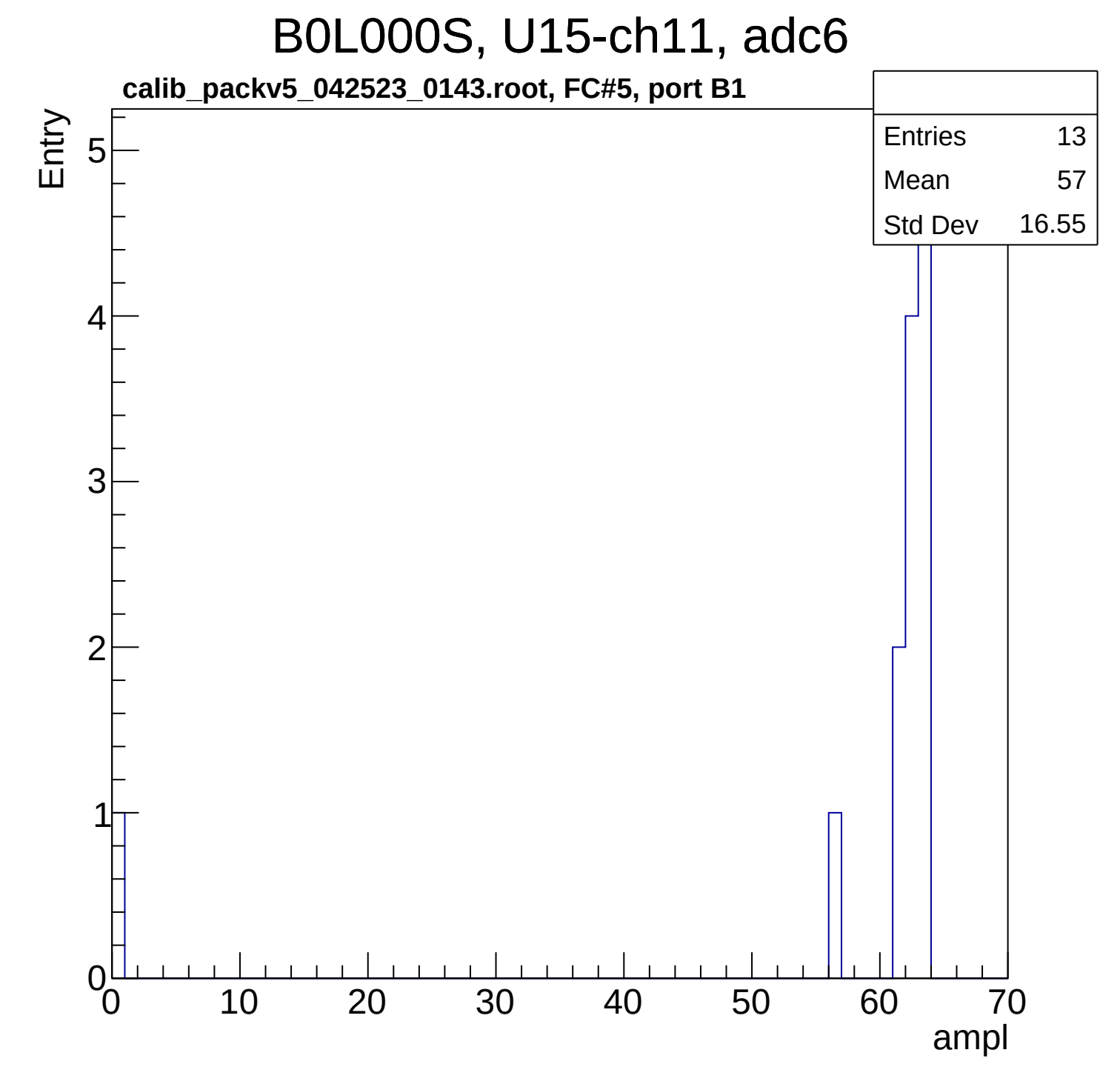
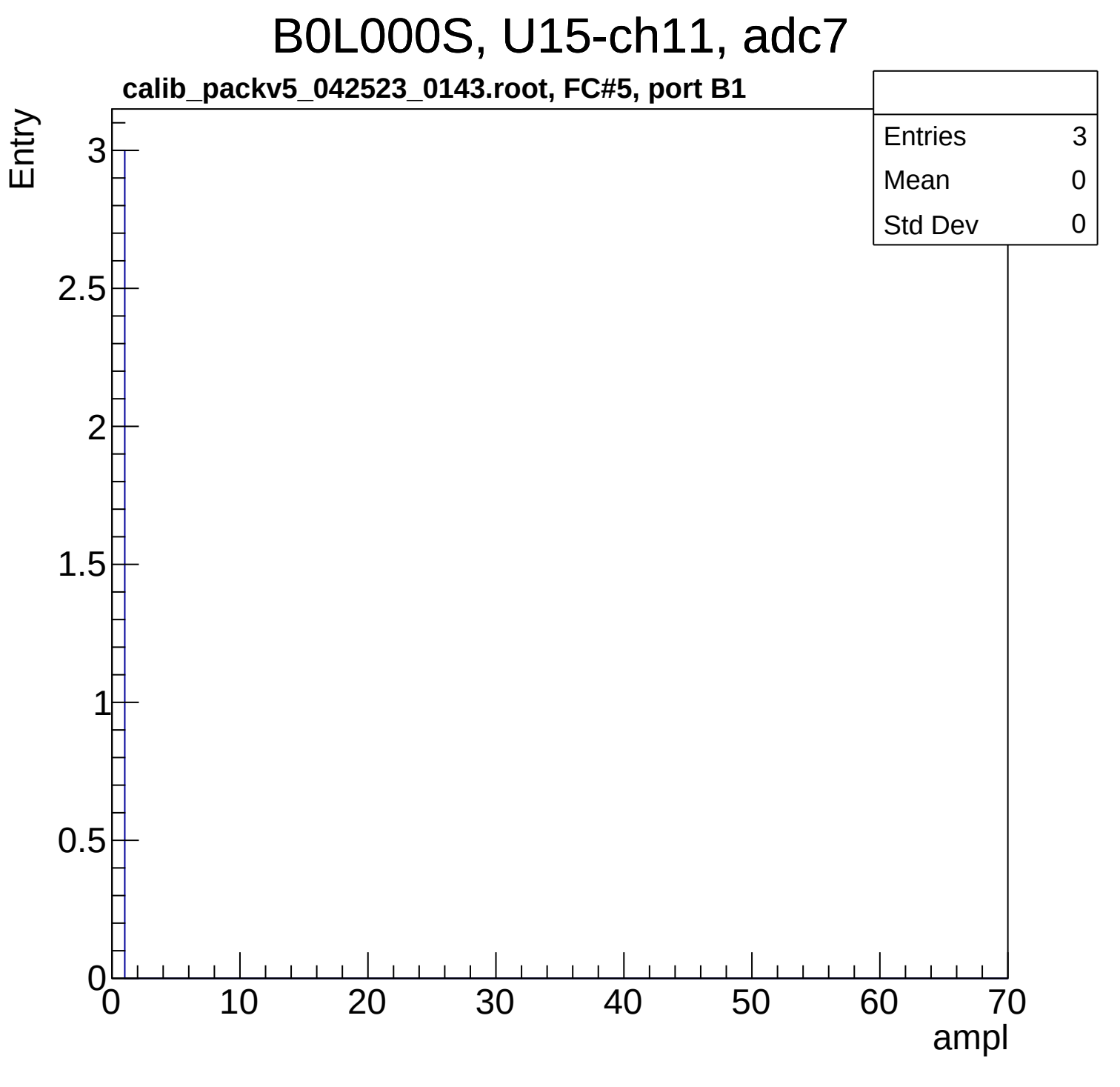
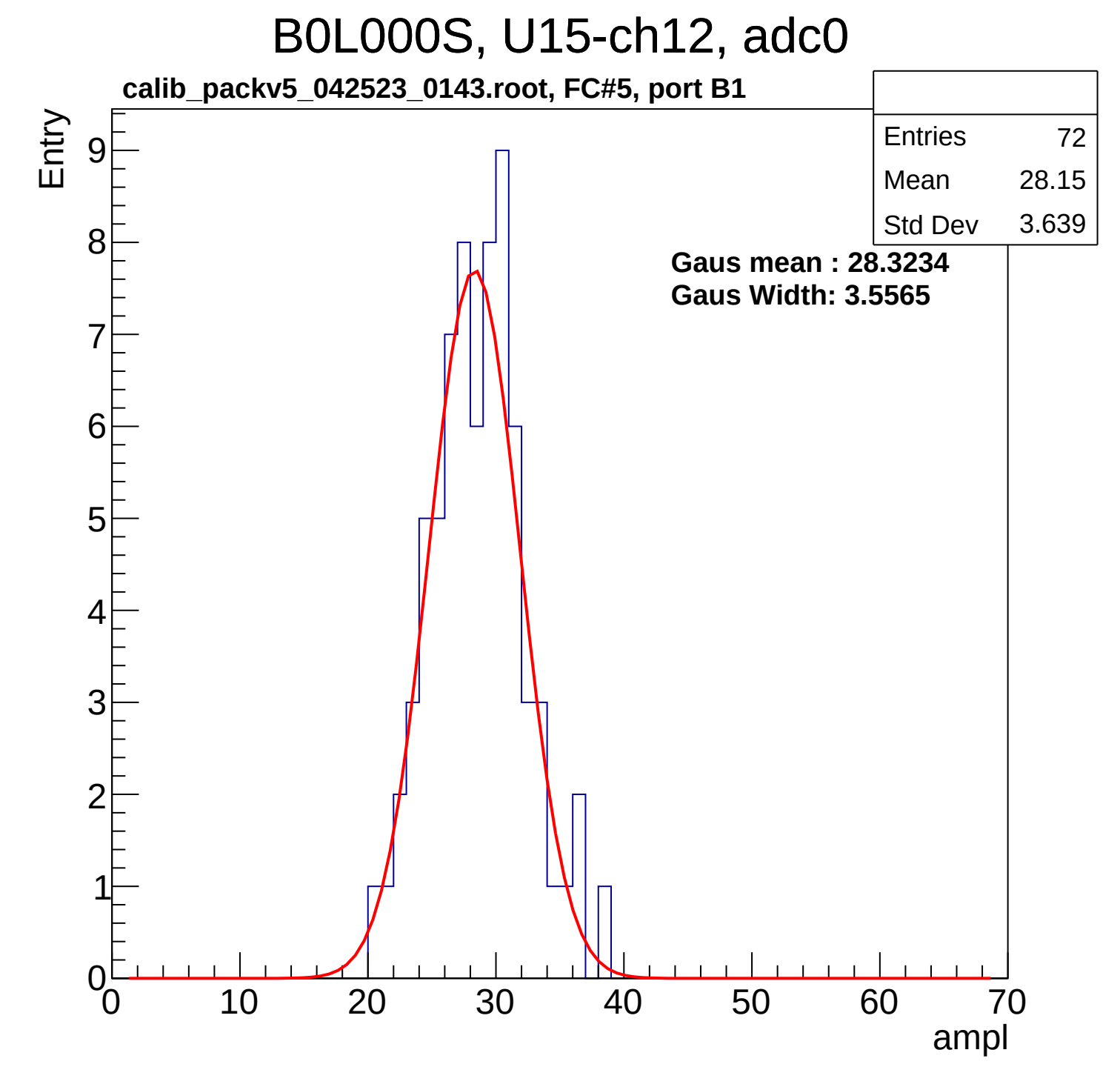

B0L000S, U15-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57
Std Dev 16.55
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.15
Std Dev 3.639
Gaus mean : 28.3234
Gaus Width: 3.5565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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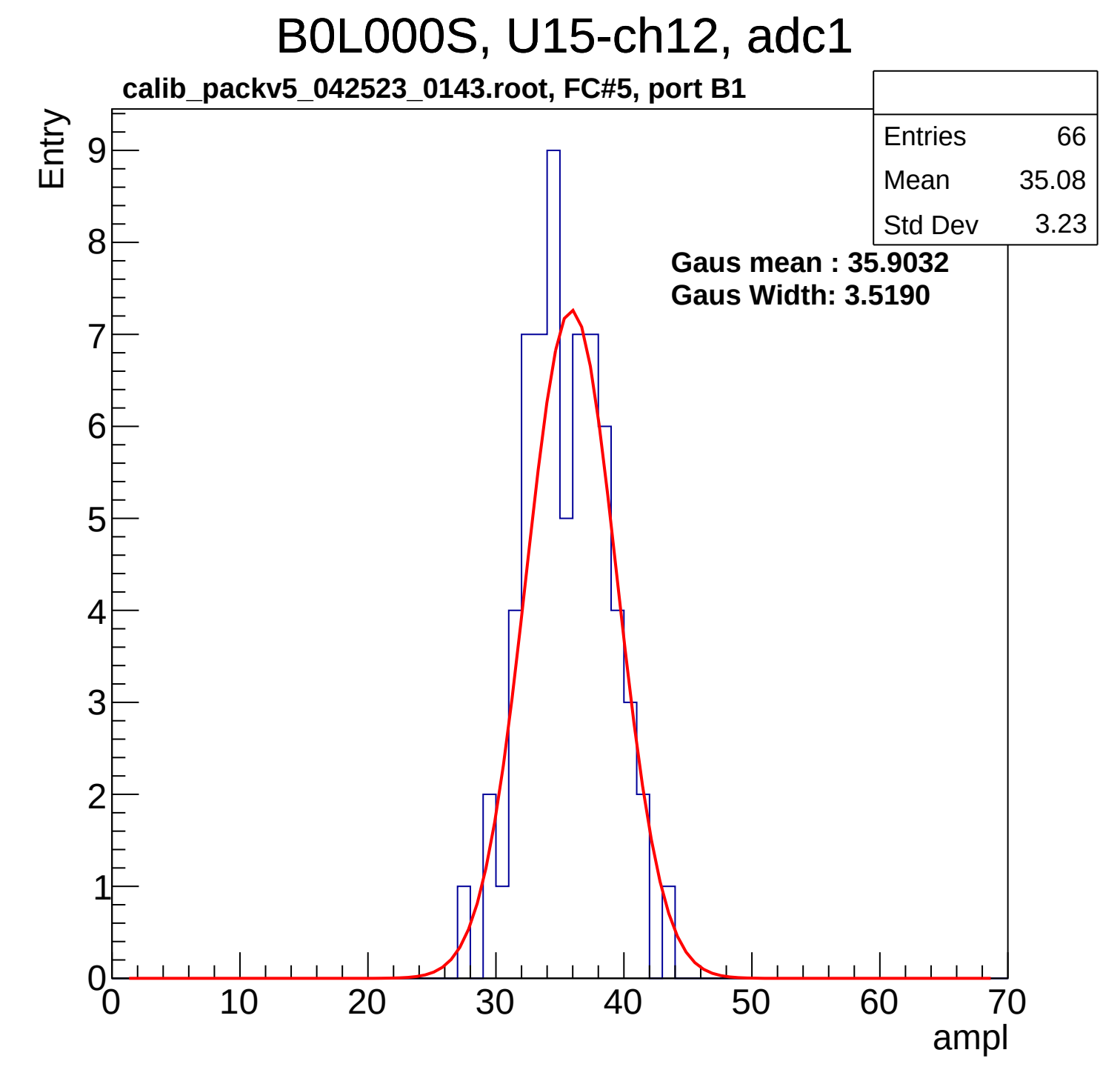
B0L000S, U15-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.08
Std Dev 3.23
Gaus mean : 35.9032
Gaus Width: 3.5190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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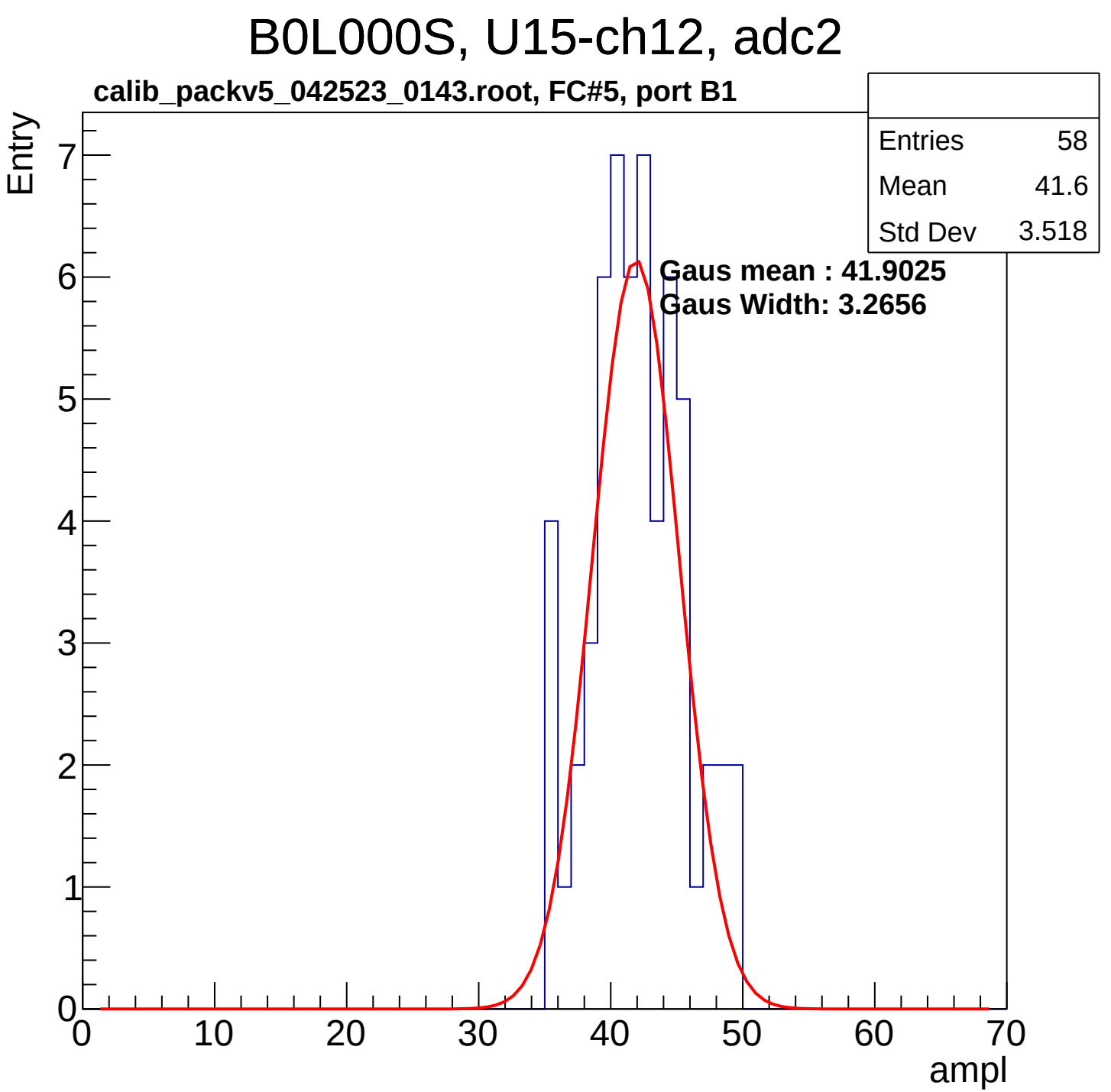
B0L000S, U15-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.6
Std Dev 3.518
Gaus mean : 41.9025
Gaus Width: 3.2656
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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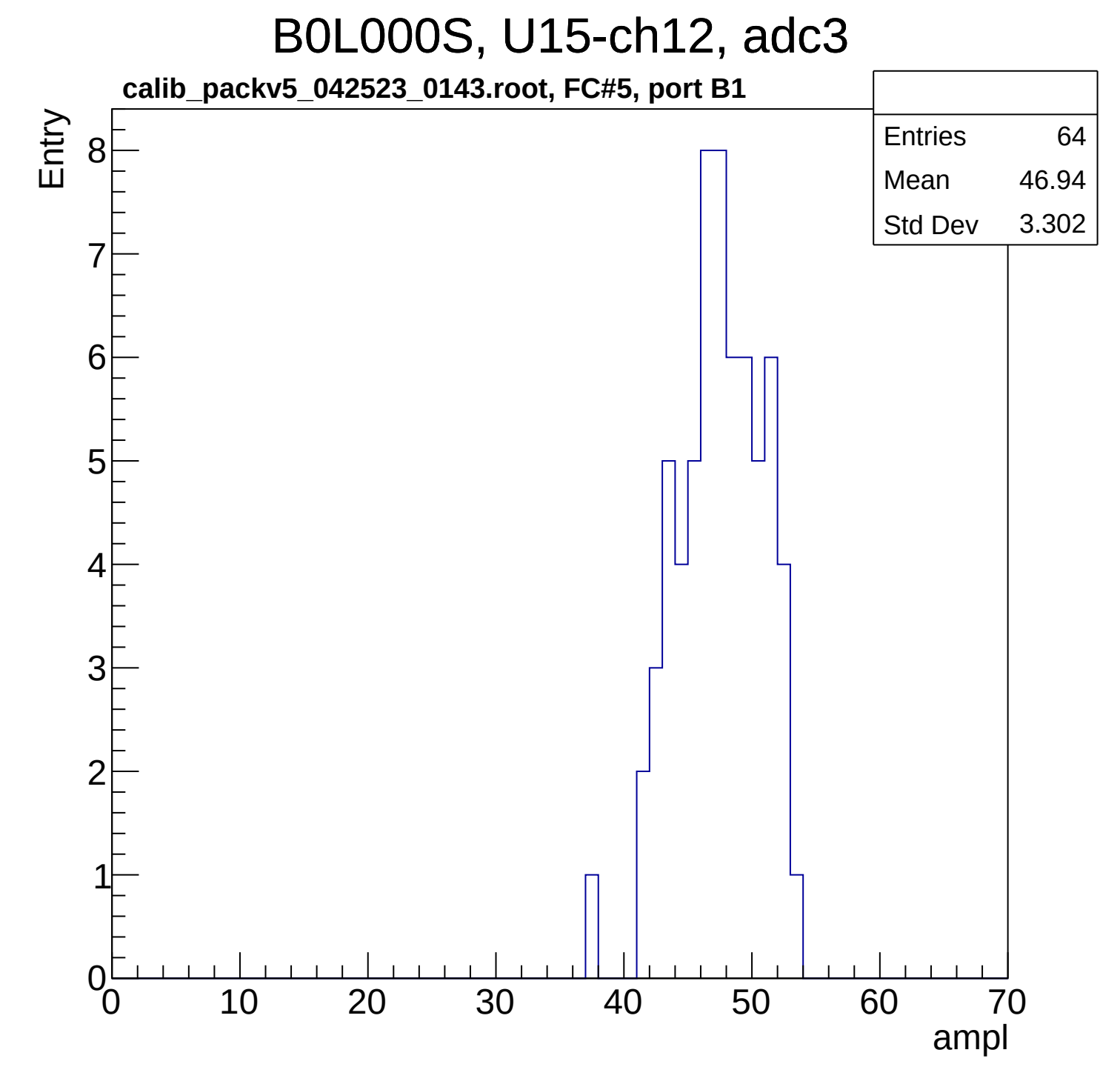
B0L000S, U15-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 46.94
Std Dev 3.302
Entry
ampl
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2
3
4
5
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0
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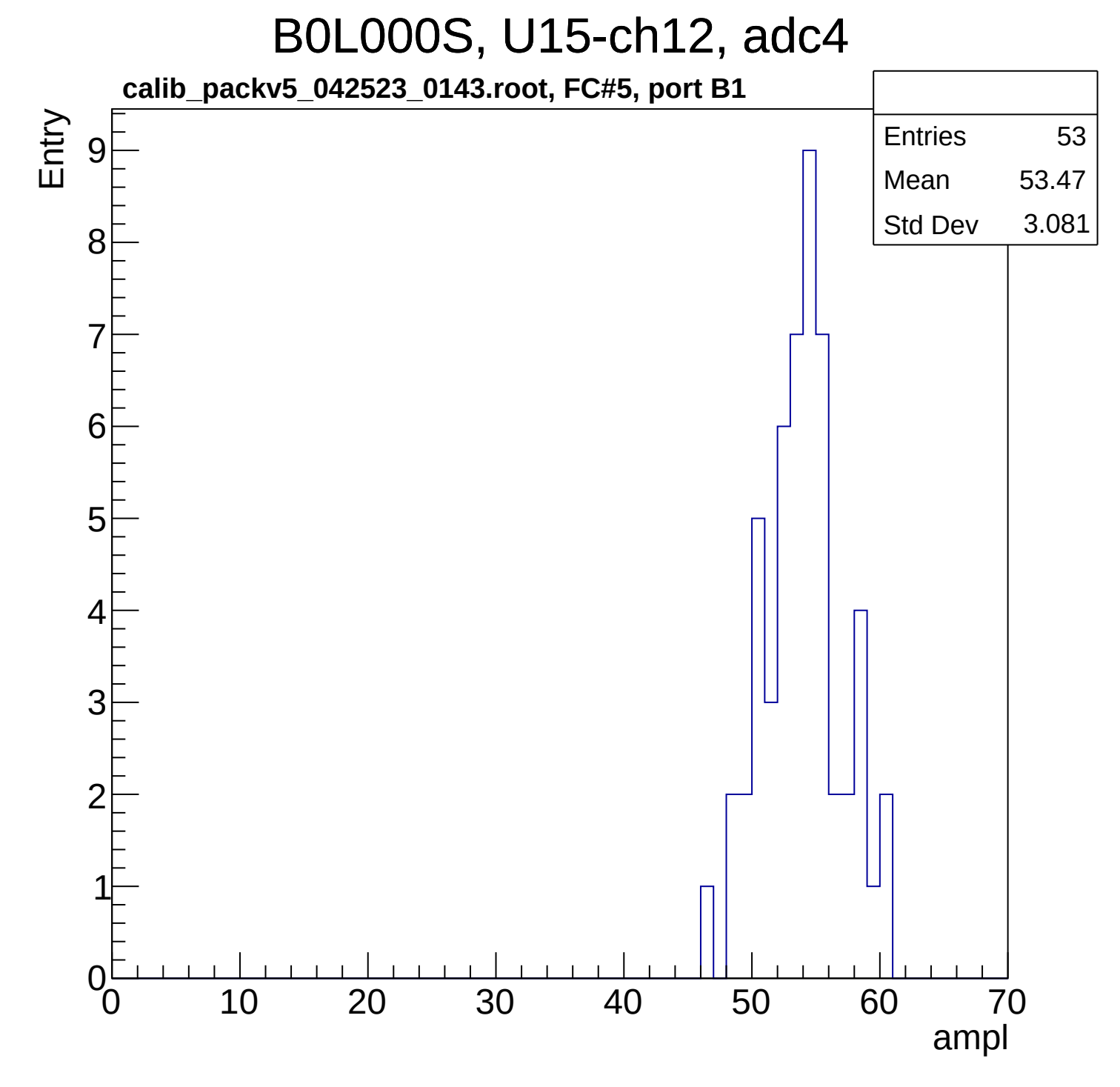
B0L000S, U15-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.47
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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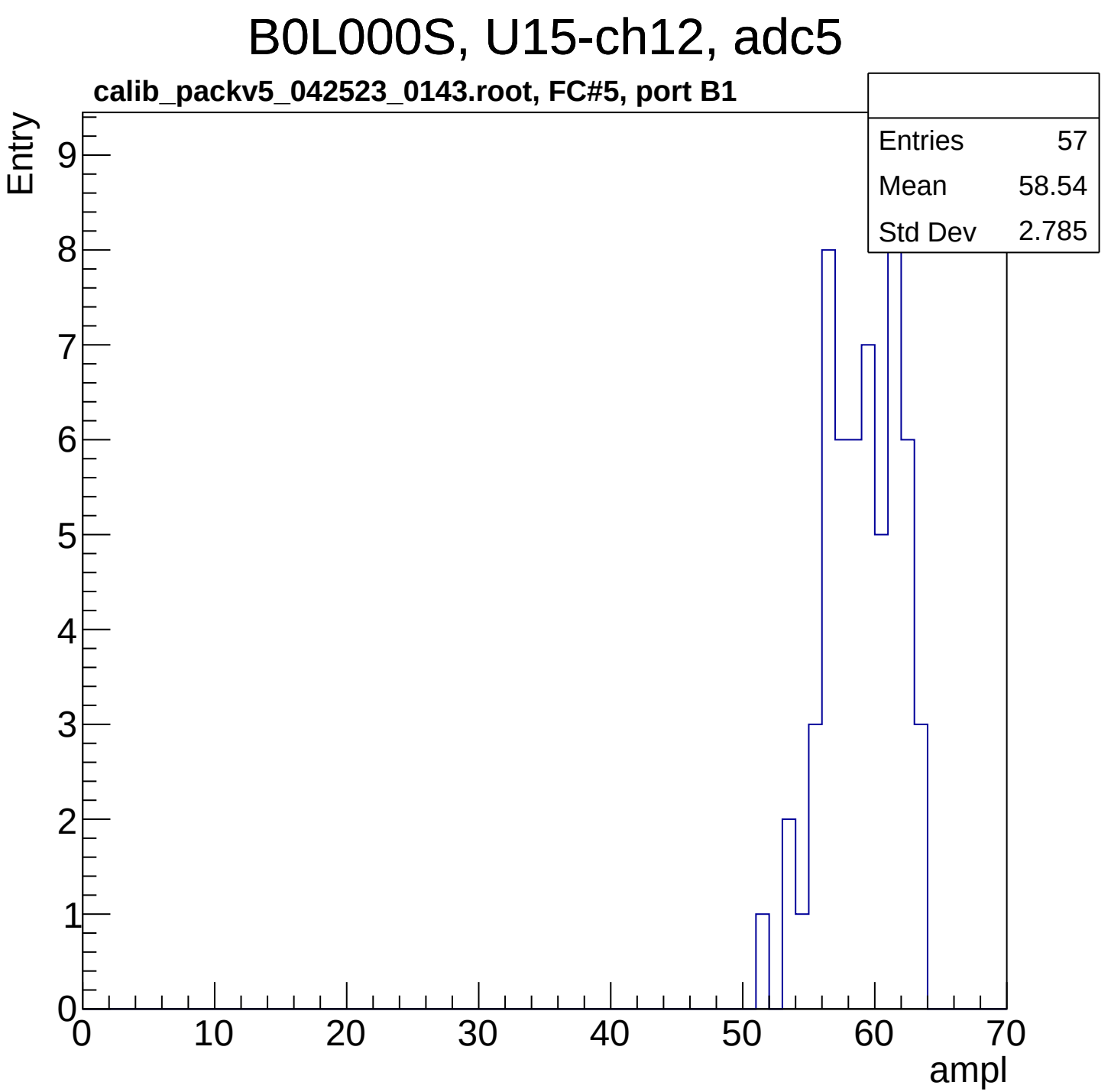
B0L000S, U15-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.54
Std Dev 2.785
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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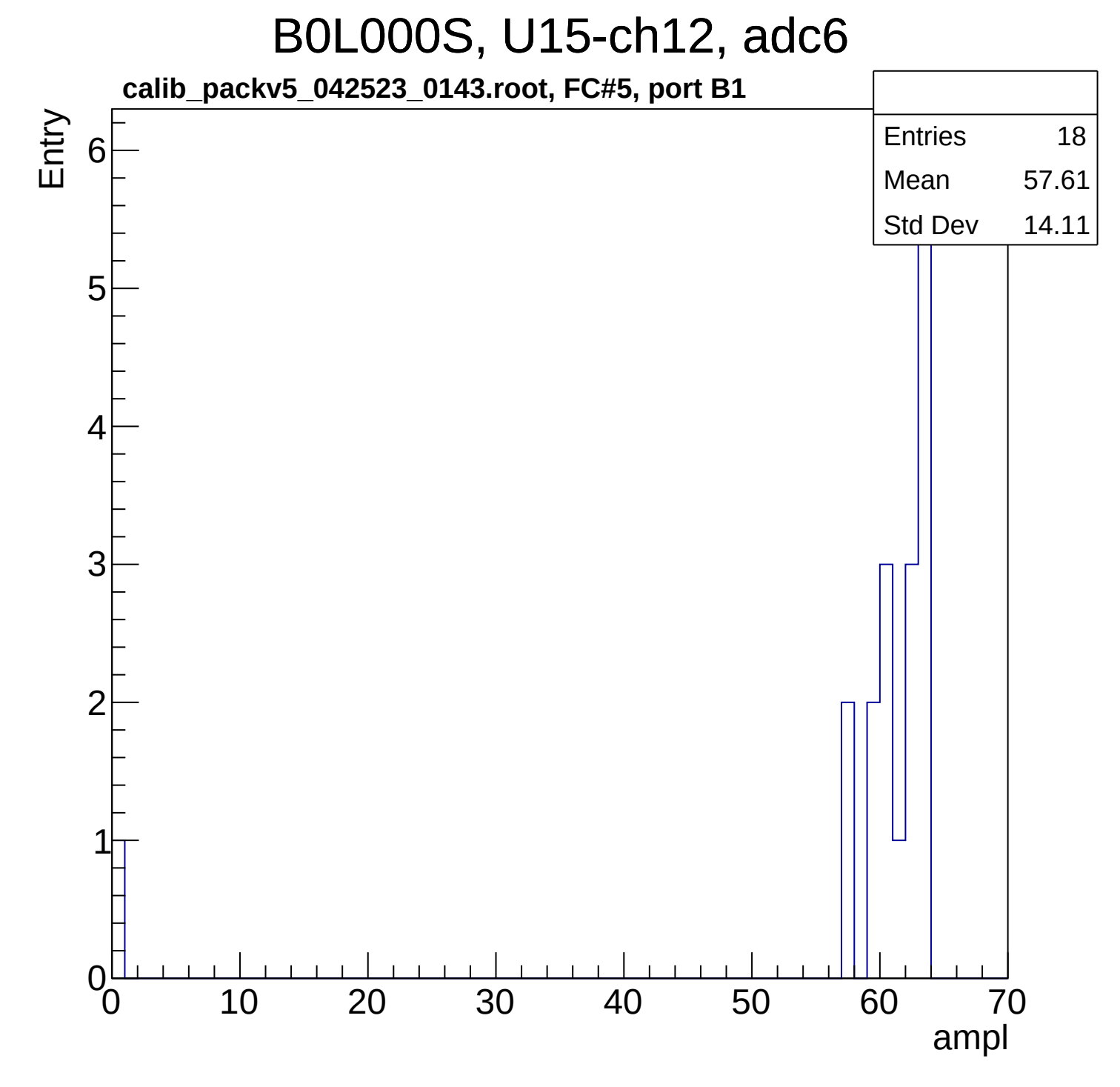
B0L000S, U15-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 57.61
Std Dev 14.11
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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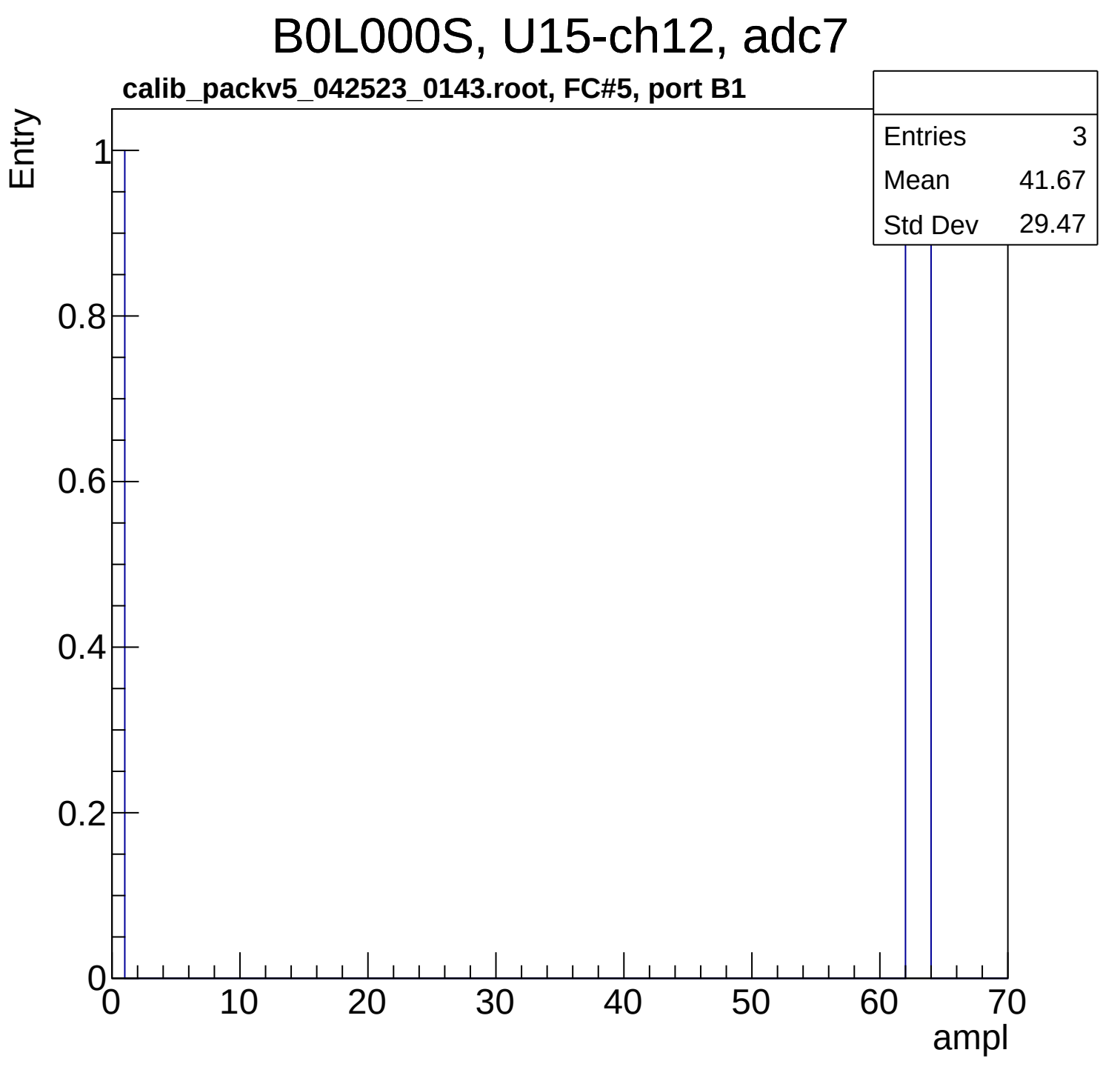
B0L000S, U15-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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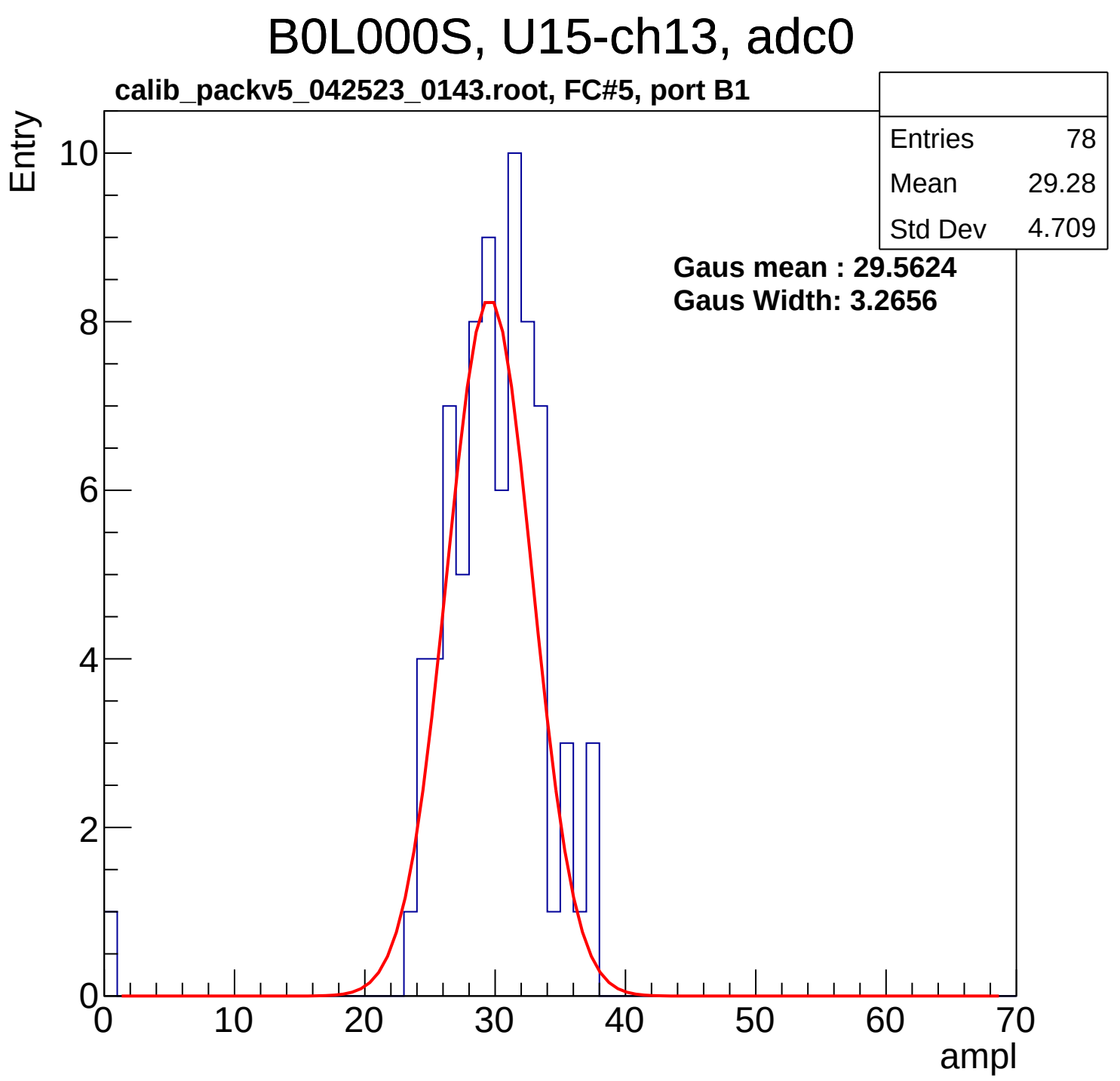
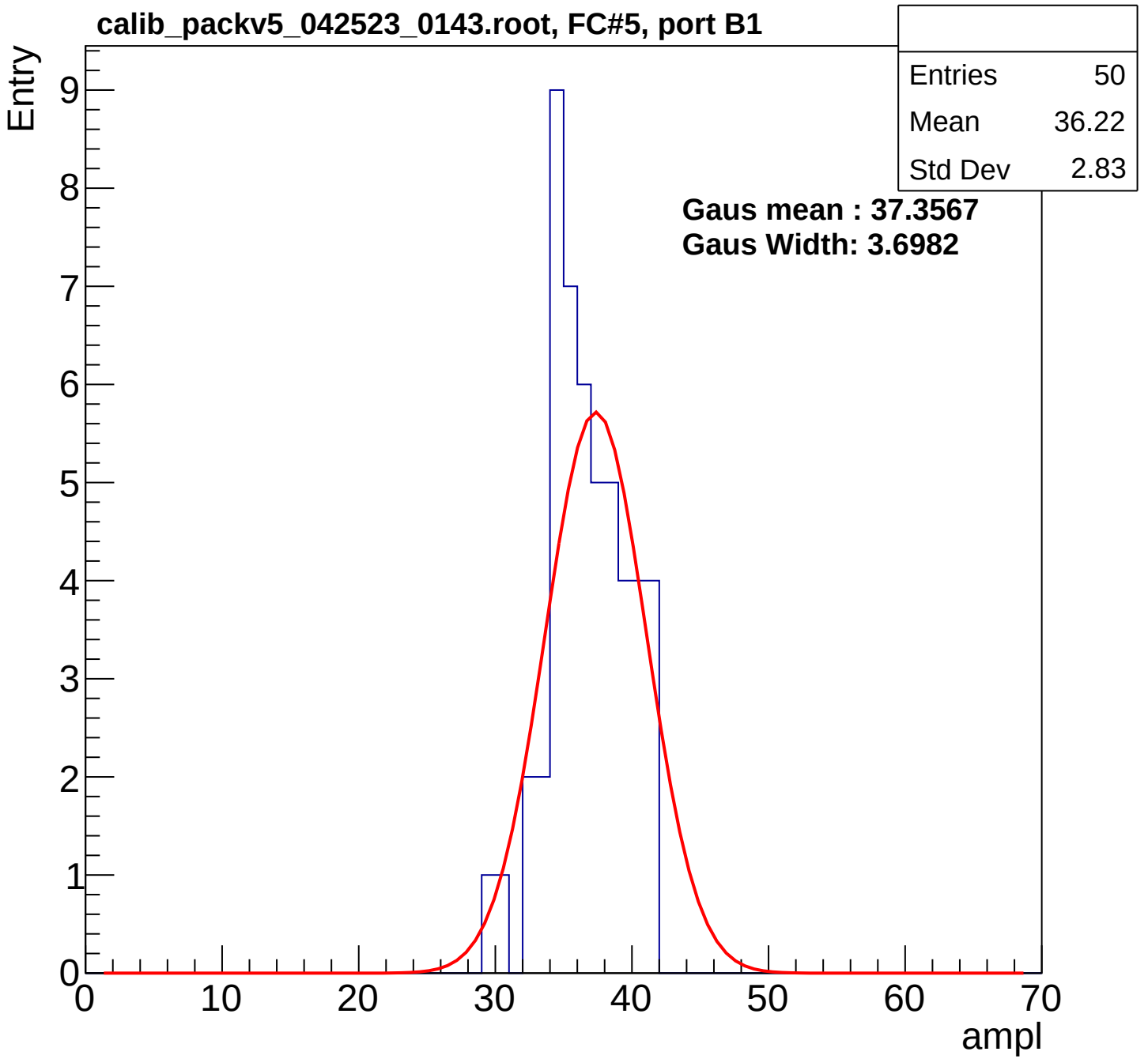
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 29.28
Std Dev 4.709
Gaus mean : 29.5624
Gaus Width: 3.2656
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 36.22
Std Dev 2.83
Gaus mean : 37.3567
Gaus Width: 3.6982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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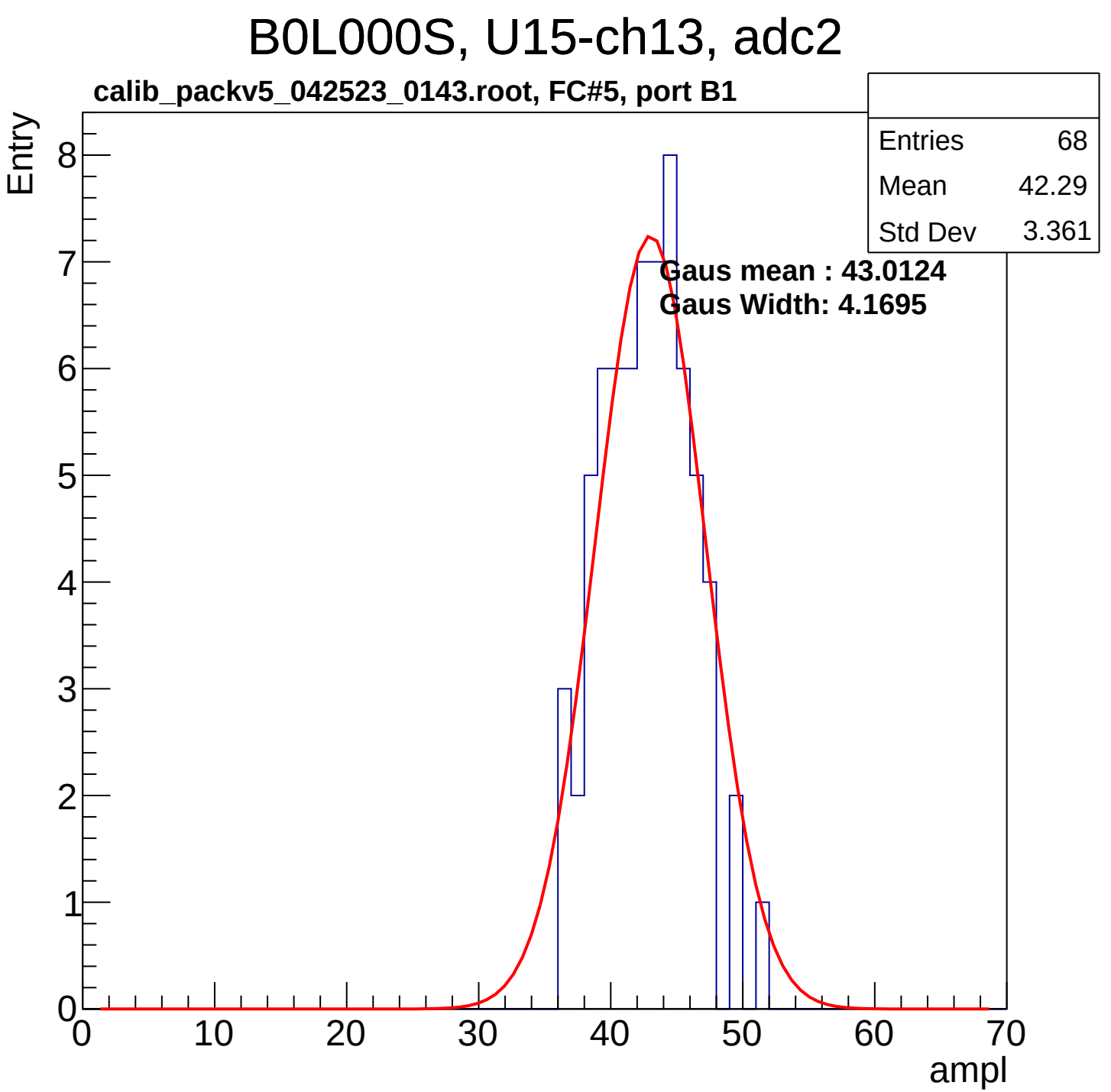
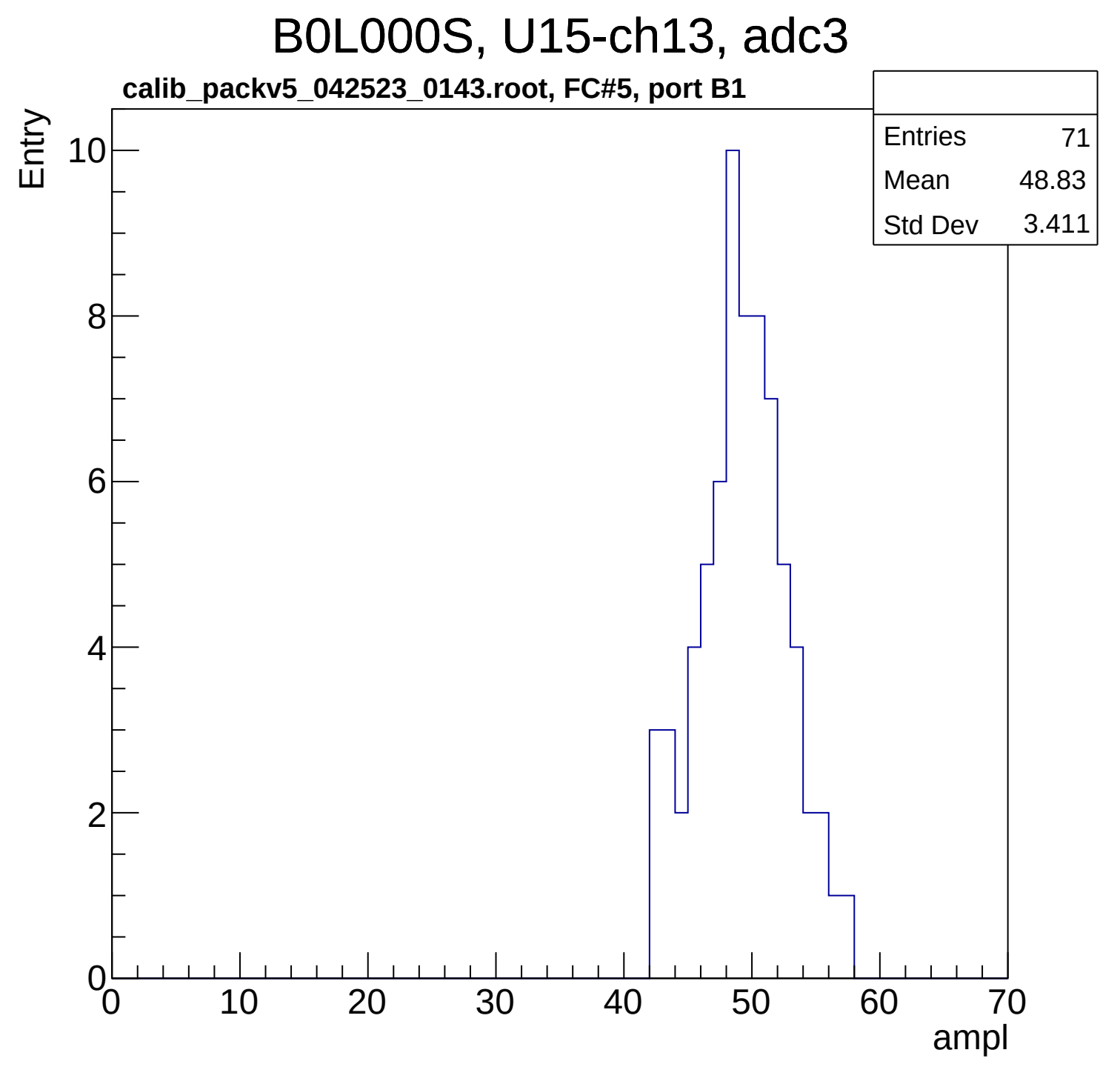
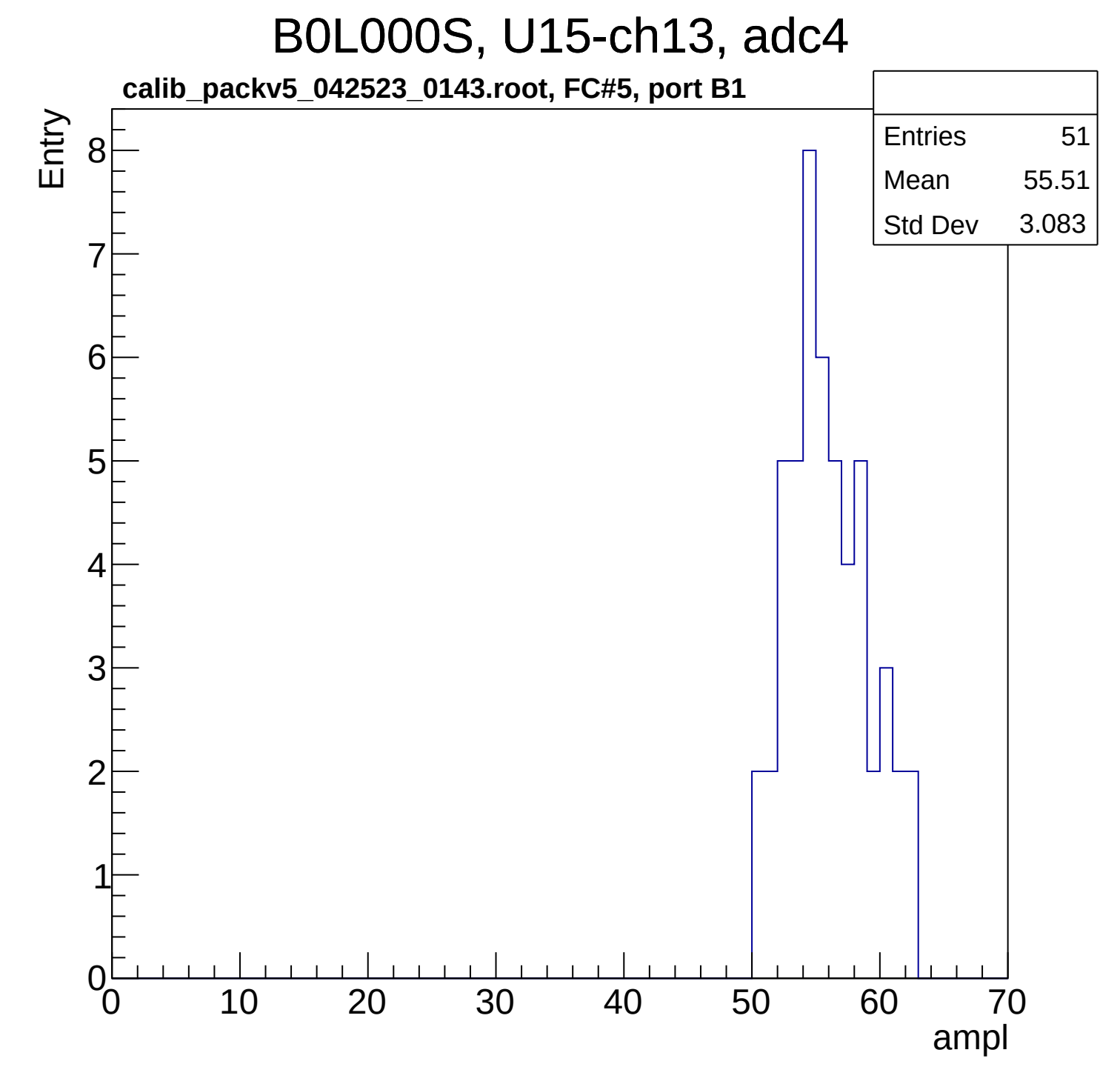

B0L000S, U15-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.29
Std Dev 3.361
Gaus mean : 43.0124
Gaus Width: 4.1695
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.83
Std Dev 3.411
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 55.51
Std Dev 3.083
Entry
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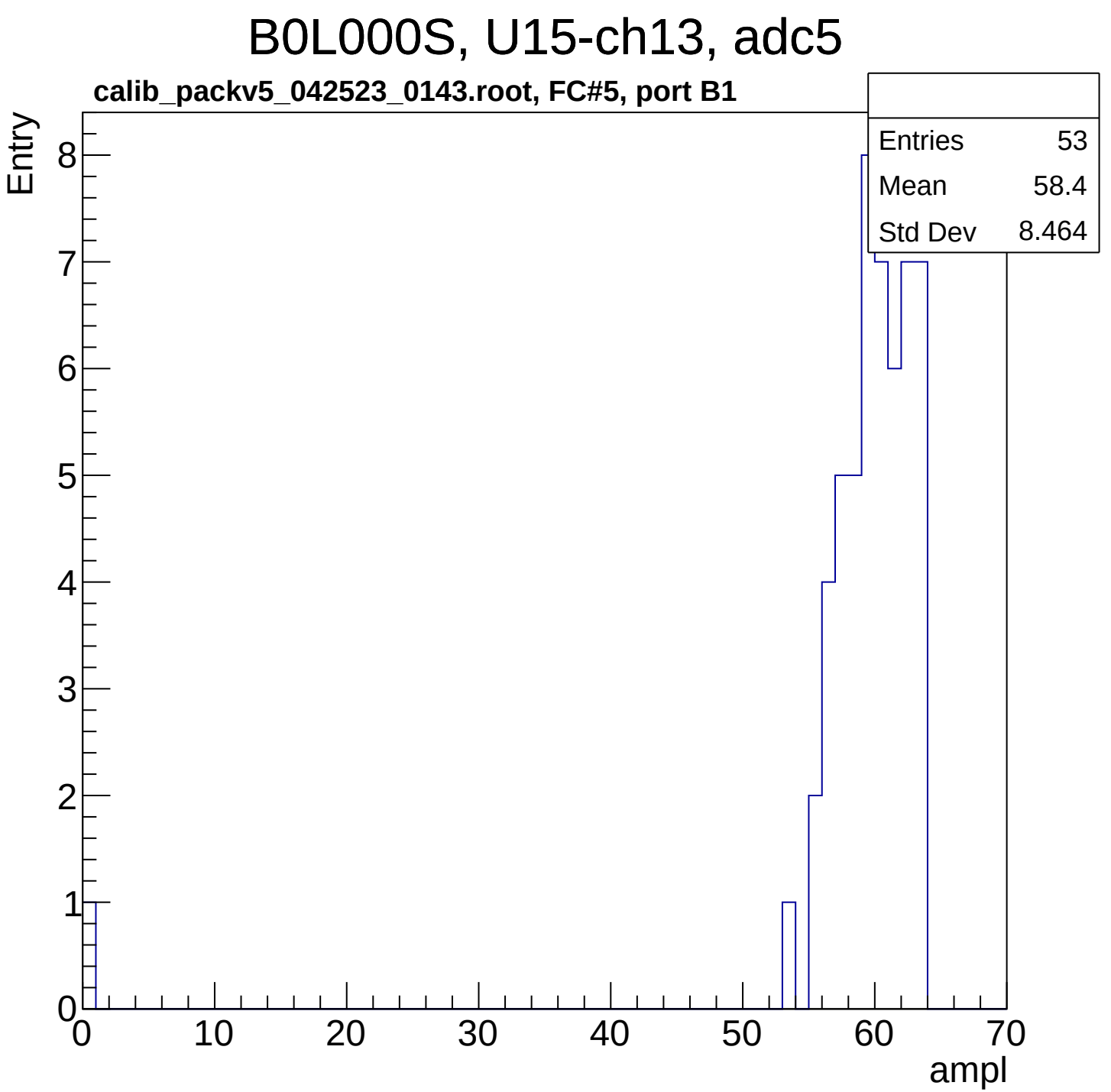
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.4
Std Dev 8.464
Entry
ampl
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2
3
4
5
6
7
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0
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60
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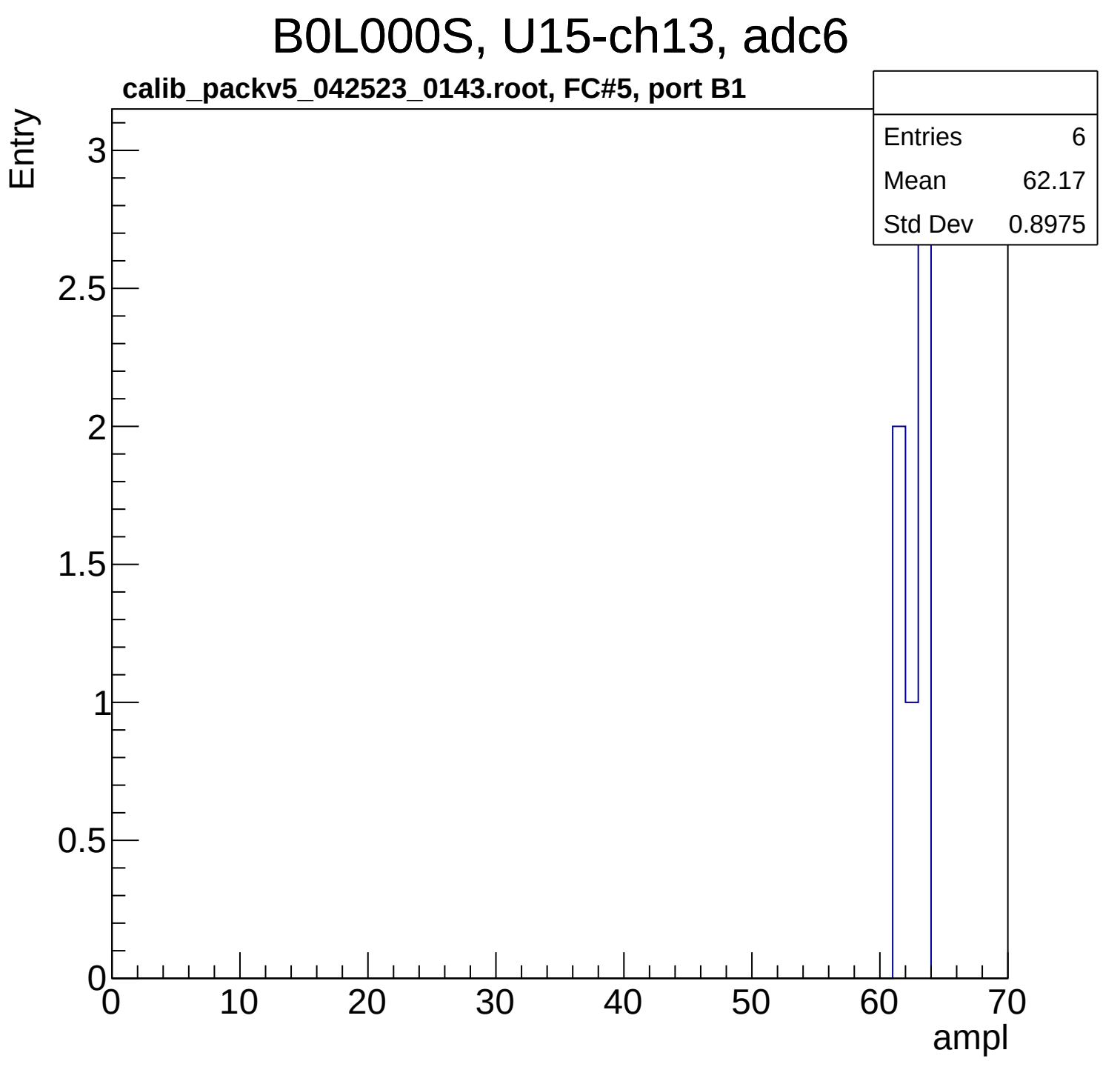
B0L000S, U15-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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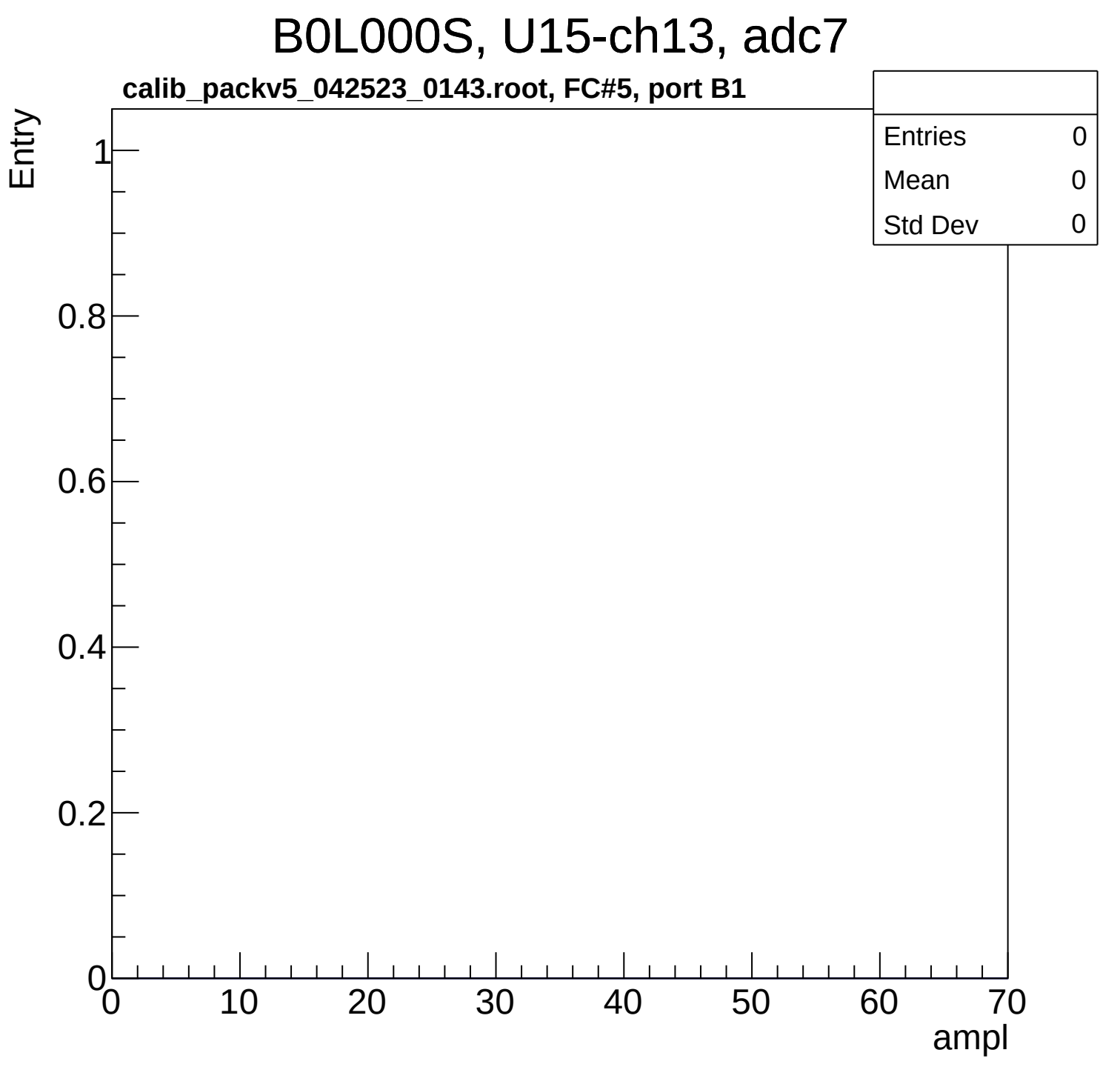
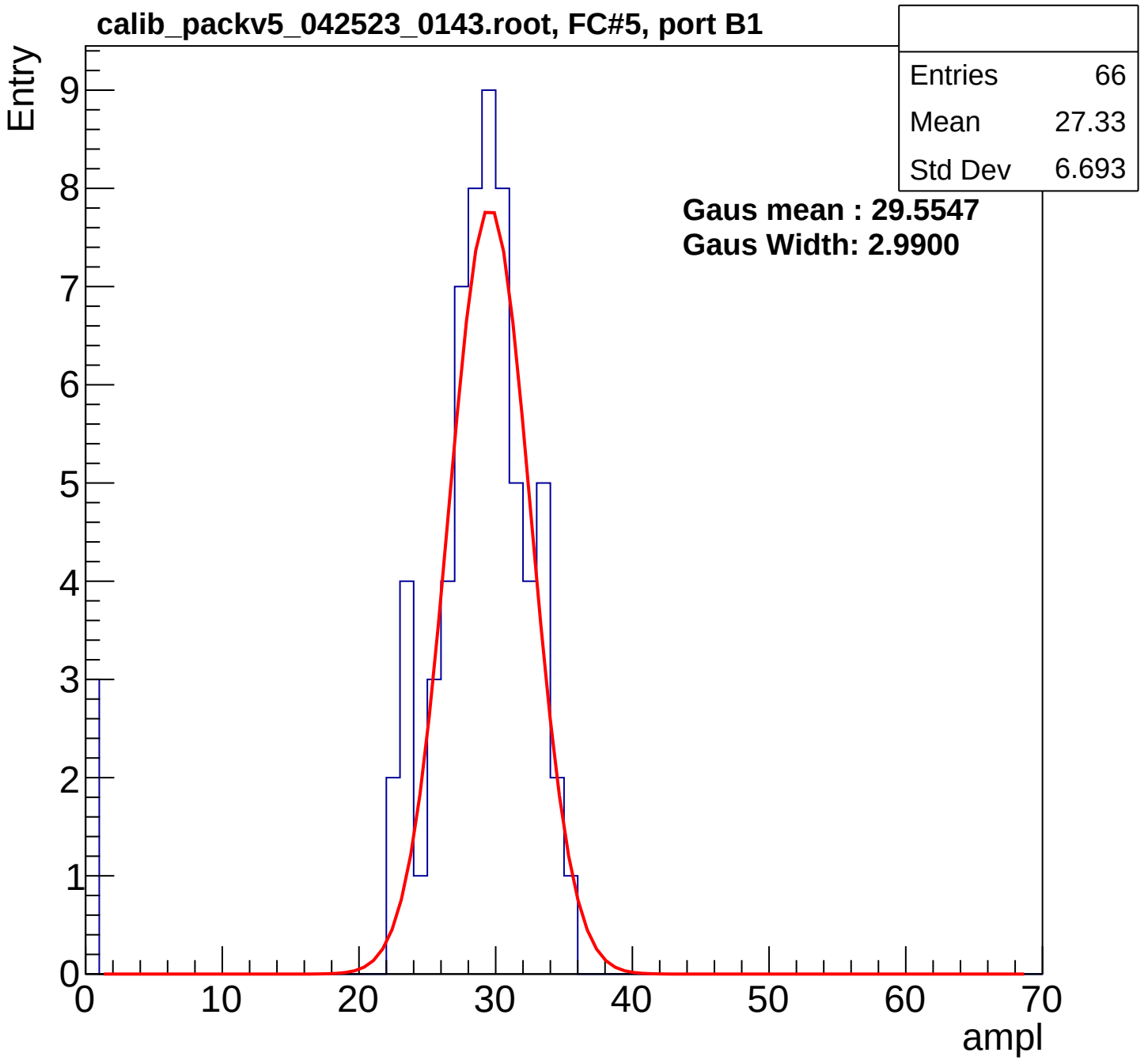

B0L000S, U15-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 27.33
Std Dev 6.693
Gaus mean : 29.5547
Gaus Width: 2.9900
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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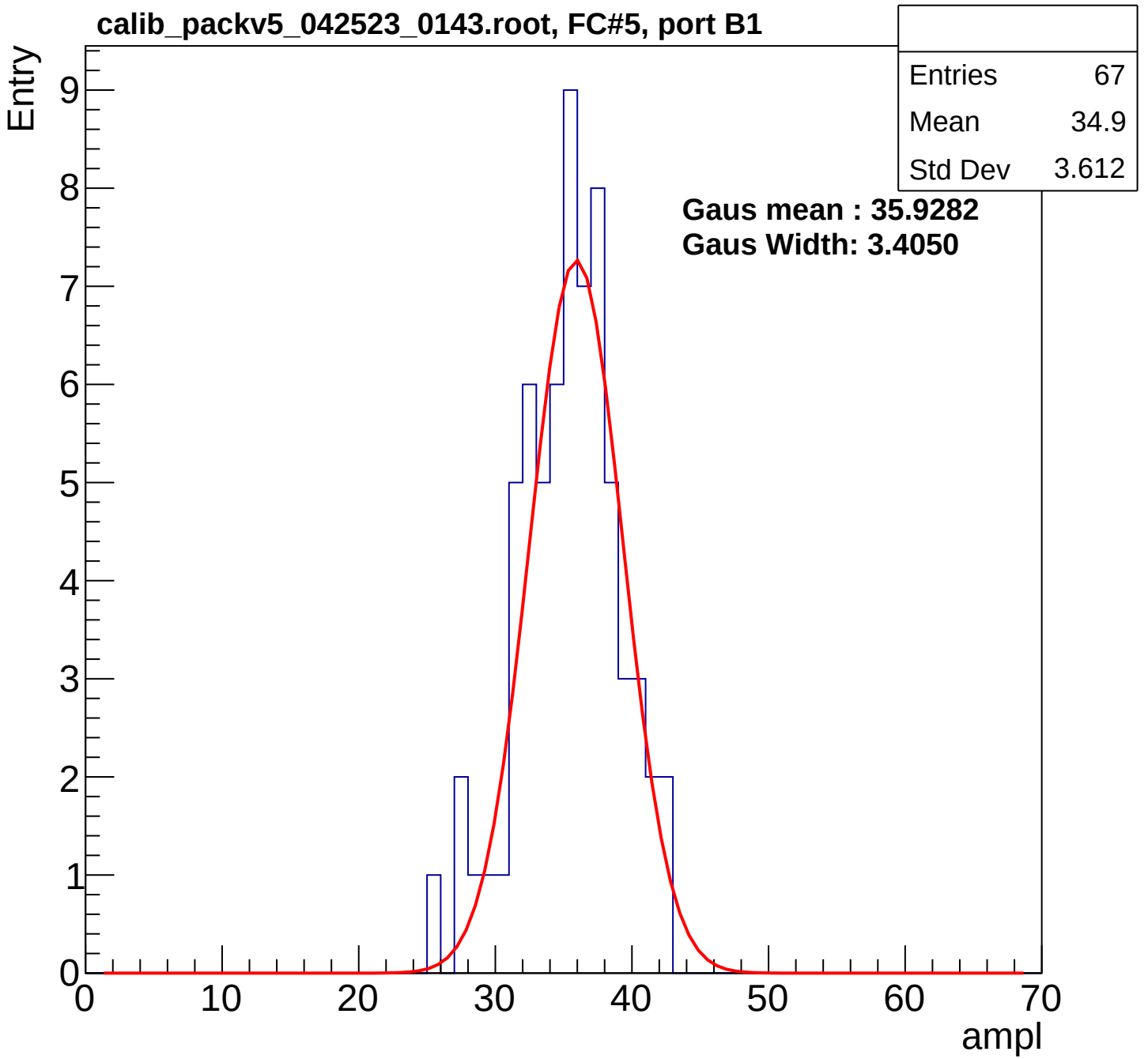
B0L000S, U15-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 34.9
Std Dev 3.612
Gaus mean : 35.9282
Gaus Width: 3.4050
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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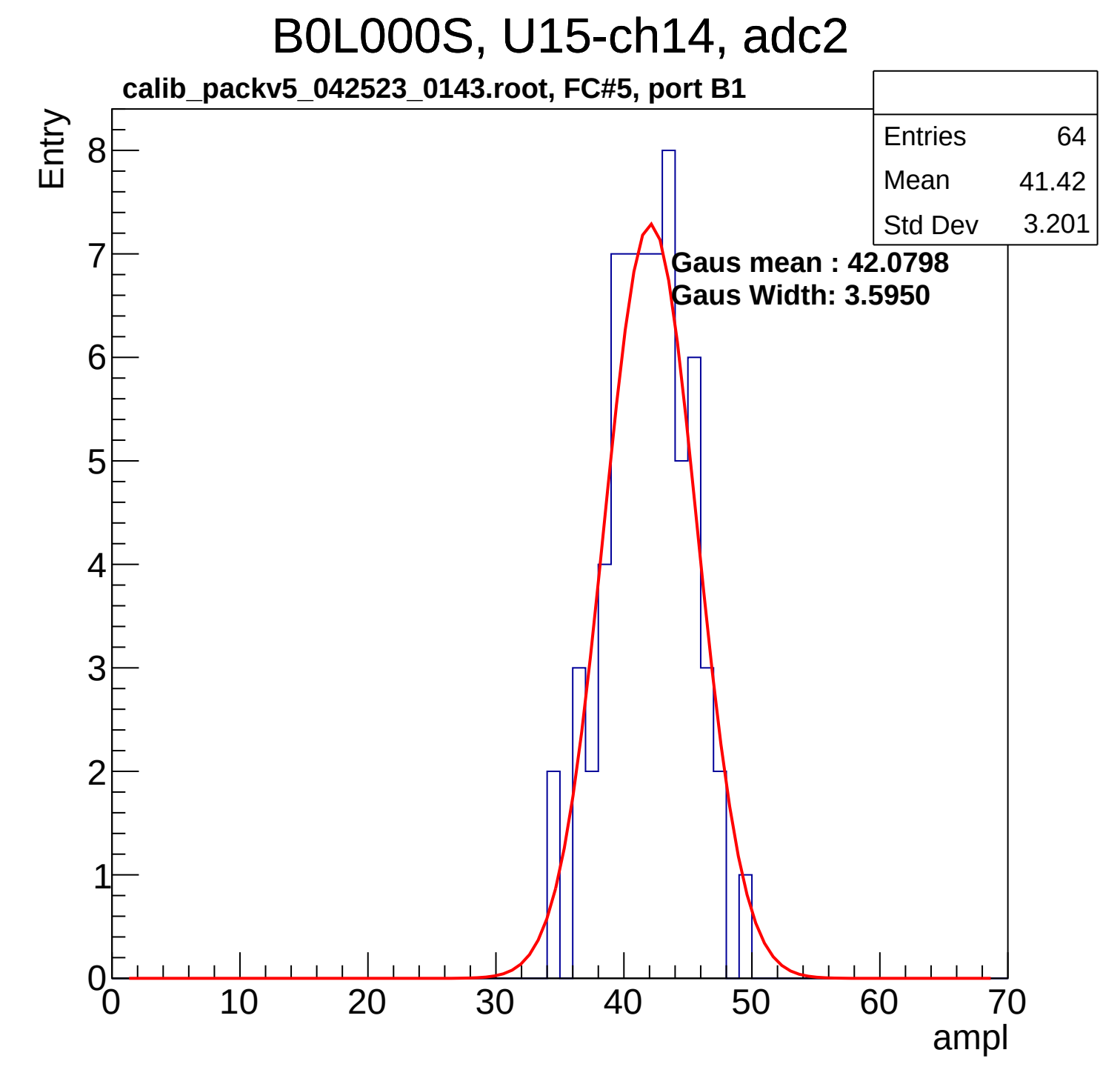
B0L000S, U15-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 41.42
Std Dev 3.201
Gaus mean : 42.0798
Gaus Width: 3.5950
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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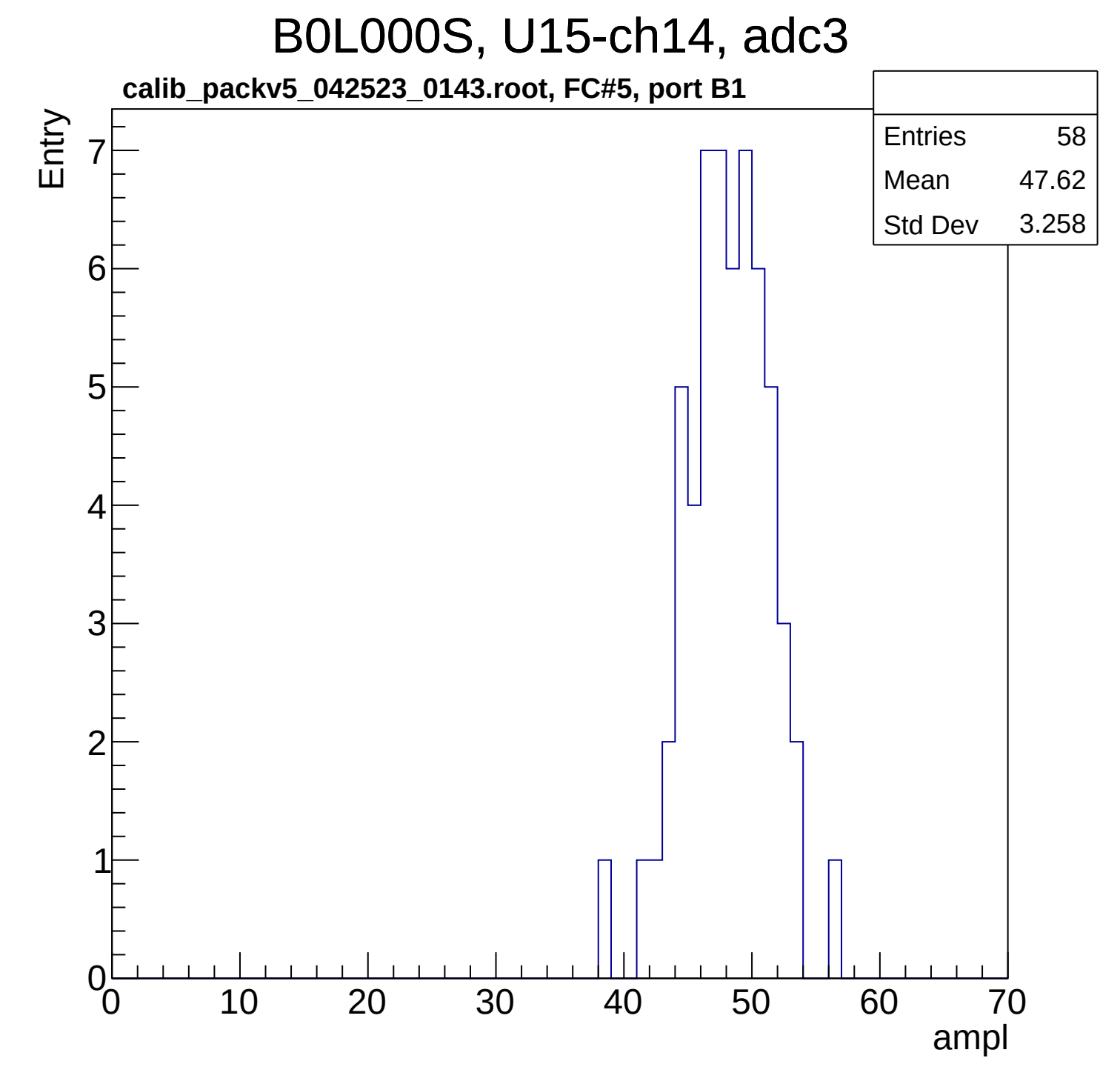
B0L000S, U15-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.62
Std Dev 3.258
Entry
ampl
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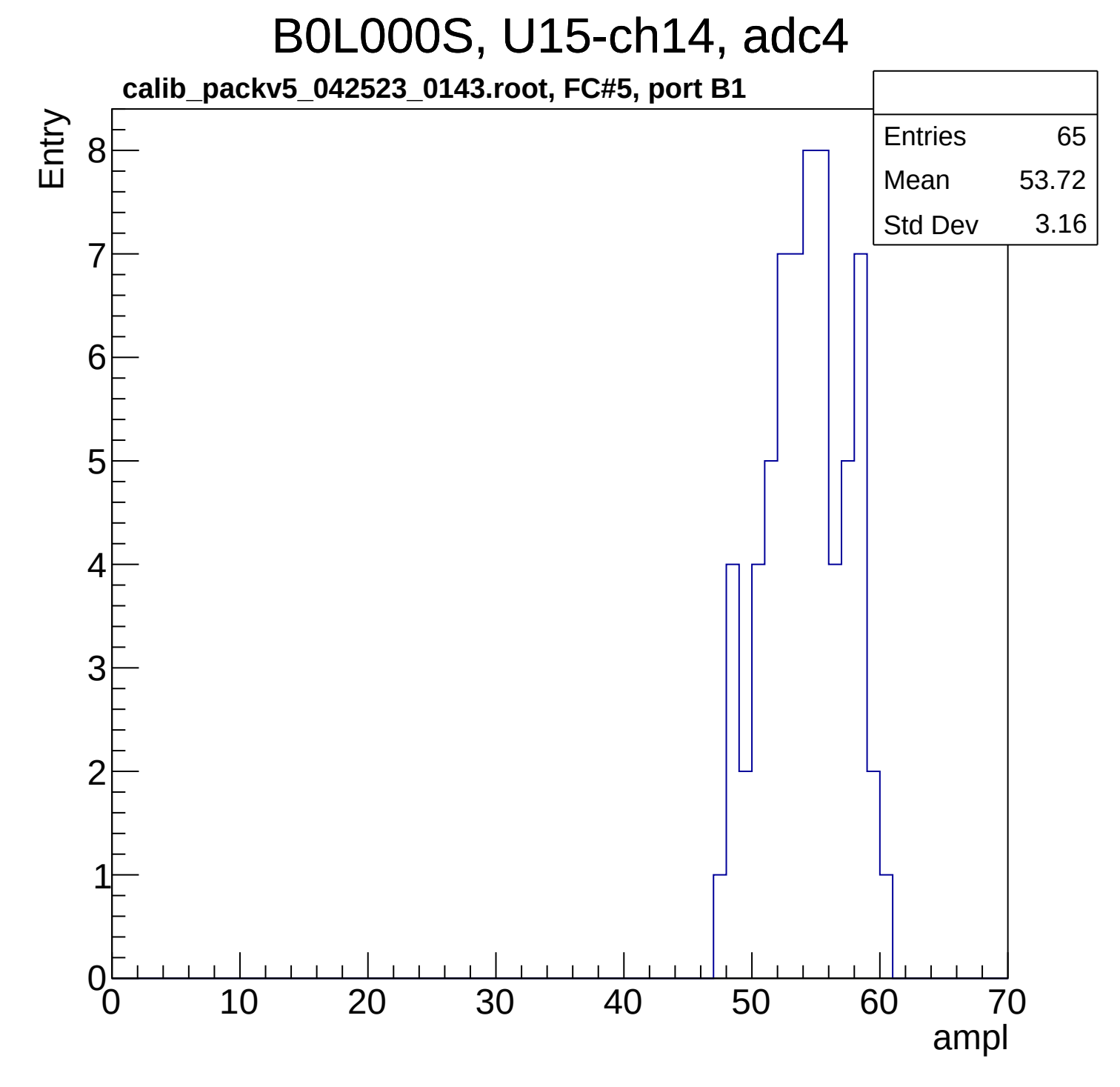
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 53.72
Std Dev 3.16
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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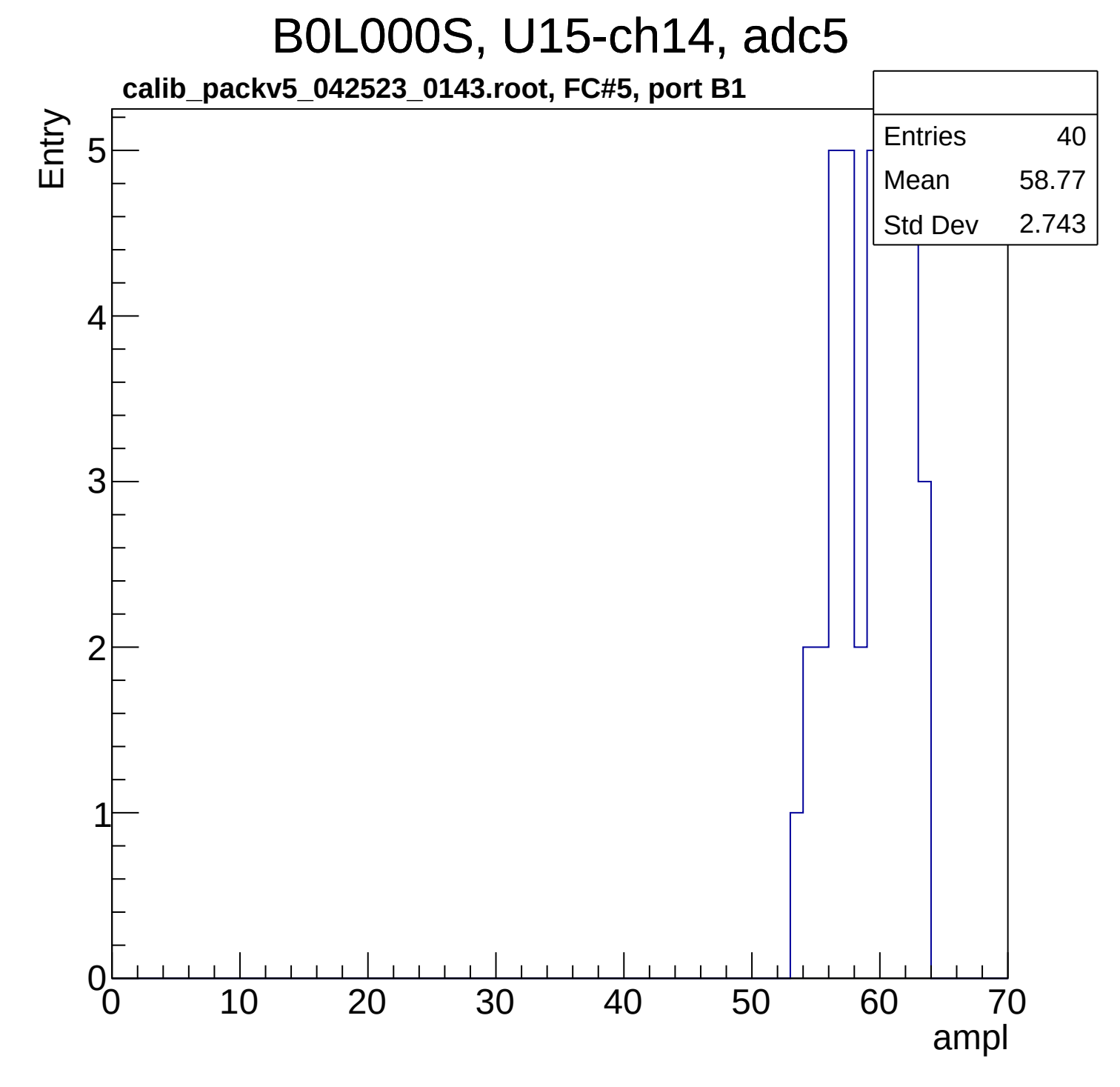
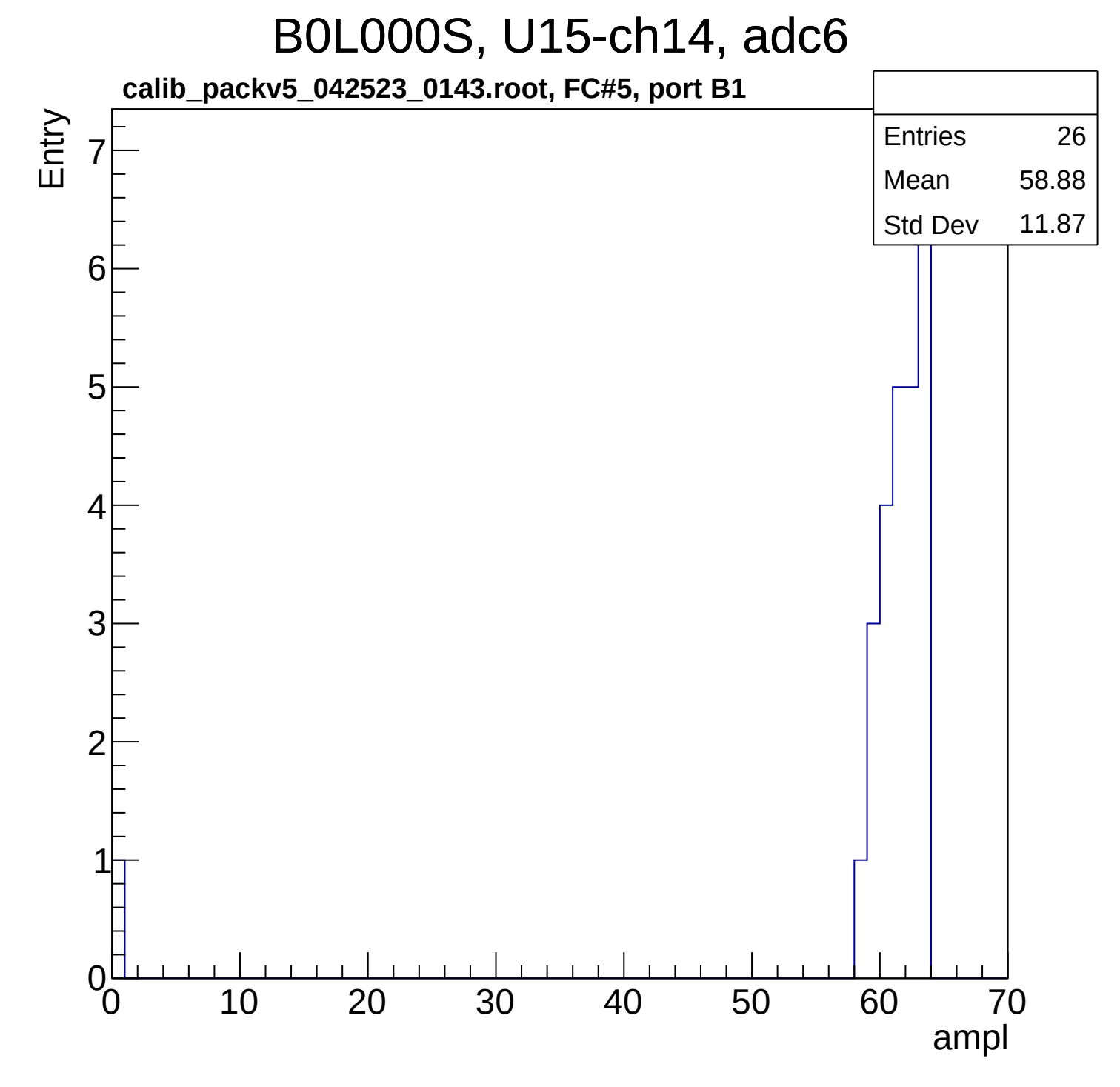

B0L000S, U15-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.77
Std Dev 2.743
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U15-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 58.88
Std Dev 11.87
Entry
ampl
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3
4
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6
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0
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60
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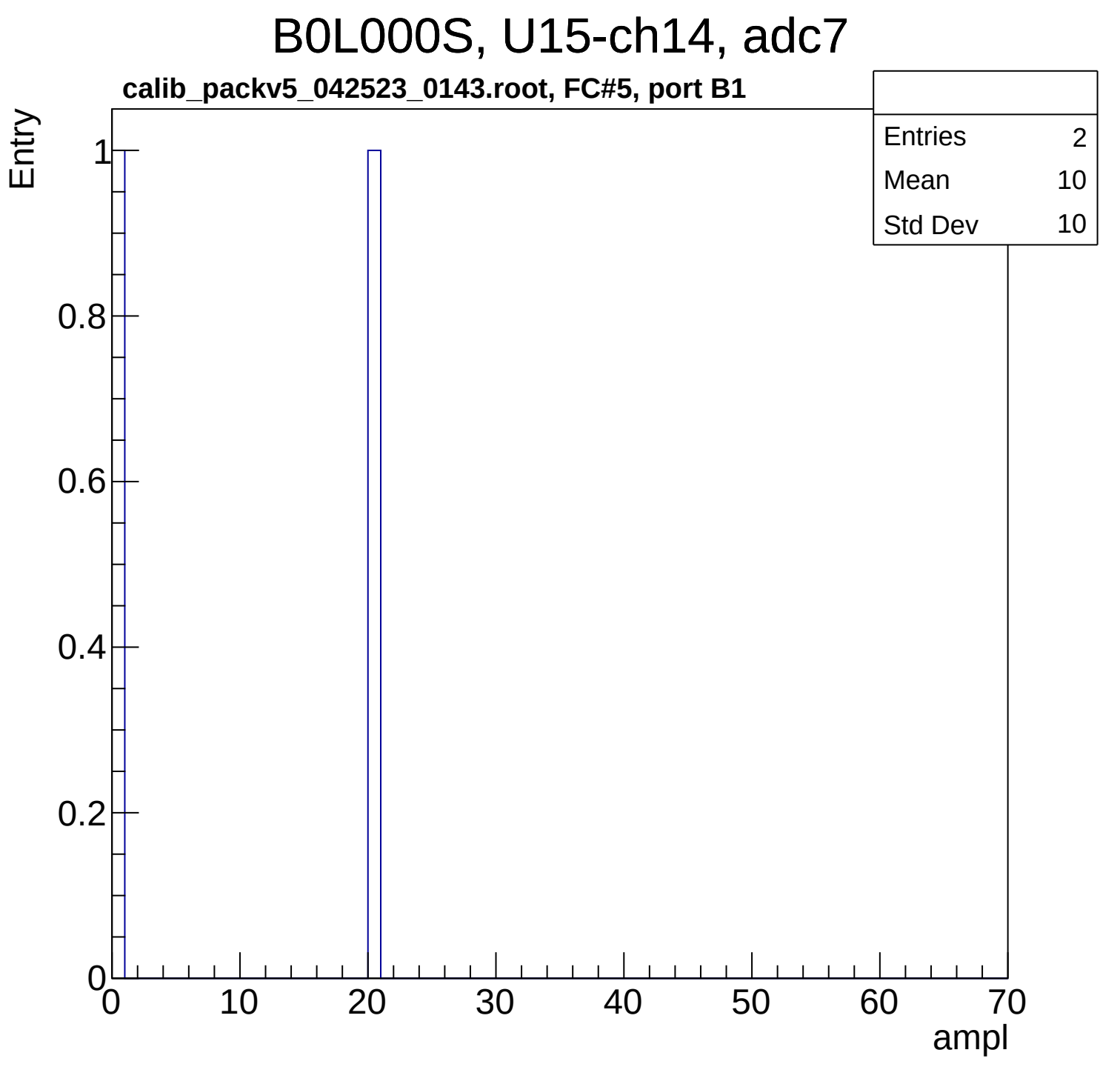
B0L000S, U15-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
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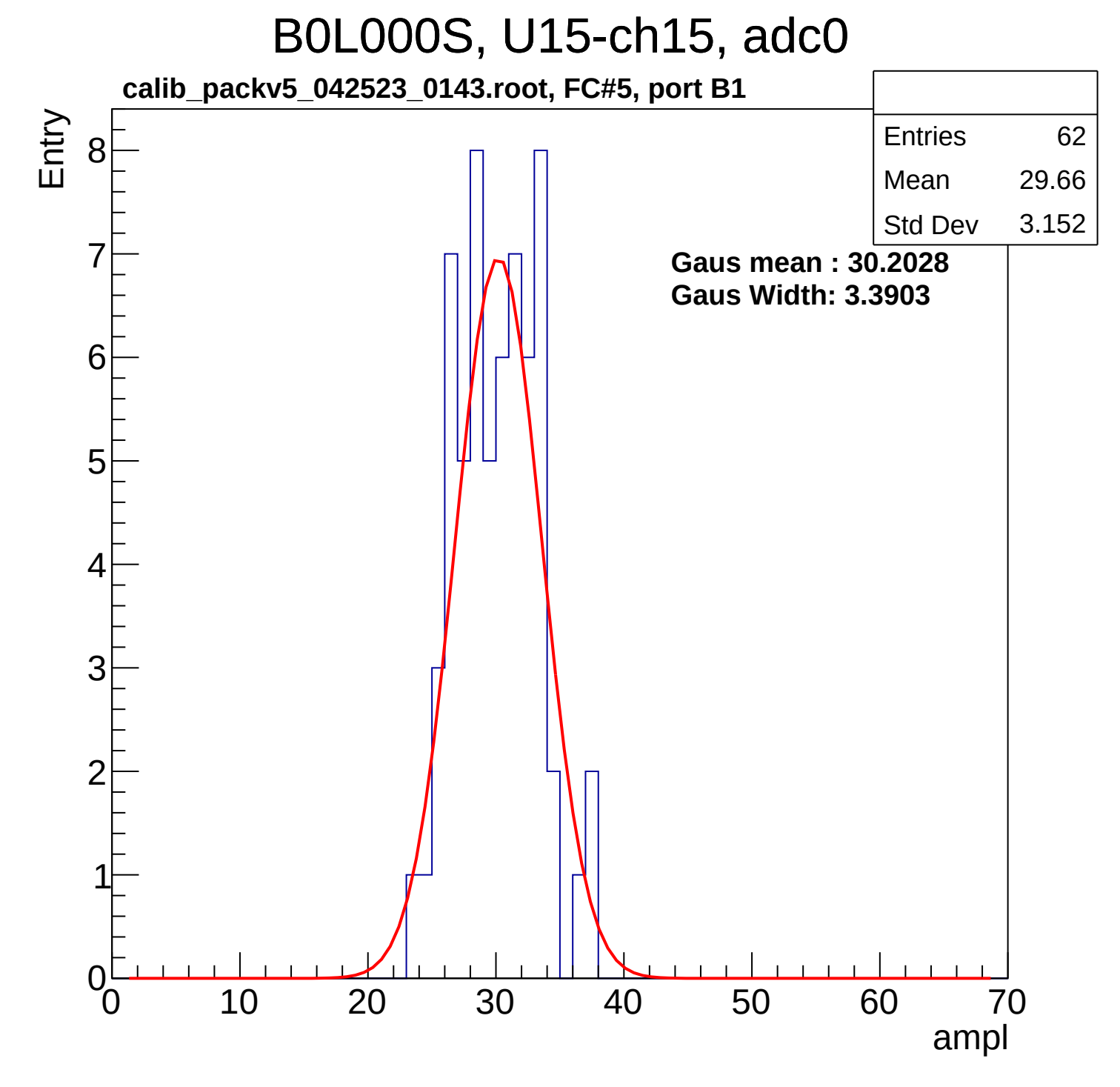
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.66
Std Dev 3.152
Gaus mean : 30.2028
Gaus Width: 3.3903
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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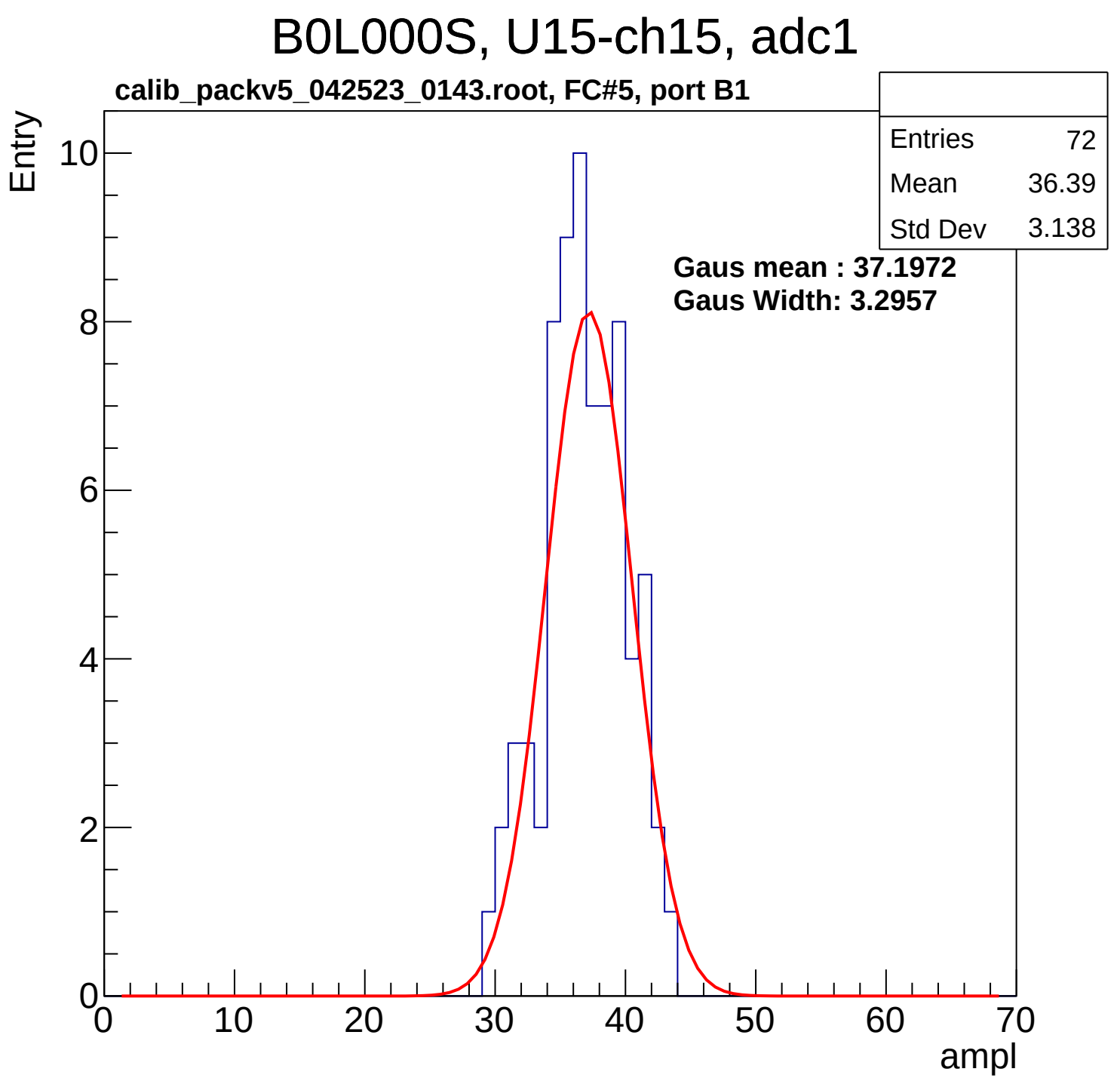
B0L000S, U15-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.39
Std Dev 3.138
Gaus mean : 37.1972
Gaus Width: 3.2957
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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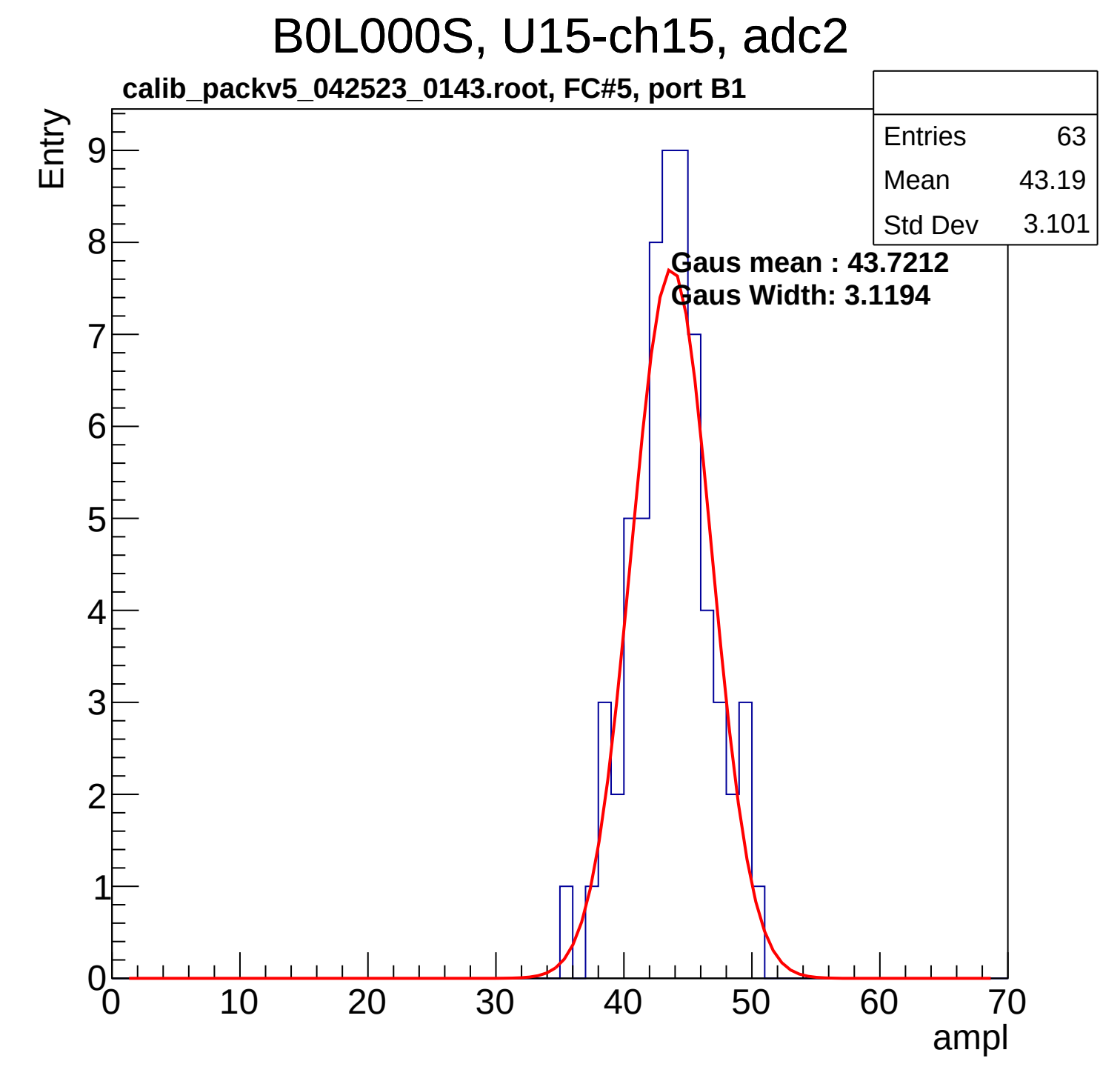
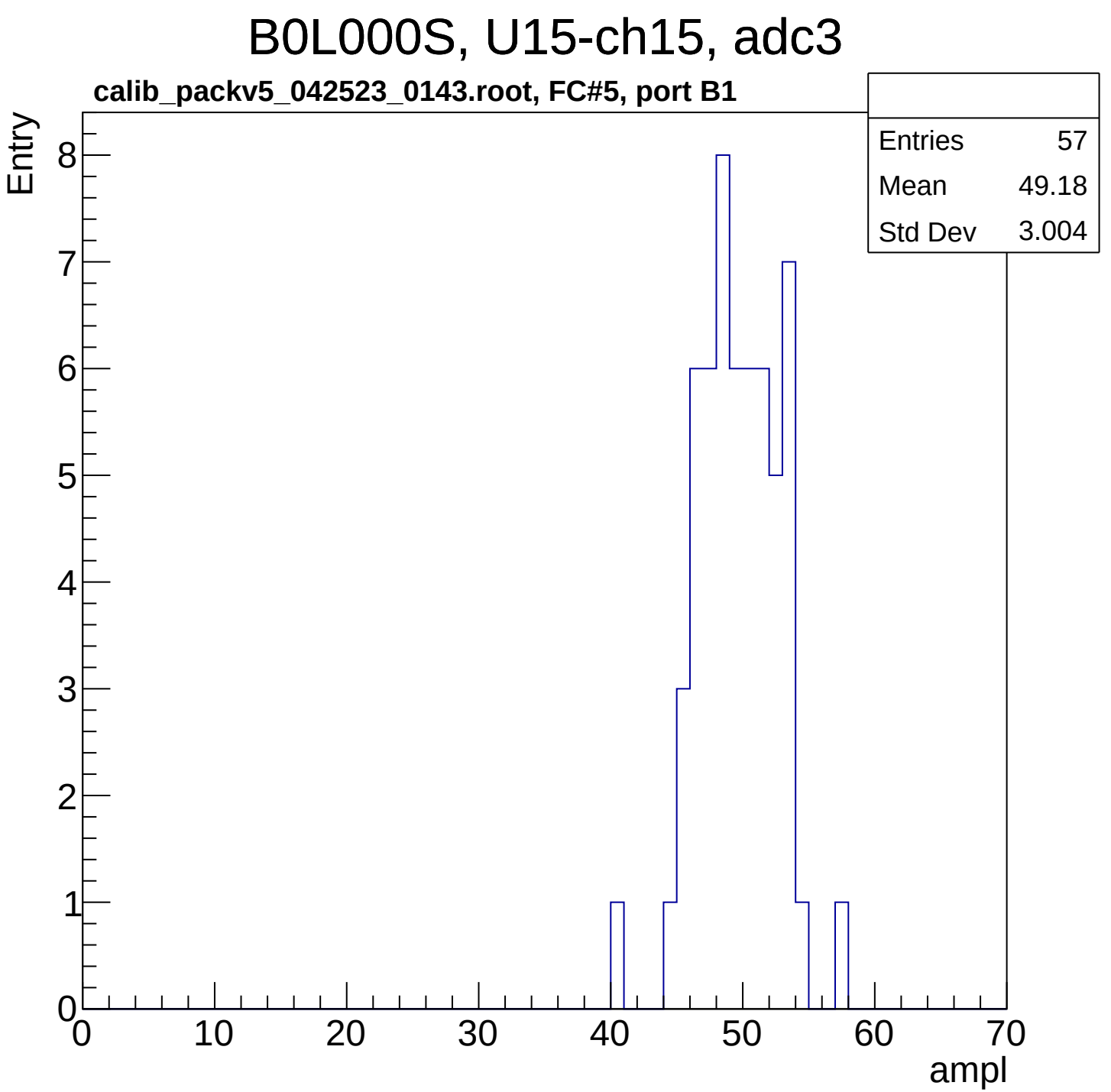

B0L000S, U15-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.19
Std Dev 3.101
Gaus mean : 43.7212
Gaus Width: 3.1194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.18
Std Dev 3.004
Entry
ampl
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2
3
4
5
6
7
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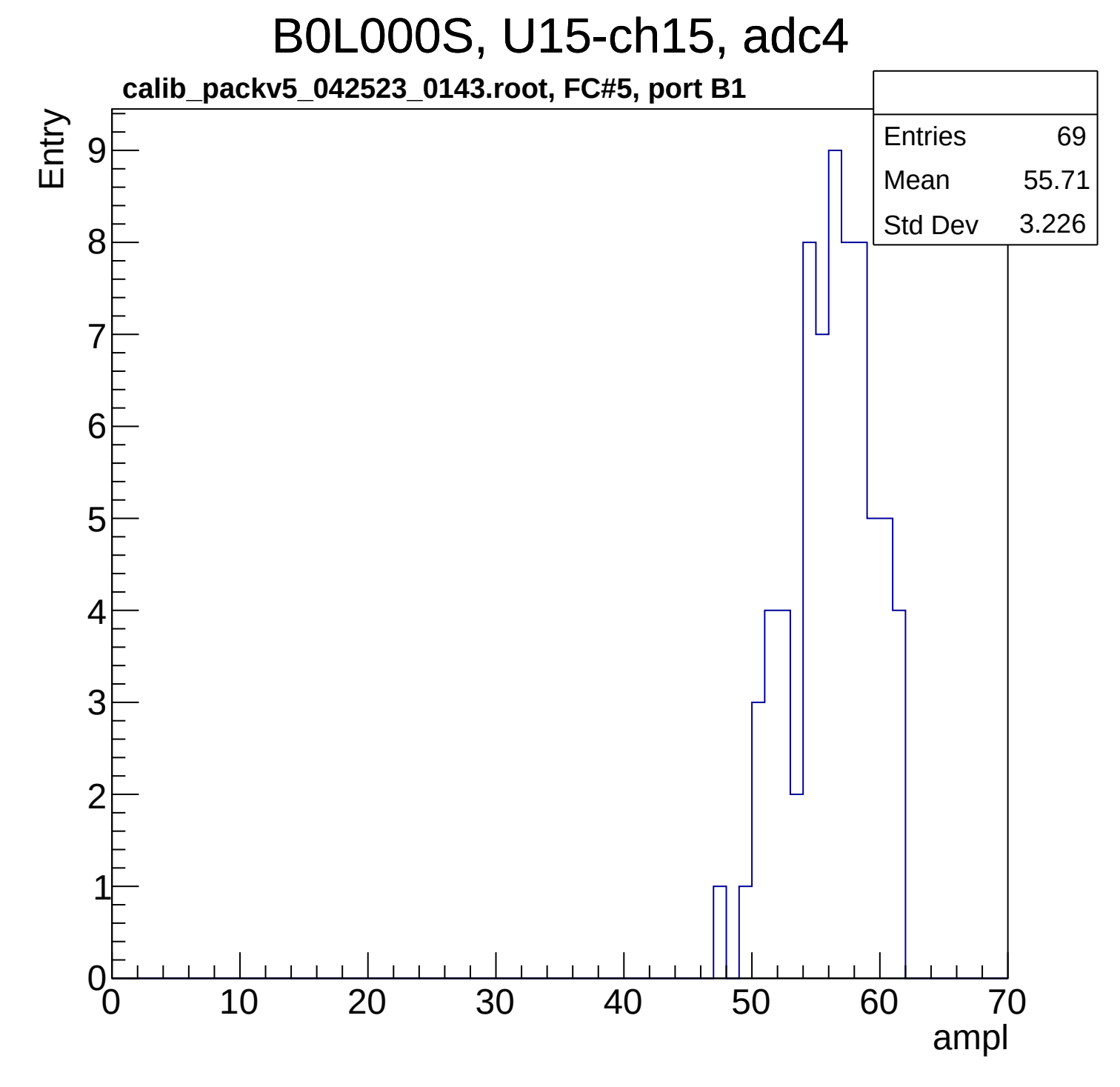
B0L000S, U15-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 55.71
Std Dev 3.226
Entry
ampl
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2
3
4
5
6
7
8
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0
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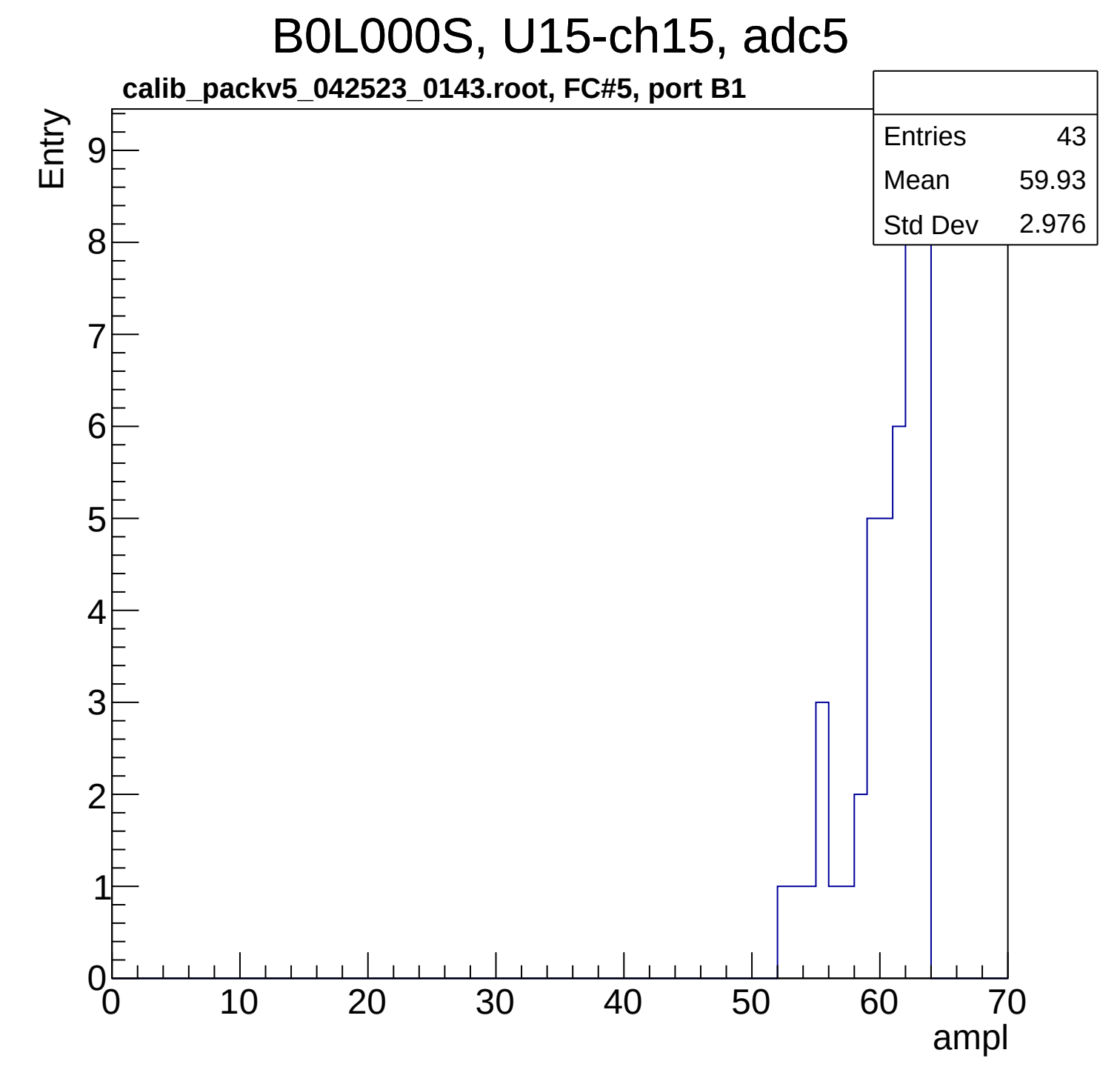
B0L000S, U15-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.93
Std Dev 2.976
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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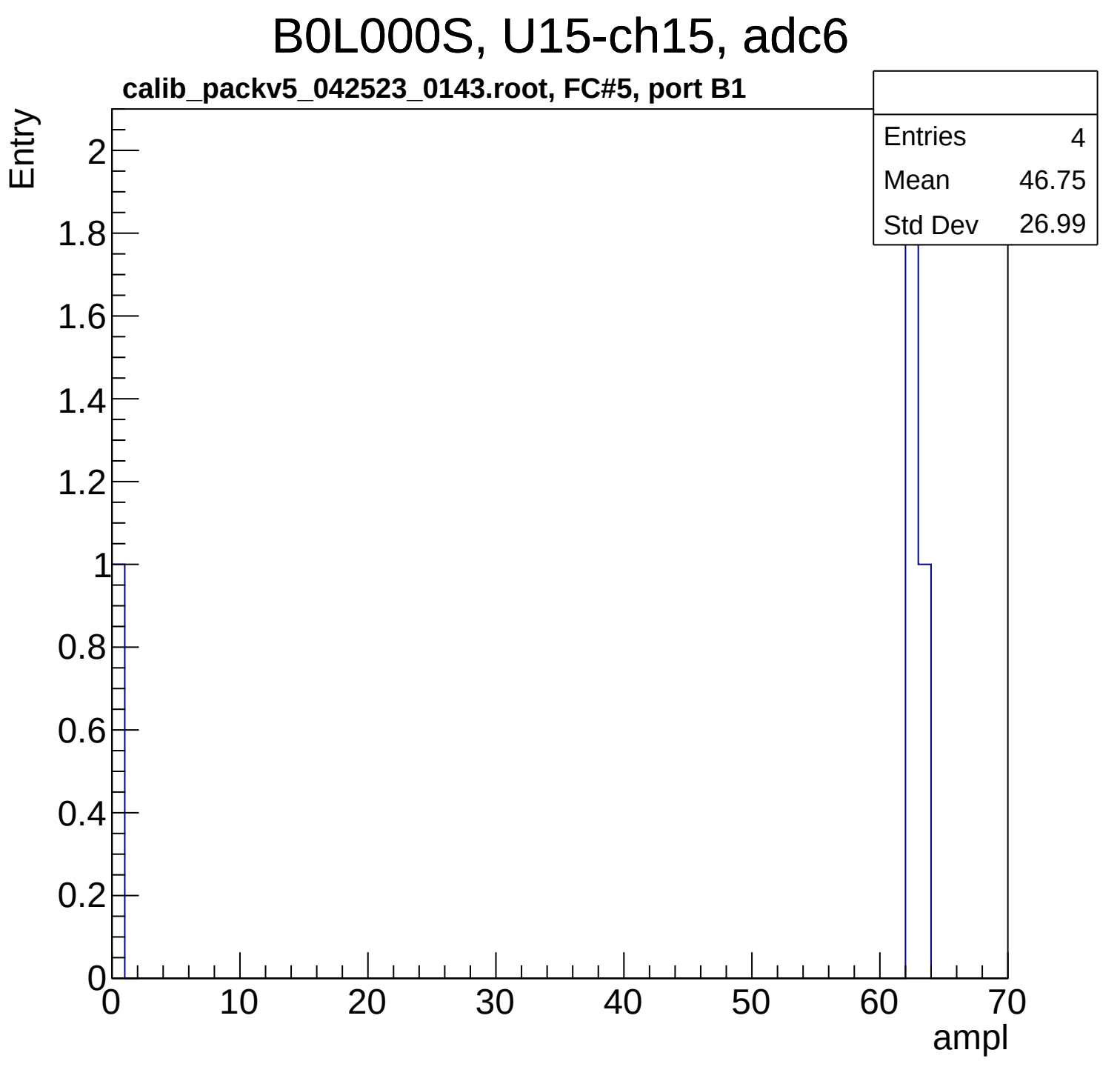
B0L000S, U15-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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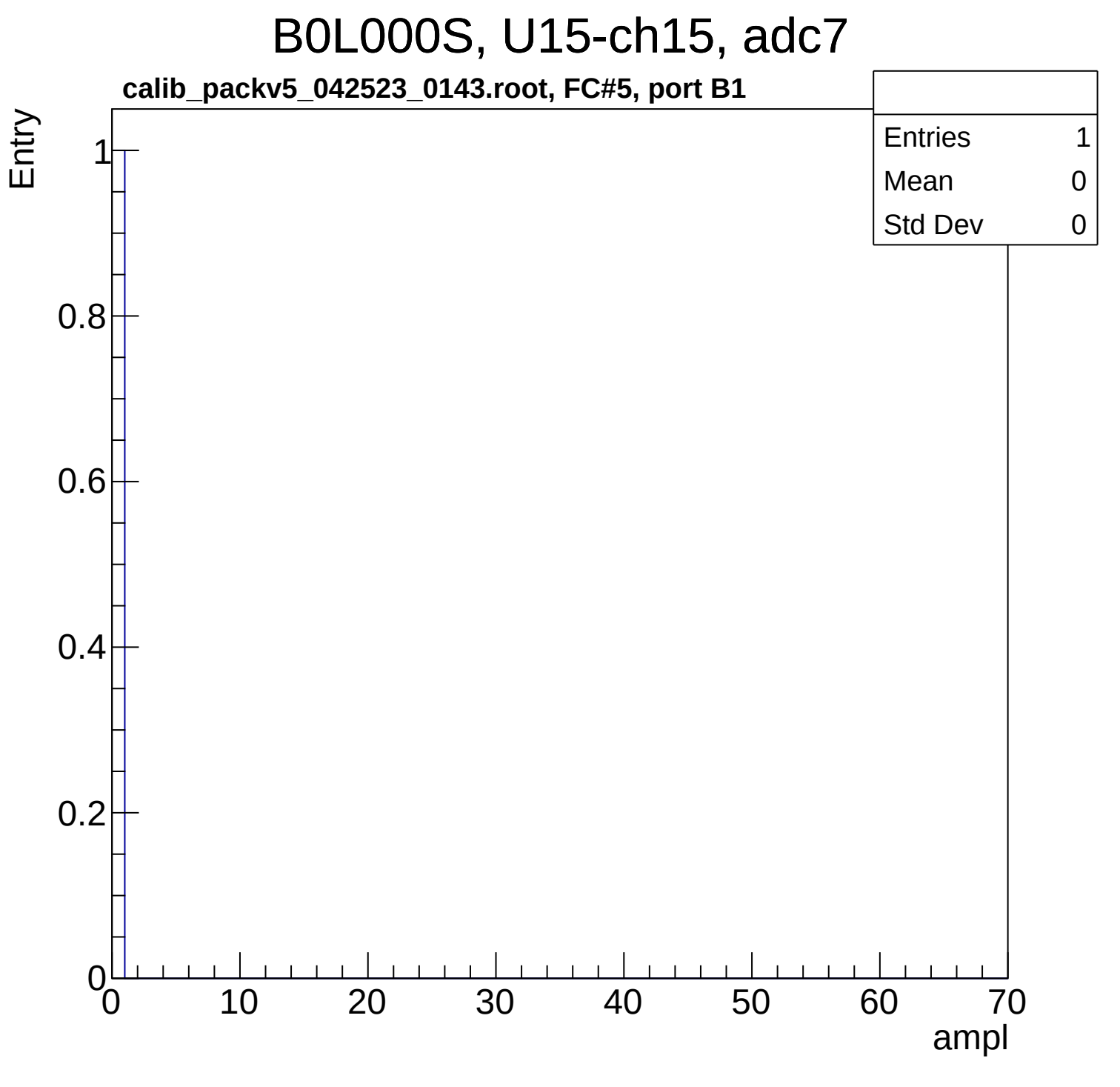
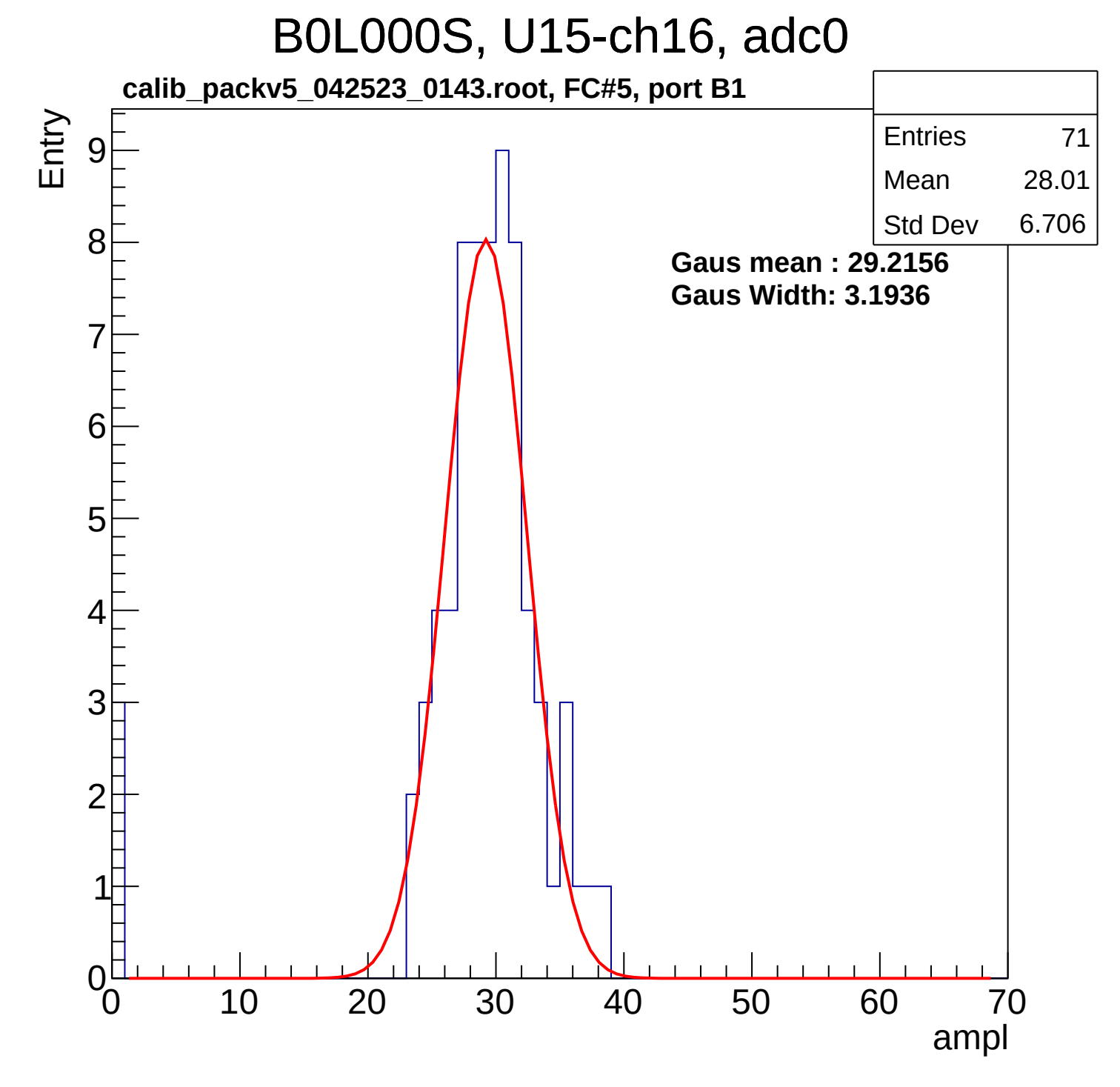

B0L000S, U15-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.01
Std Dev 6.706
Gaus mean : 29.2156
Gaus Width: 3.1936
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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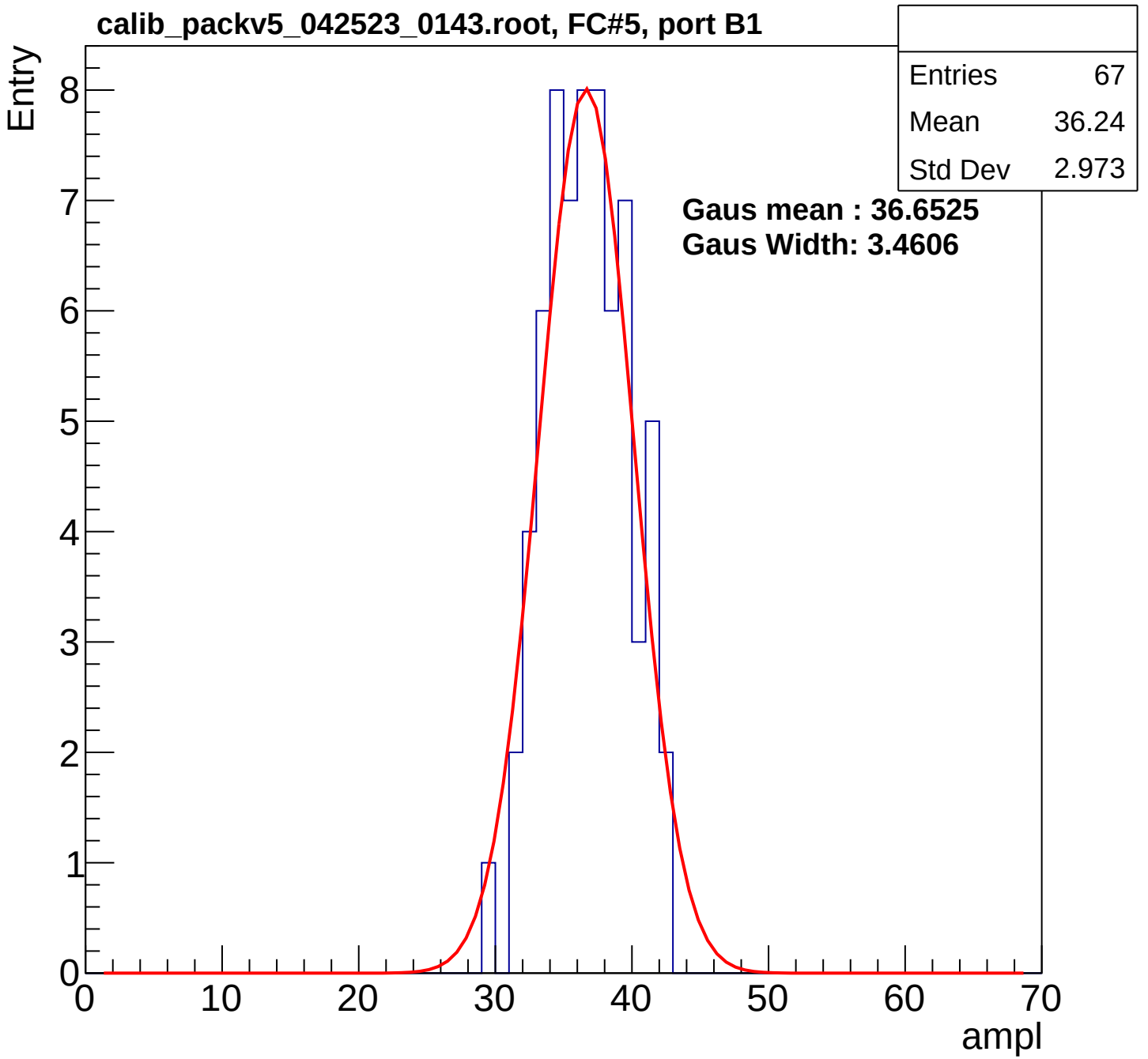
B0L000S, U15-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.24
Std Dev 2.973
Gaus mean : 36.6525
Gaus Width: 3.4606
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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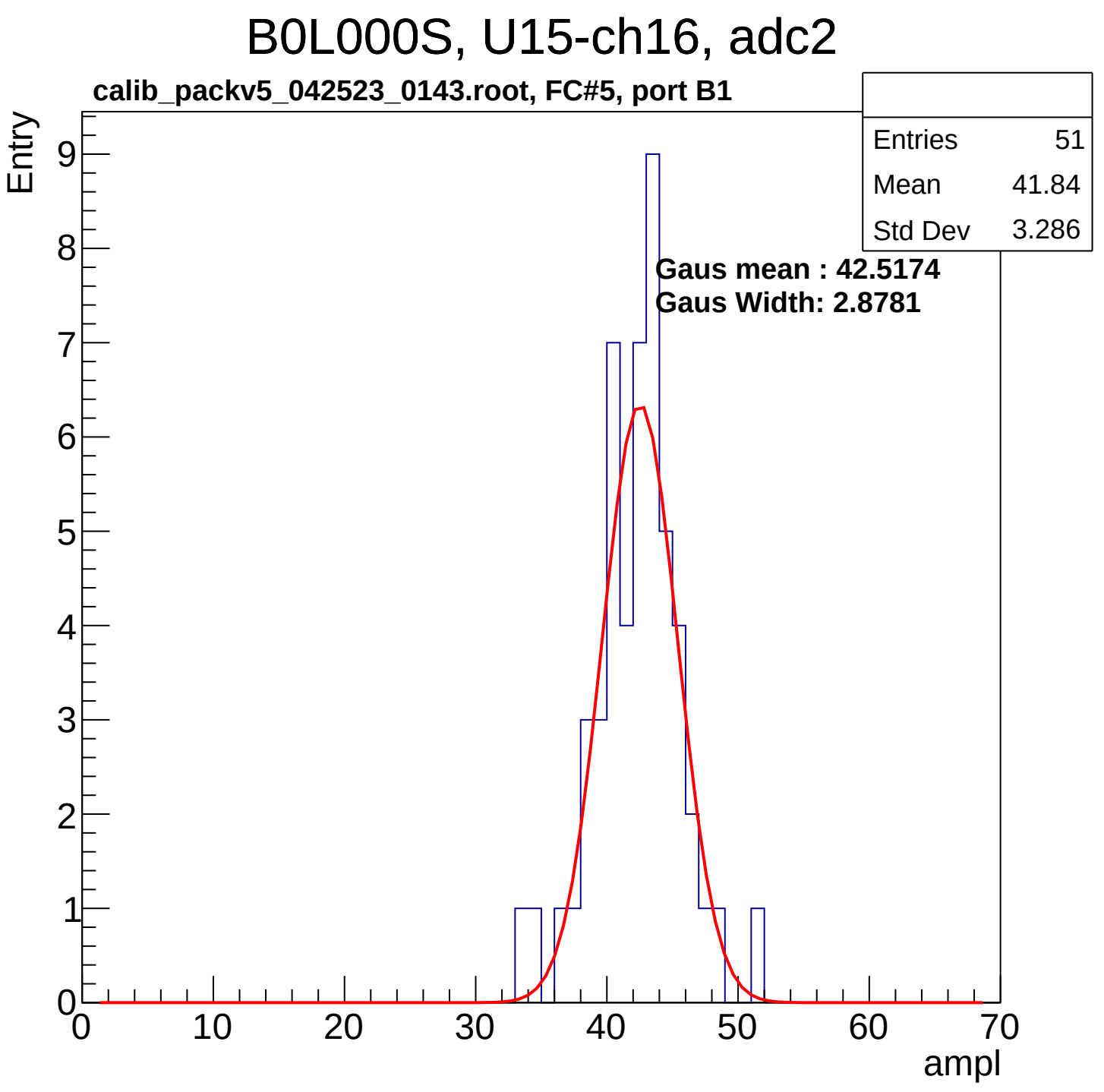
B0L000S, U15-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 41.84
Std Dev 3.286
Gaus mean : 42.5174
Gaus Width: 2.8781
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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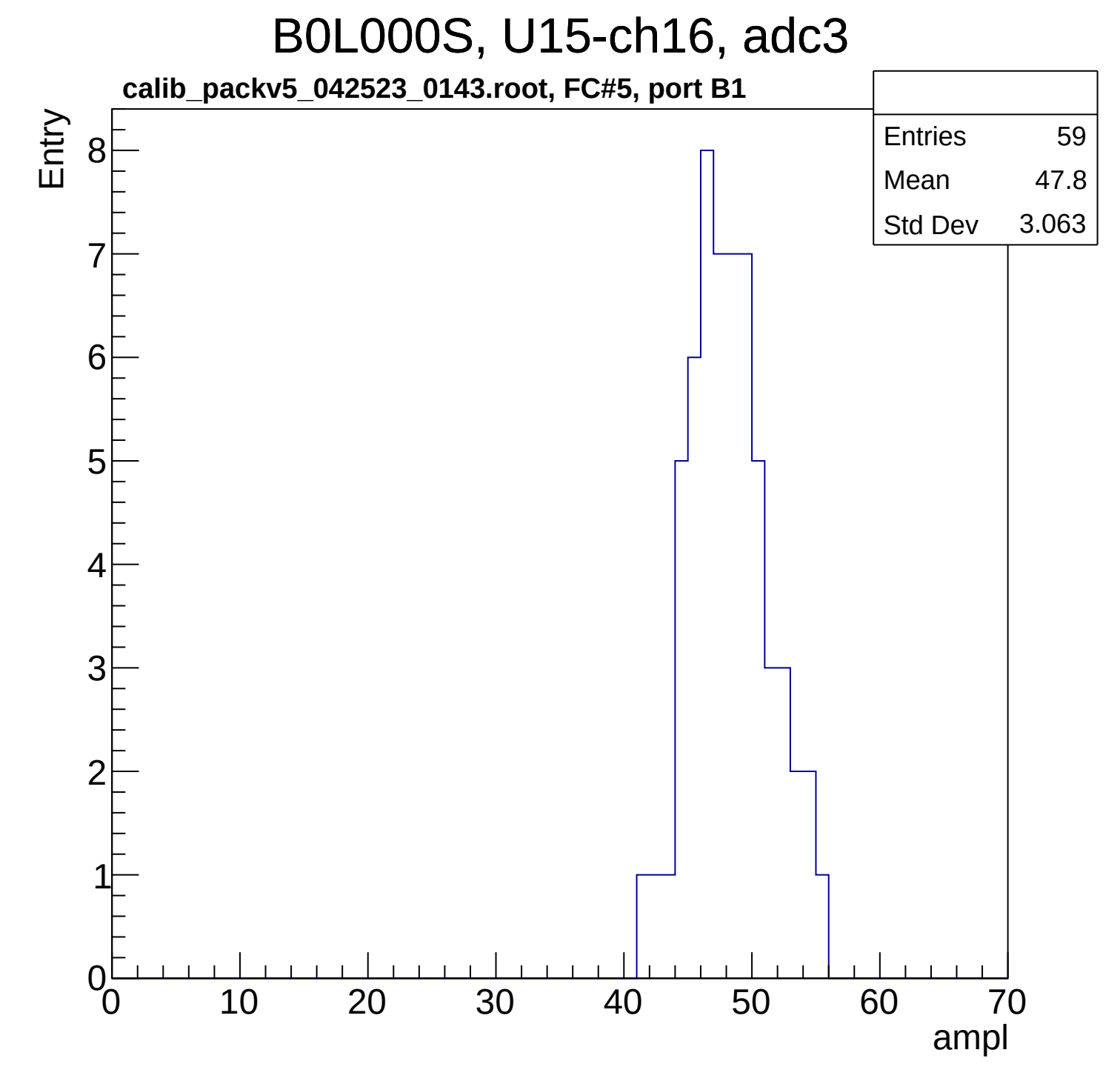
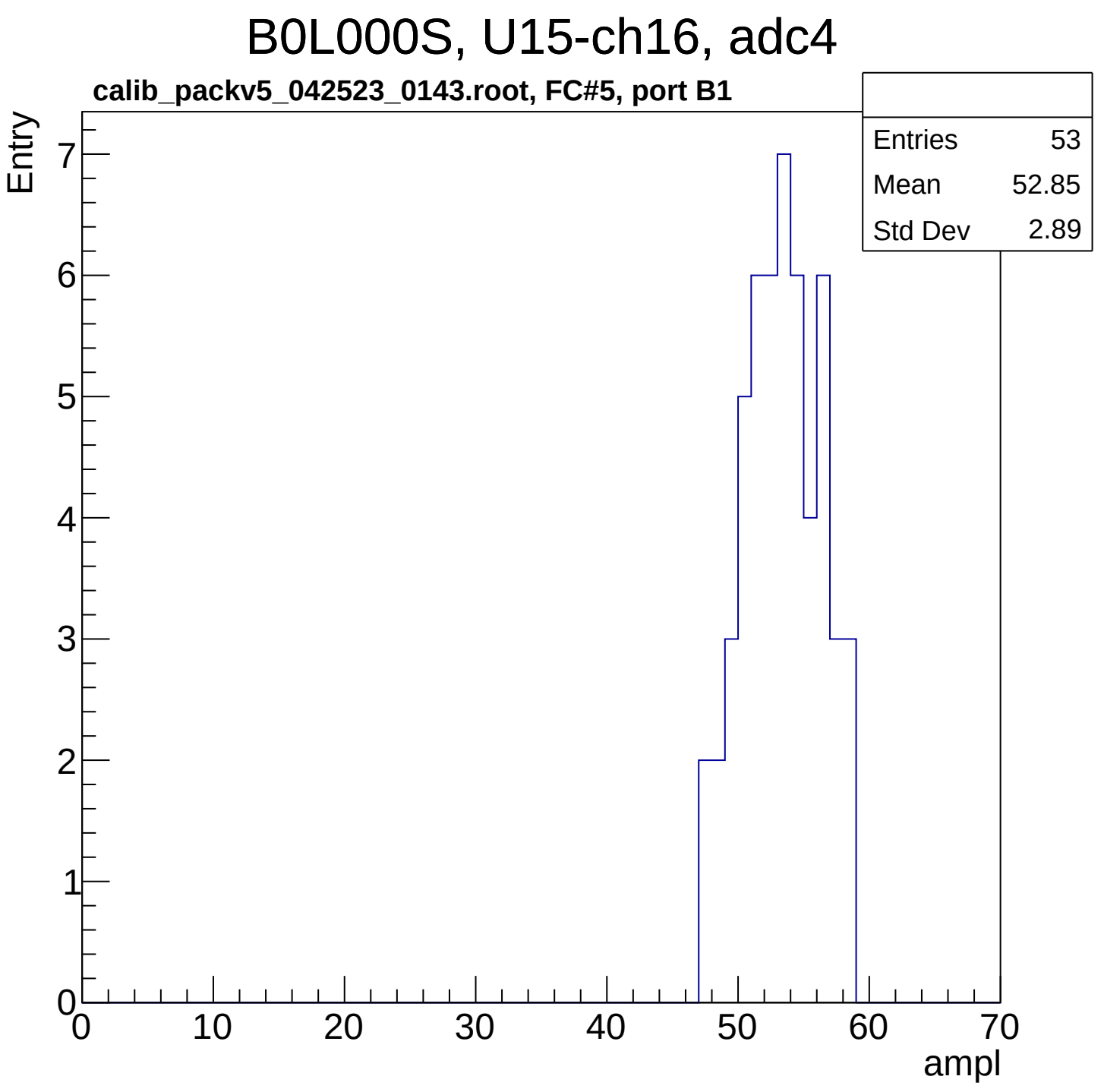
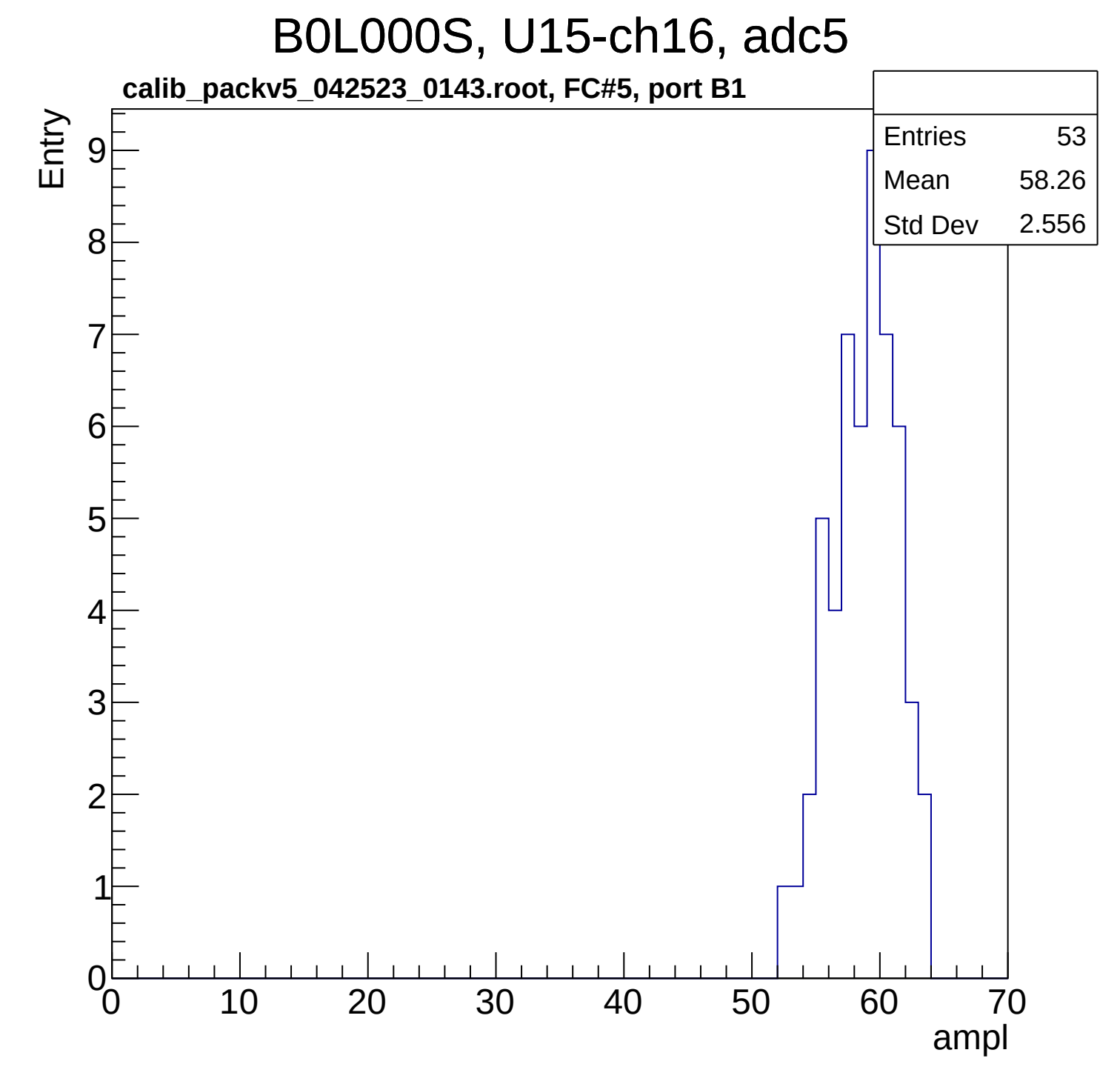

B0L000S, U15-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 47.8
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 52.85
Std Dev 2.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.26
Std Dev 2.556
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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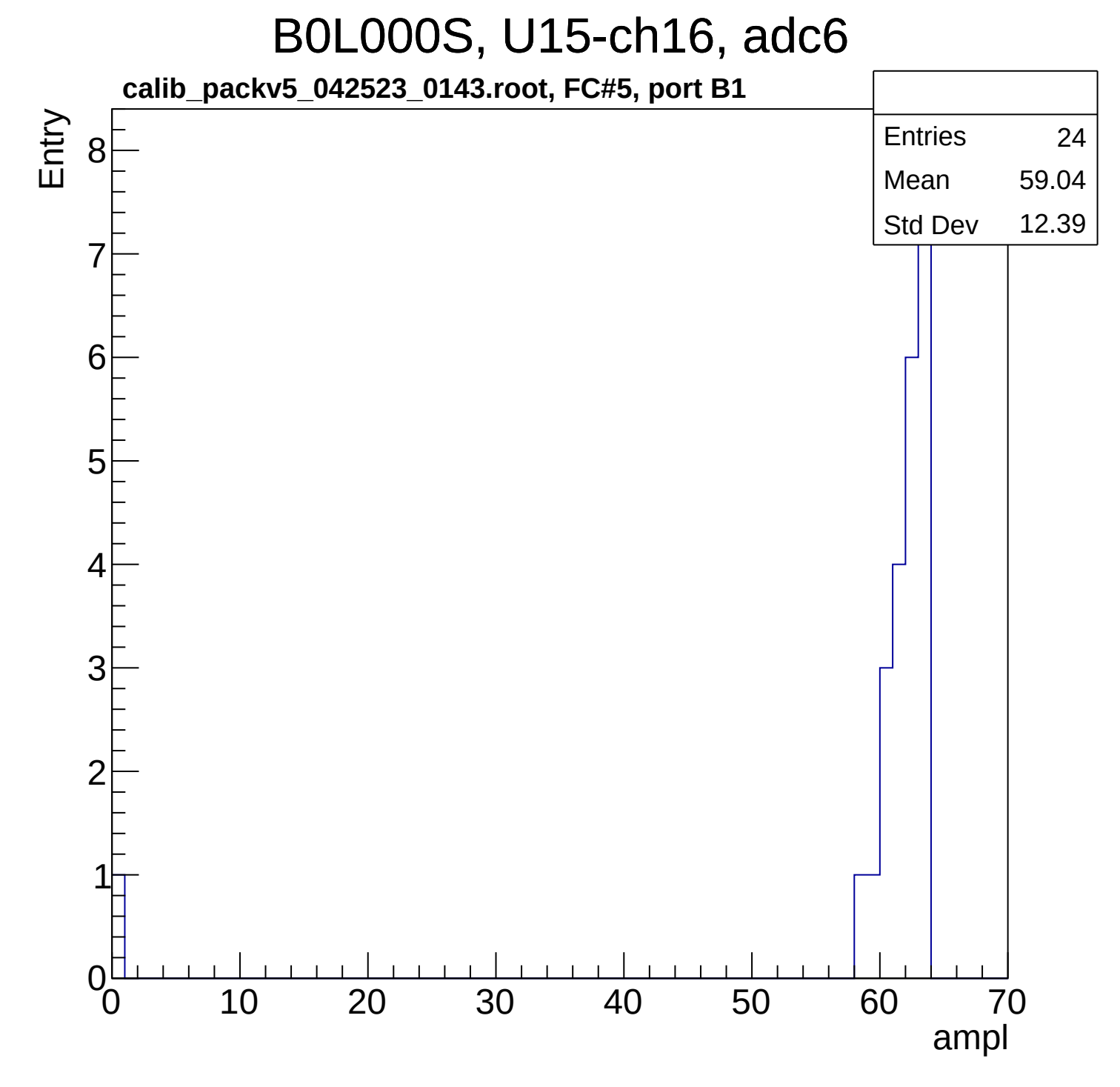
B0L000S, U15-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 59.04
Std Dev 12.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
50
60
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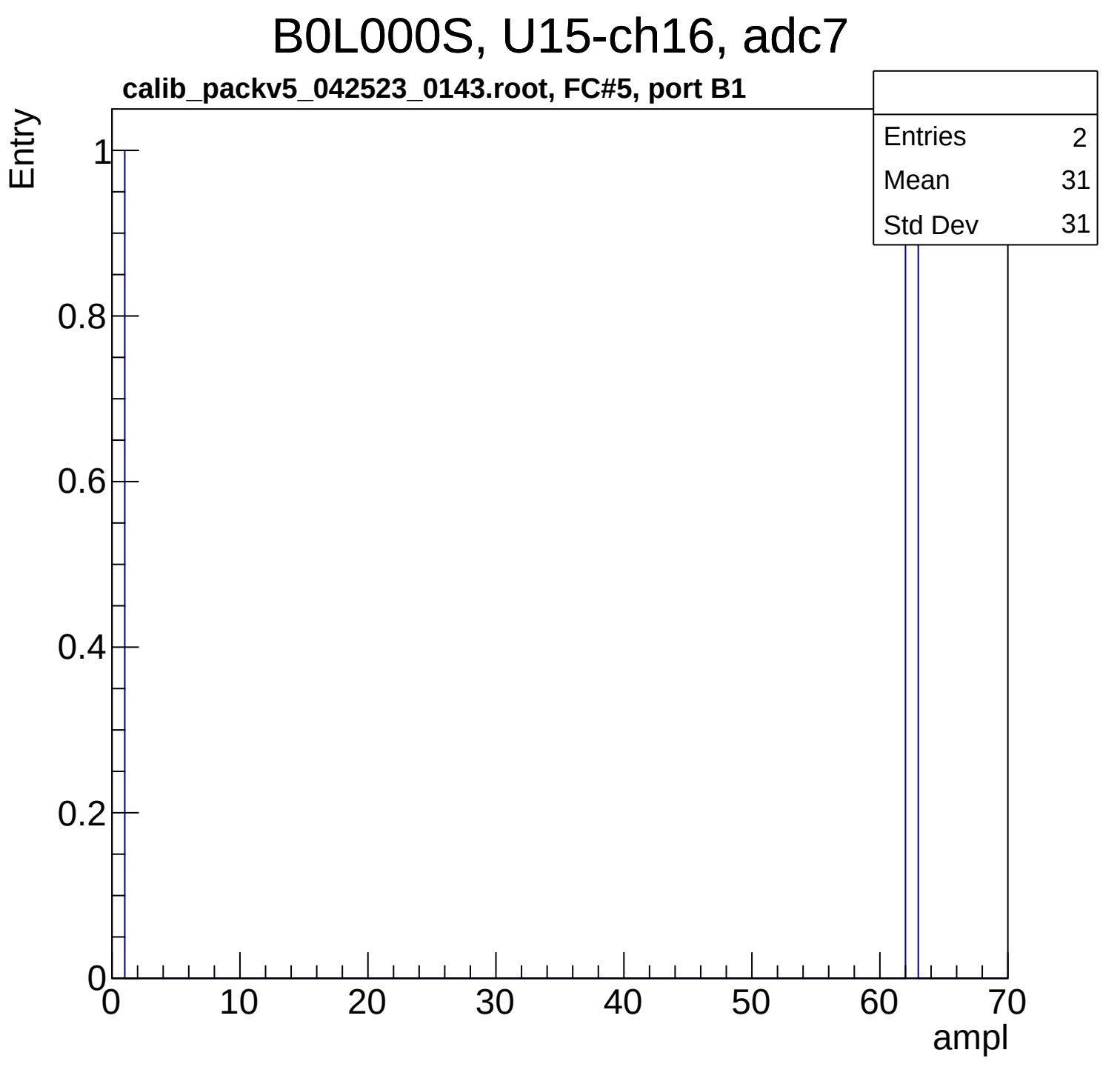
B0L000S, U15-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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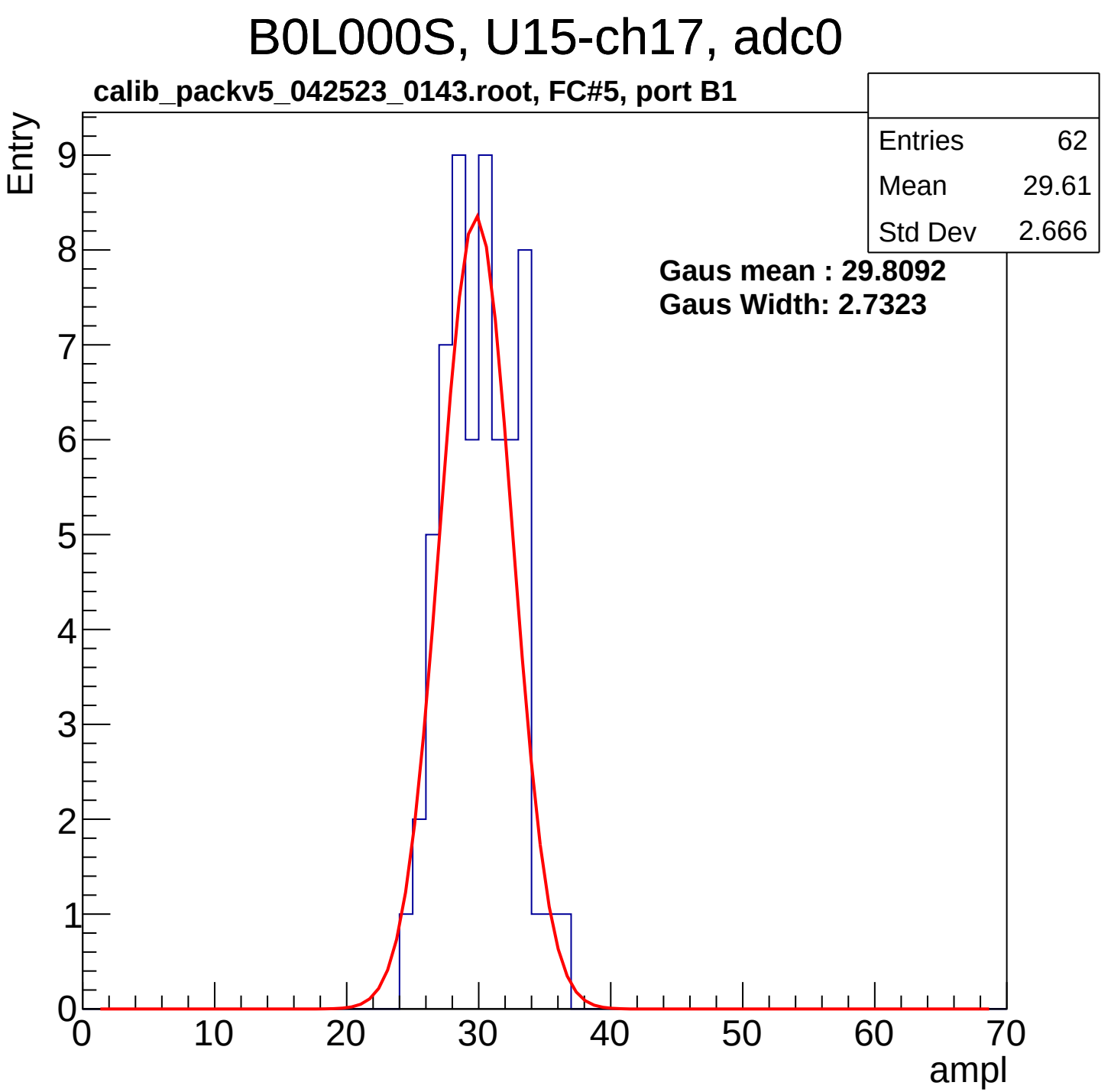
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.61
Std Dev 2.666
Gaus mean : 29.8092
Gaus Width: 2.7323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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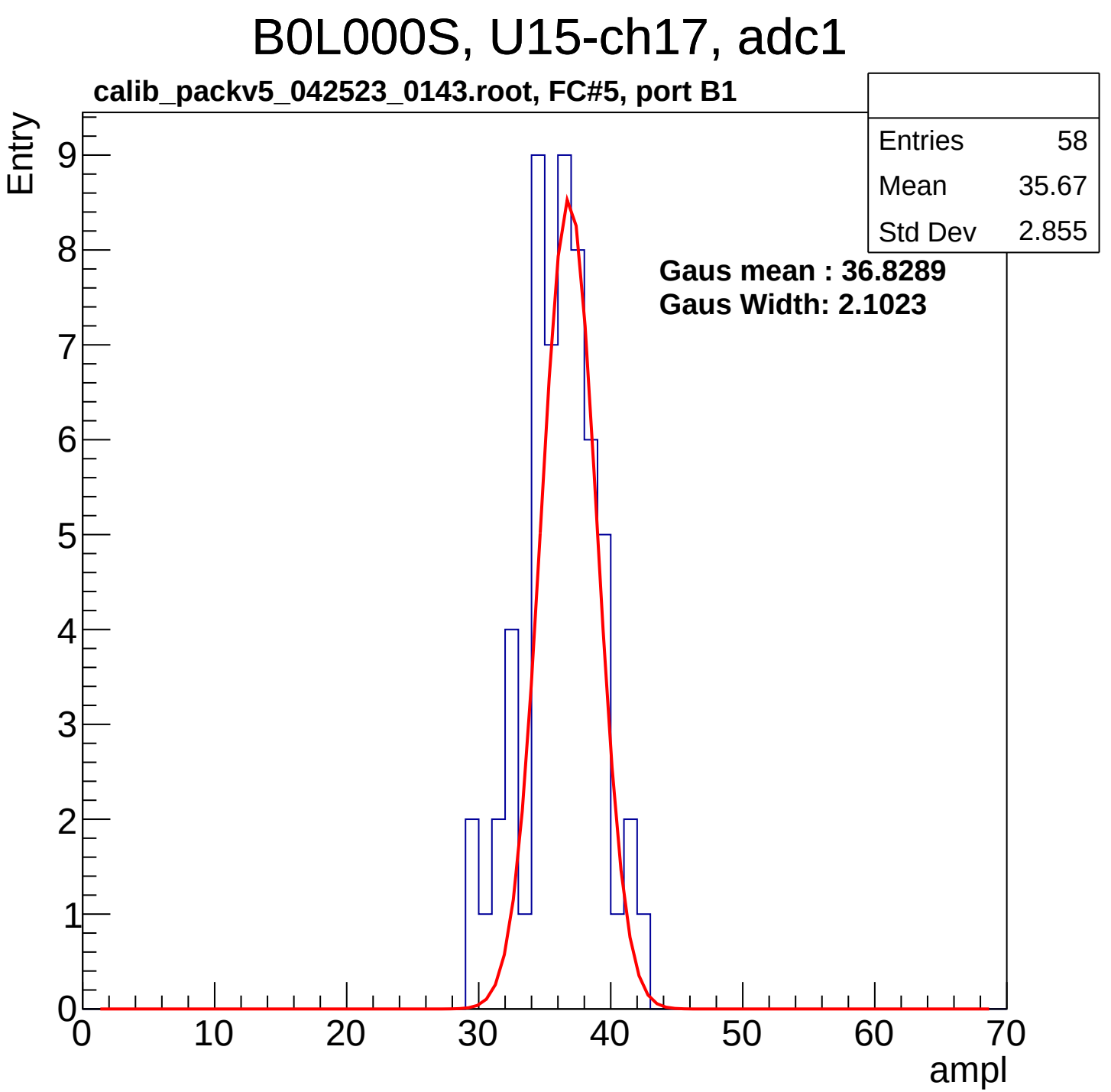
B0L000S, U15-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.67
Std Dev 2.855
Gaus mean : 36.8289
Gaus Width: 2.1023
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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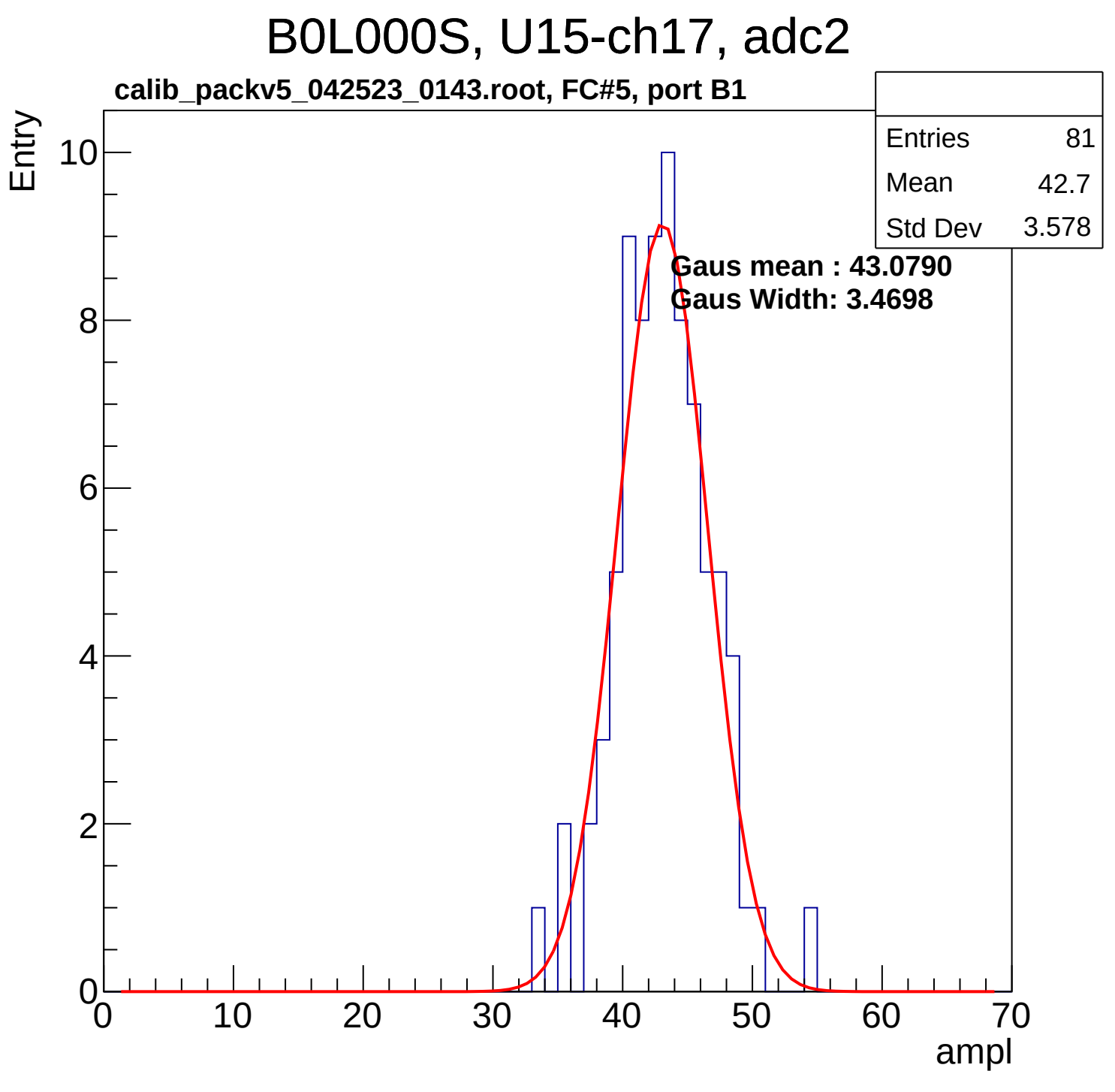
B0L000S, U15-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 42.7
Std Dev 3.578
Gaus mean : 43.0790
Gaus Width: 3.4698
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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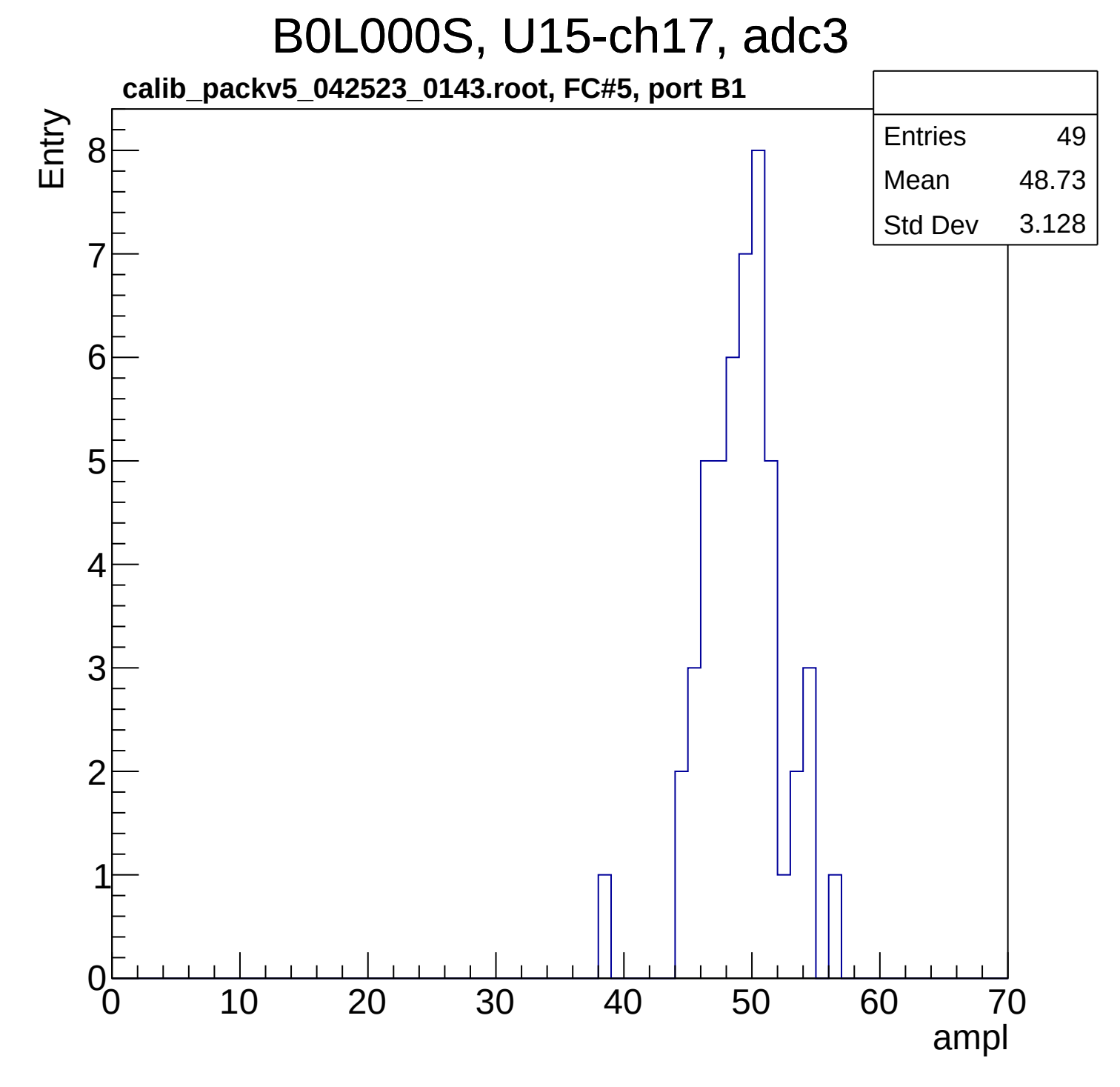
B0L000S, U15-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 48.73
Std Dev 3.128
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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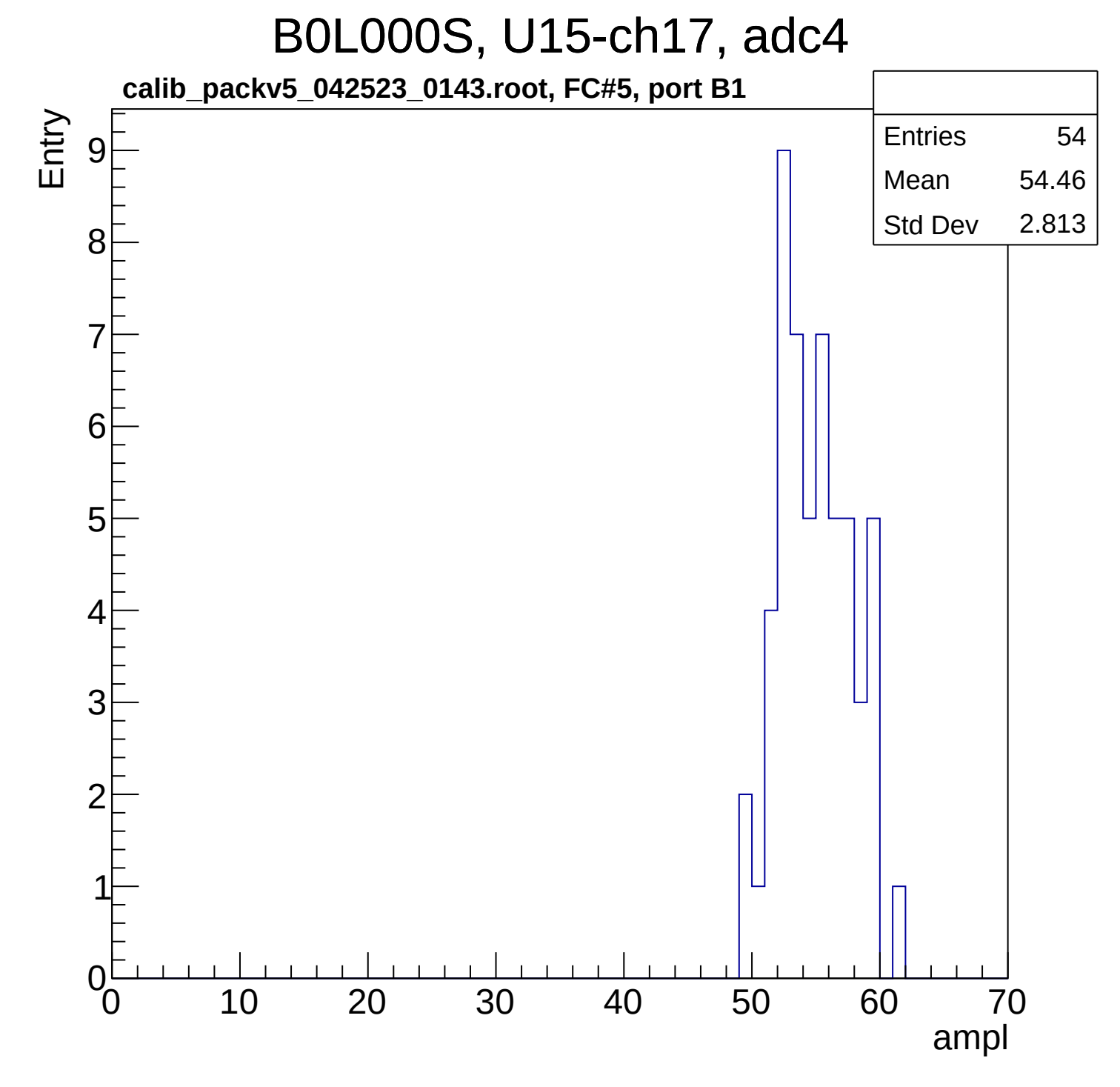
B0L000S, U15-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.46
Std Dev 2.813
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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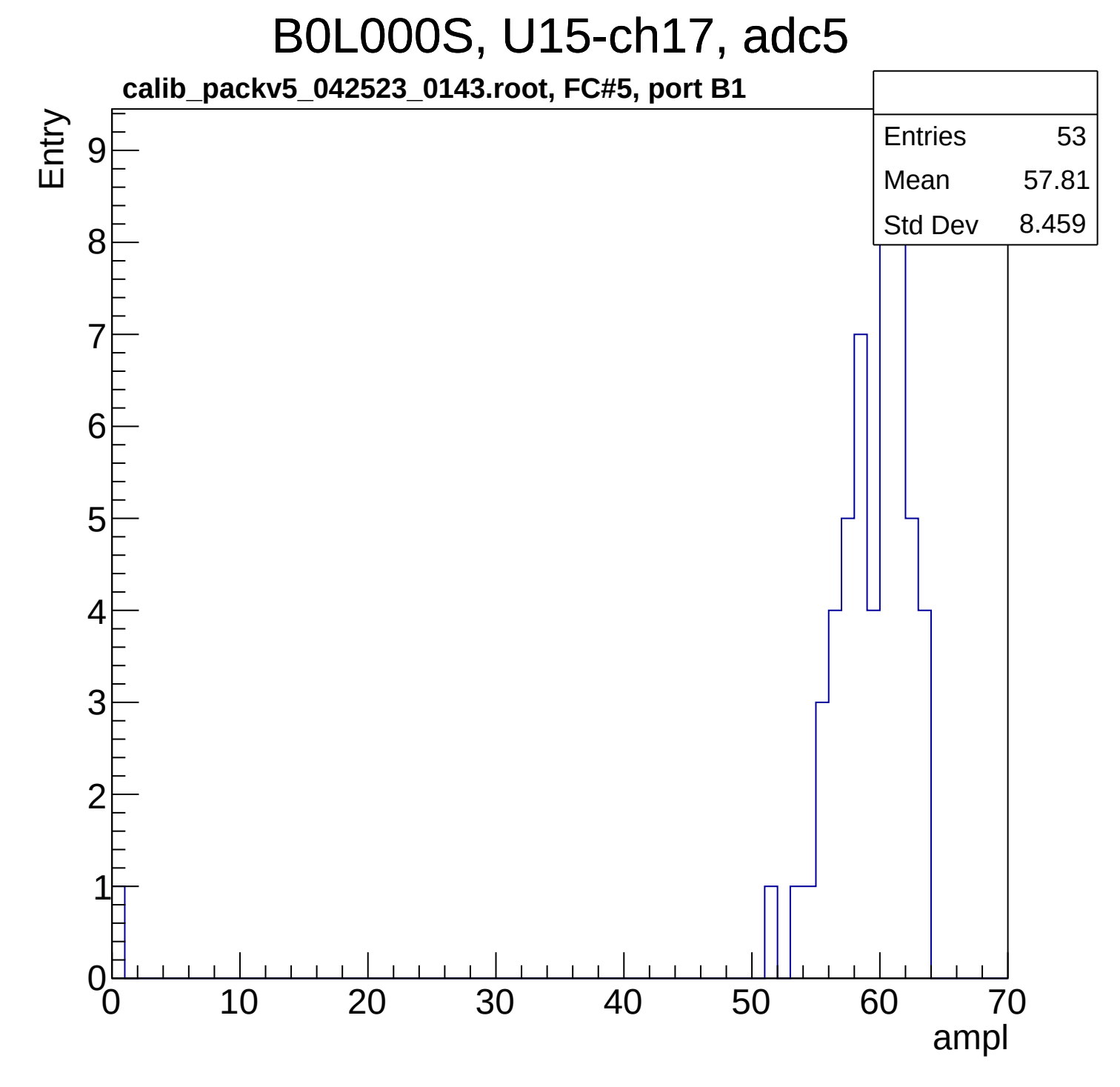
B0L000S, U15-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.81
Std Dev 8.459
Entry
ampl
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5
6
7
8
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0
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60
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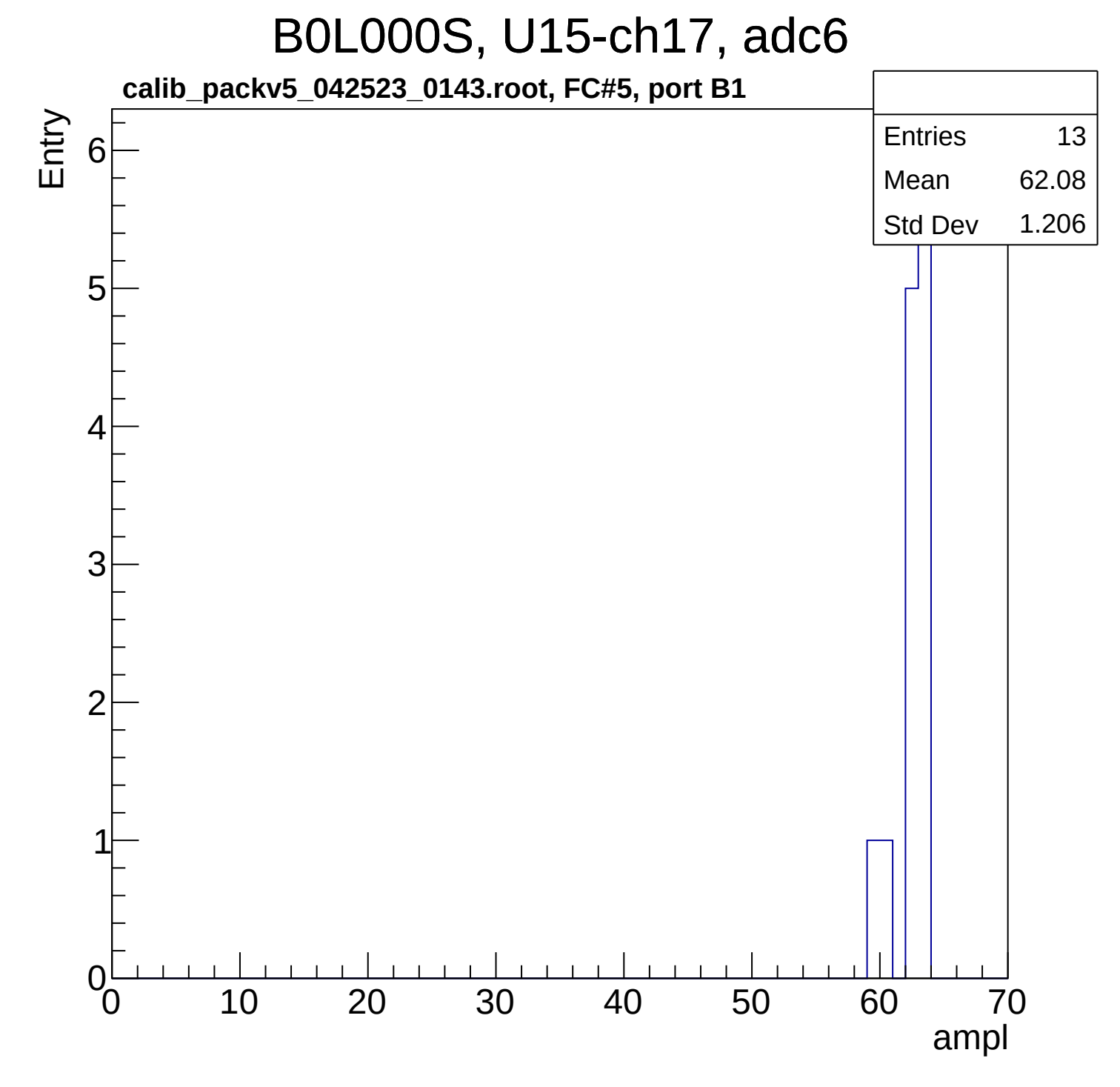
B0L000S, U15-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 62.08
Std Dev 1.206
Entry
ampl
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3
4
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0
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20
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60
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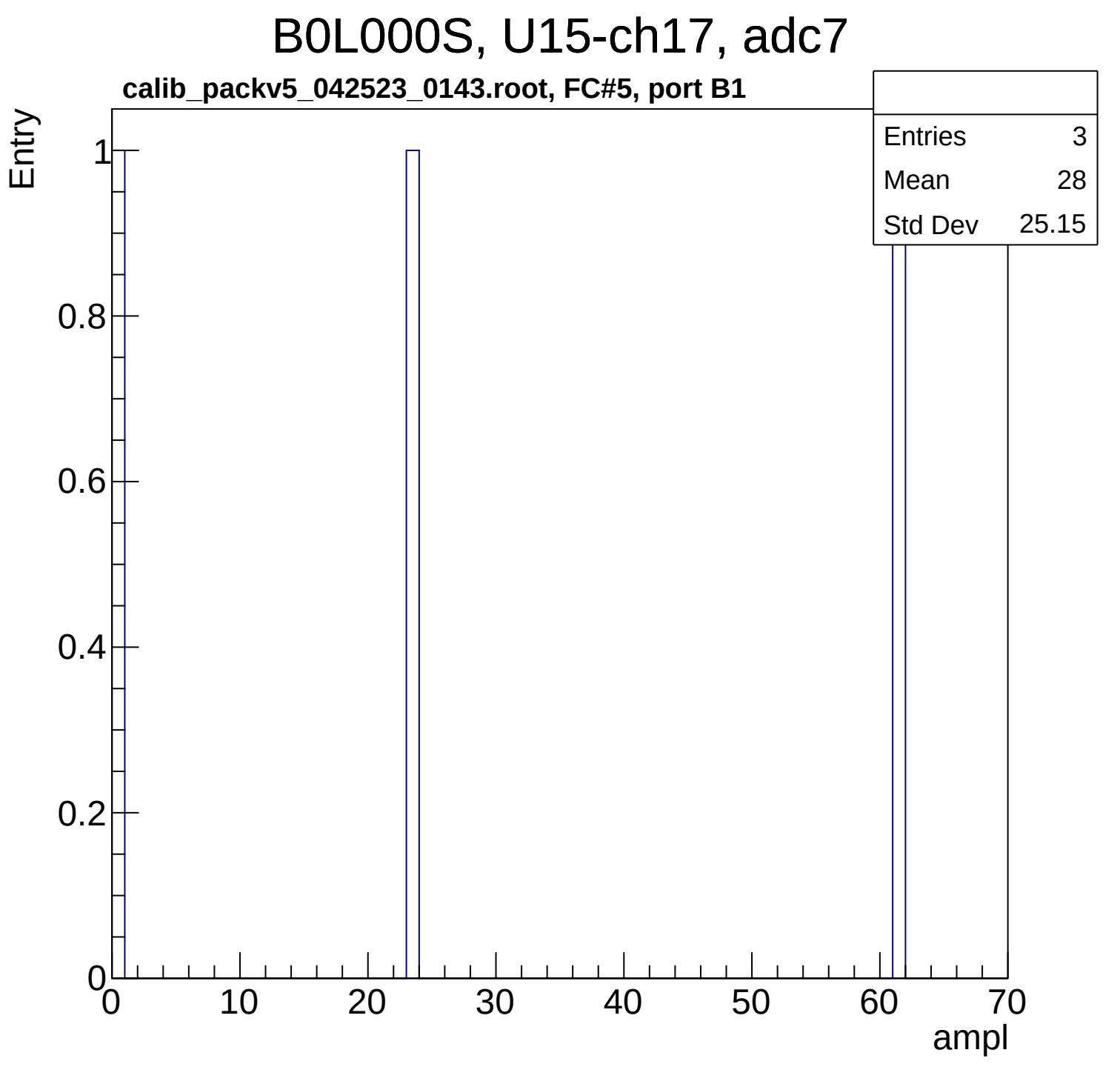
B0L000S, U15-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28
Std Dev 25.15
Entry
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0.4
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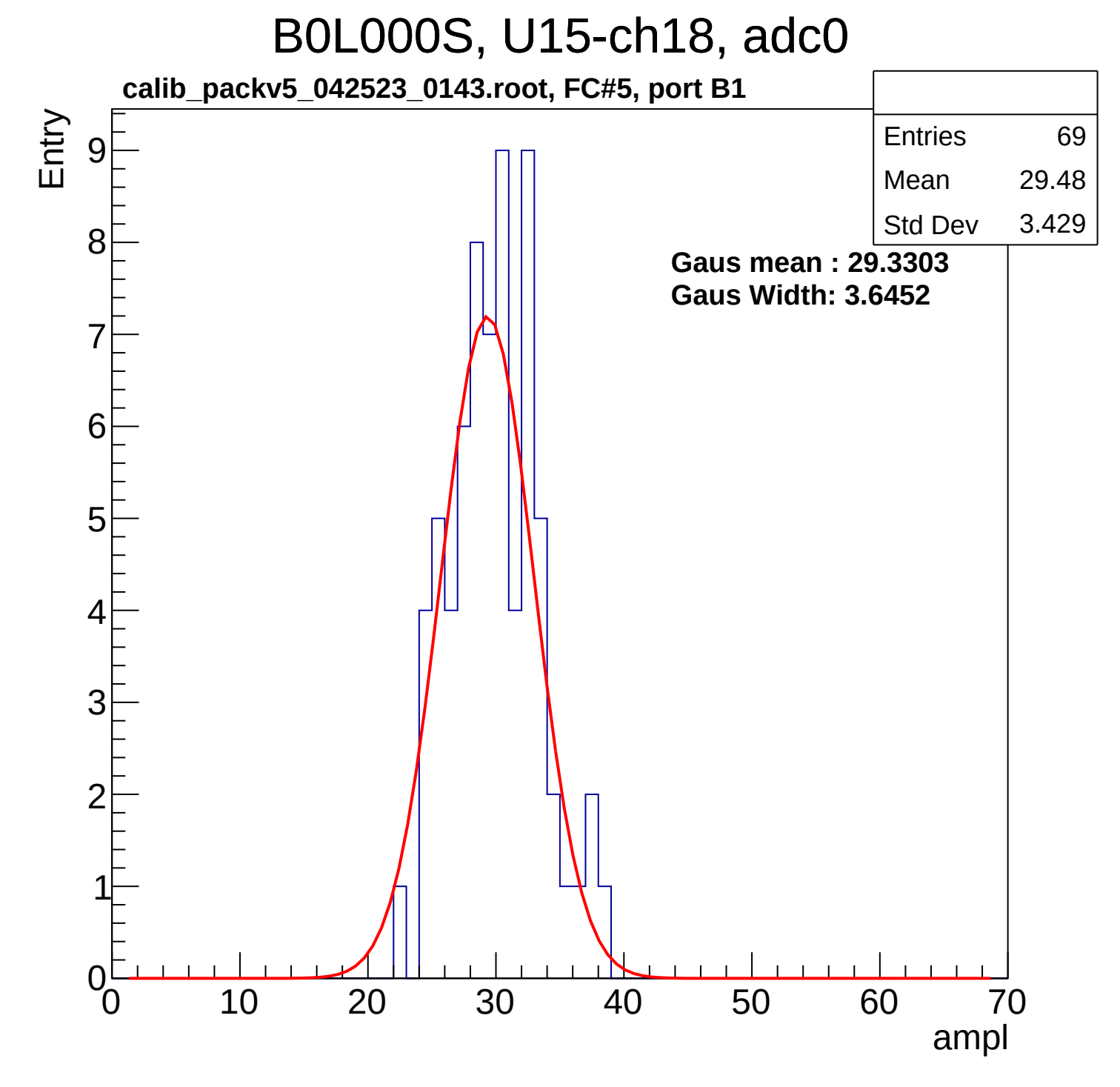
60
70
ampl


B0L000S, U15-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.48
Std Dev 3.429
Gaus mean : 29.3303
Gaus Width: 3.6452
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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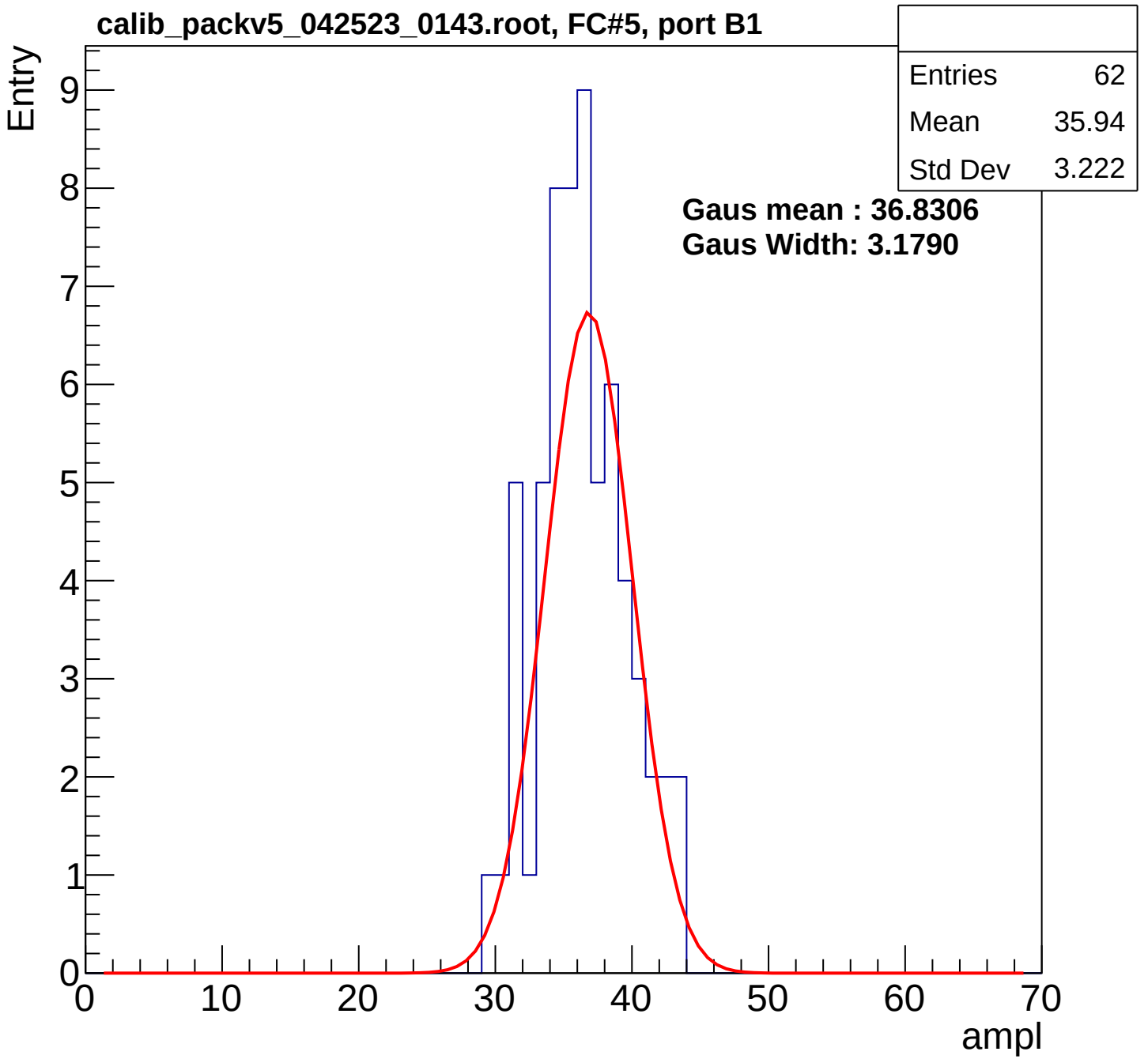
B0L000S, U15-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.94
Std Dev 3.222
Gaus mean : 36.8306
Gaus Width: 3.1790
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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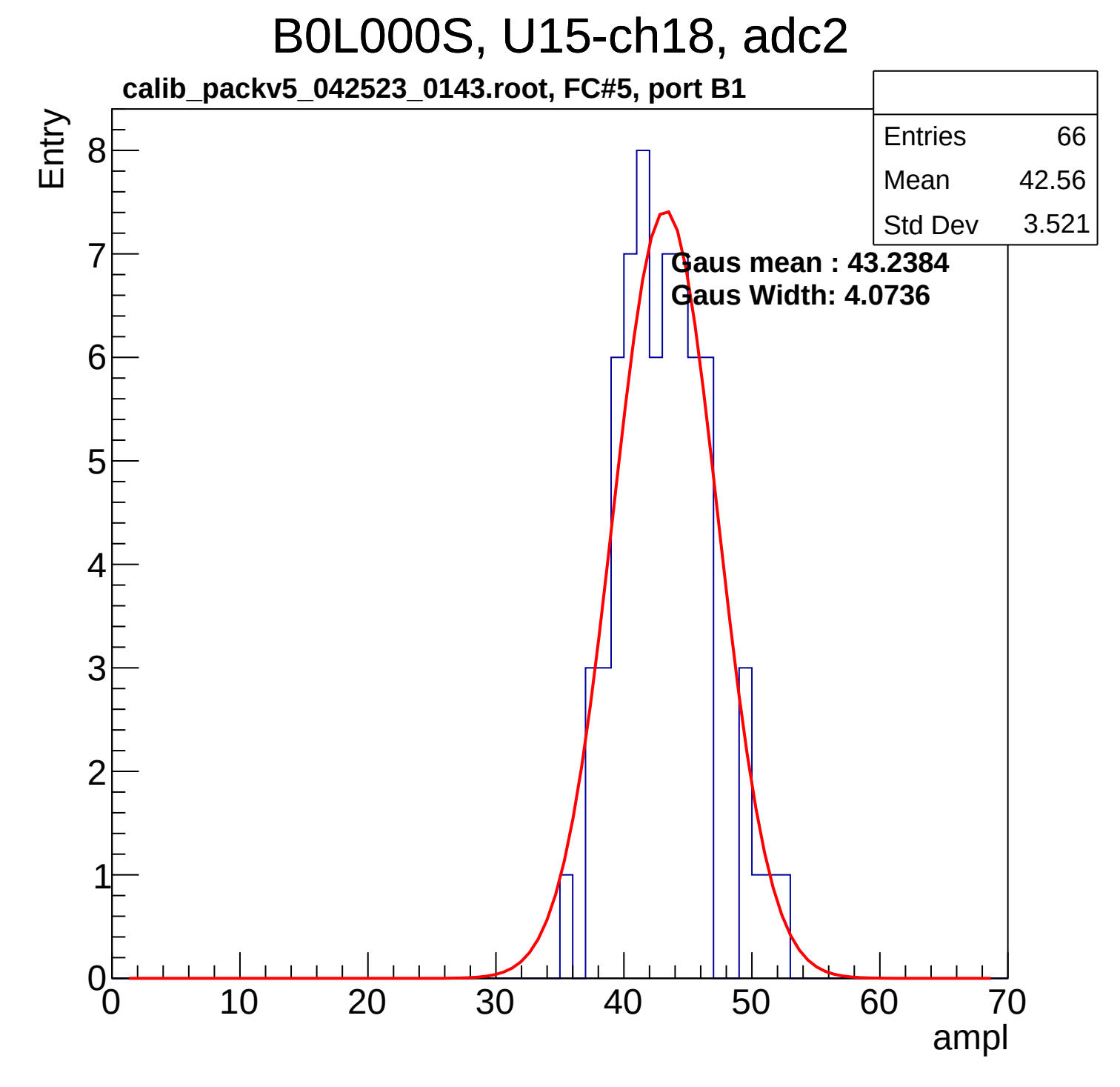
B0L000S, U15-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.56
Std Dev 3.521
Gaus mean : 43.2384
Gaus Width: 4.0736
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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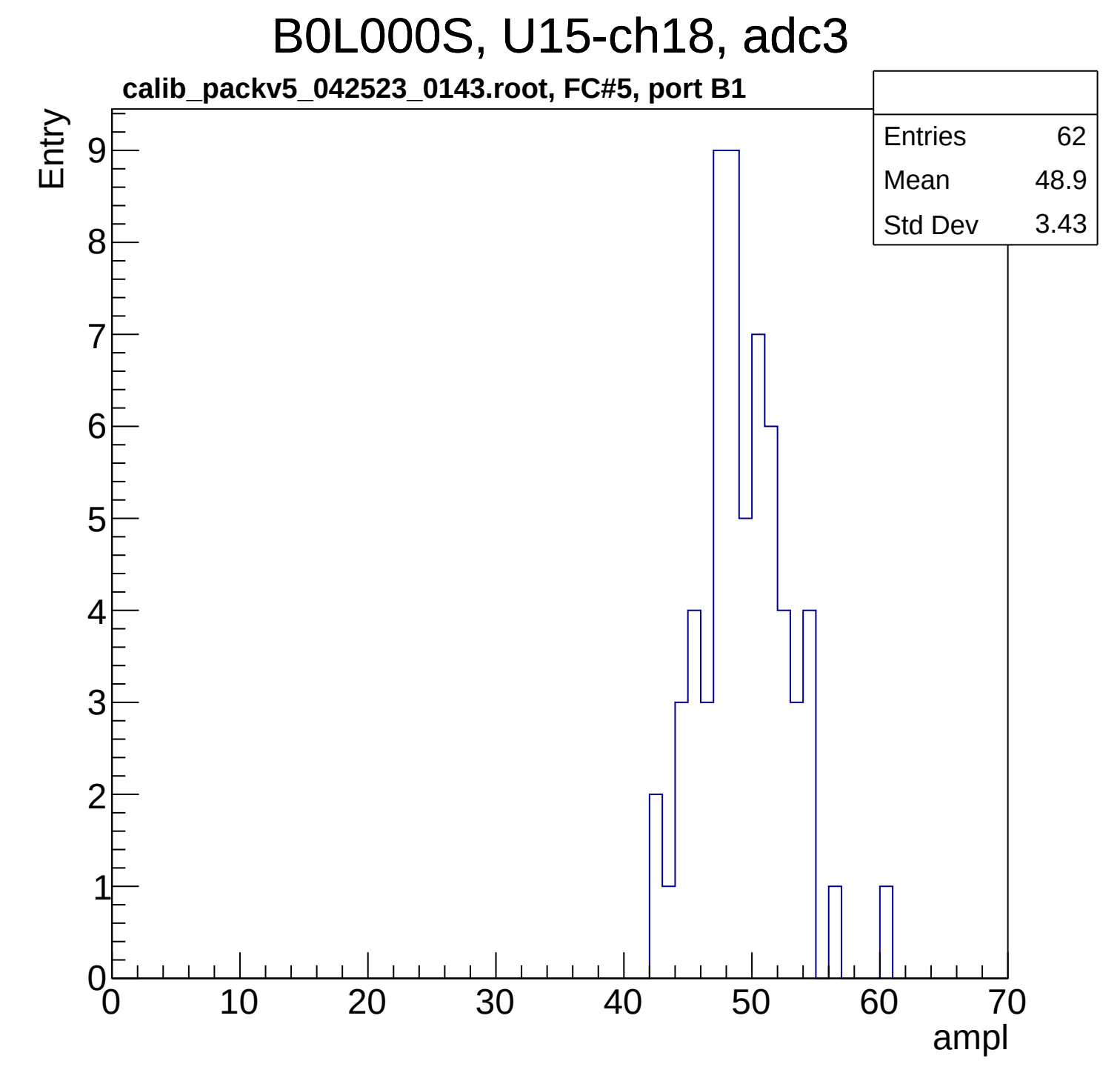
B0L000S, U15-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.9
Std Dev 3.43
Entry
ampl
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7
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0
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60
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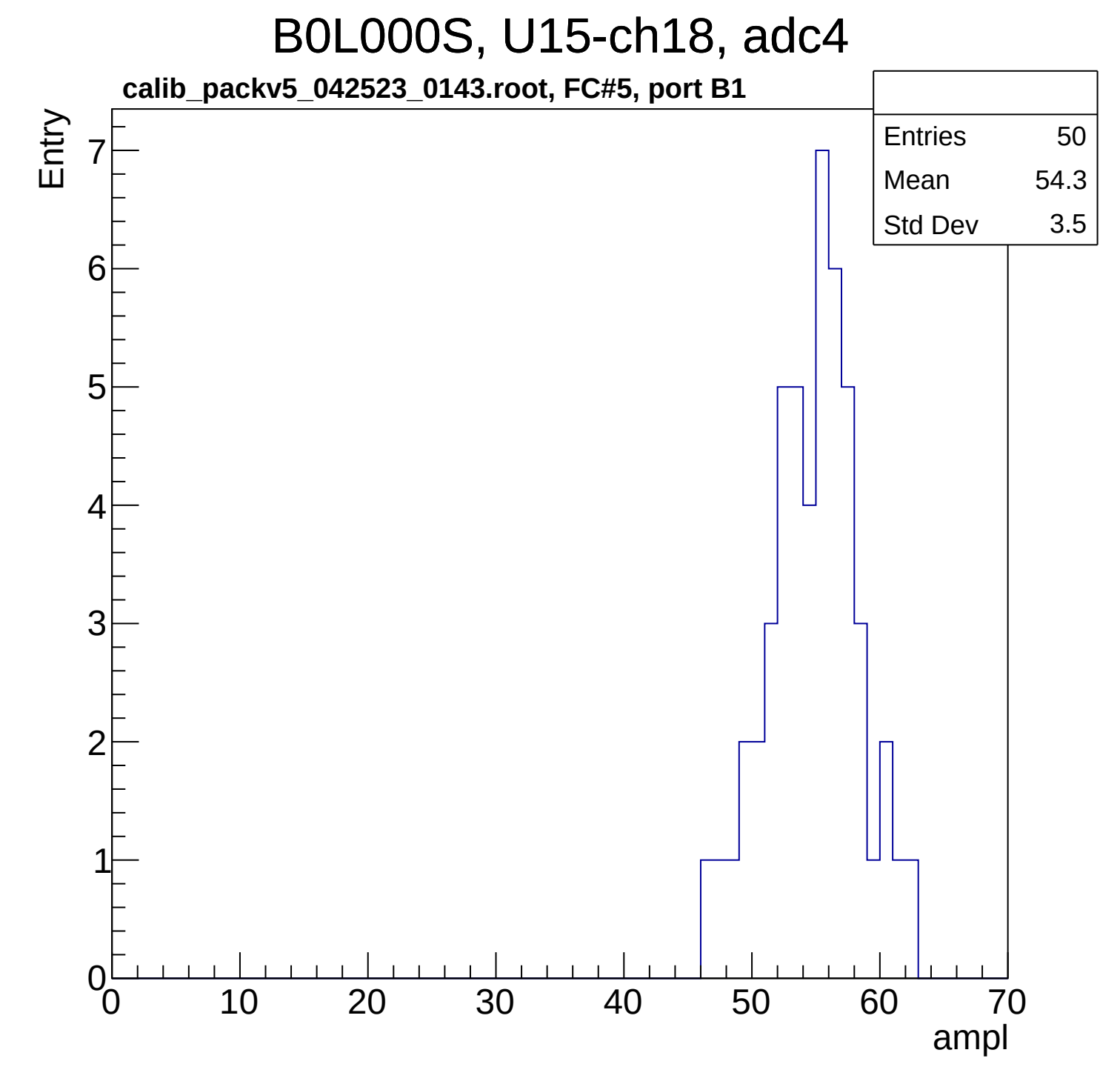
B0L000S, U15-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.3
Std Dev 3.5
Entry
ampl
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3
4
5
6
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60
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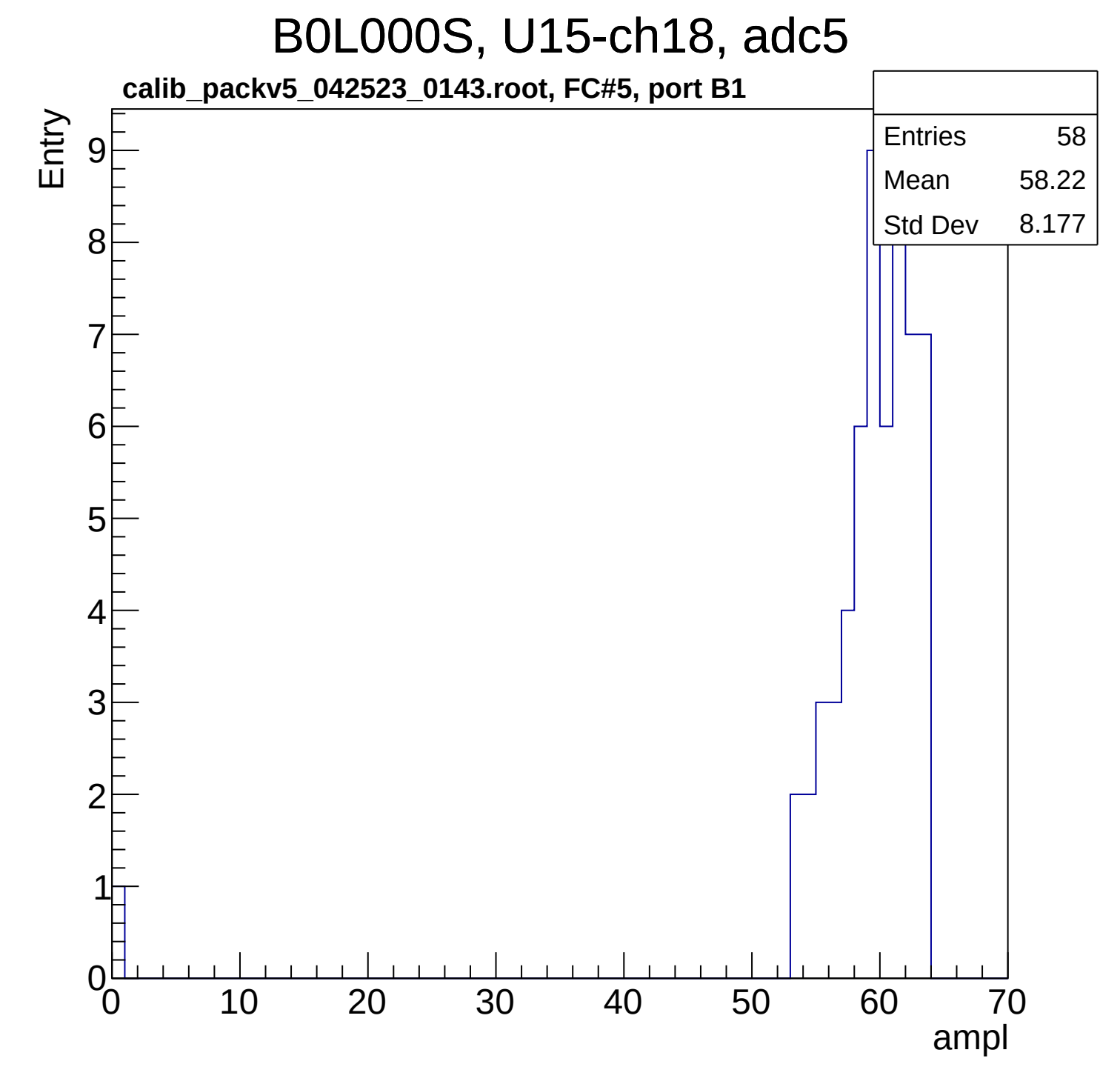
B0L000S, U15-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.22
Std Dev 8.177
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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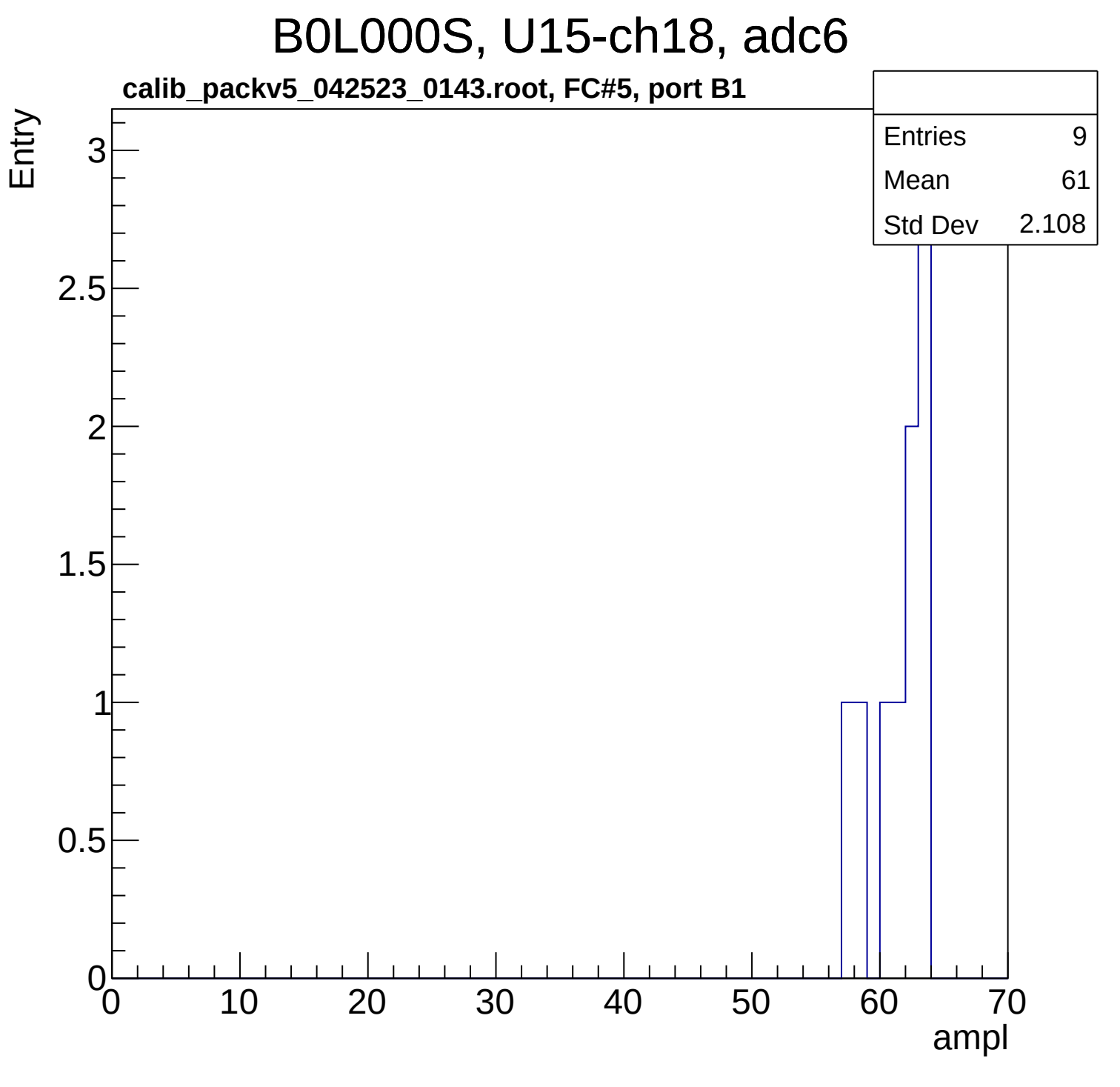
B0L000S, U15-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61
Std Dev 2.108
Entry
ampl
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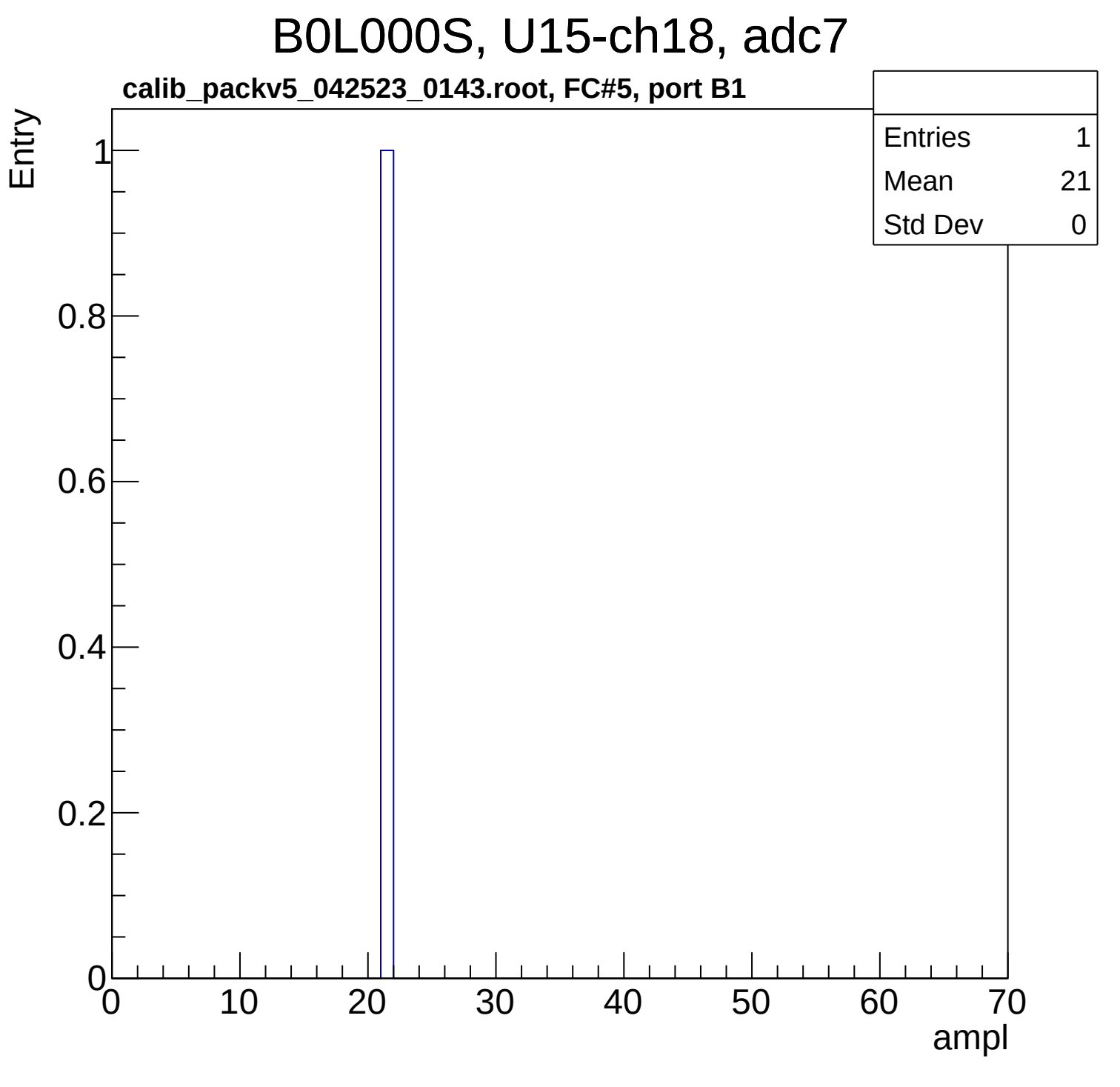
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.4
0.2
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10
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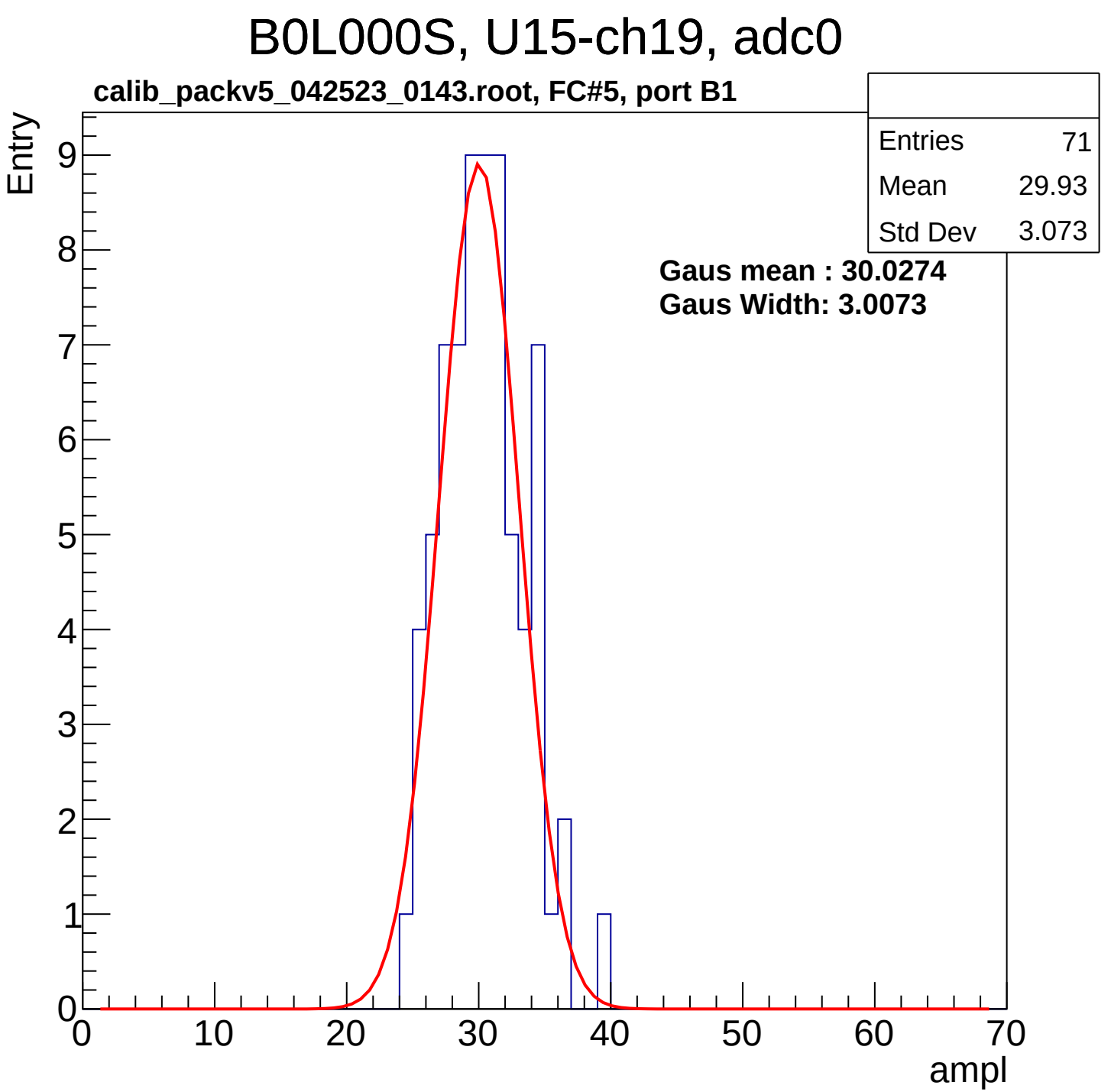
40
50
60
70
ampl


B0L000S, U15-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.93
Std Dev 3.073
Gaus mean : 30.0274
Gaus Width: 3.0073
Entry
ampl
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2
3
4
5
6
7
8
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0
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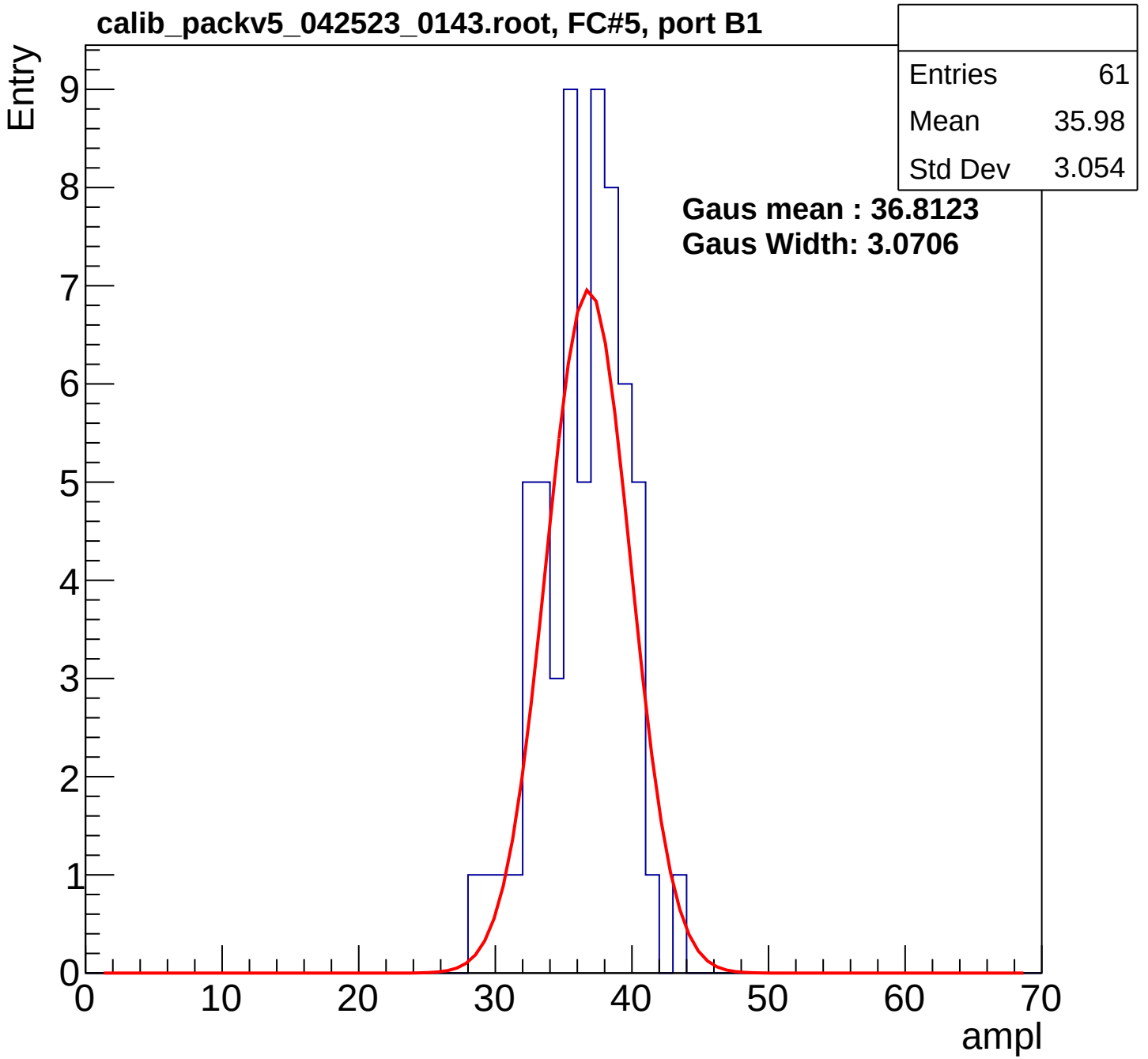
B0L000S, U15-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.98
Std Dev 3.054
Gaus mean : 36.8123
Gaus Width: 3.0706
Entry
ampl
0
1
2
3
4
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6
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8
9
0
10
20
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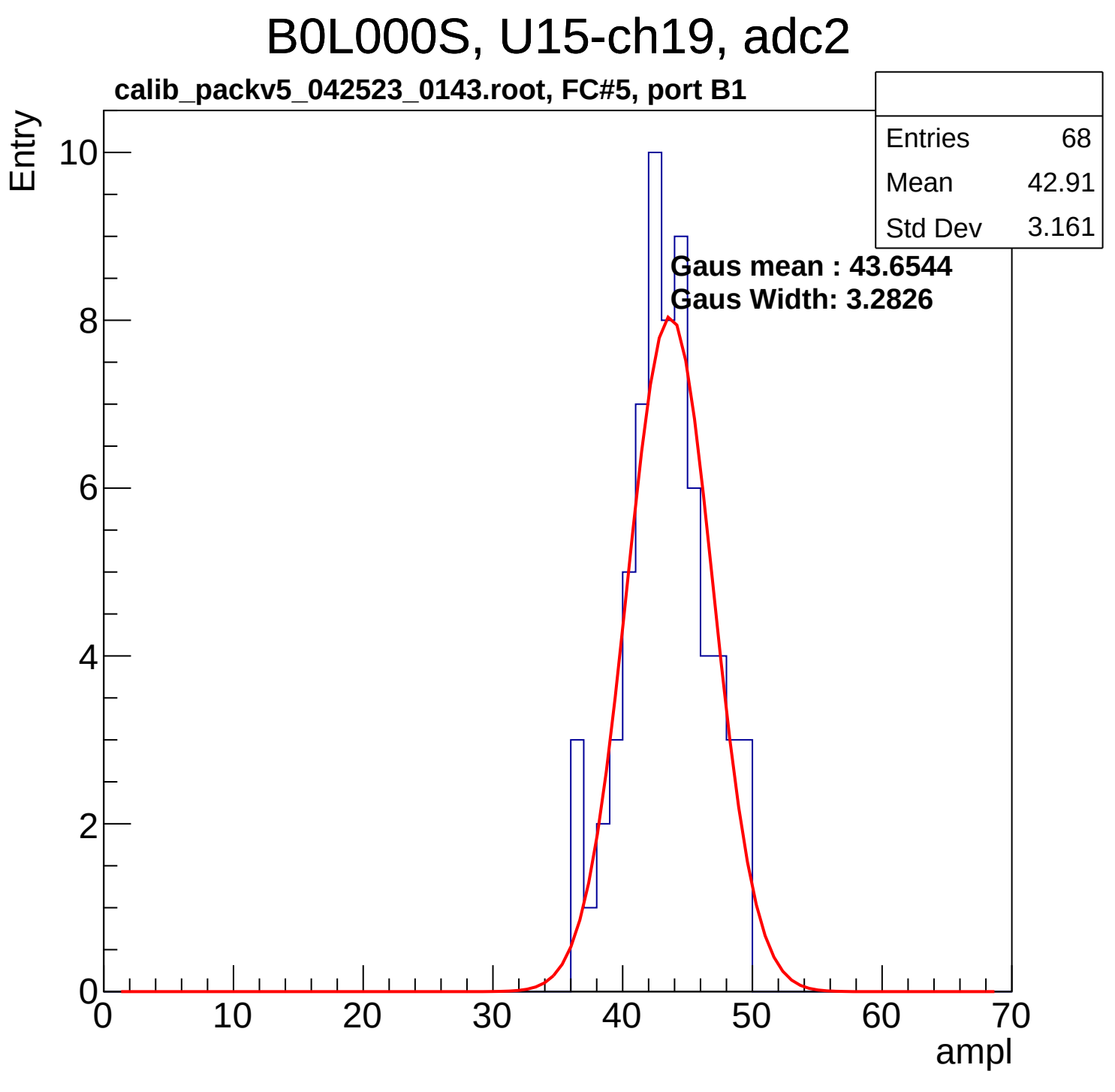
60
70


B0L000S, U15-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.91
Std Dev 3.161
Gaus mean : 43.6544
Gaus Width: 3.2826
Entry
ampl
0
2
4
6
8
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0
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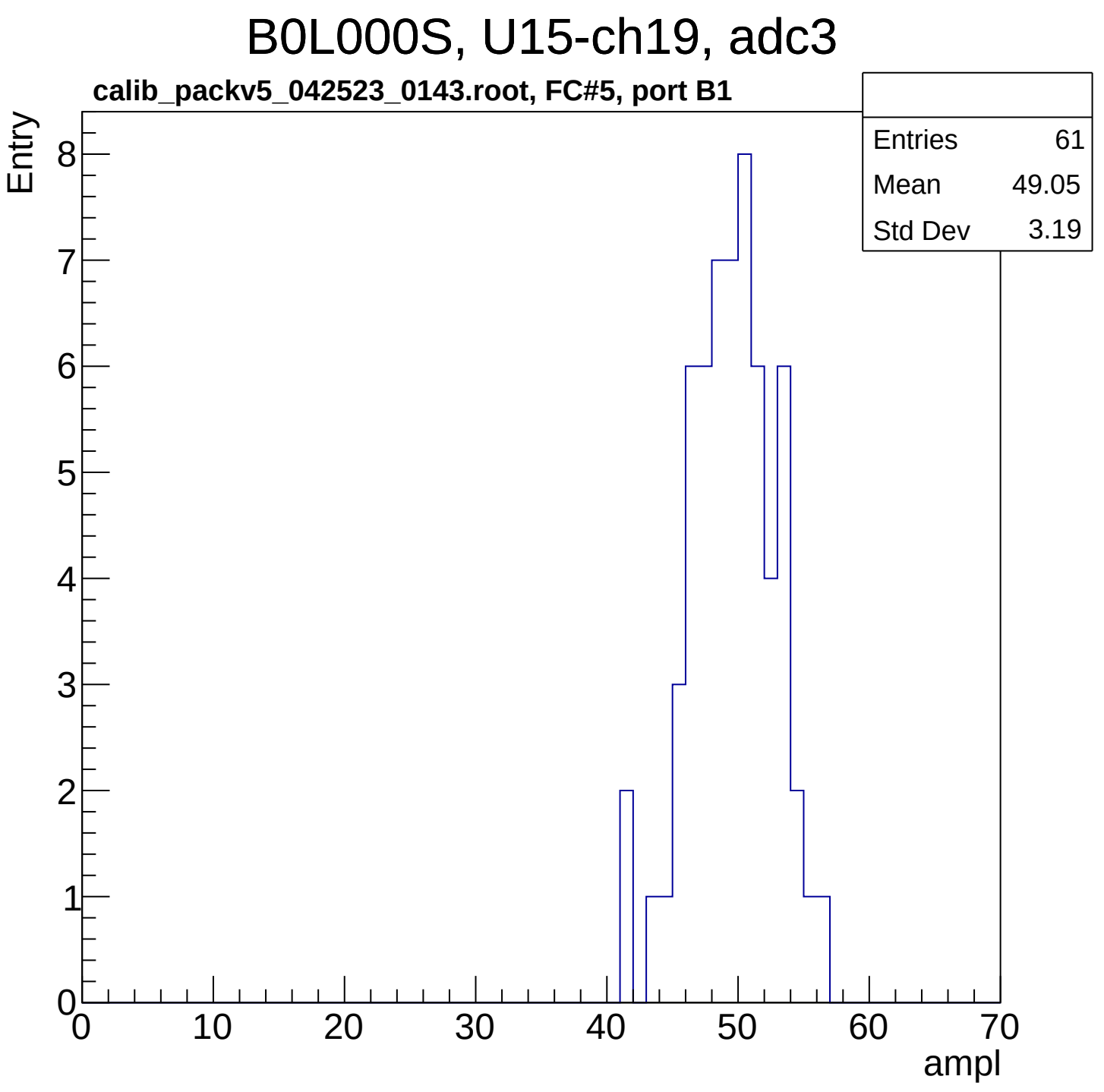
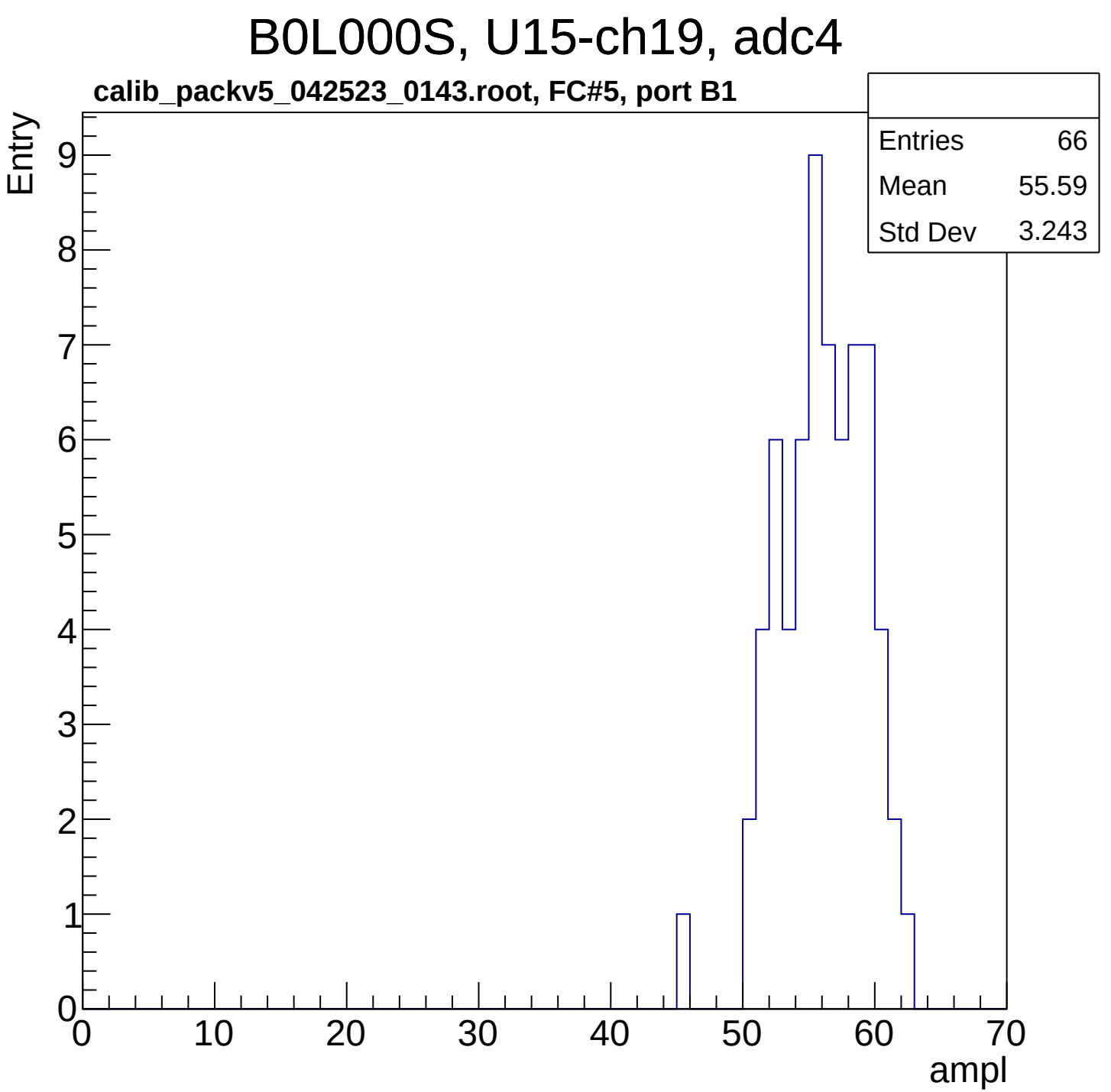

B0L000S, U15-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.05
Std Dev 3.19
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 55.59
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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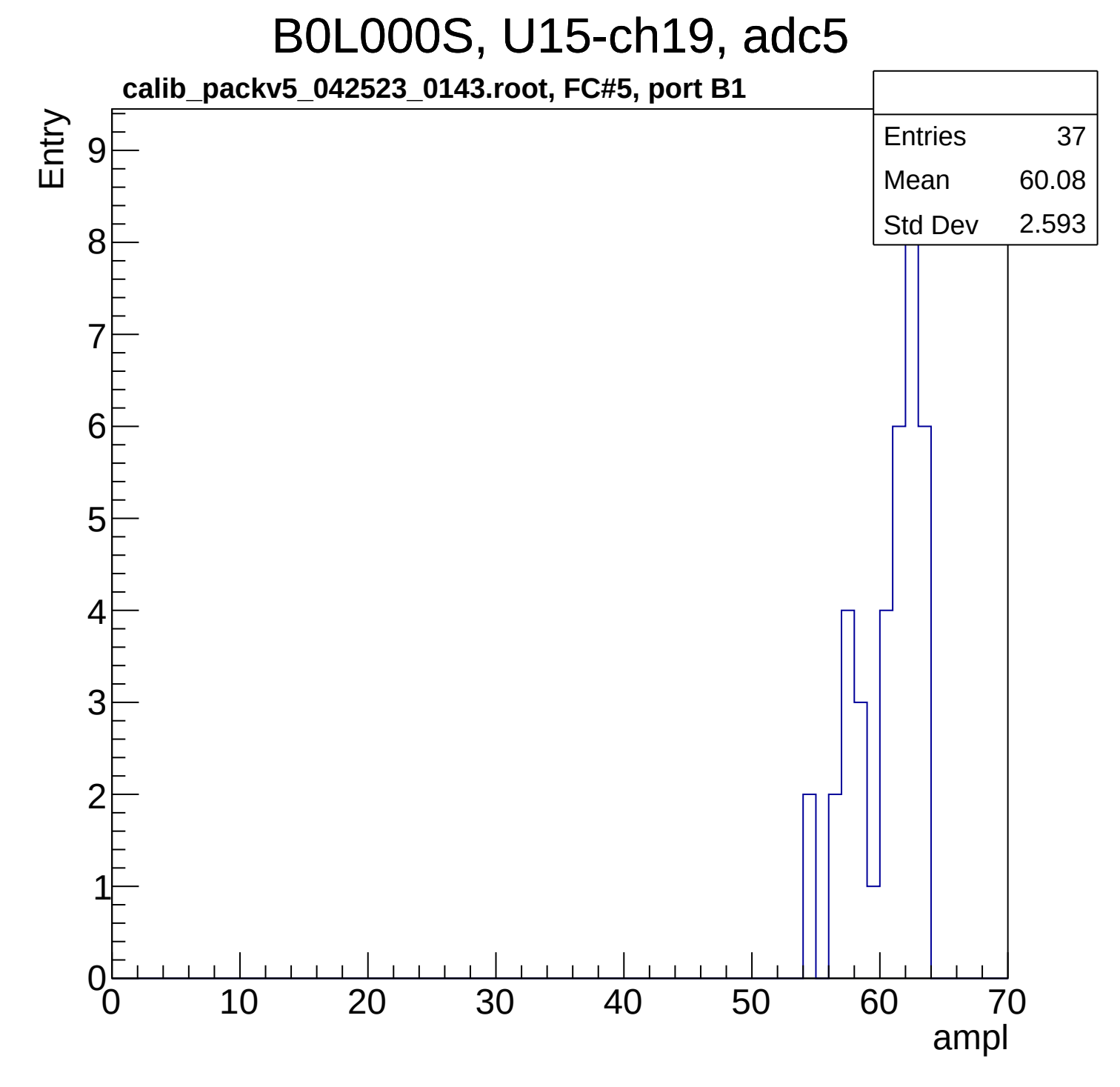
B0L000S, U15-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.08
Std Dev 2.593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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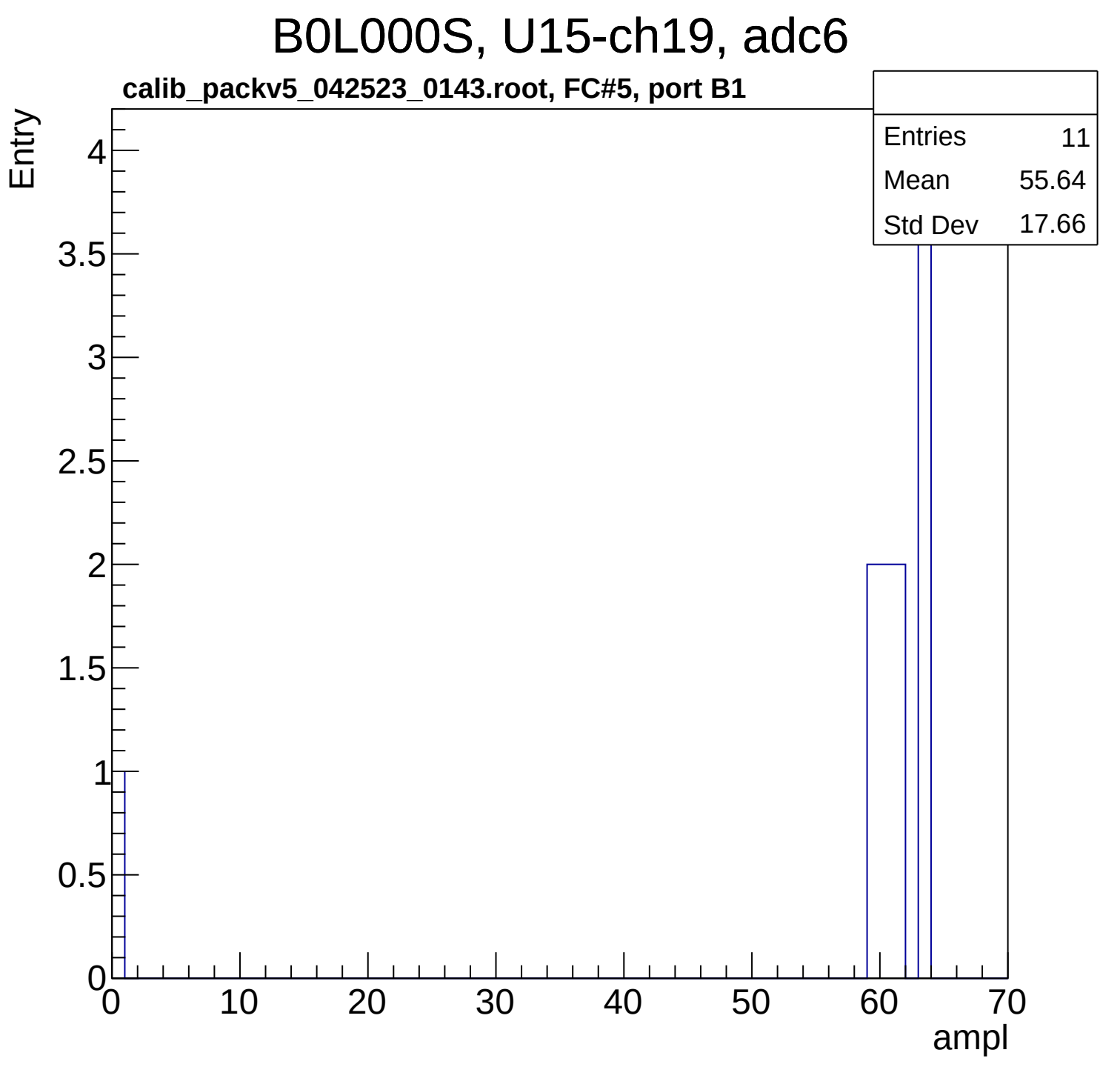
B0L000S, U15-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 55.64
Std Dev 17.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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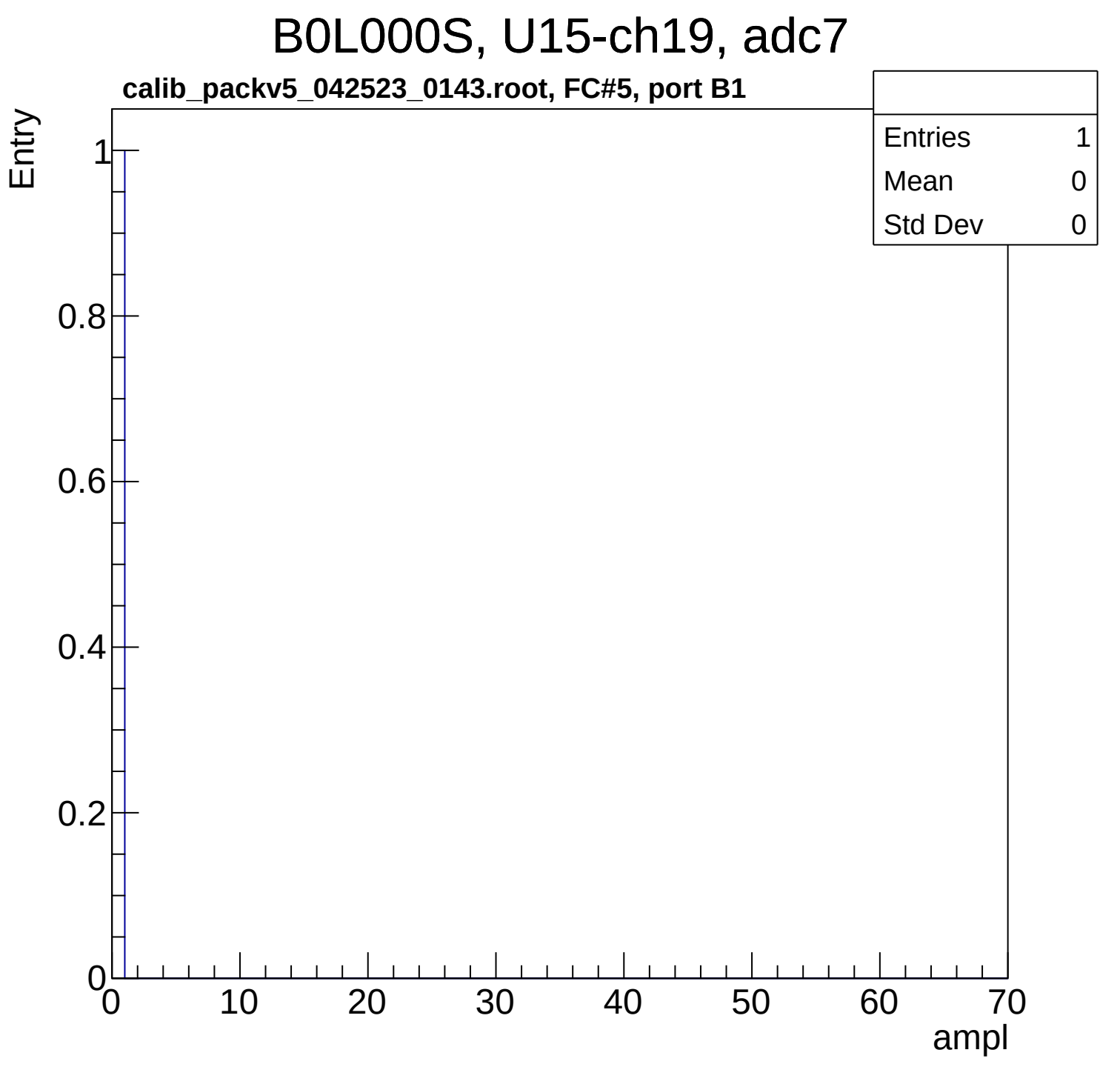
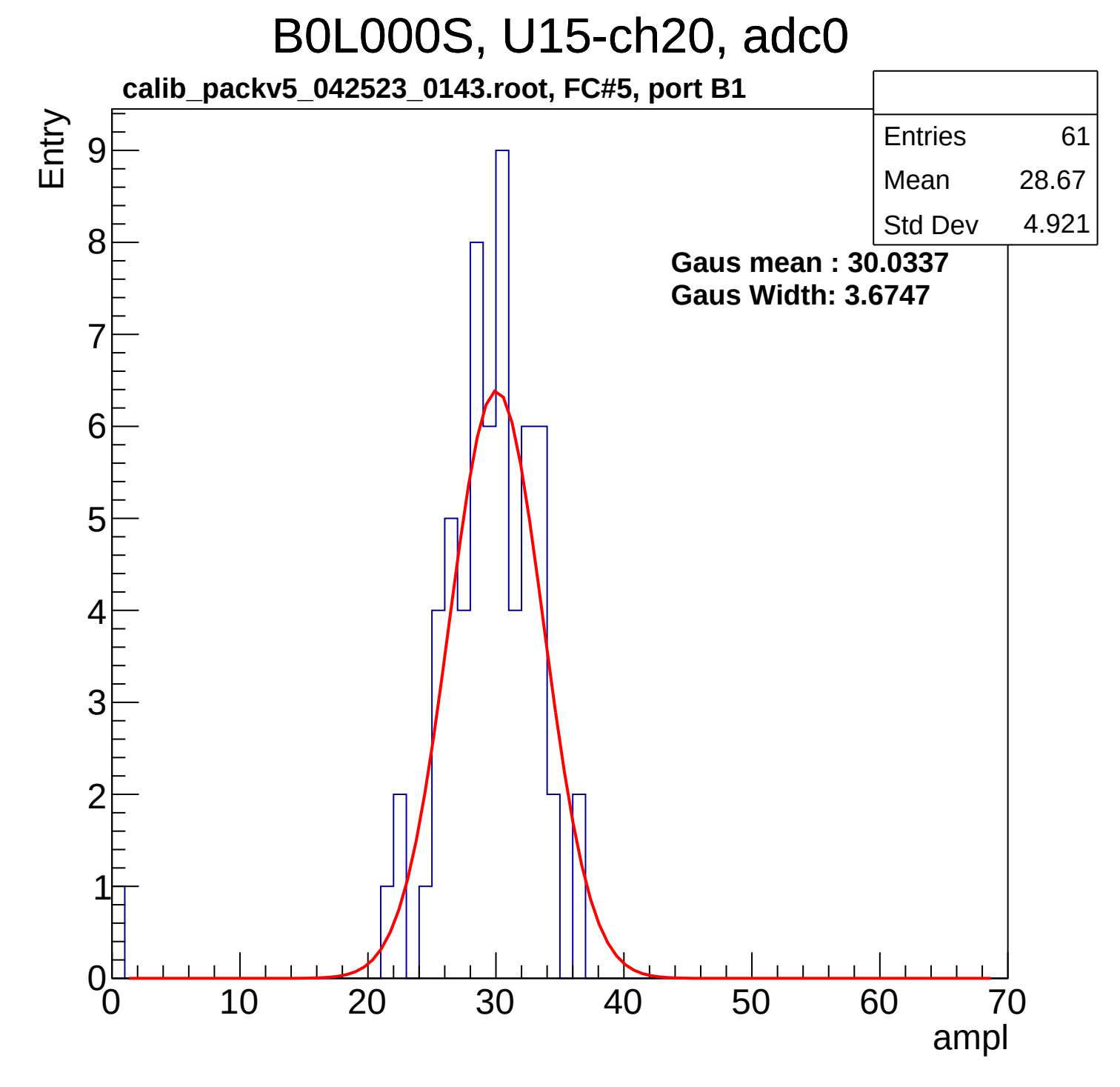
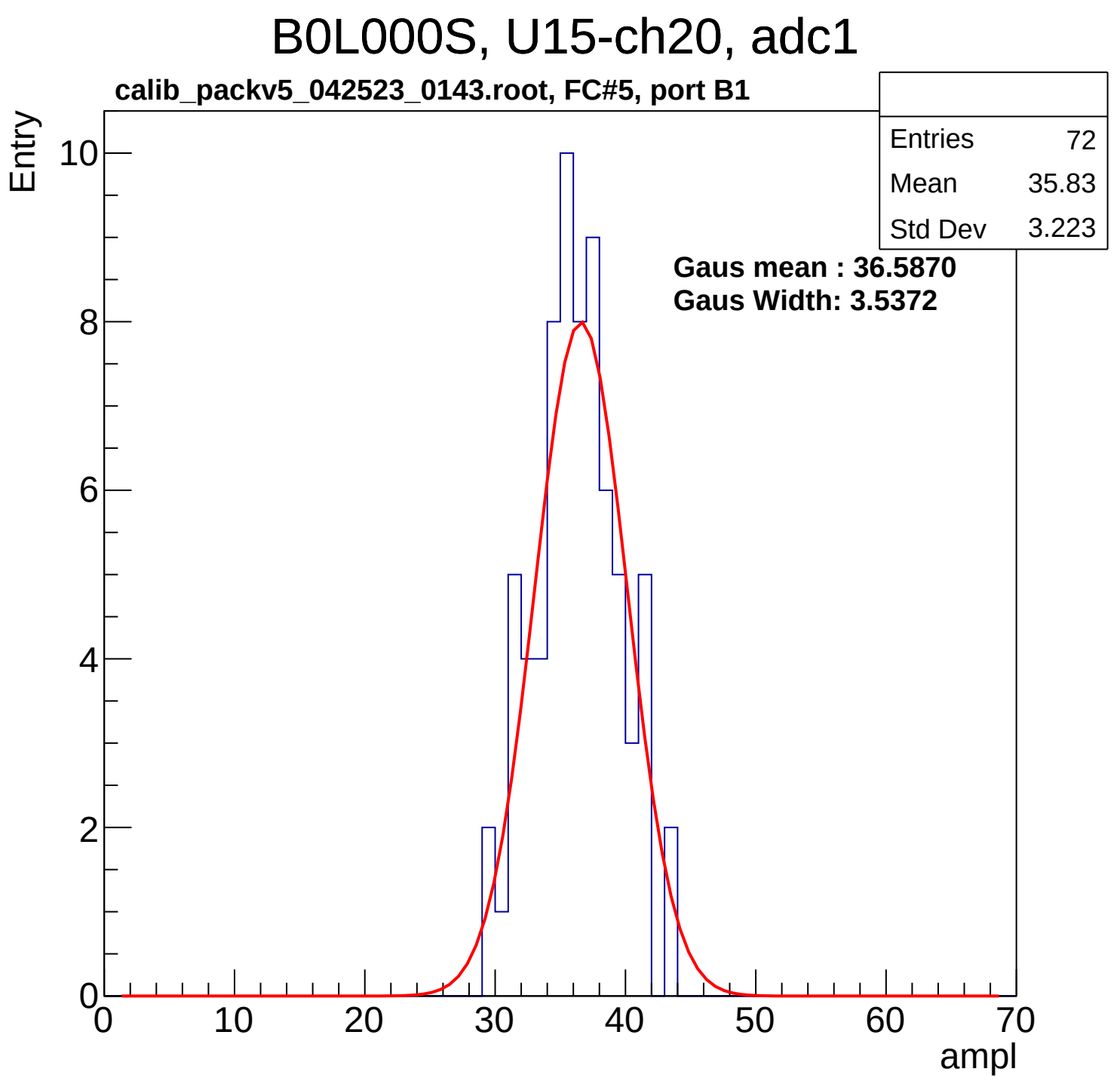
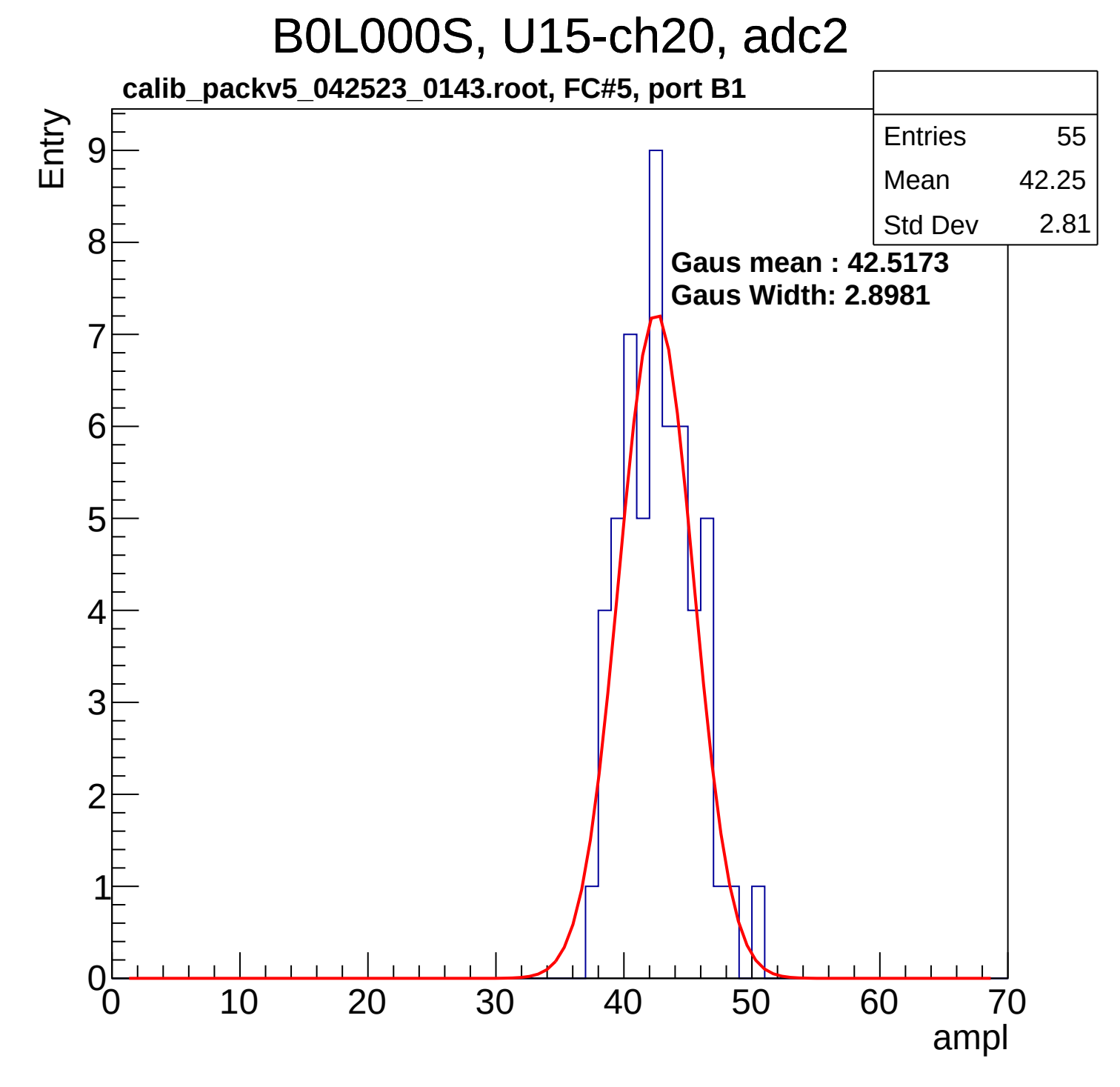
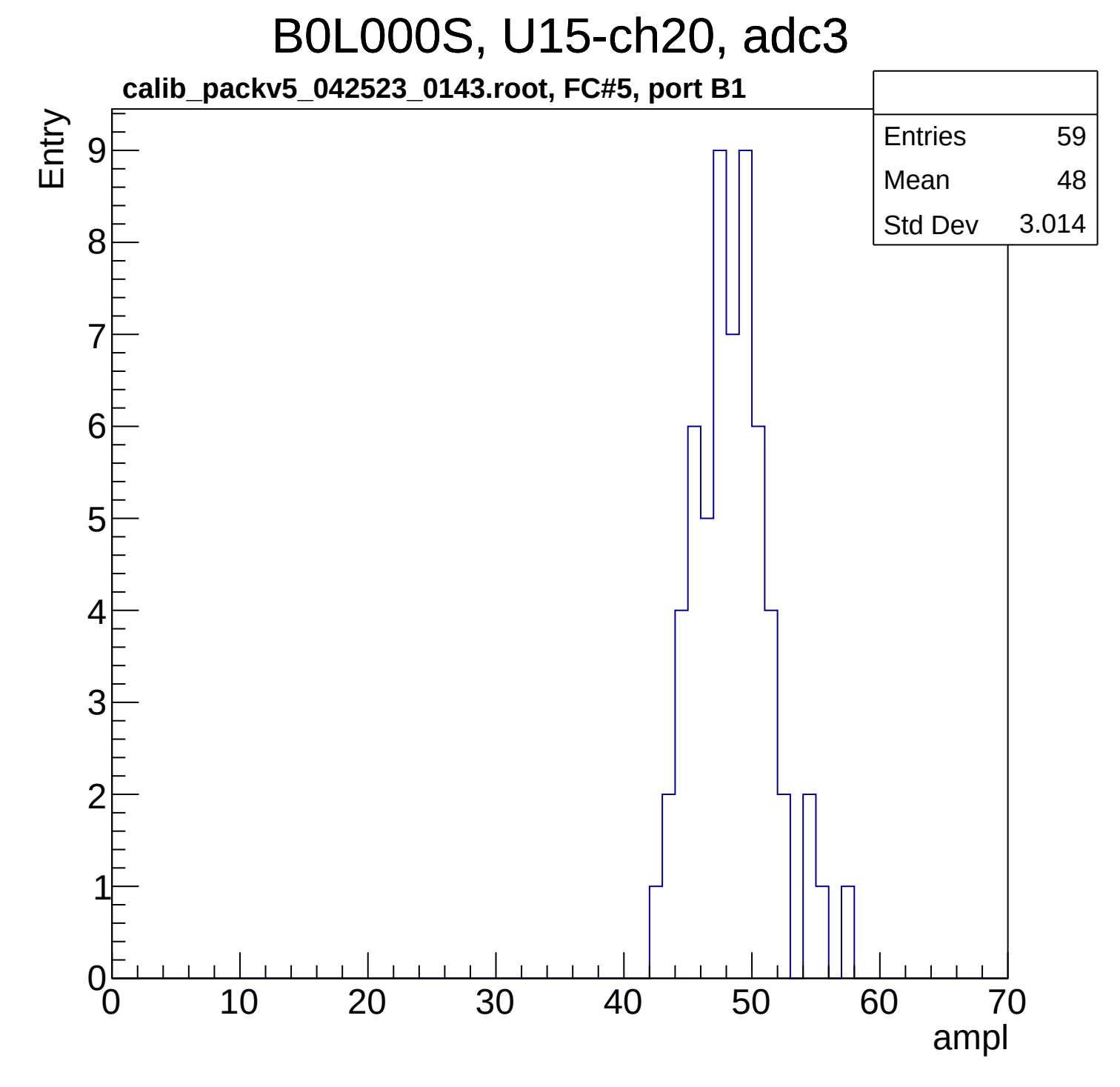

B0L000S, U15-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.67
Std Dev 4.921
Gaus mean : 30.0337
Gaus Width: 3.6747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.83
Std Dev 3.223
Gaus mean : 36.5870
Gaus Width: 3.5372
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.25
Std Dev 2.81
Gaus mean : 42.5173
Gaus Width: 2.8981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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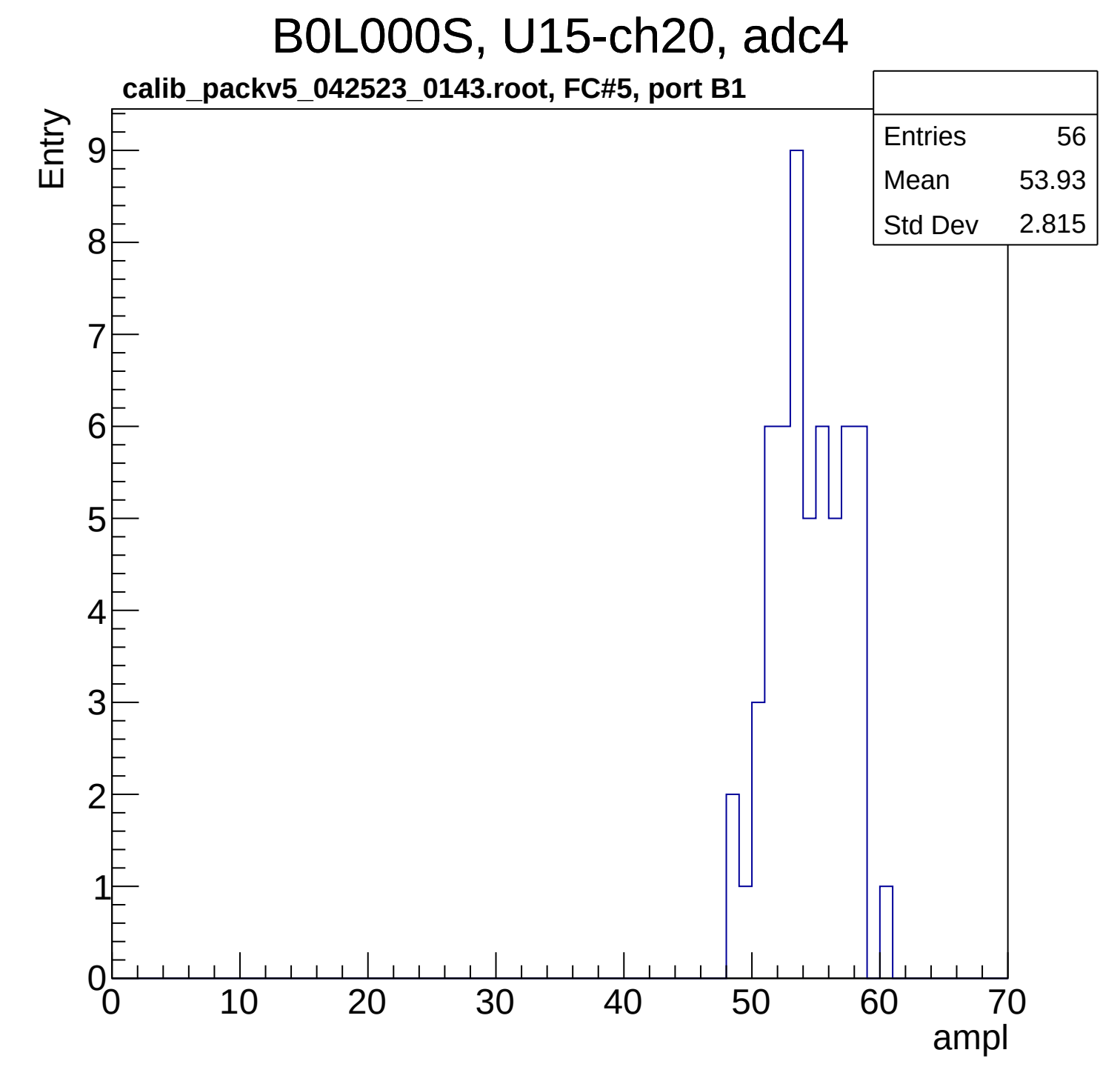
B0L000S, U15-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.93
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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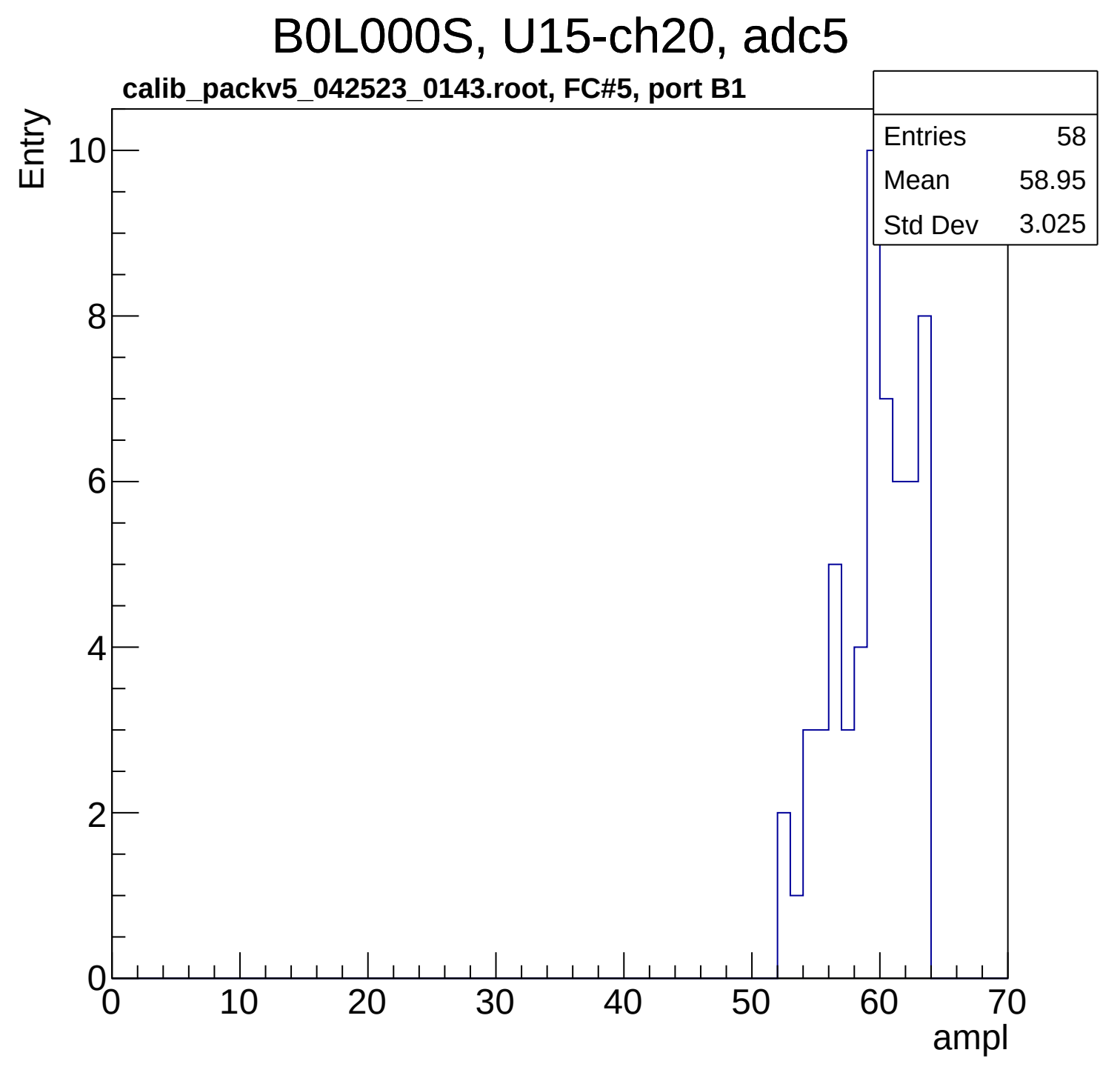
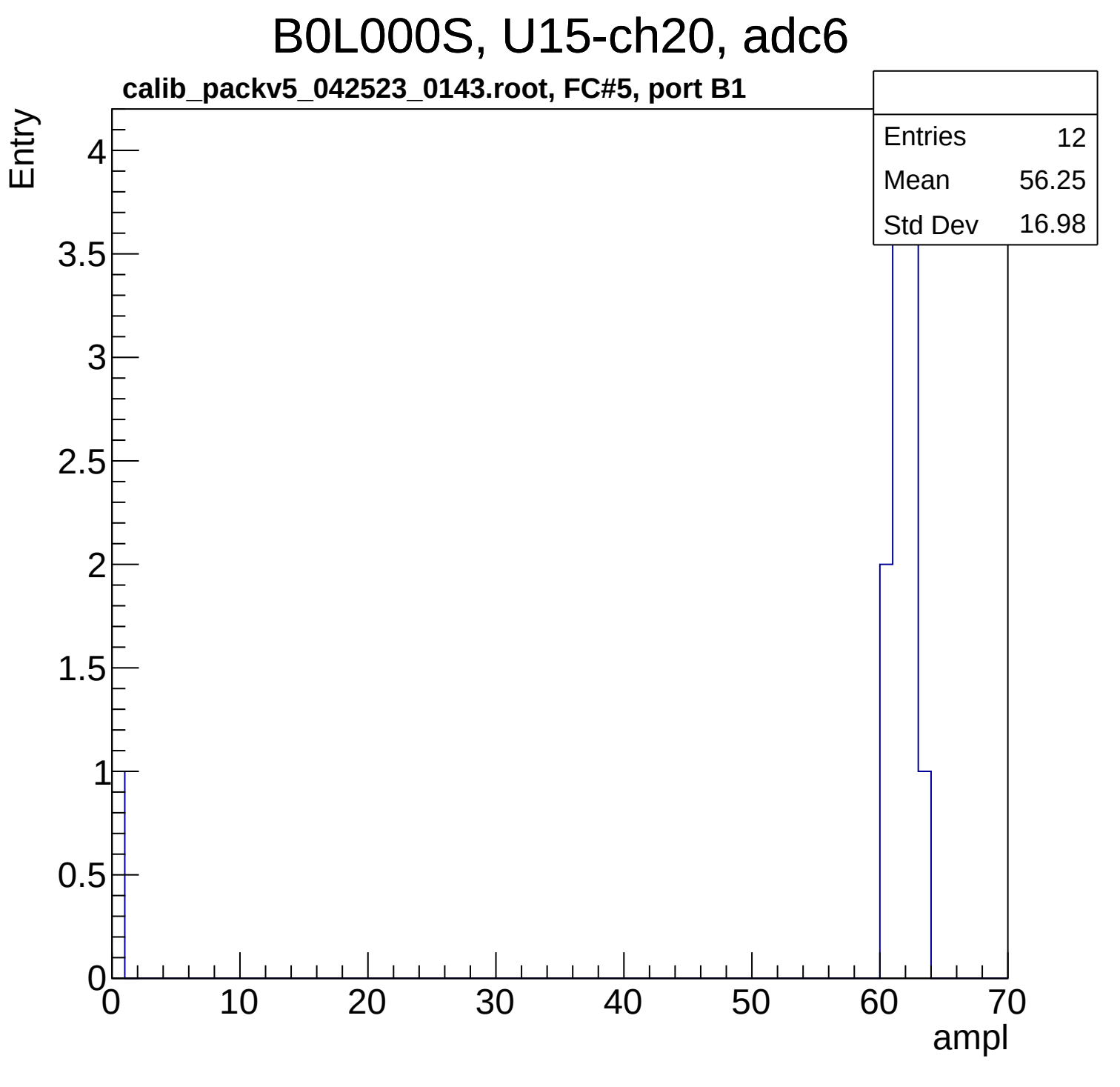
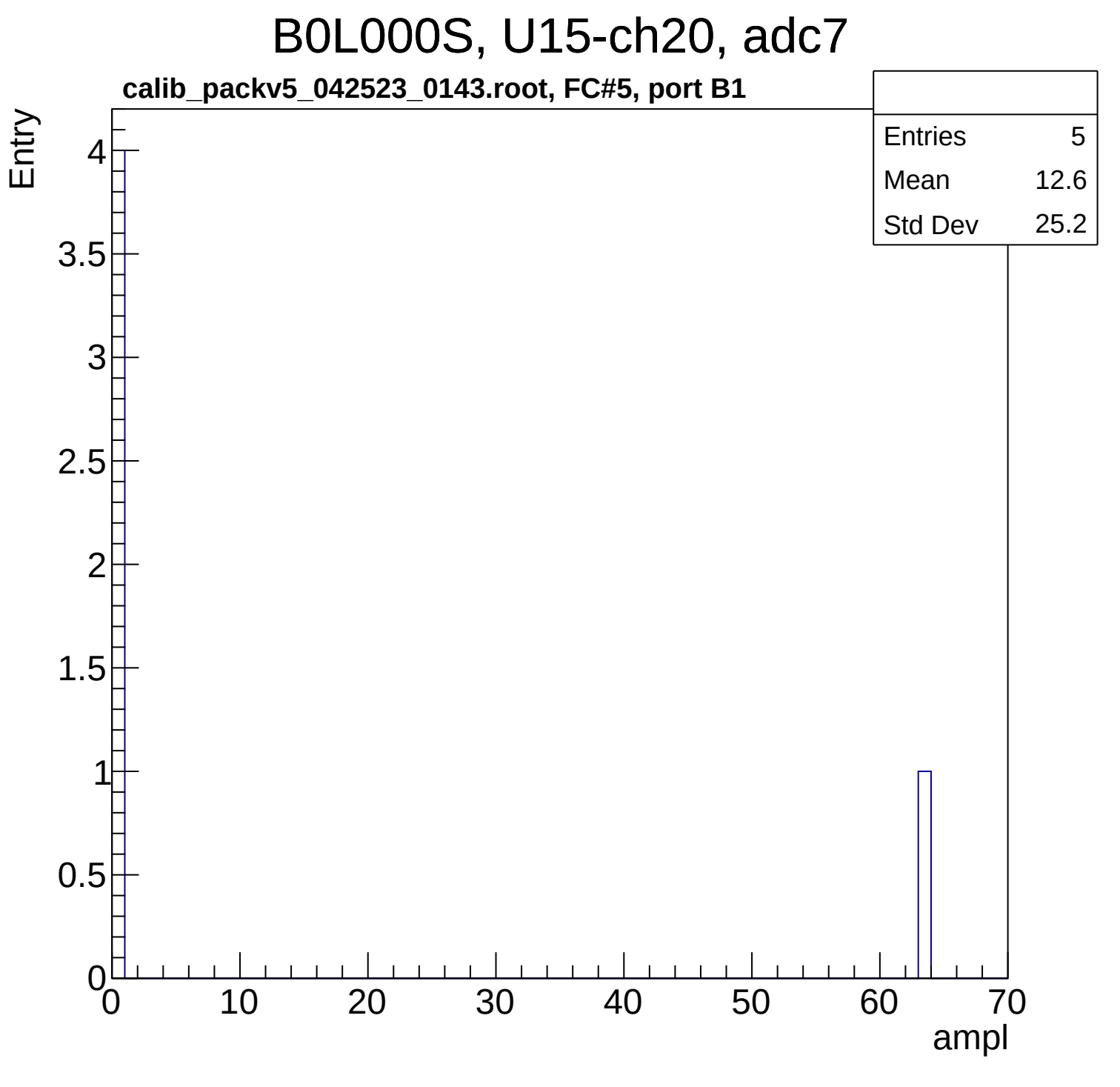
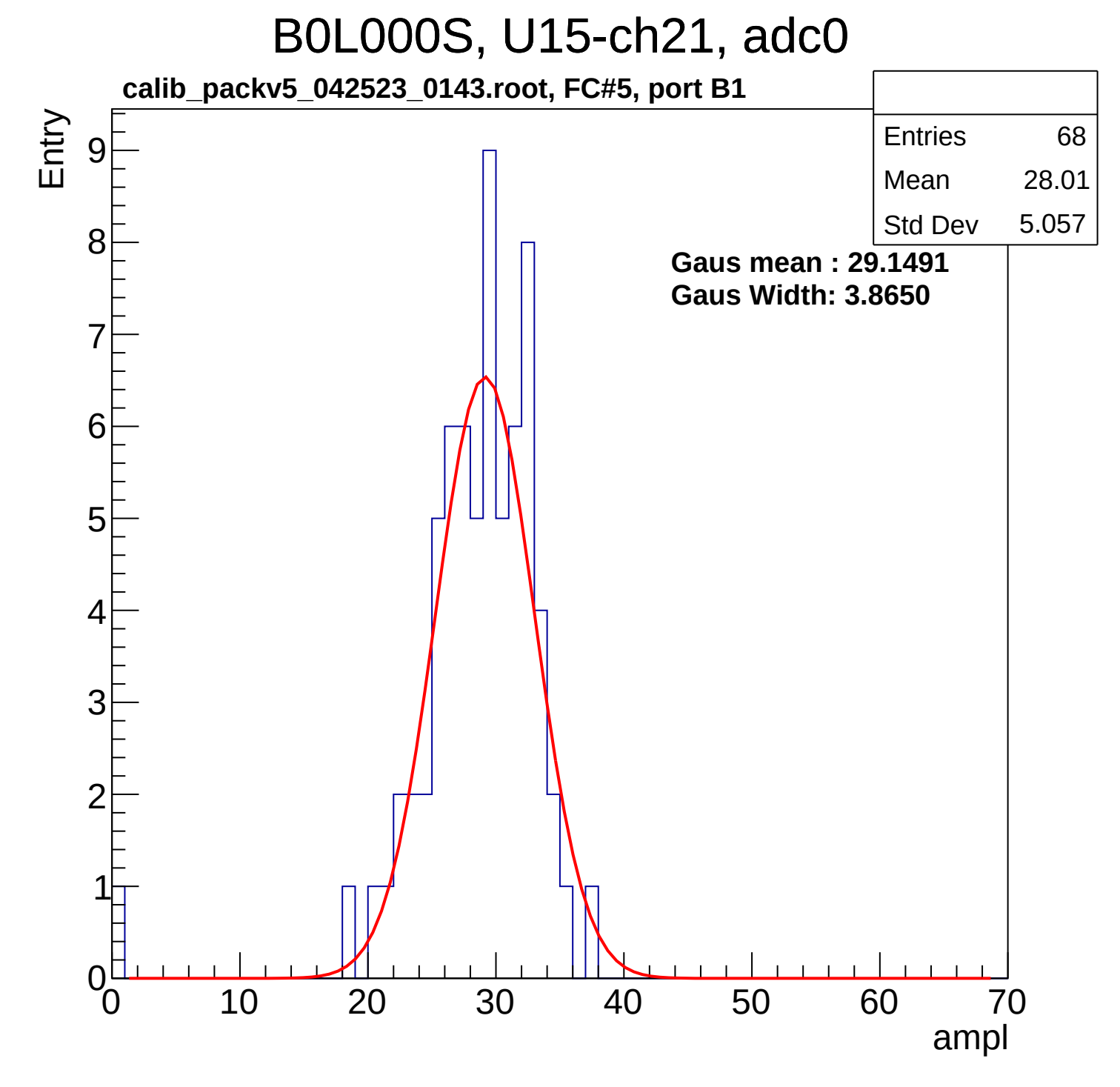
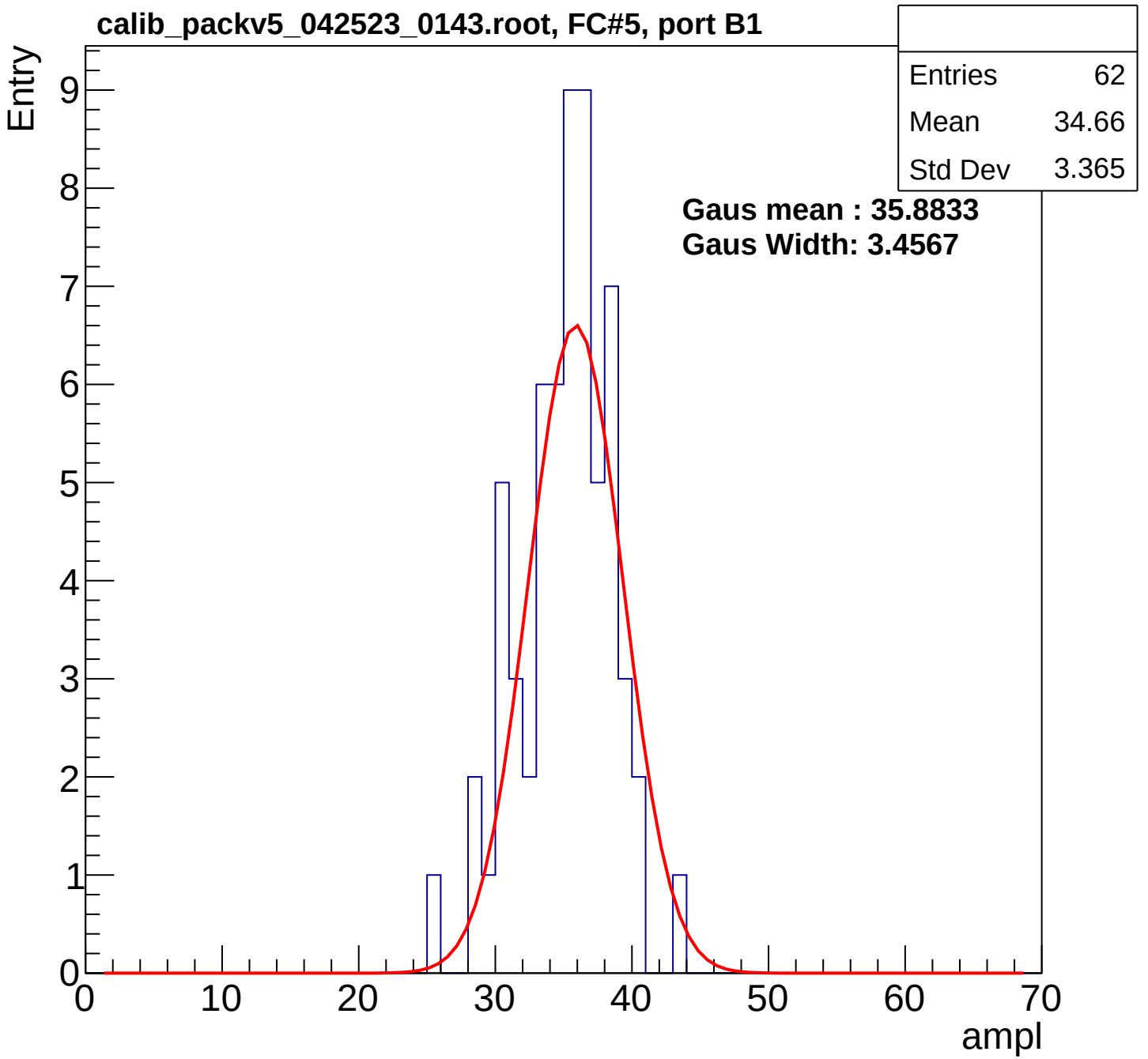
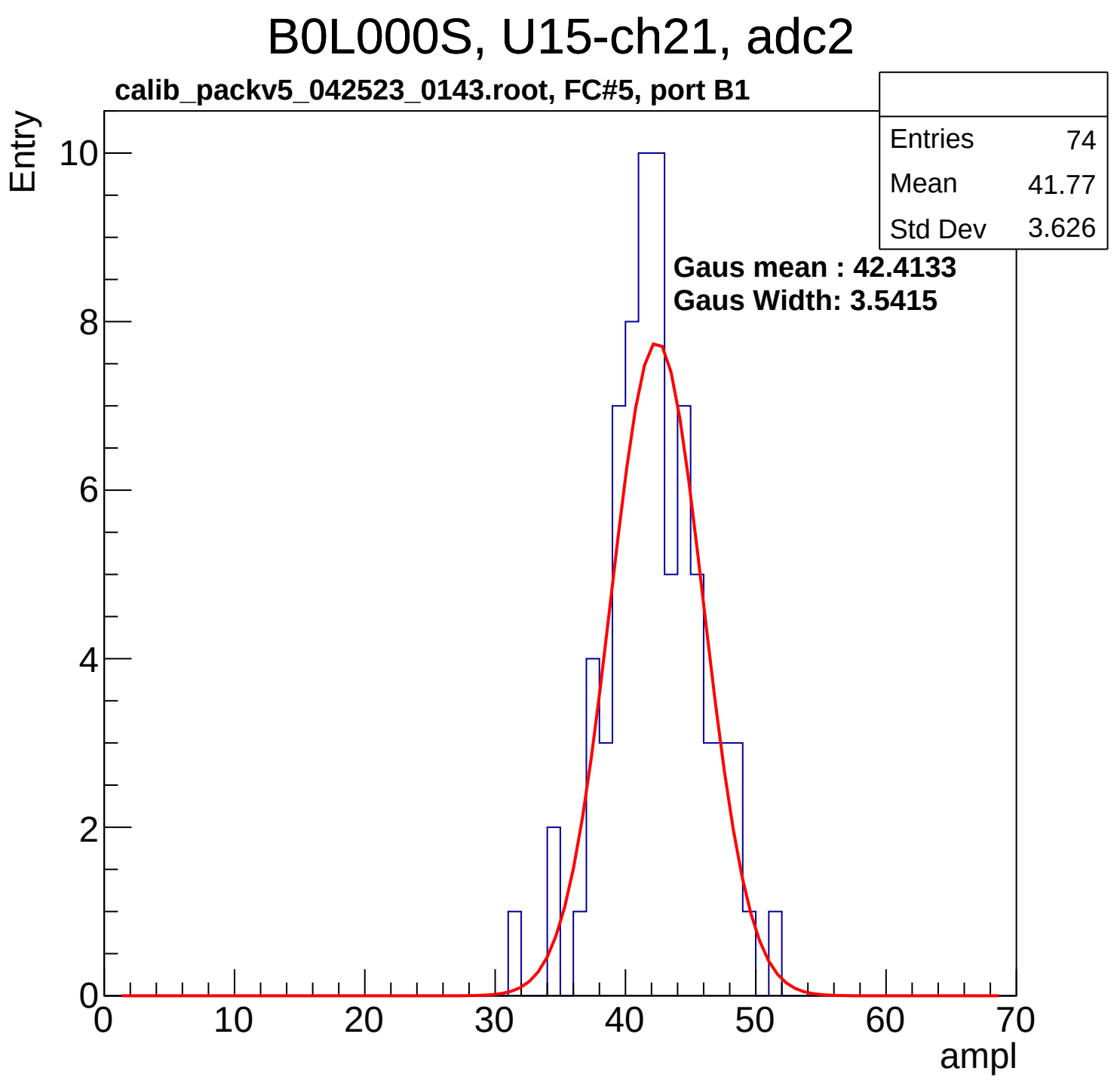

B0L000S, U15-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.95
Std Dev 3.025
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

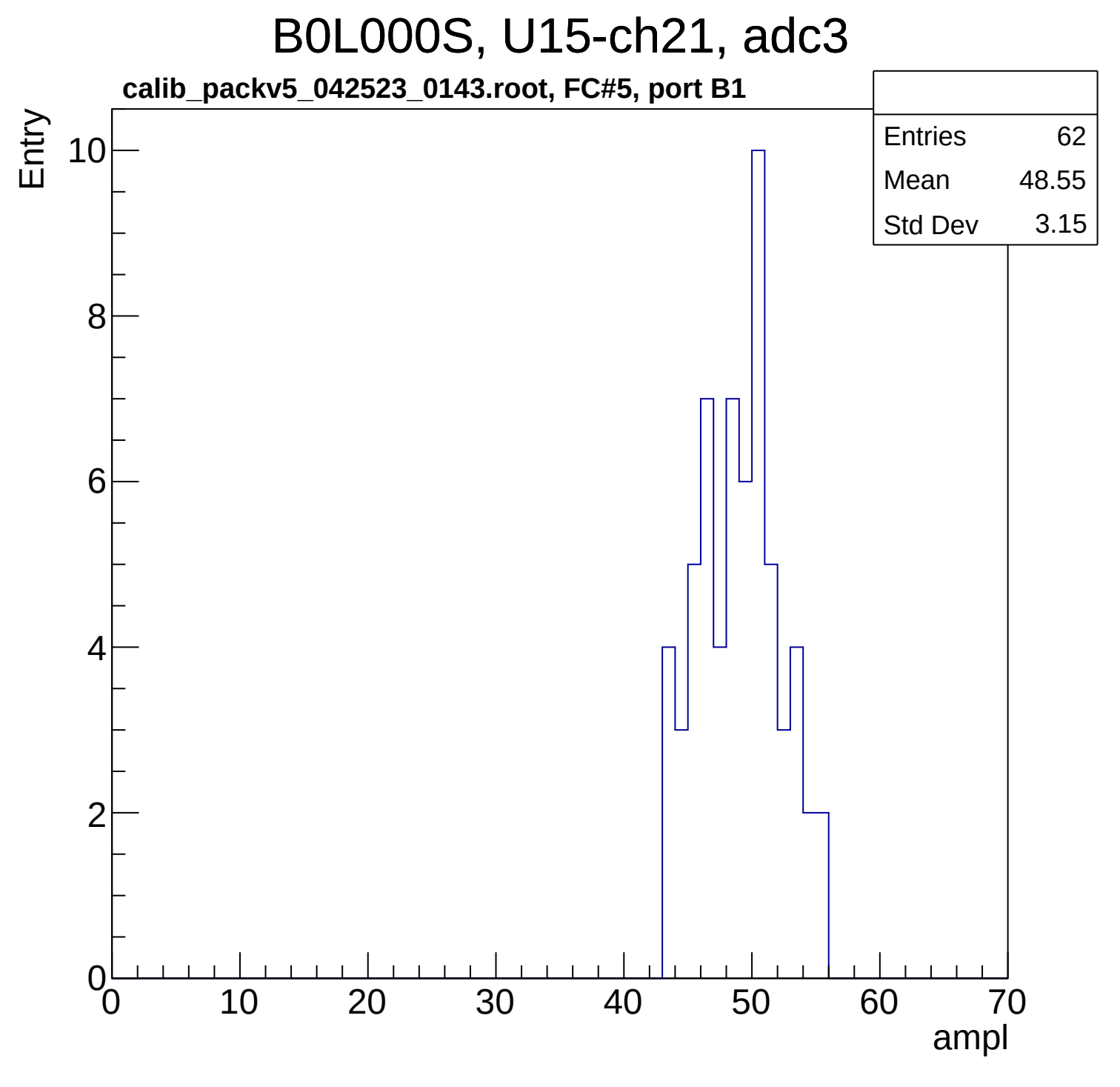

B0L000S, U15-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.25
Std Dev 16.98
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

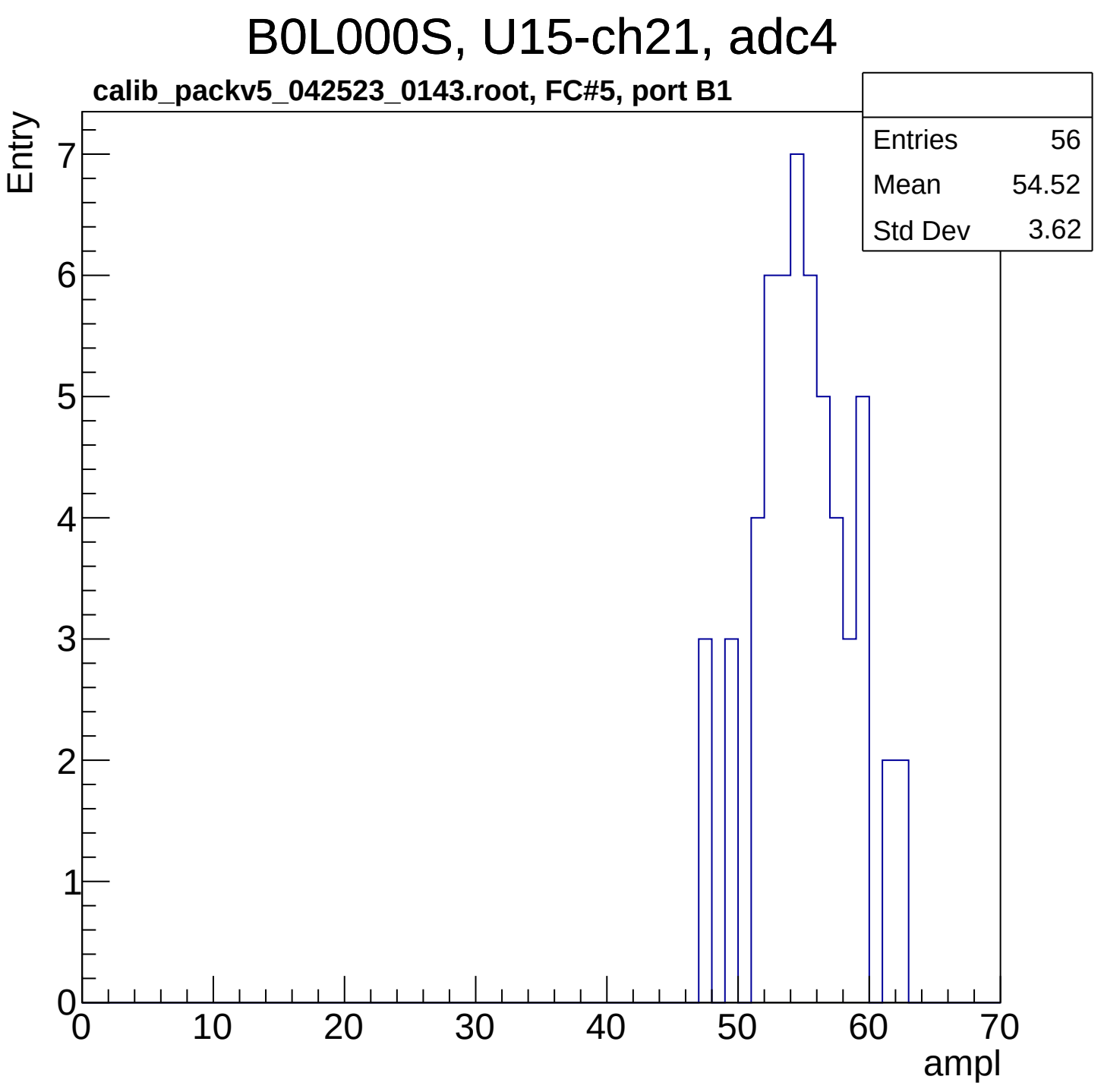

B0L000S, U15-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 12.6
Std Dev 25.2
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

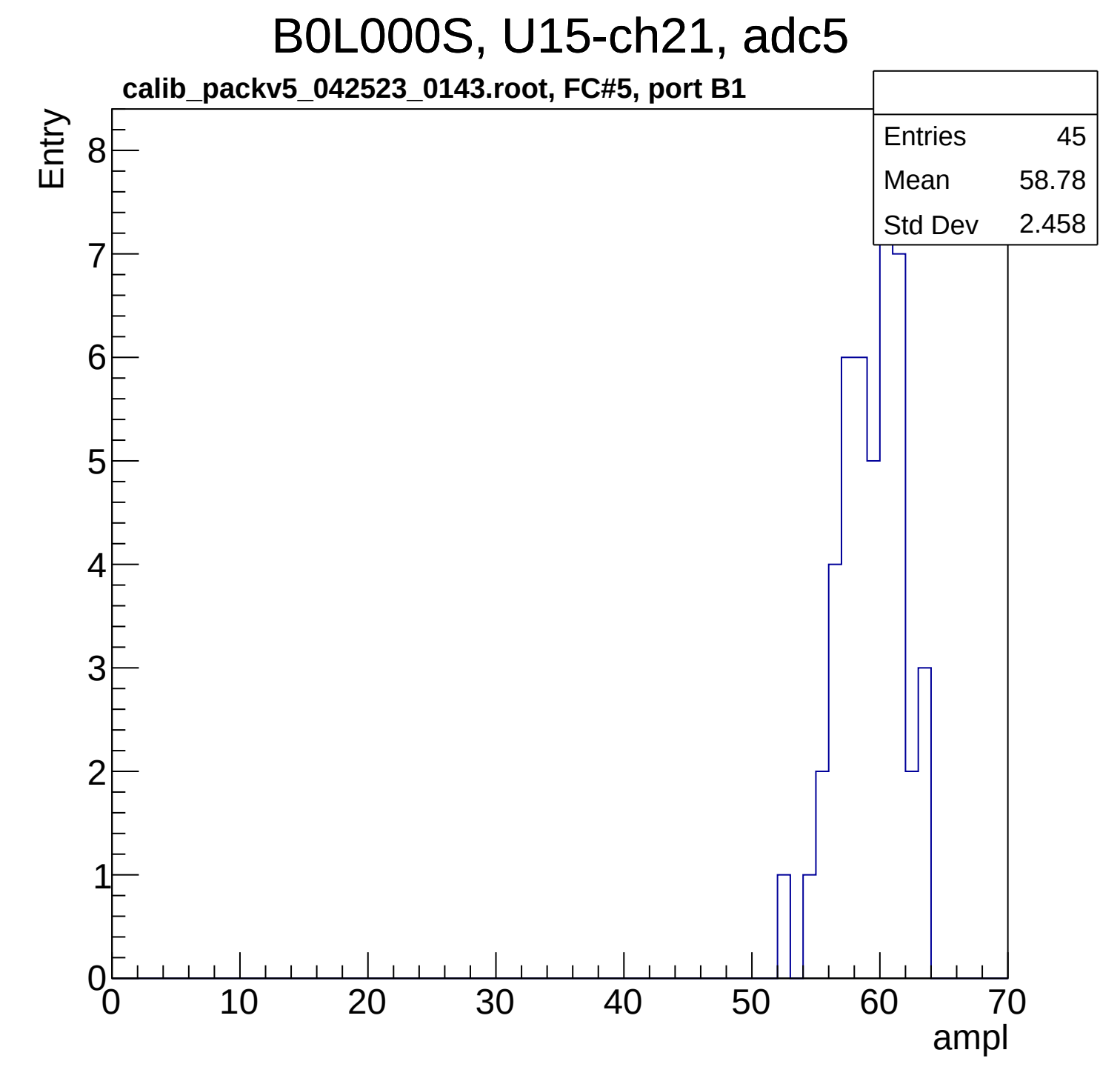

B0L000S, U15-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.01
Std Dev 5.057
Gaus mean : 29.1491
Gaus Width: 3.8650
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

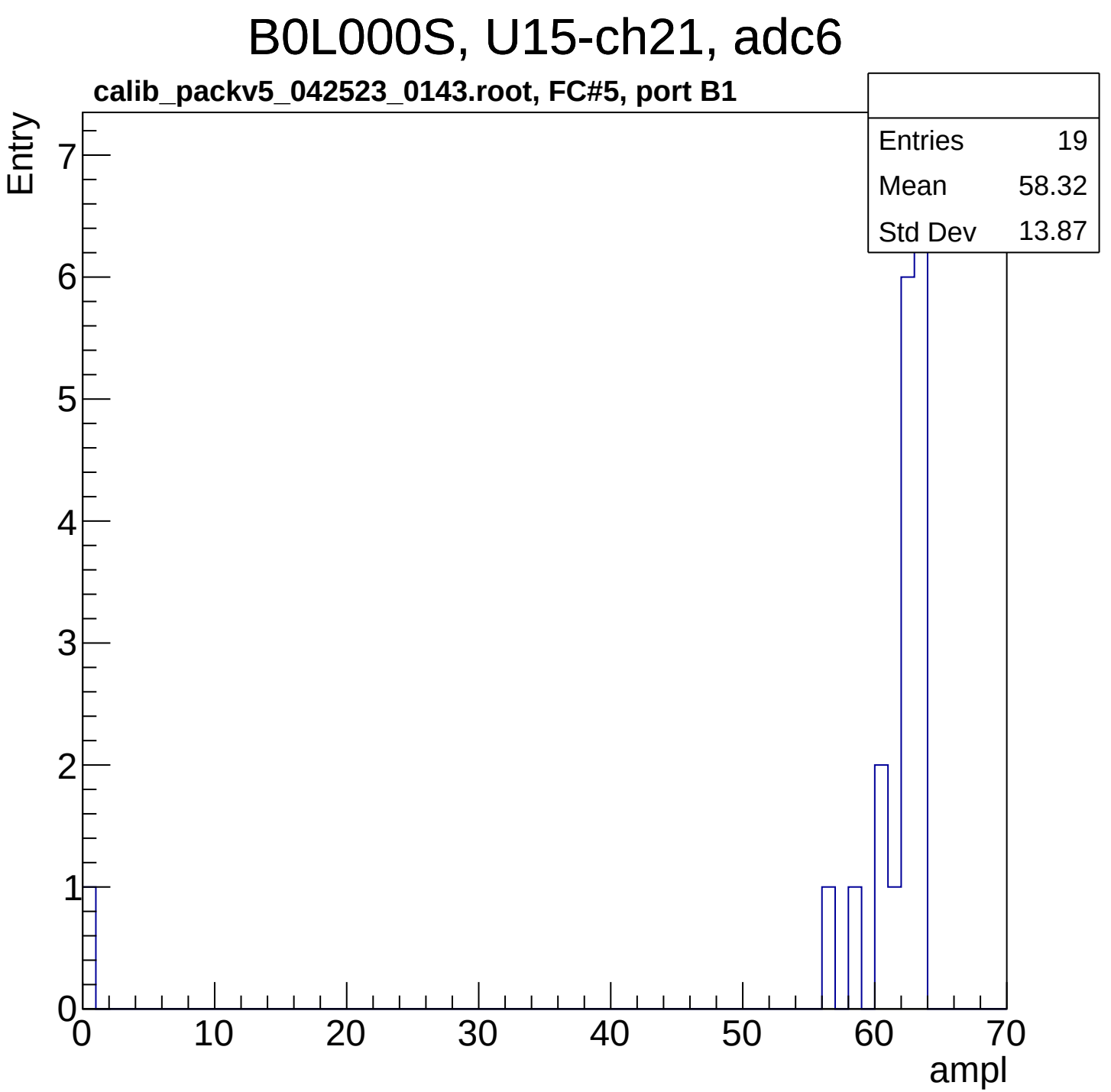

B0L000S, U15-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 34.66
Std Dev 3.365
Gaus mean : 35.8833
Gaus Width: 3.4567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

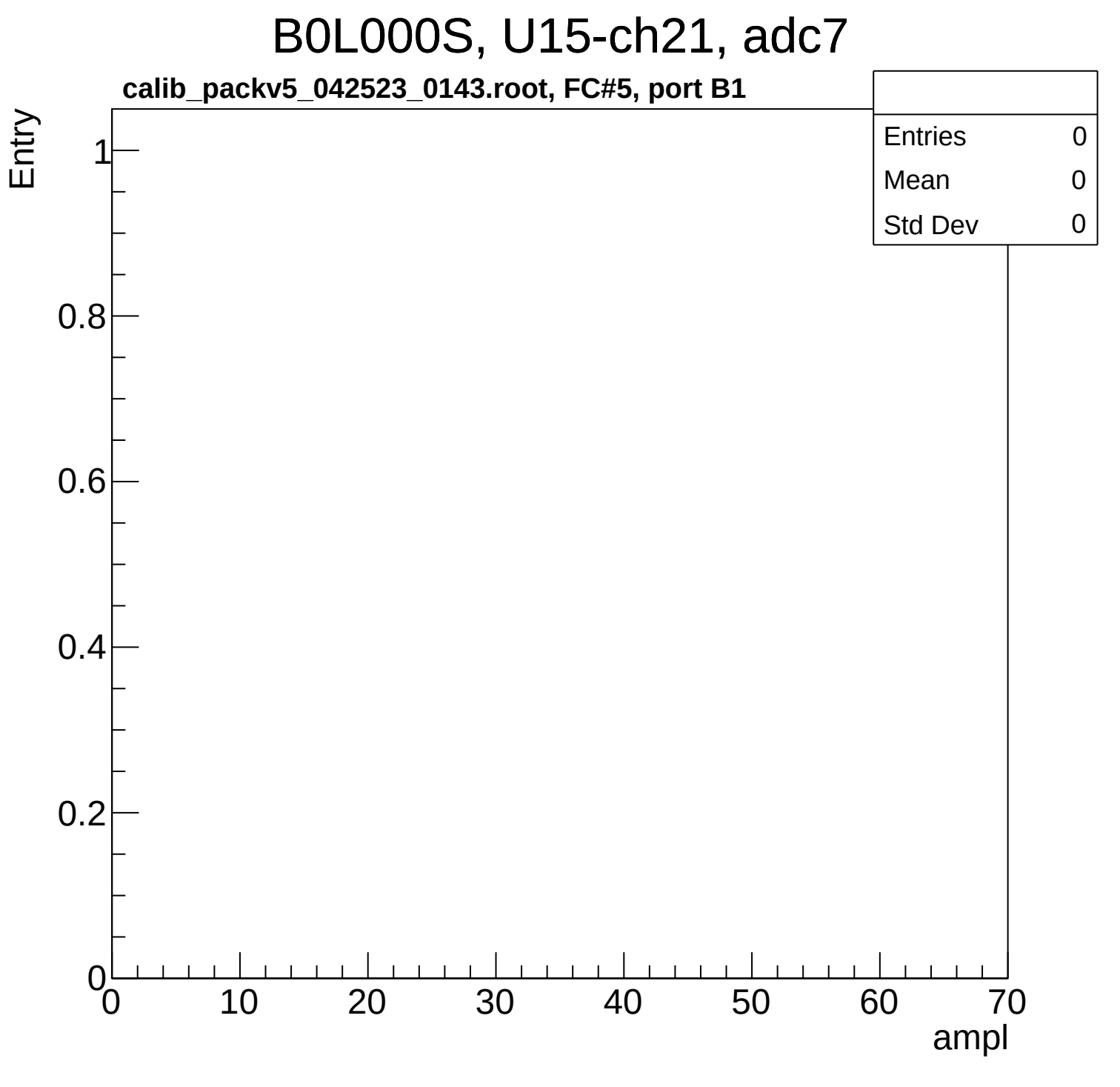

B0L000S, U15-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 41.77
Std Dev 3.626
Gaus mean : 42.4133
Gaus Width: 3.5415
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.55
Std Dev 3.15
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.52
Std Dev 3.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.78
Std Dev 2.458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.32
Std Dev 13.87
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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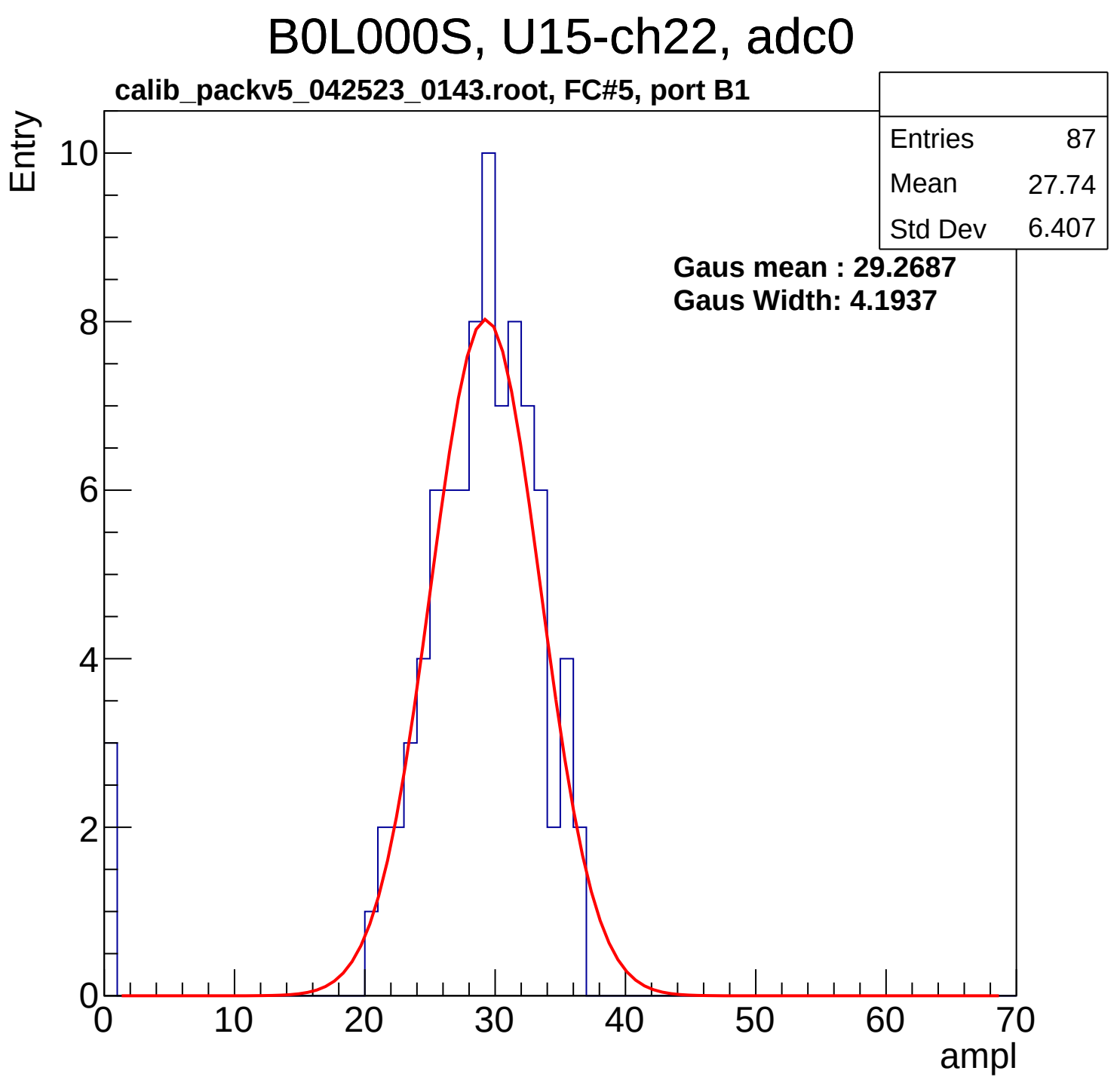
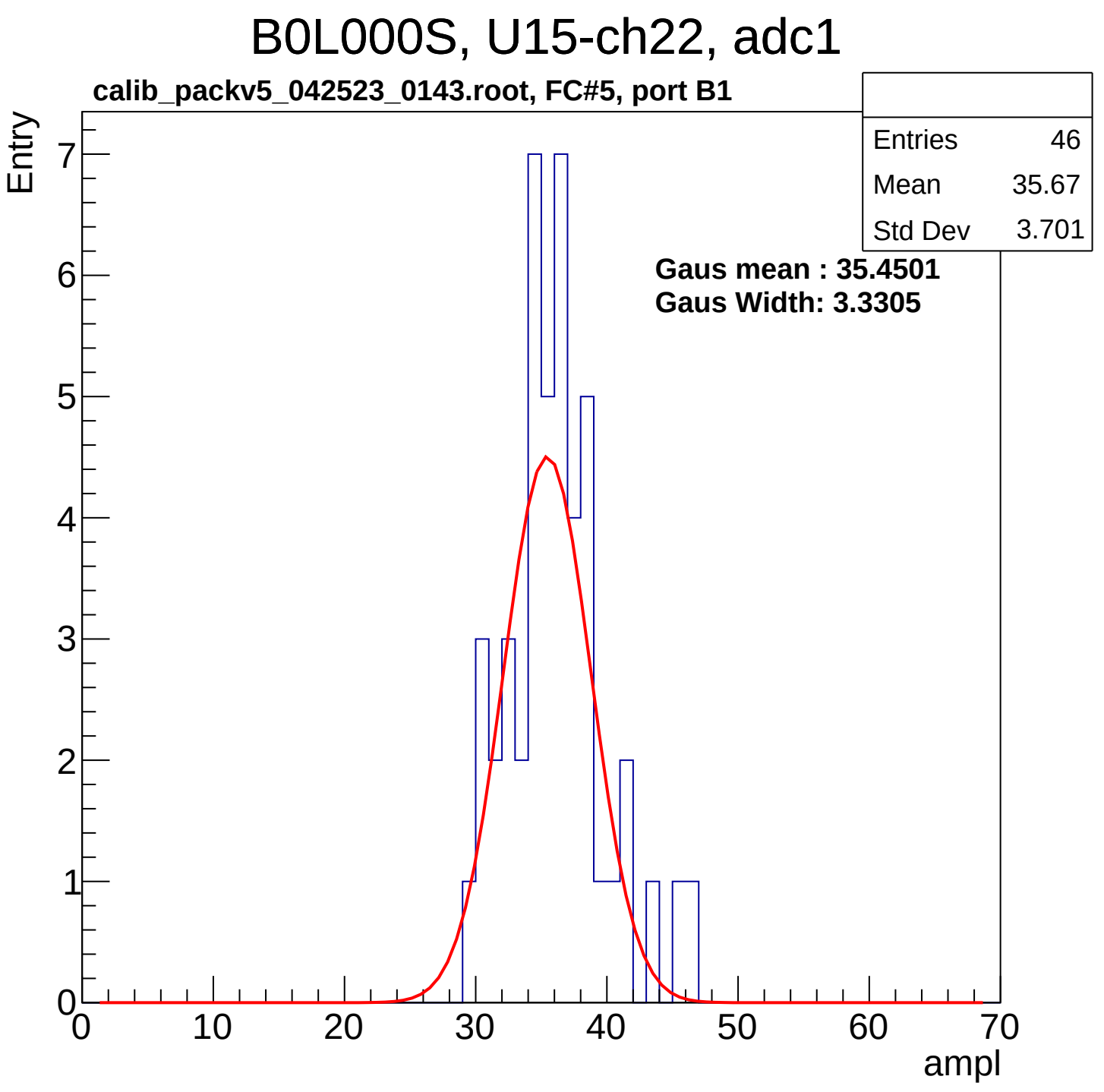
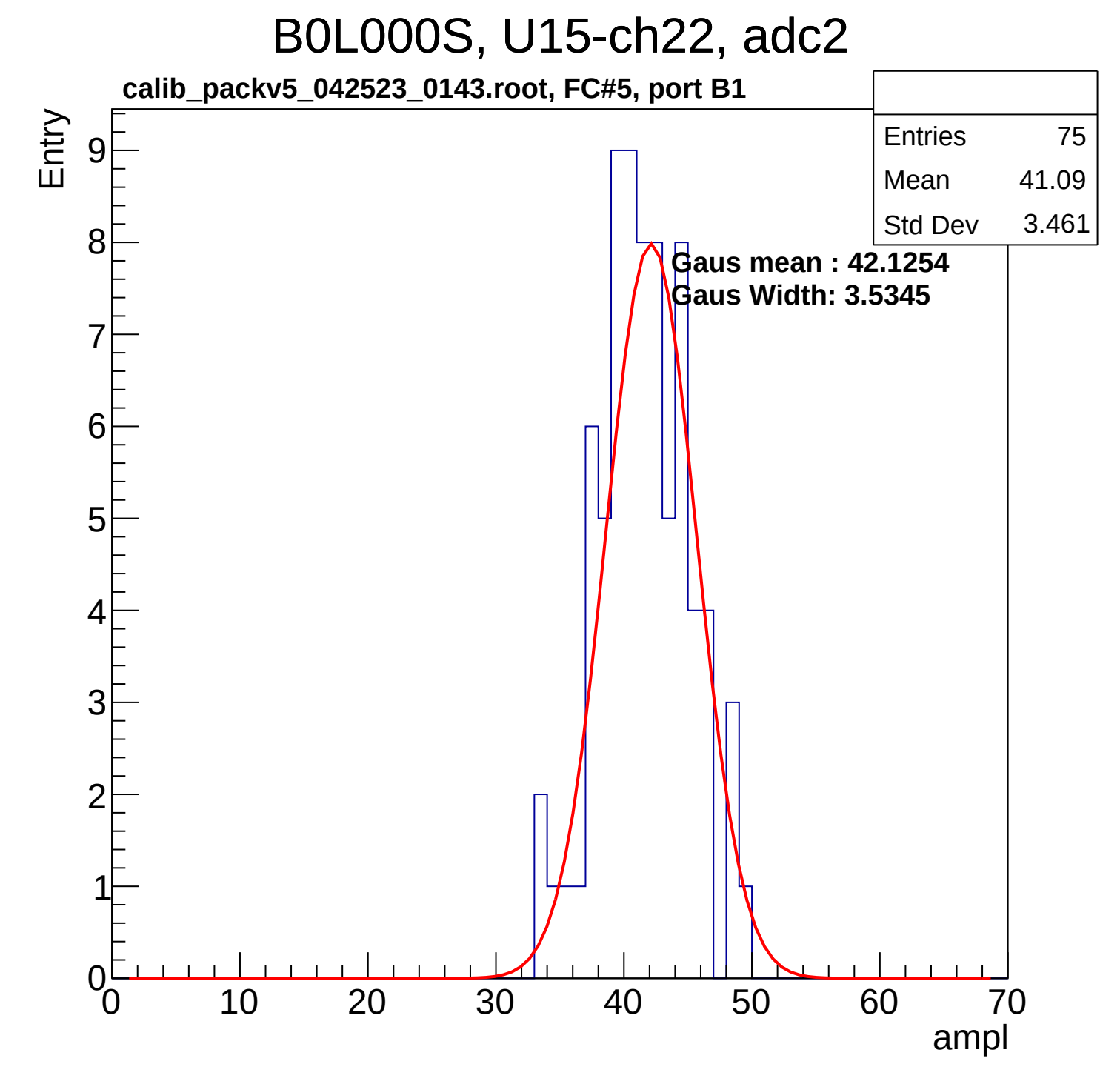
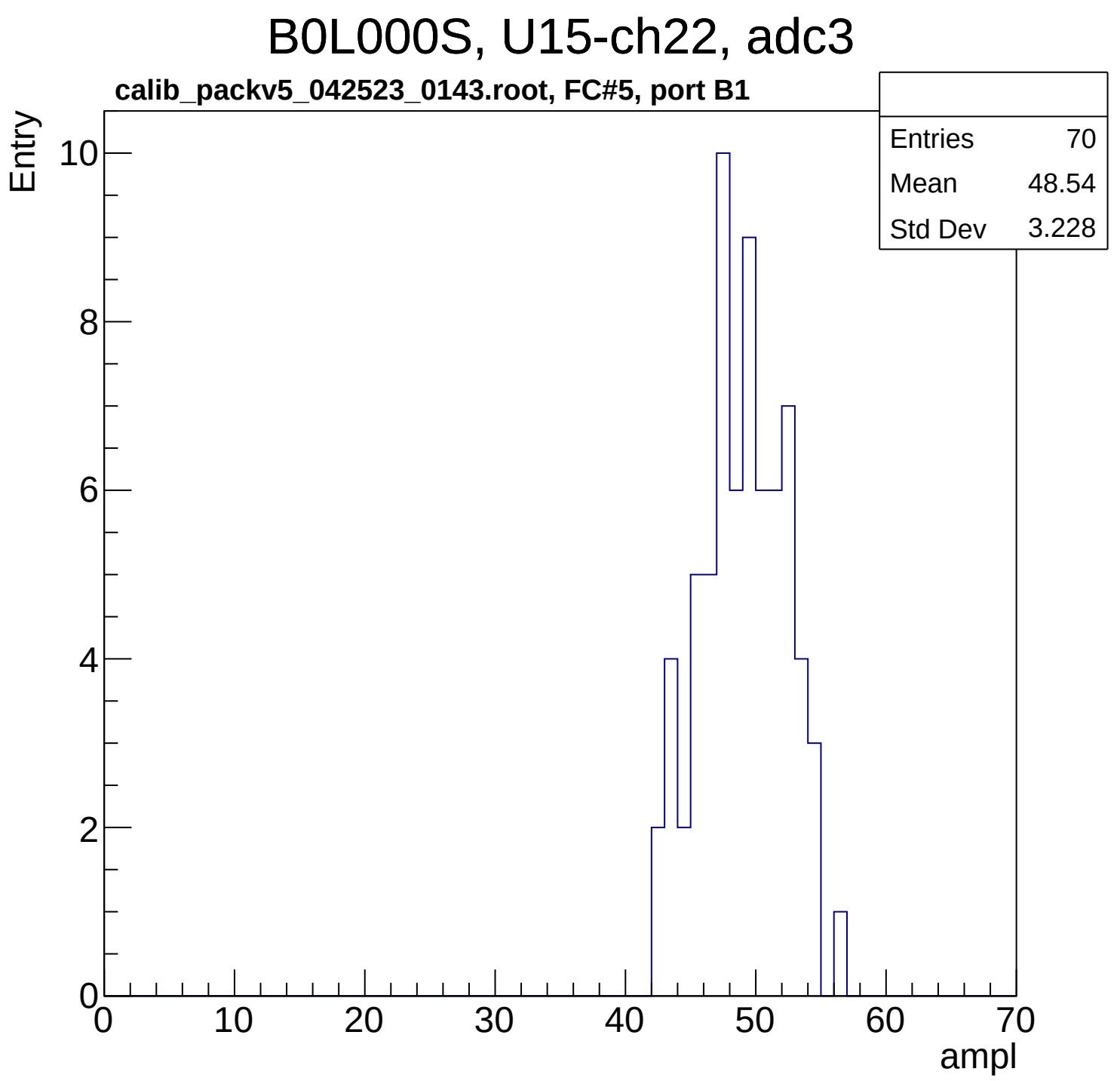
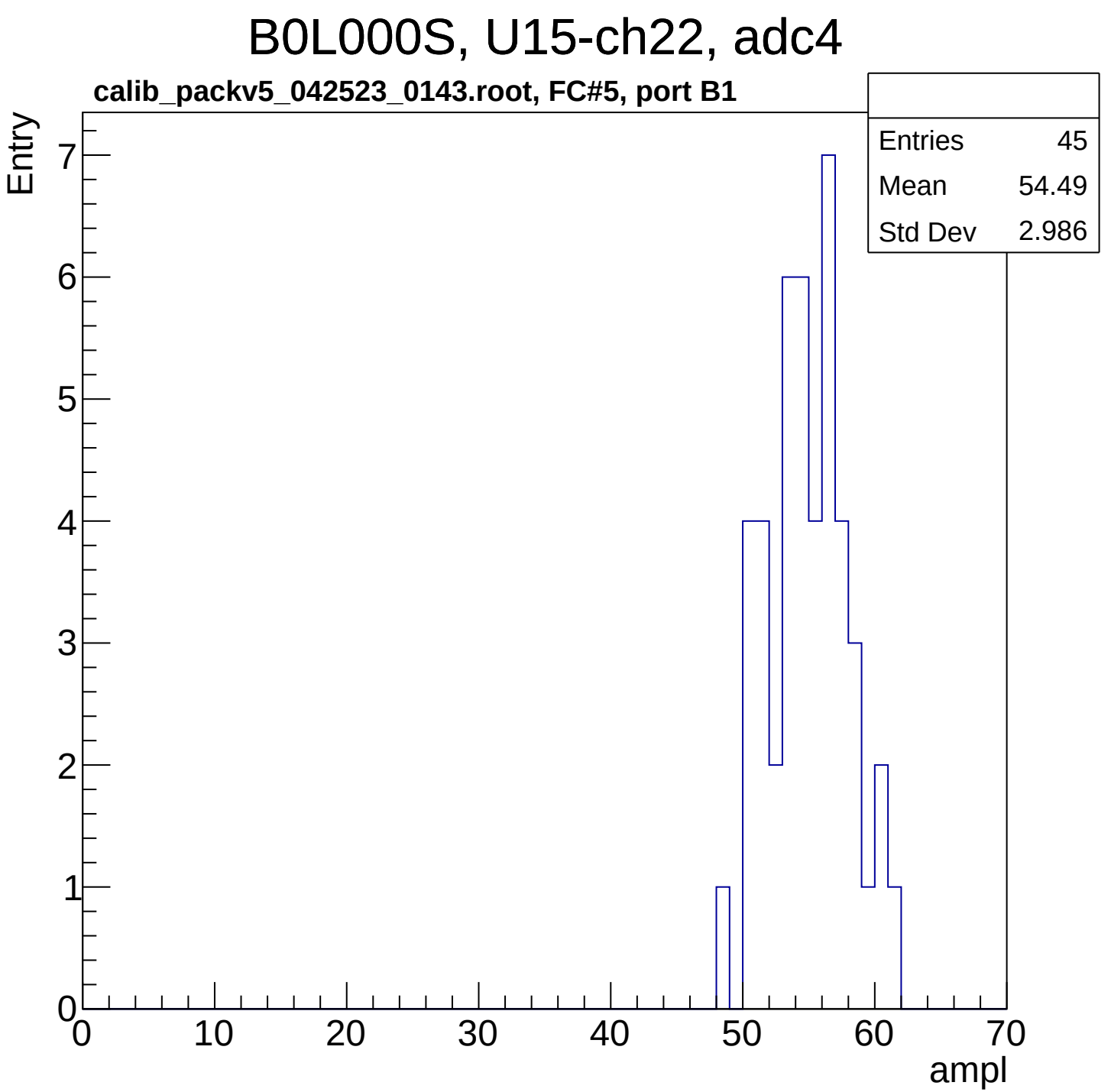
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

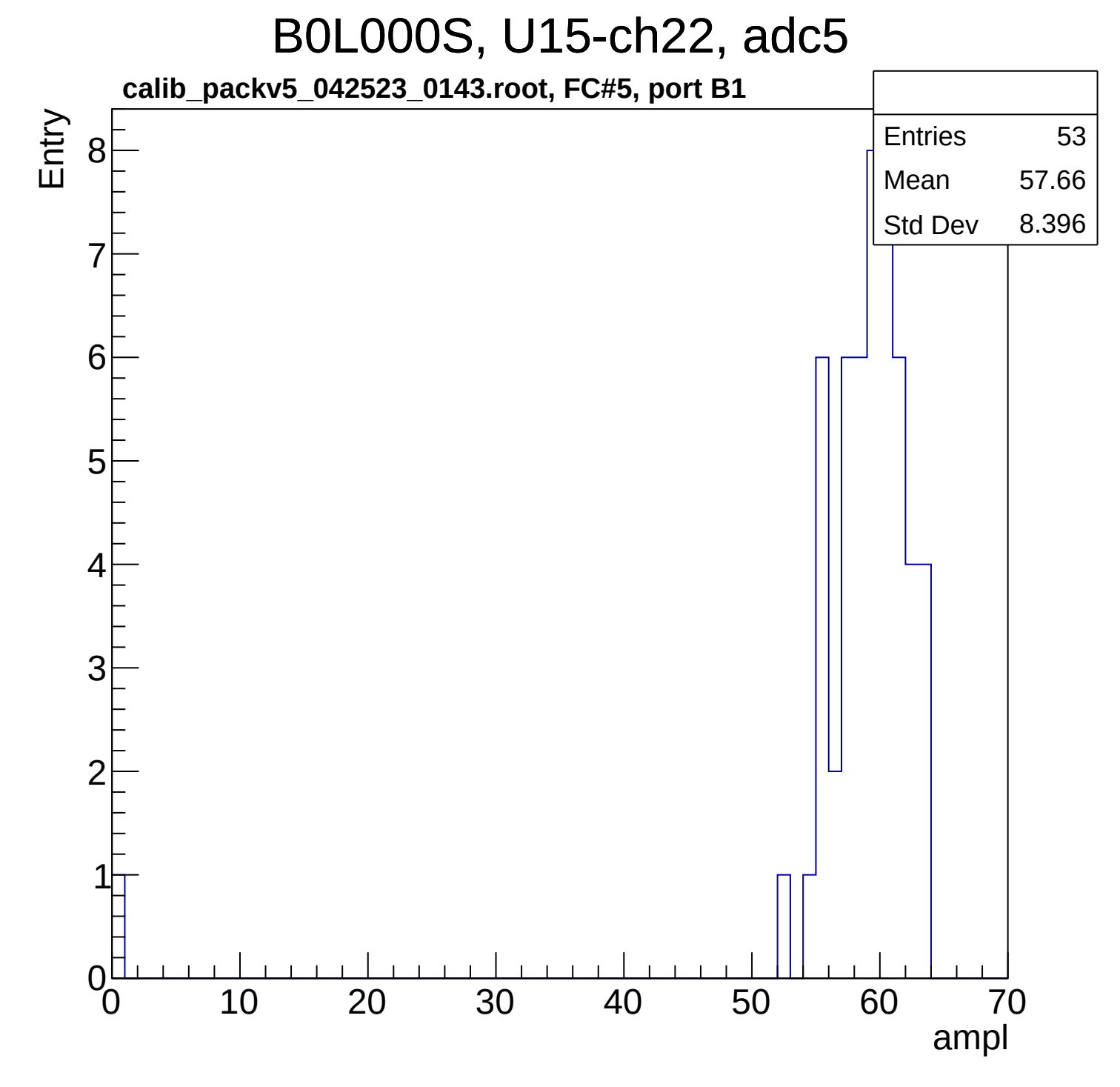

B0L000S, U15-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 27.74
Std Dev 6.407
Gaus mean : 29.2687
Gaus Width: 4.1937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

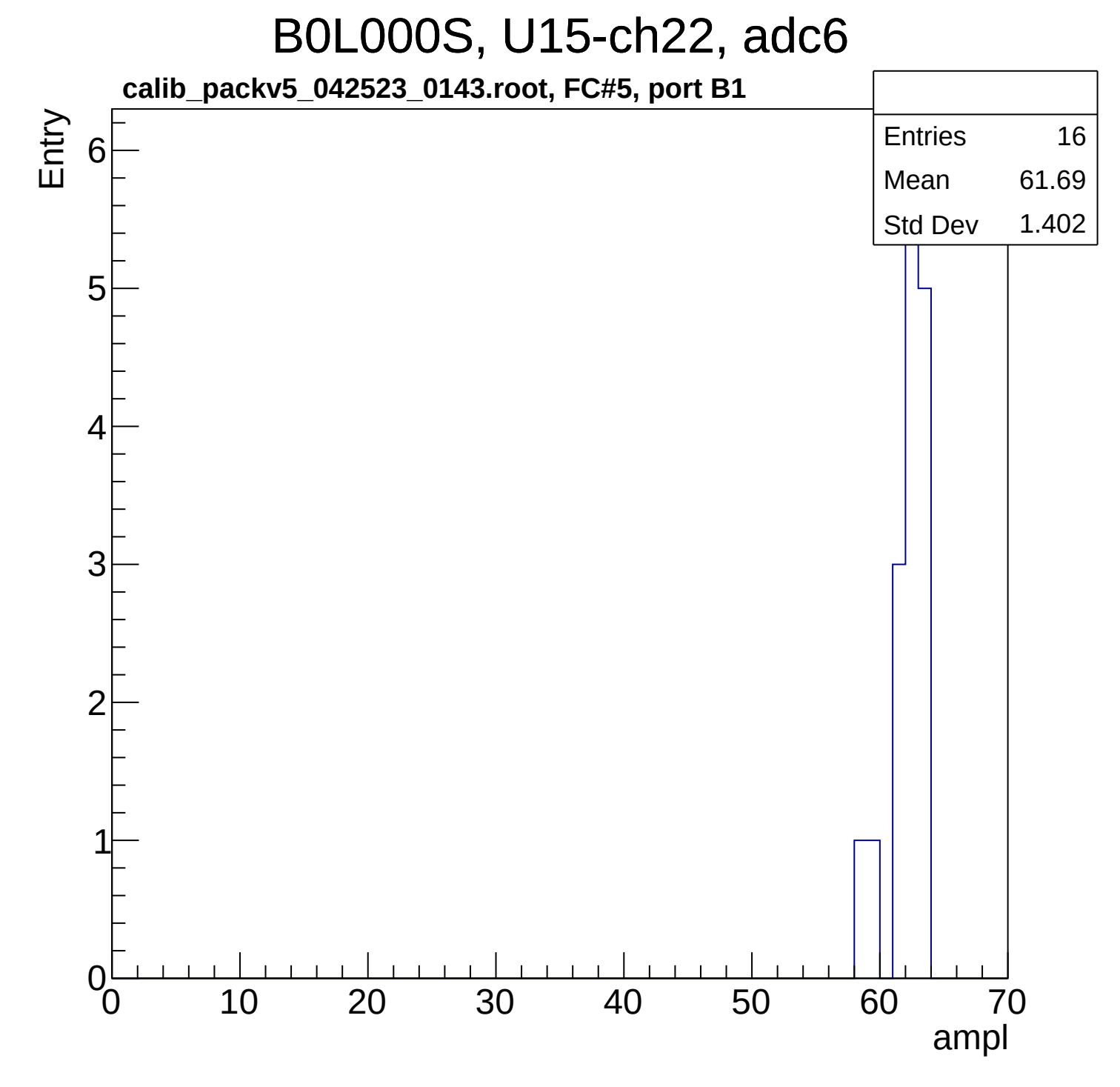

B0L000S, U15-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 35.67
Std Dev 3.701
Gaus mean : 35.4501
Gaus Width: 3.3305
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 41.09
Std Dev 3.461
Gaus mean : 42.1254
Gaus Width: 3.5345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.54
Std Dev 3.228
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 54.49
Std Dev 2.986
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.66
Std Dev 8.396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.69
Std Dev 1.402
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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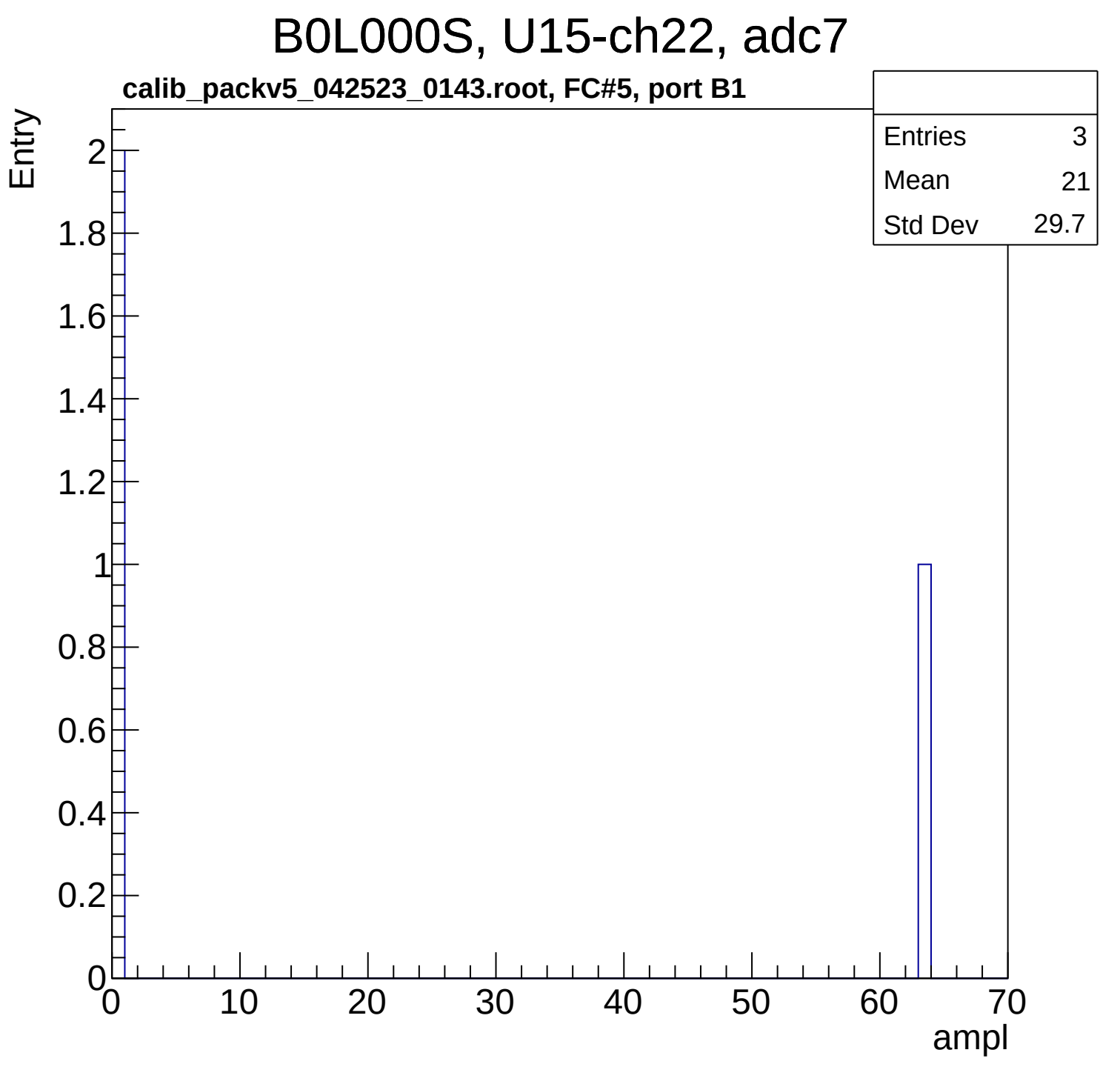
B0L000S, U15-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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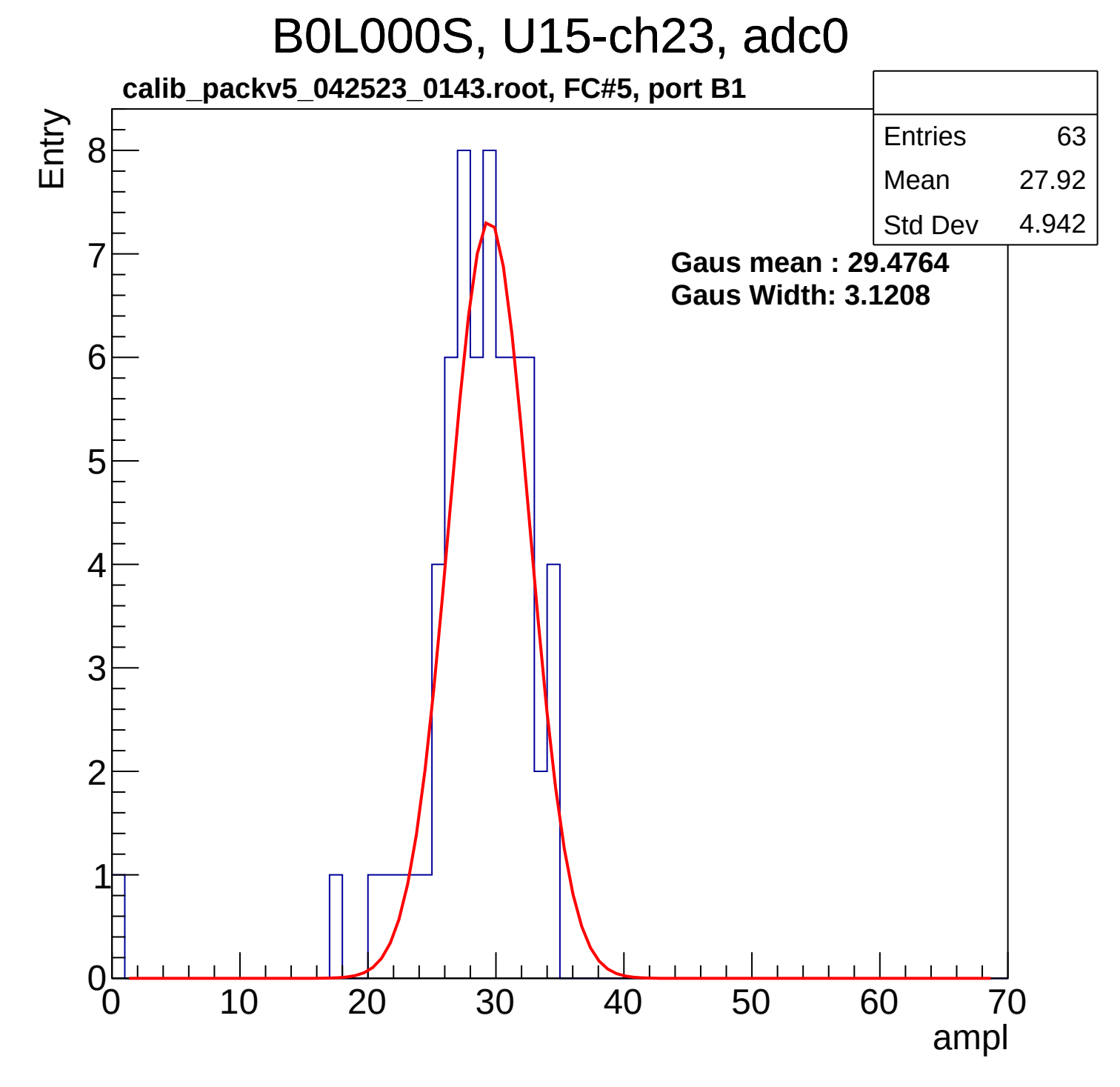
40
50
60
70


B0L000S, U15-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 27.92
Std Dev 4.942
Gaus mean : 29.4764
Gaus Width: 3.1208
Entry
ampl
0
1
2
3
4
5
6
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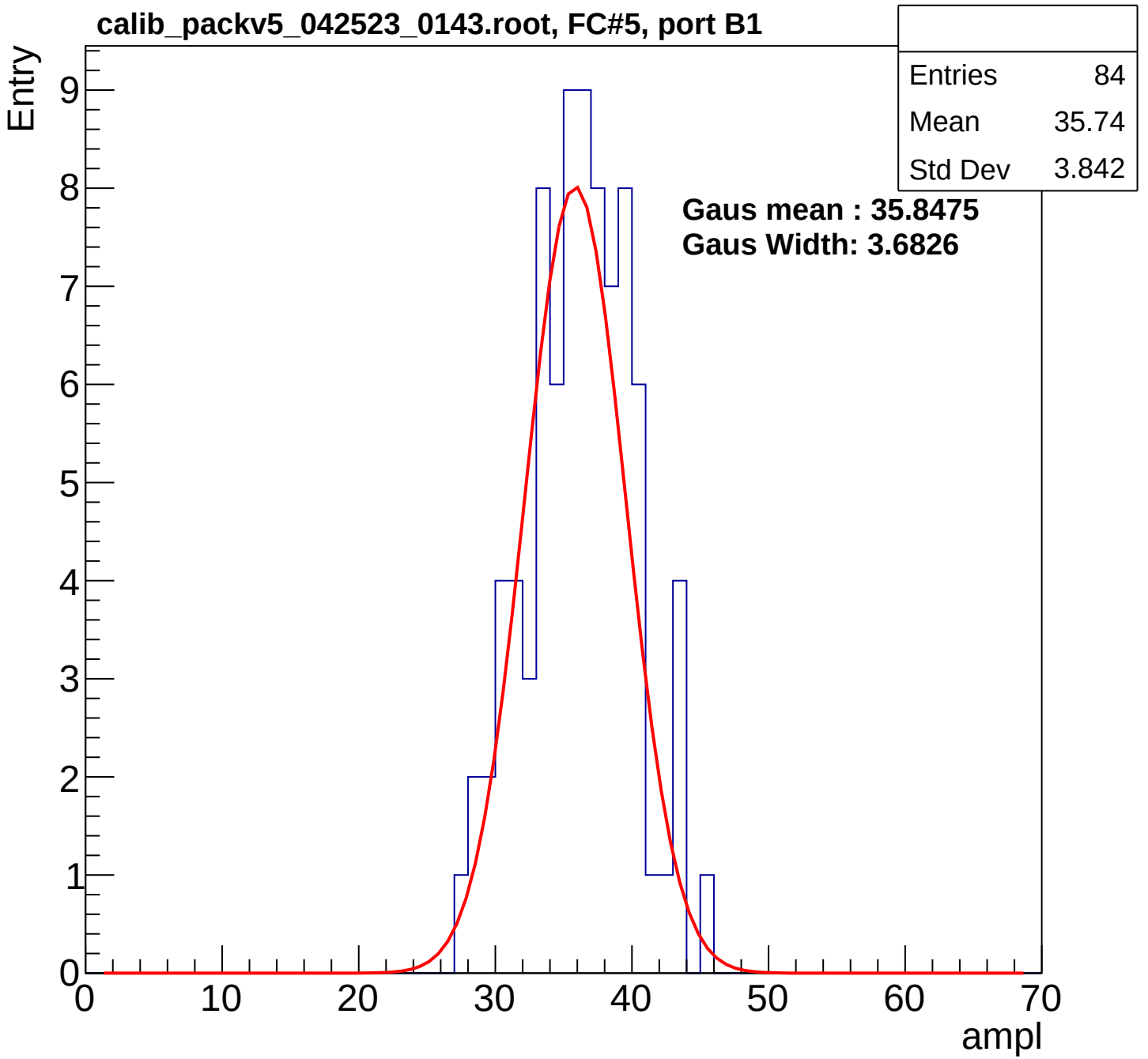
B0L000S, U15-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 35.74
Std Dev 3.842
Gaus mean : 35.8475
Gaus Width: 3.6826
Entry
ampl
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10
20
30
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2
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6
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8
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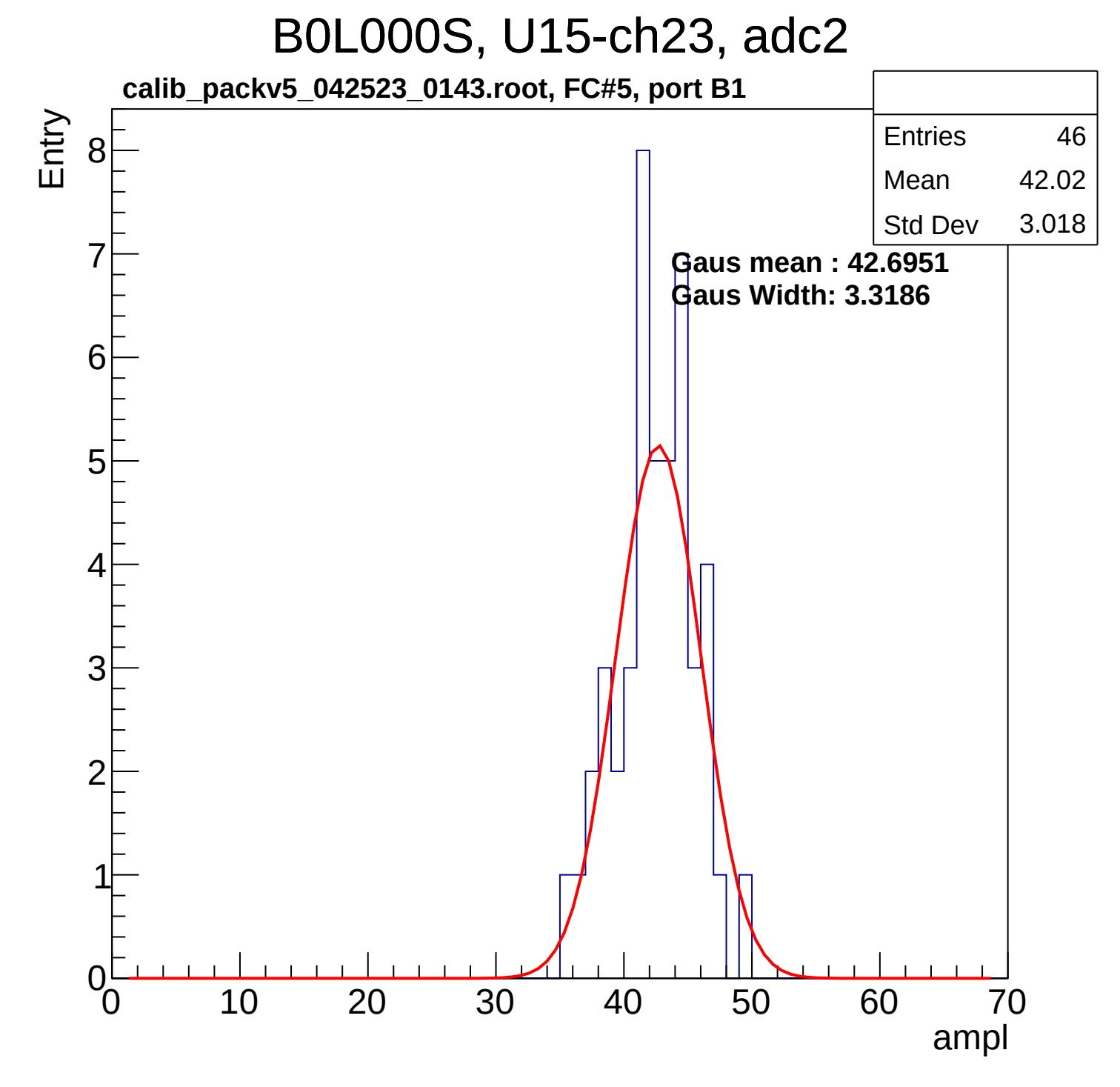
B0L000S, U15-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 42.02
Std Dev 3.018
Gaus mean : 42.6951
Gaus Width: 3.3186
Entry
ampl
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1
2
3
4
5
6
7
8
0
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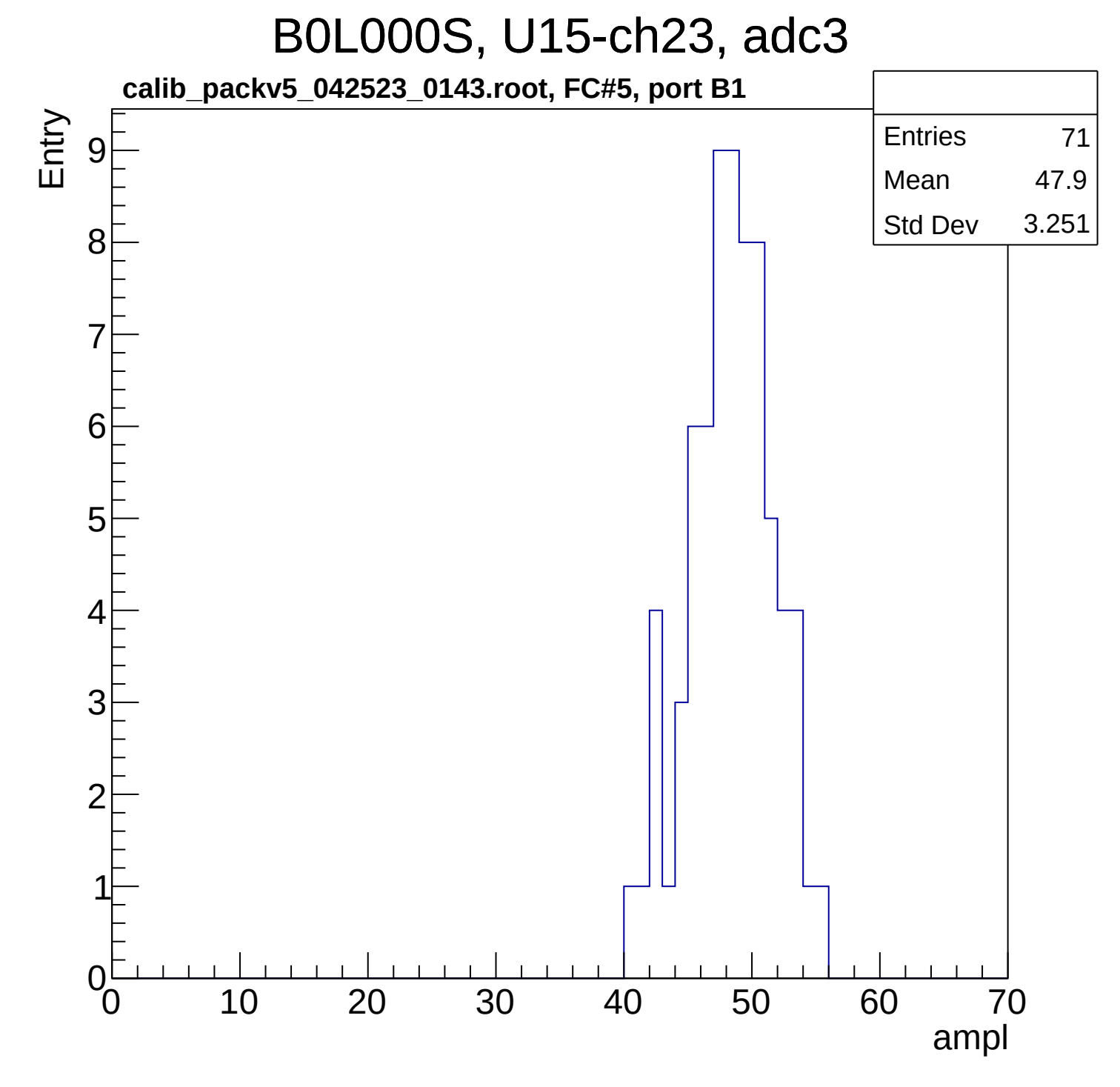
B0L000S, U15-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 47.9
Std Dev 3.251
Entry
ampl
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3
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6
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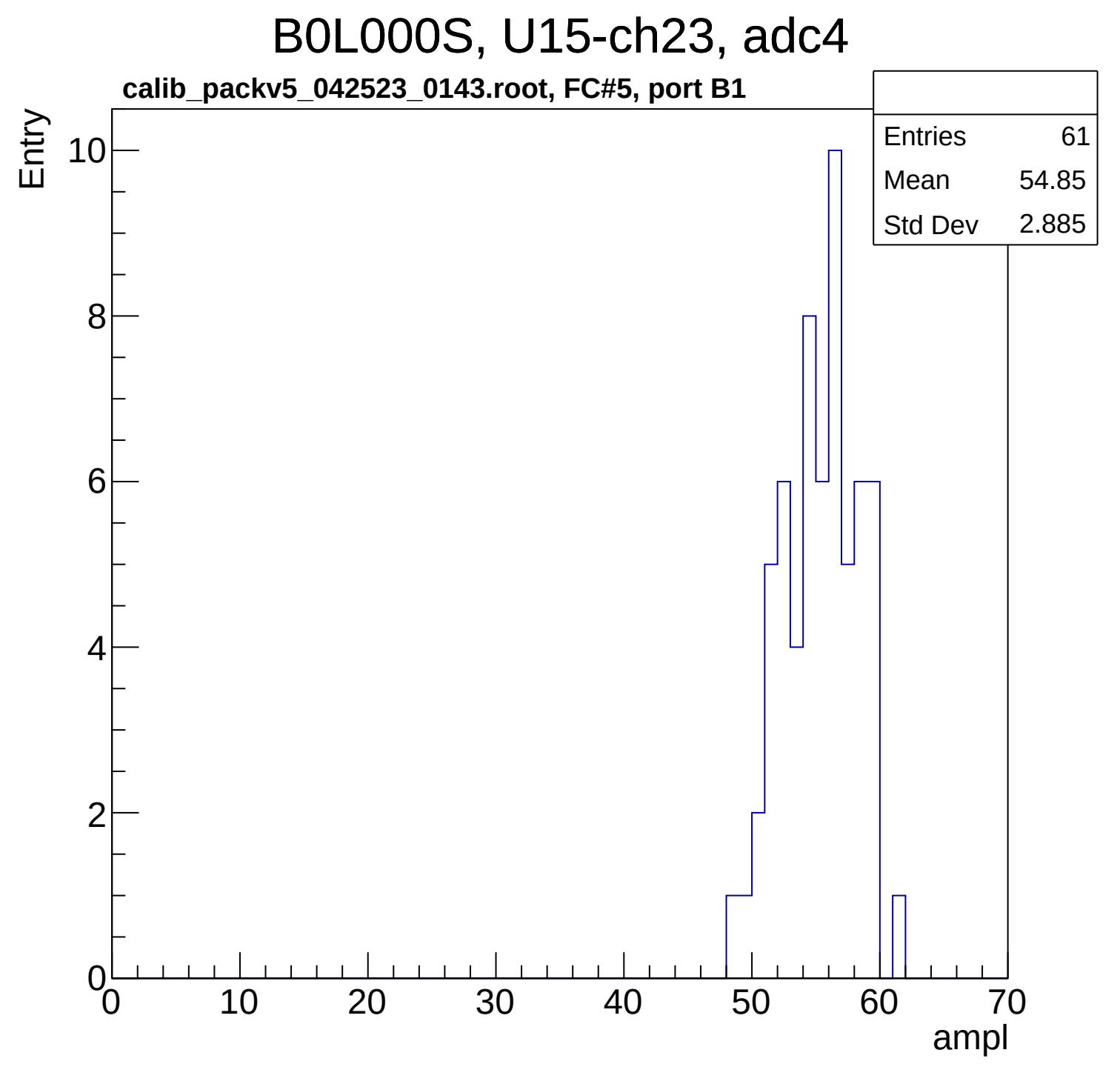
B0L000S, U15-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.85
Std Dev 2.885
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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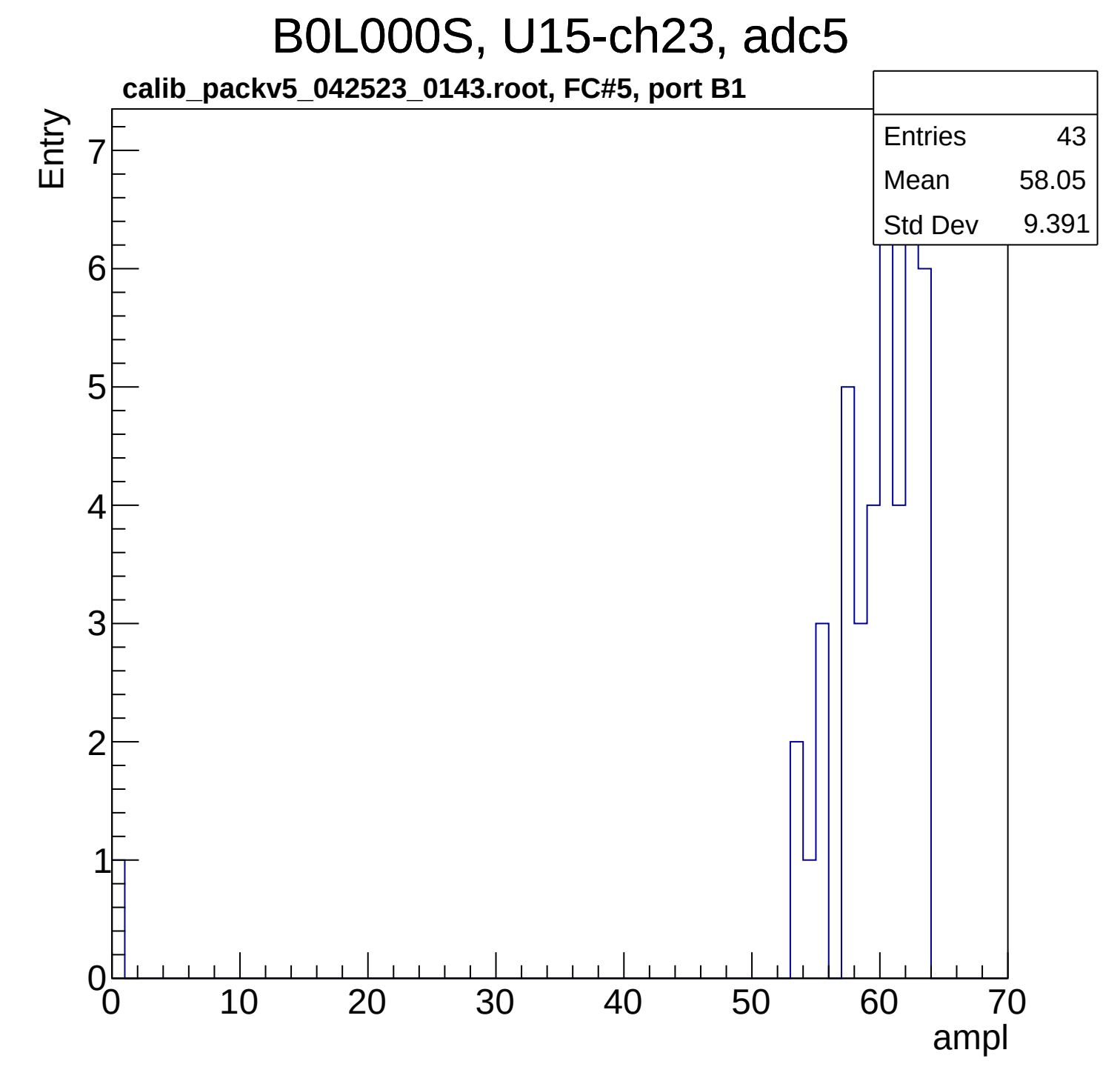
B0L000S, U15-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.05
Std Dev 9.391
Entry
ampl
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2
3
4
5
6
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0
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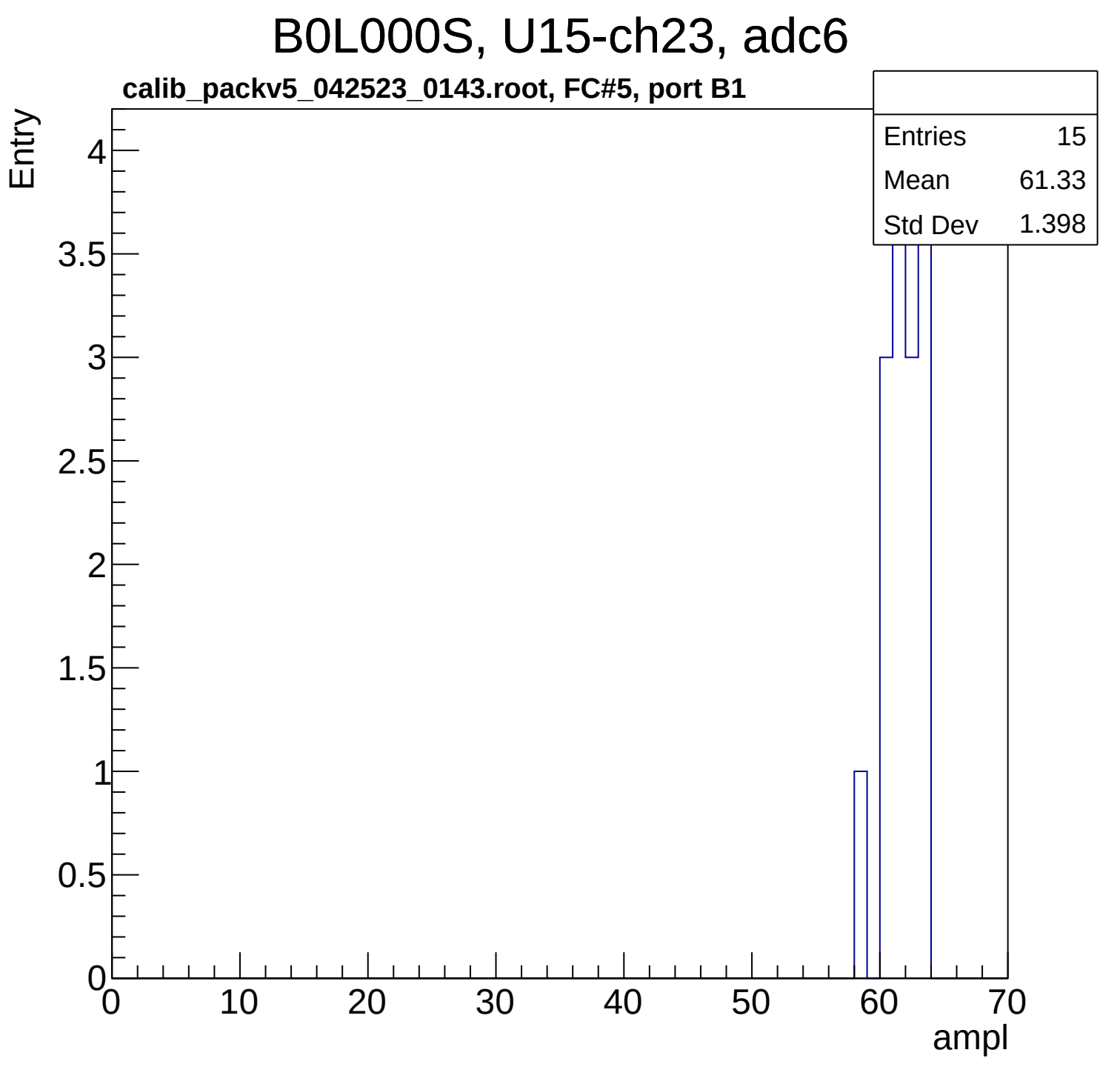
B0L000S, U15-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.33
Std Dev 1.398
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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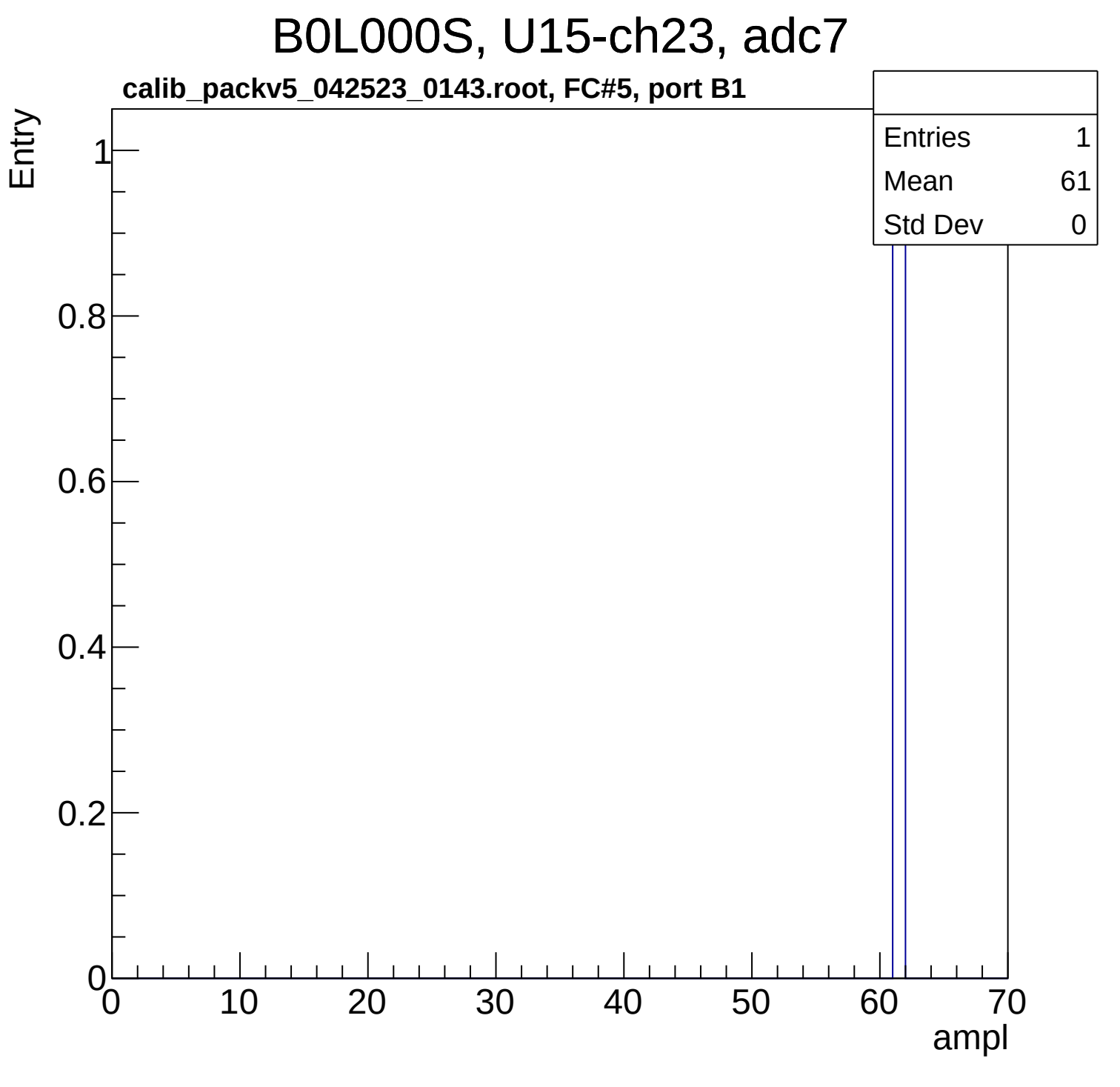
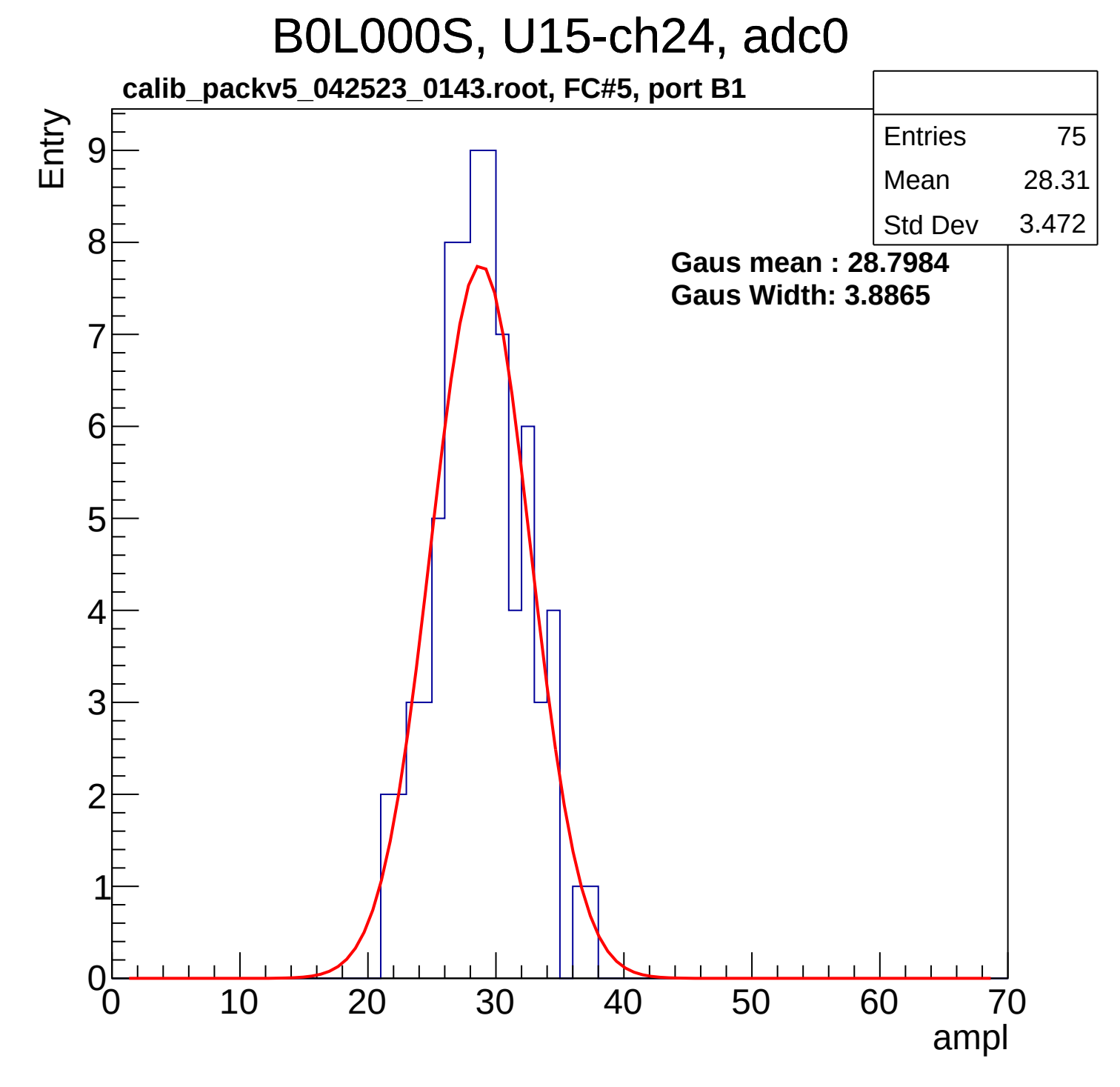

B0L000S, U15-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.31
Std Dev 3.472
Gaus mean : 28.7984
Gaus Width: 3.8865
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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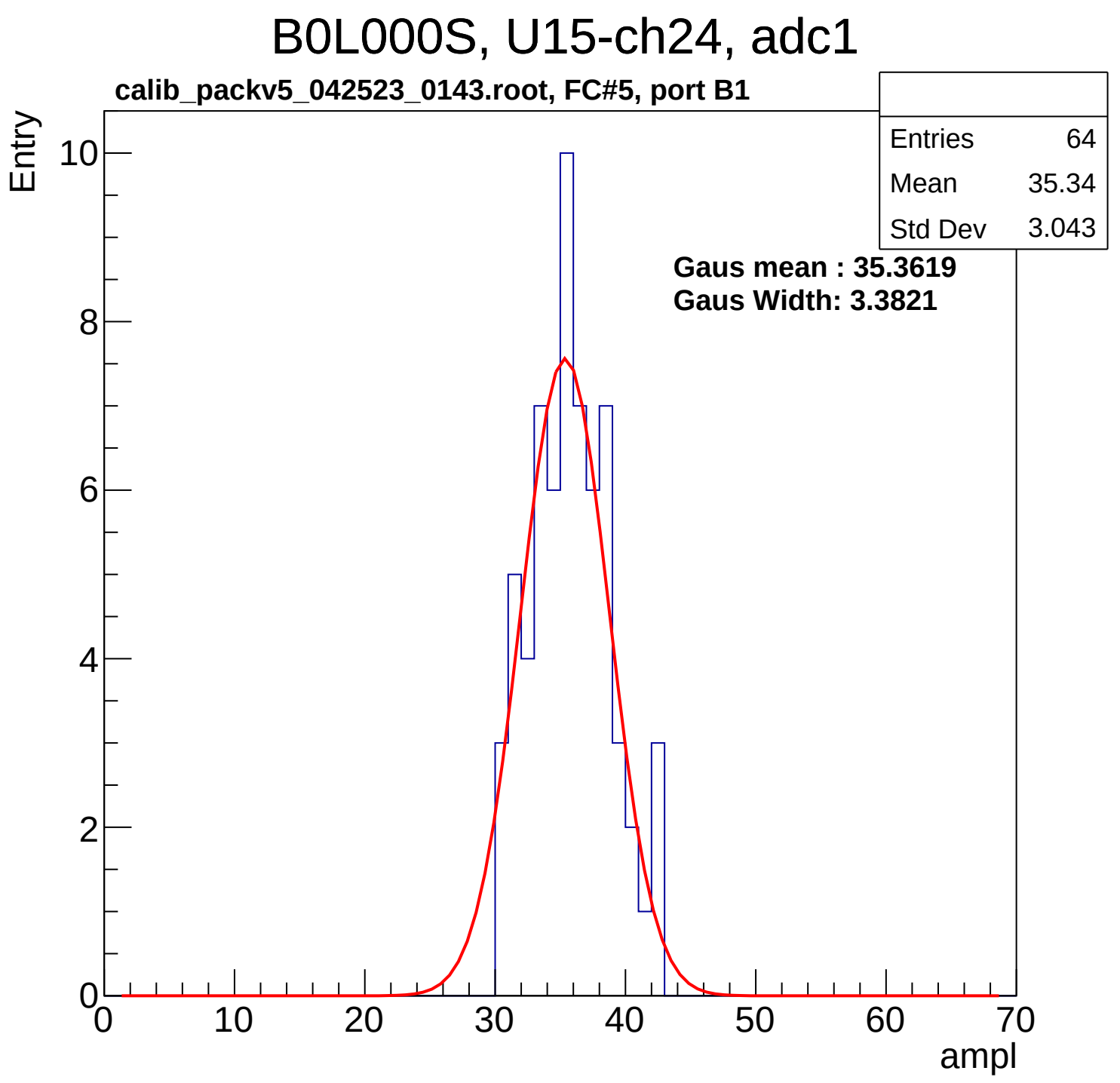
B0L000S, U15-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.34
Std Dev 3.043
Gaus mean : 35.3619
Gaus Width: 3.3821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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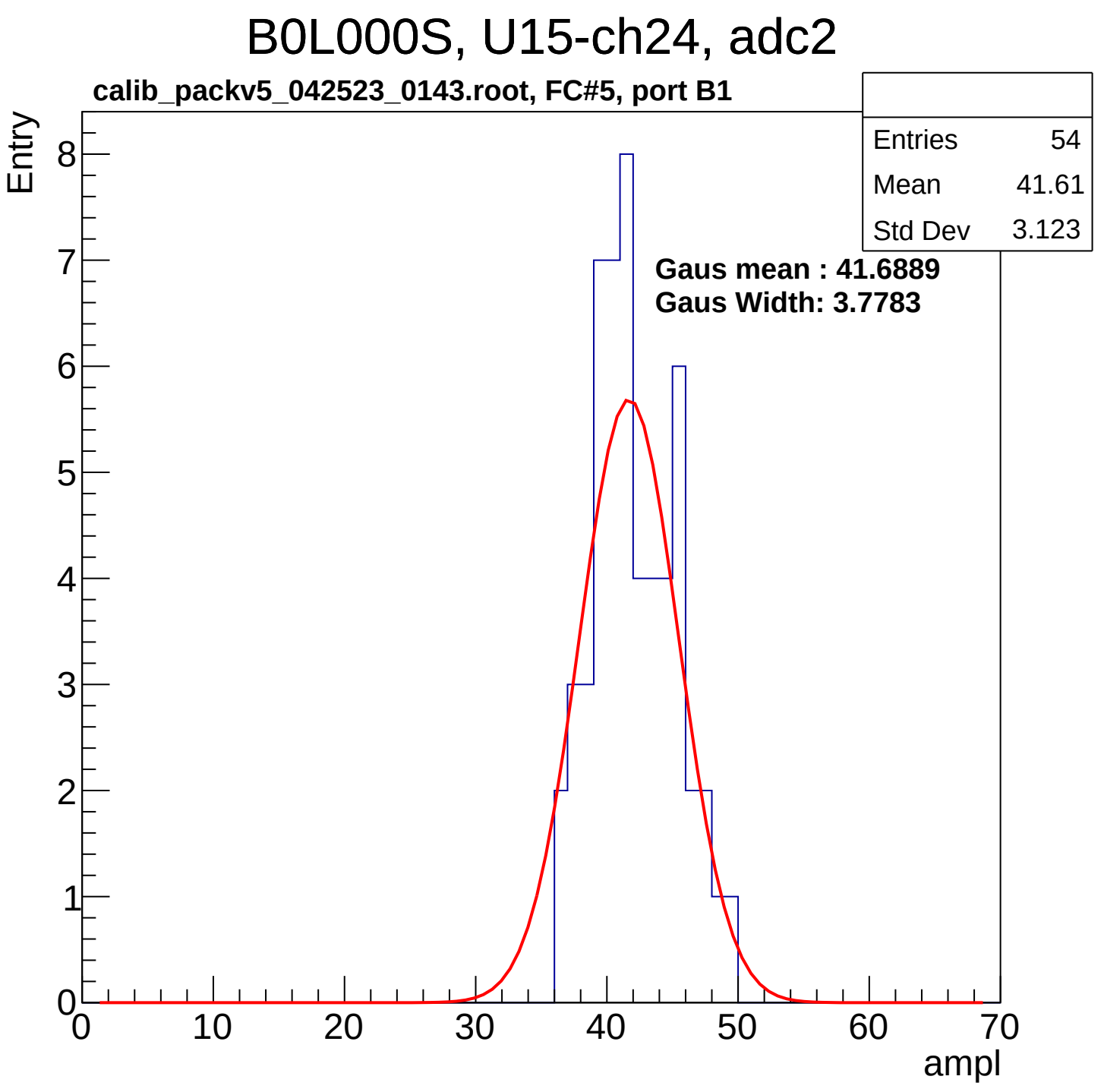
B0L000S, U15-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 41.61
Std Dev 3.123
Gaus mean : 41.6889
Gaus Width: 3.7783
Entry
ampl
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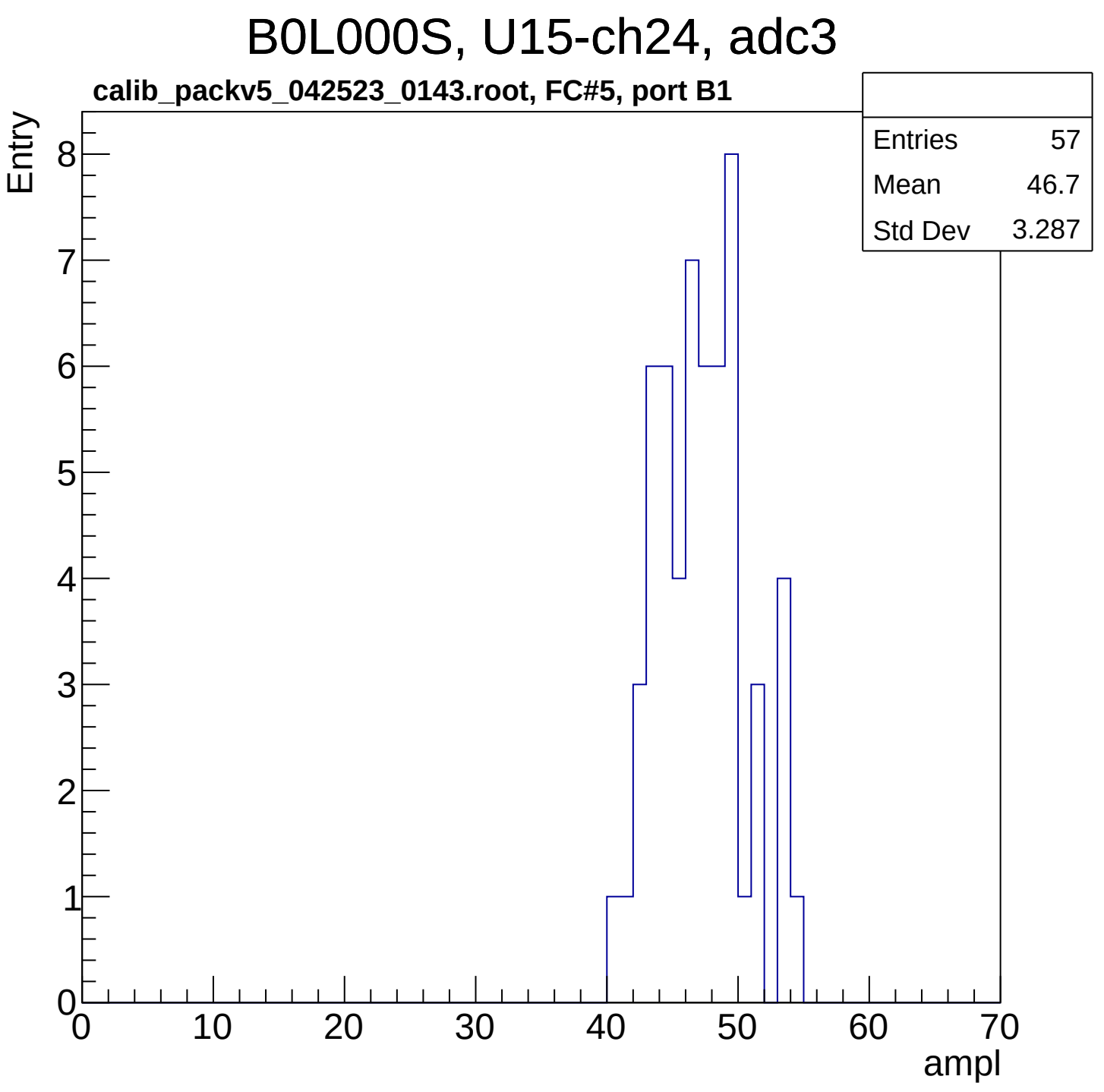
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 46.7
Std Dev 3.287
Entry
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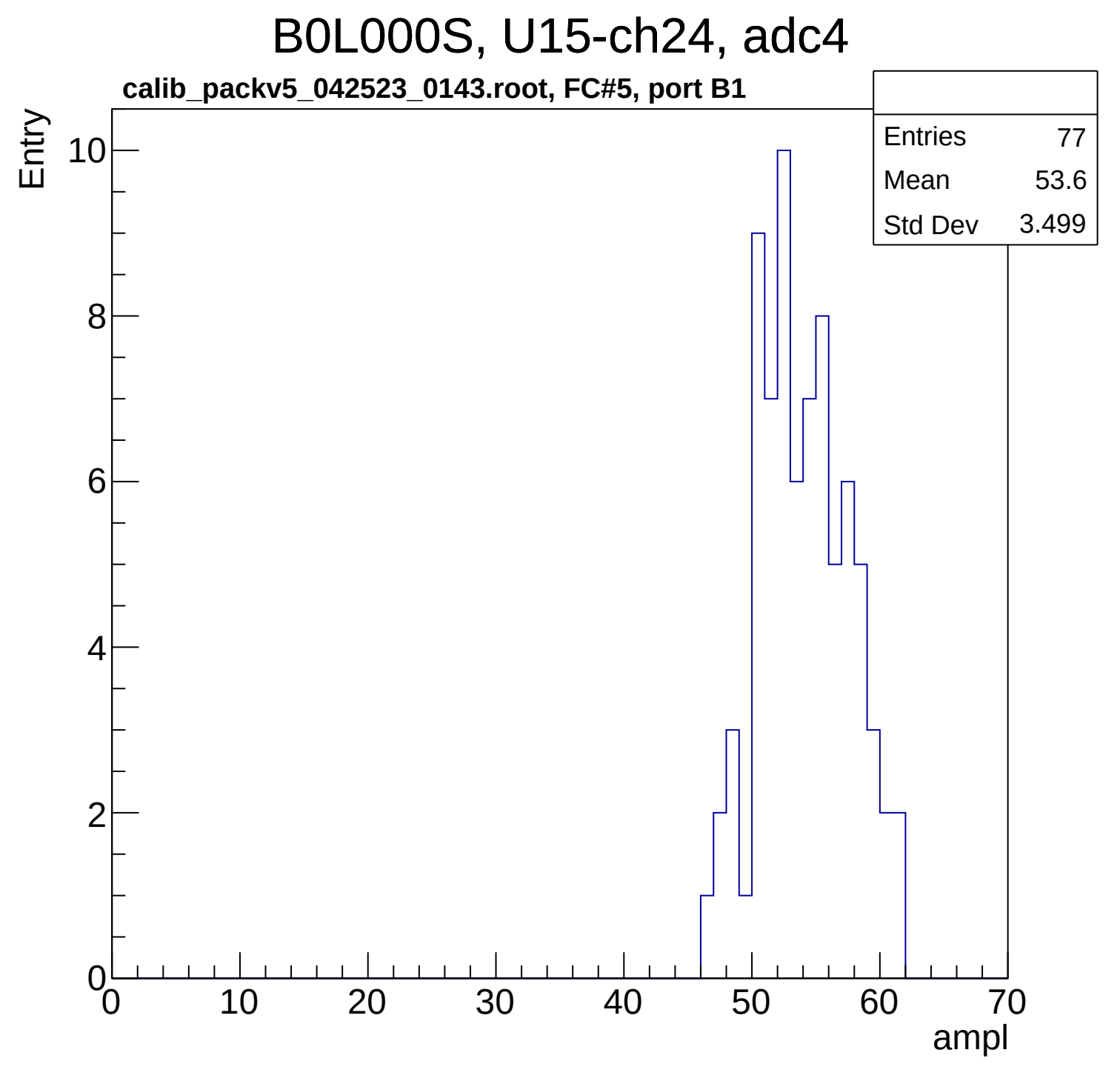
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 53.6
Std Dev 3.499
Entry
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2
4
6
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10
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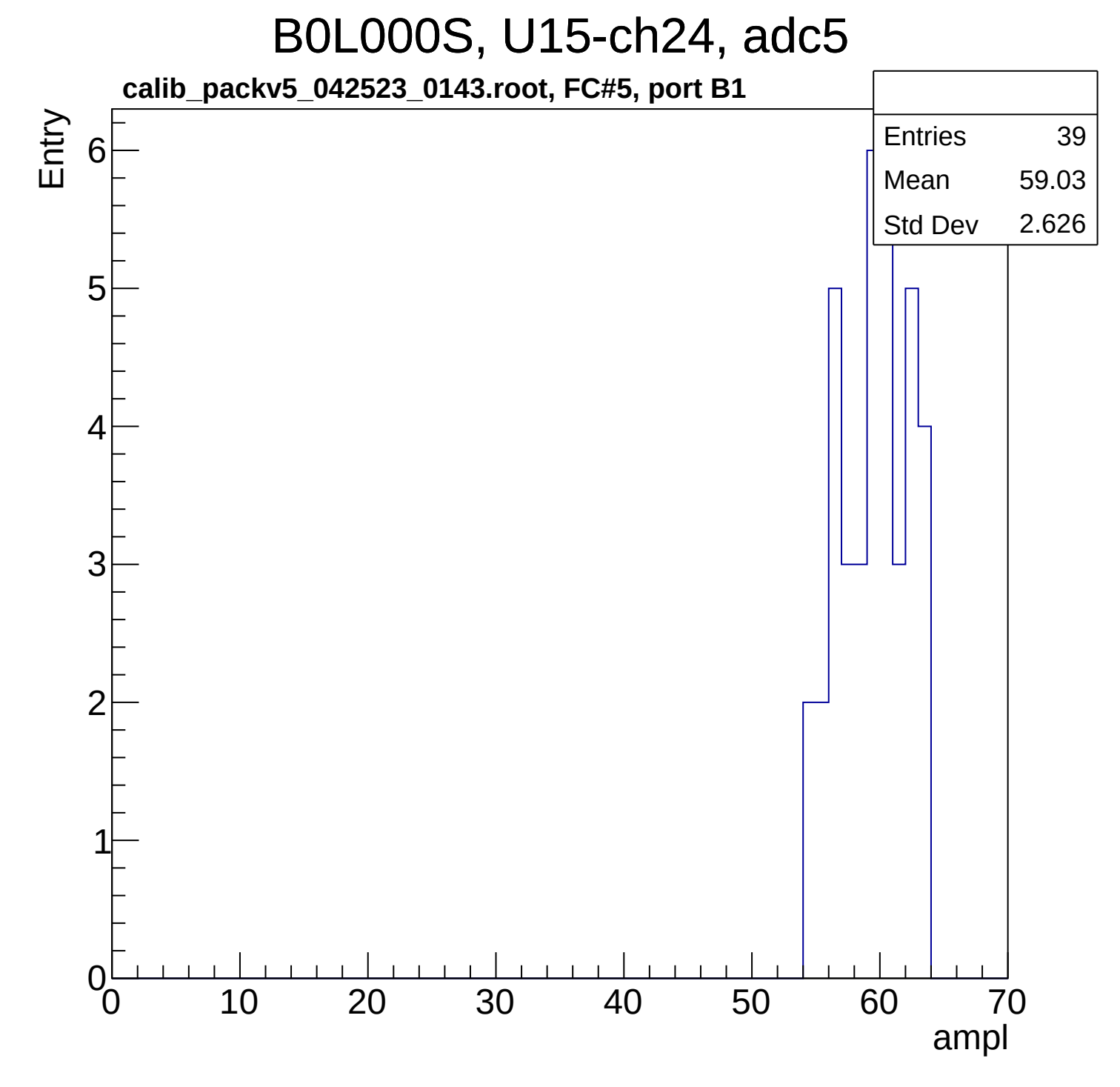
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59.03
Std Dev 2.626
Entry
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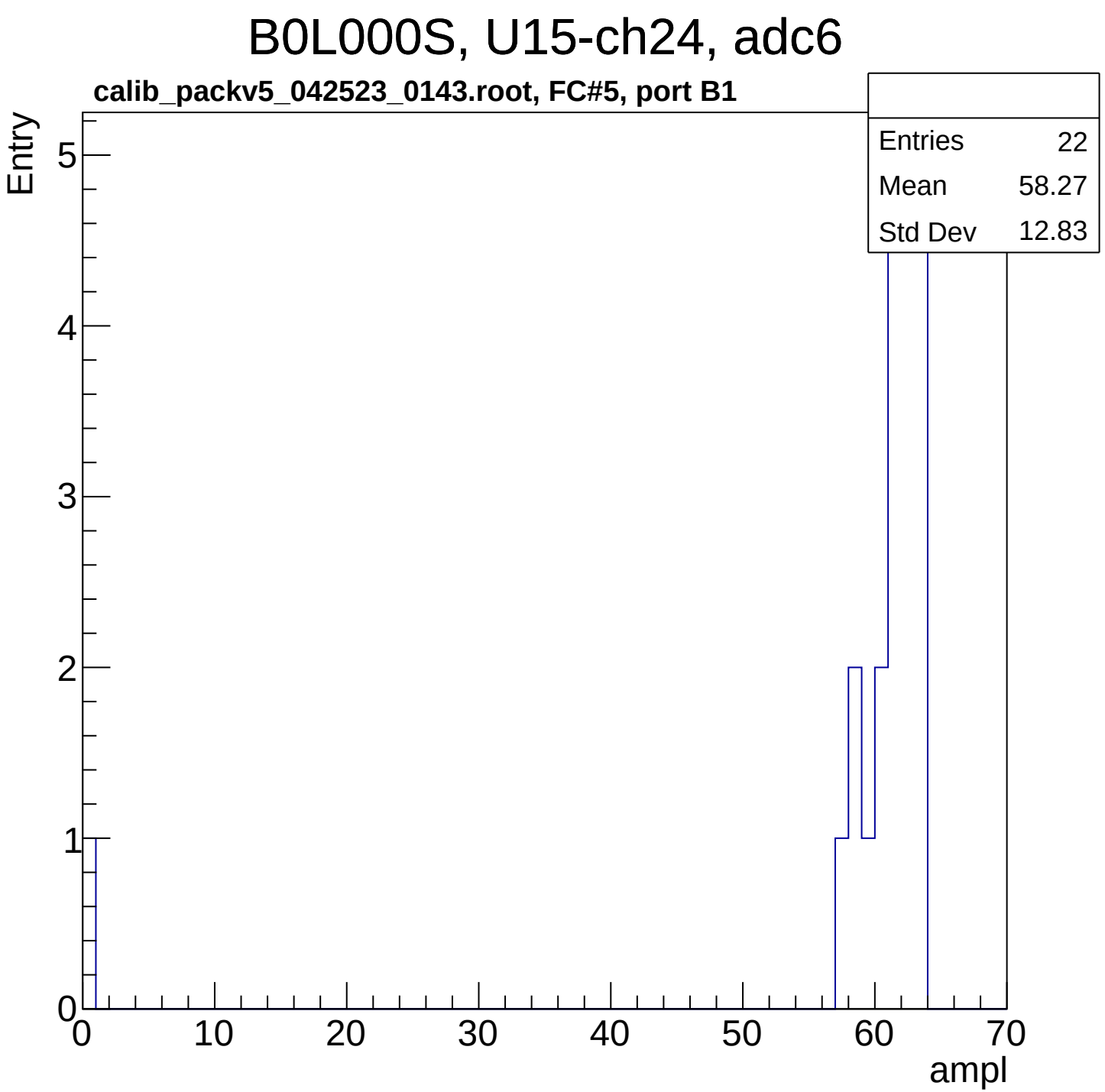
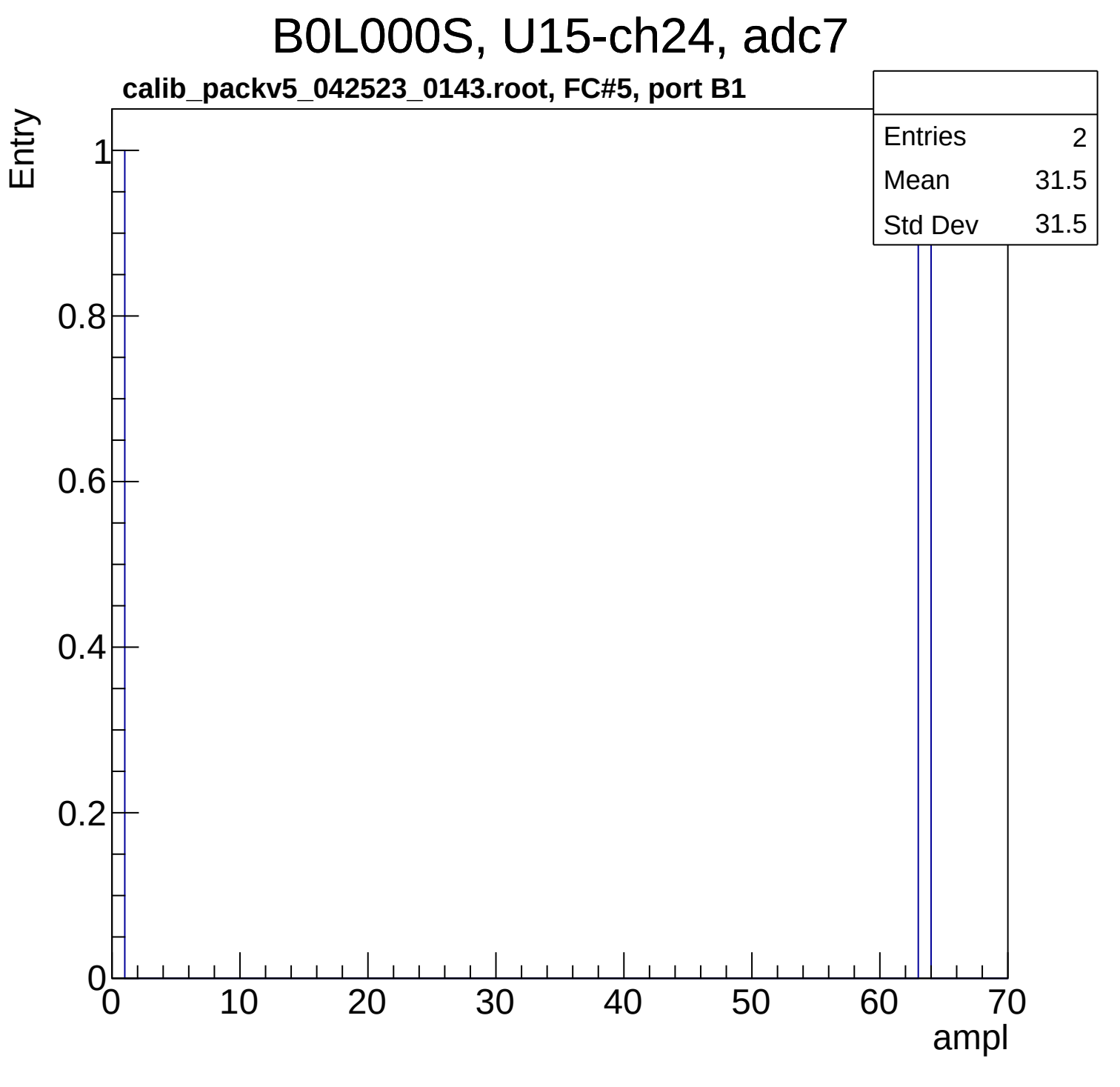
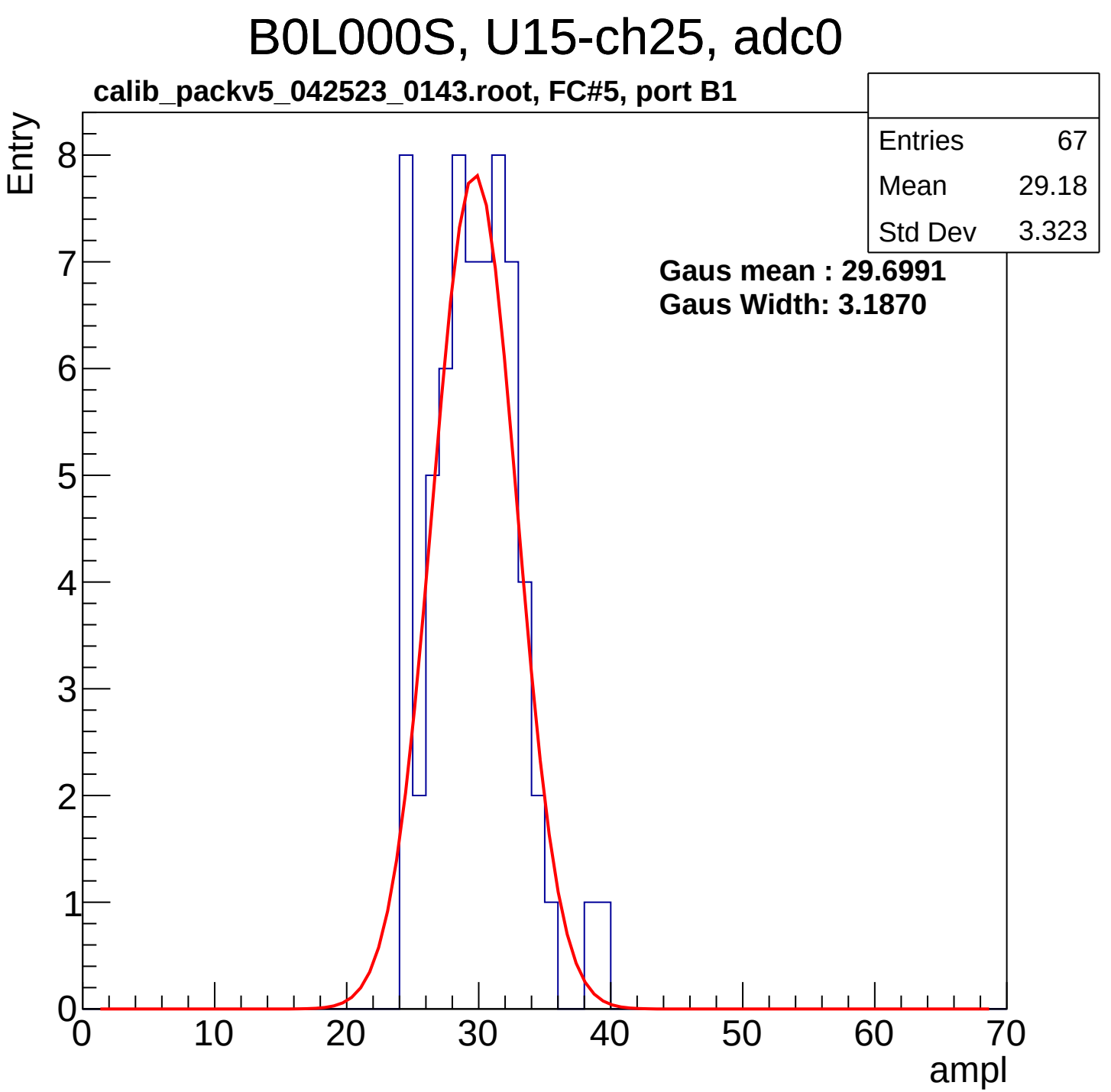
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.27
Std Dev 12.83
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.18
Std Dev 3.323
Gaus mean : 29.6991
Gaus Width: 3.1870
Entry
ampl
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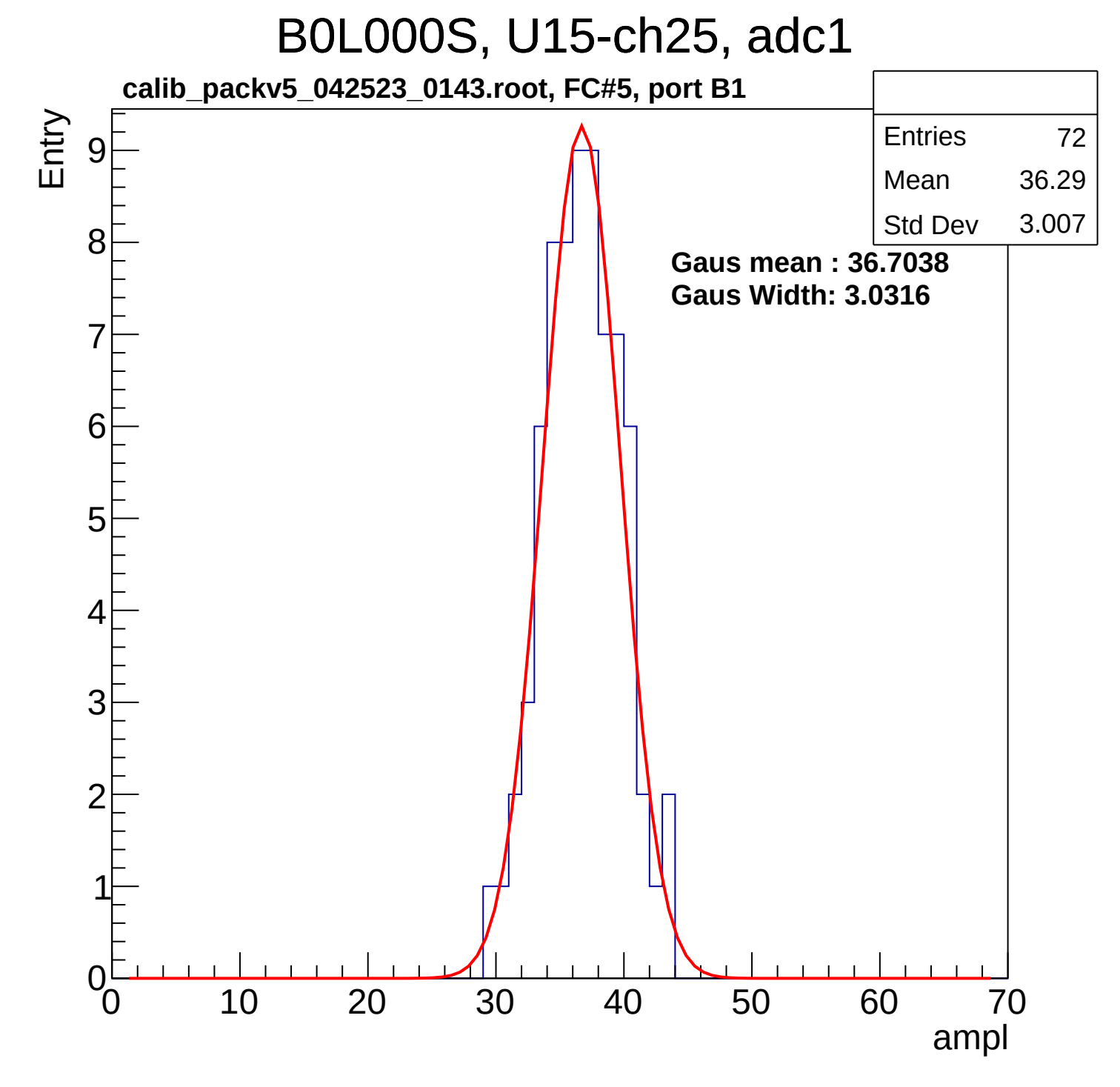
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.29
Std Dev 3.007
Gaus mean : 36.7038
Gaus Width: 3.0316
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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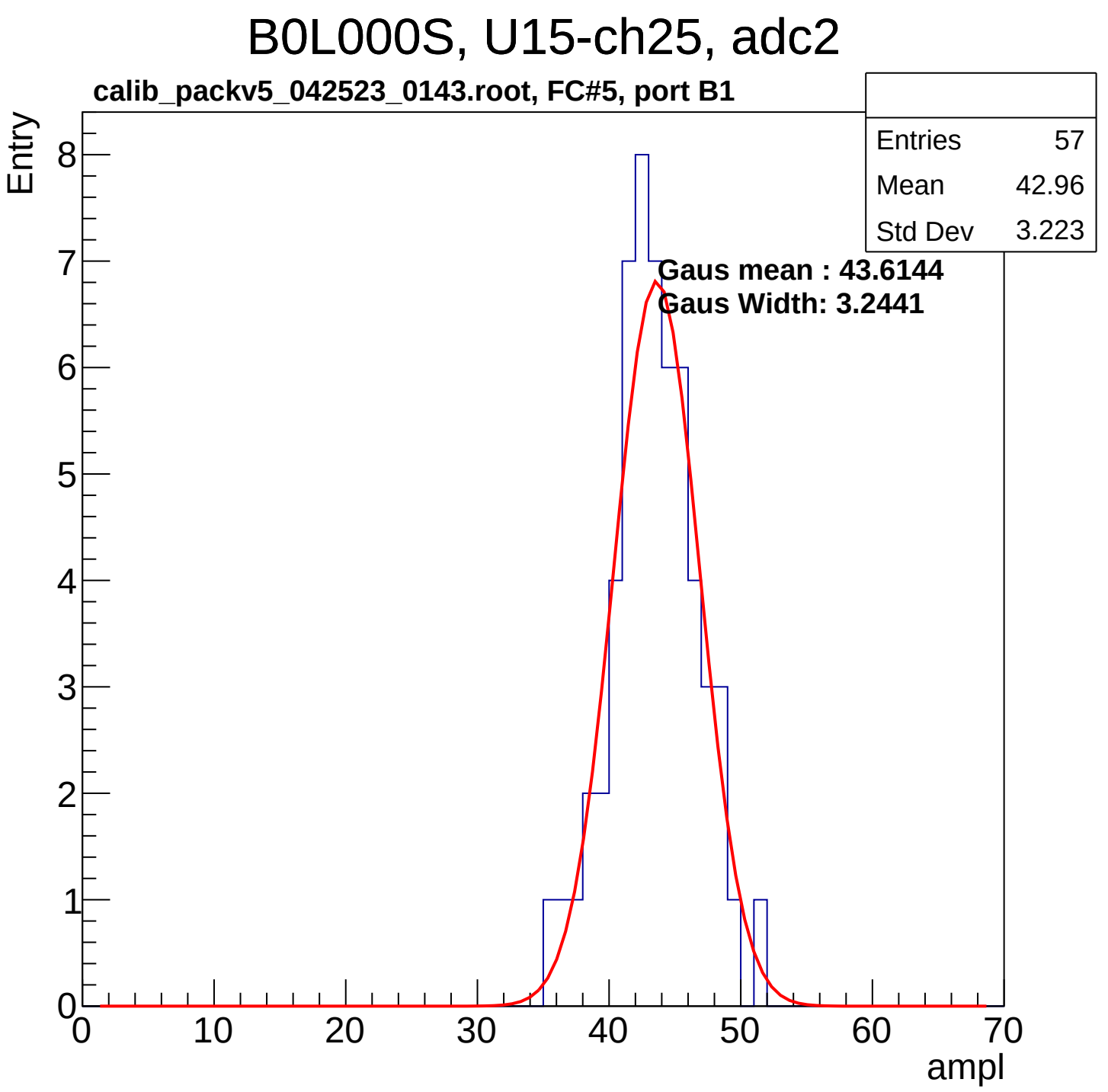
B0L000S, U15-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.96
Std Dev 3.223
Gaus mean : 43.6144
Gaus Width: 3.2441
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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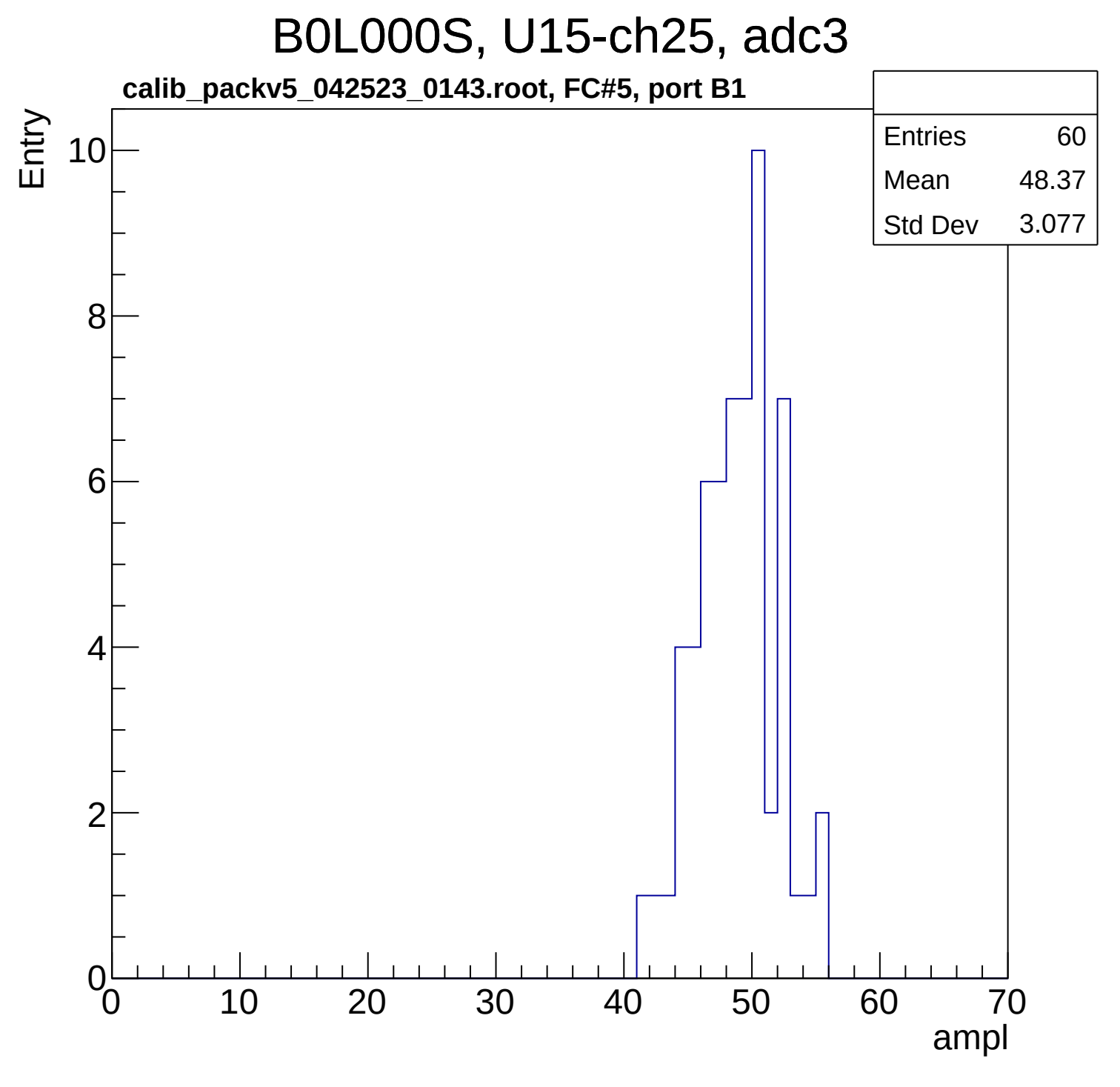
B0L000S, U15-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.37
Std Dev 3.077
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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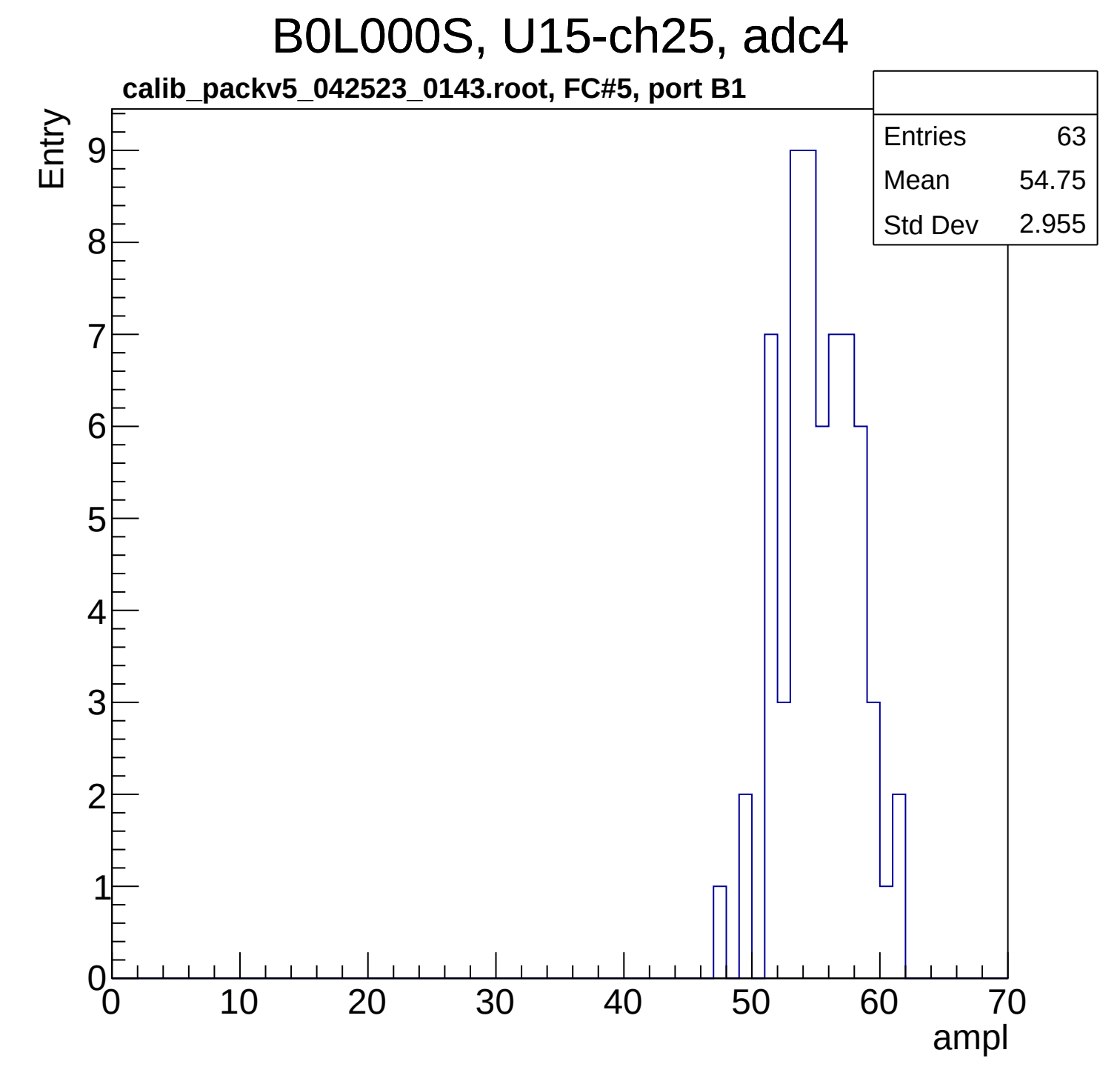
60
70


B0L000S, U15-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.75
Std Dev 2.955
Entry
ampl
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3
4
5
6
7
8
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0
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60
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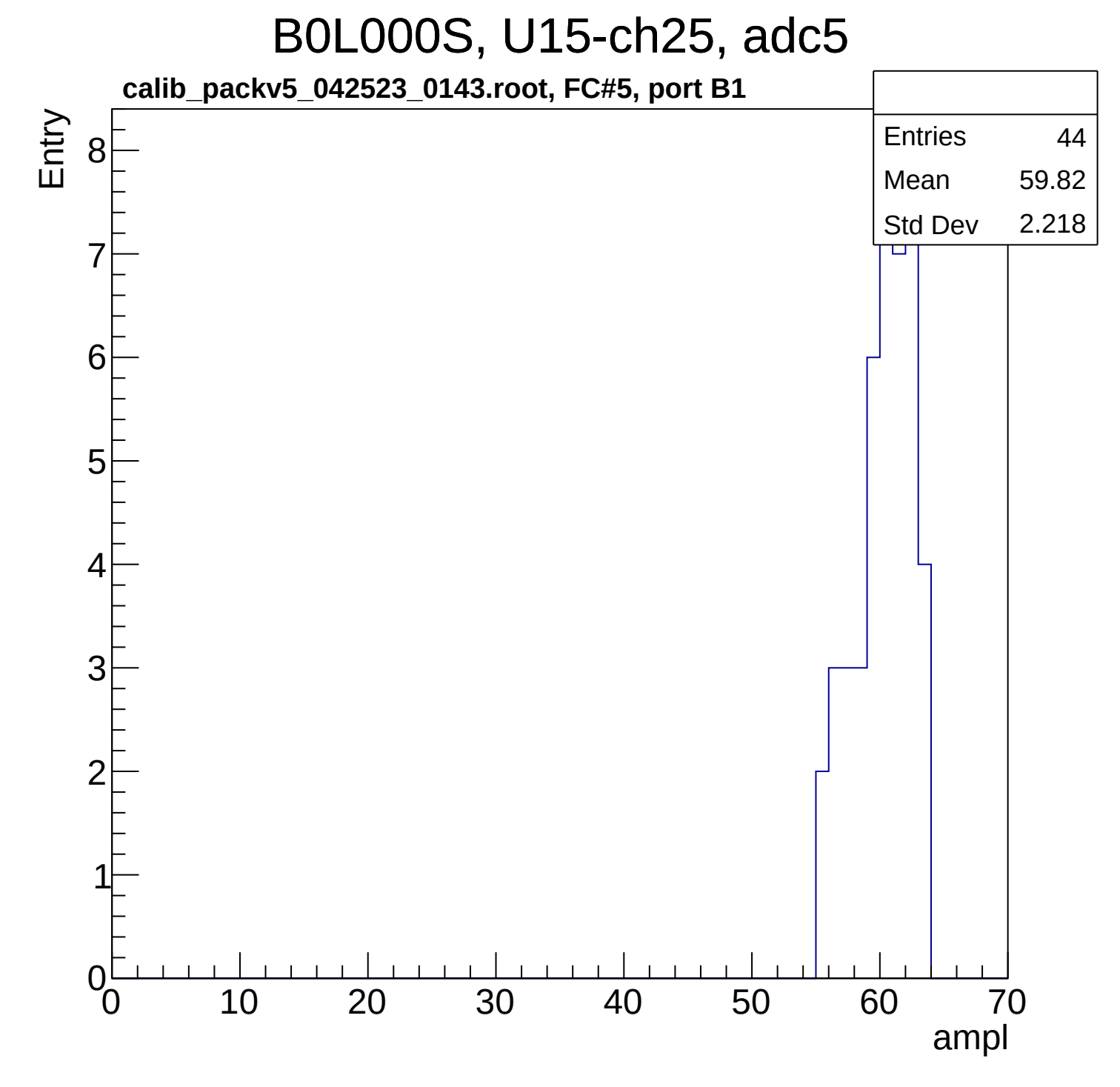
B0L000S, U15-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.82
Std Dev 2.218
Entry
ampl
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4
5
6
7
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0
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60
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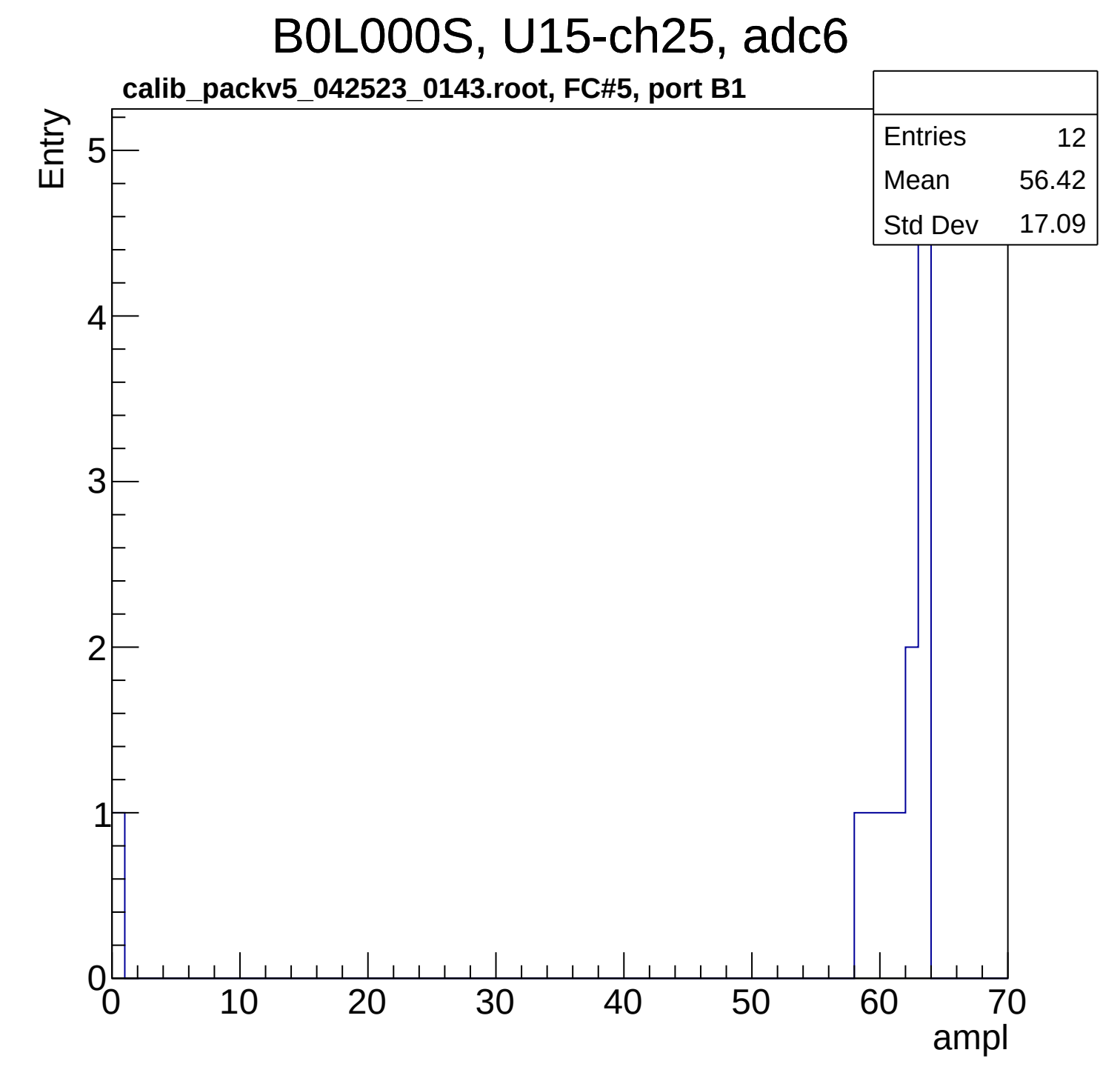
B0L000S, U15-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.42
Std Dev 17.09
Entry
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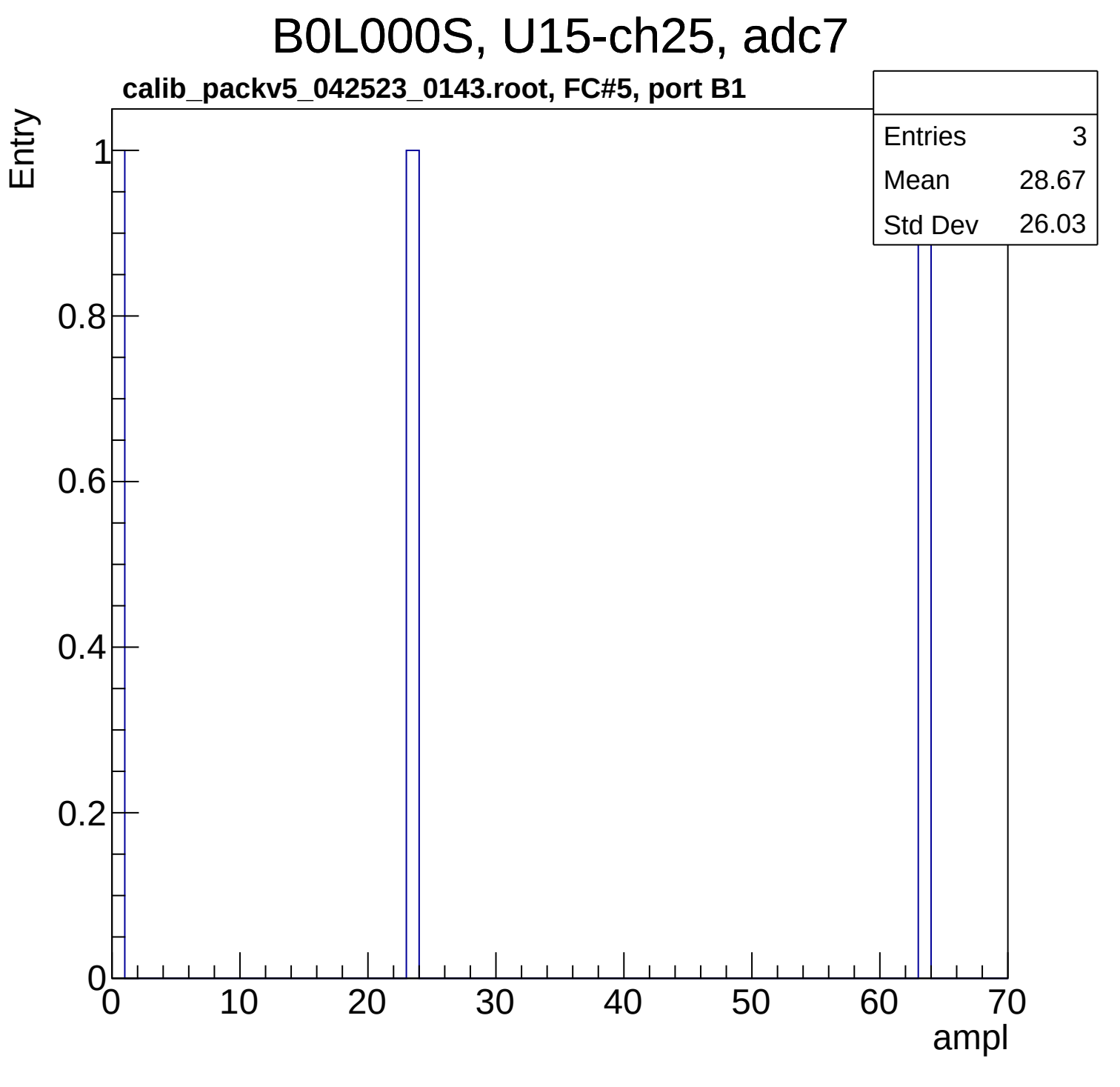
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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0.4
0.6
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0
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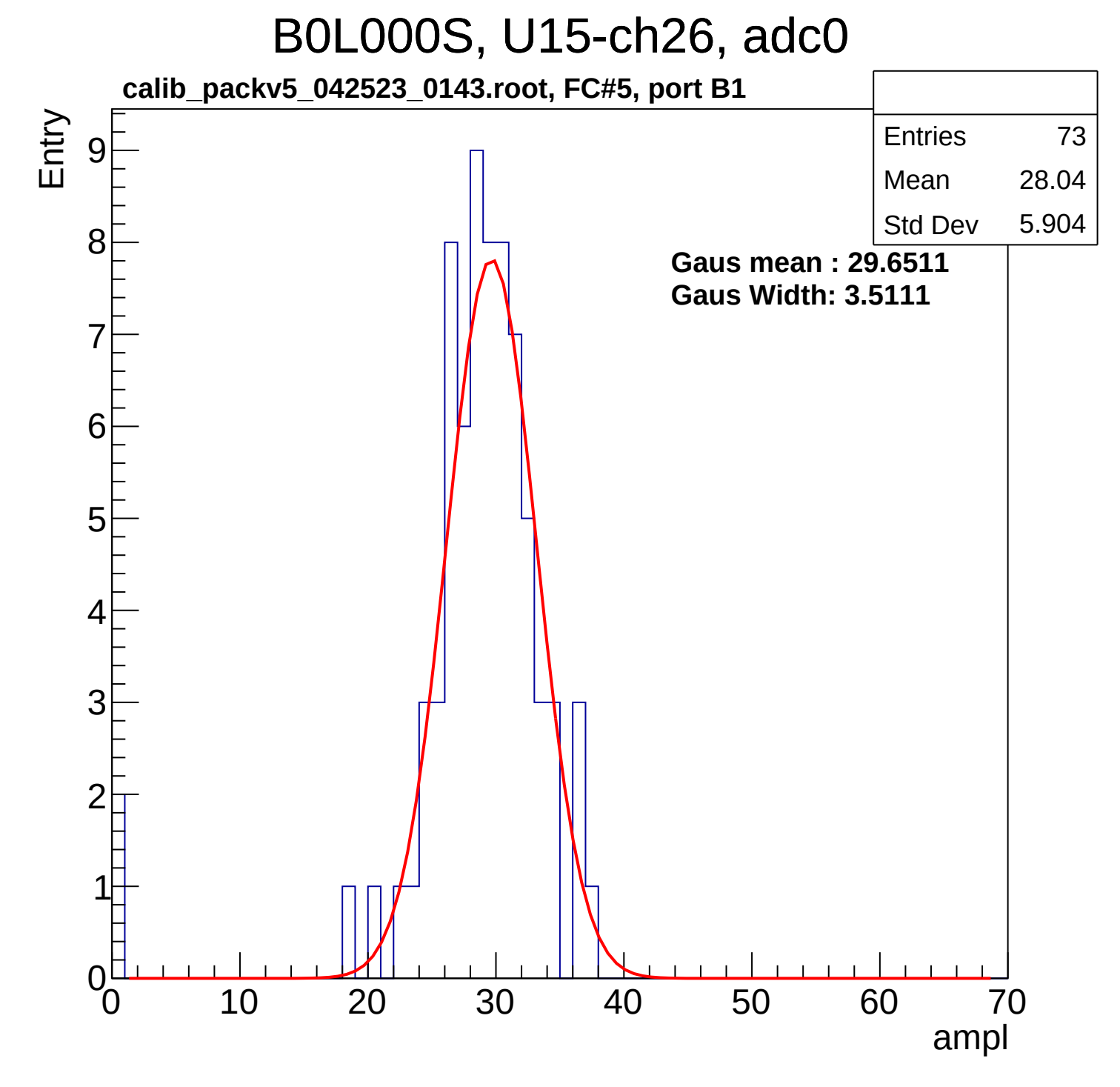
B0L000S, U15-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.04
Std Dev 5.904
Gaus mean : 29.6511
Gaus Width: 3.5111
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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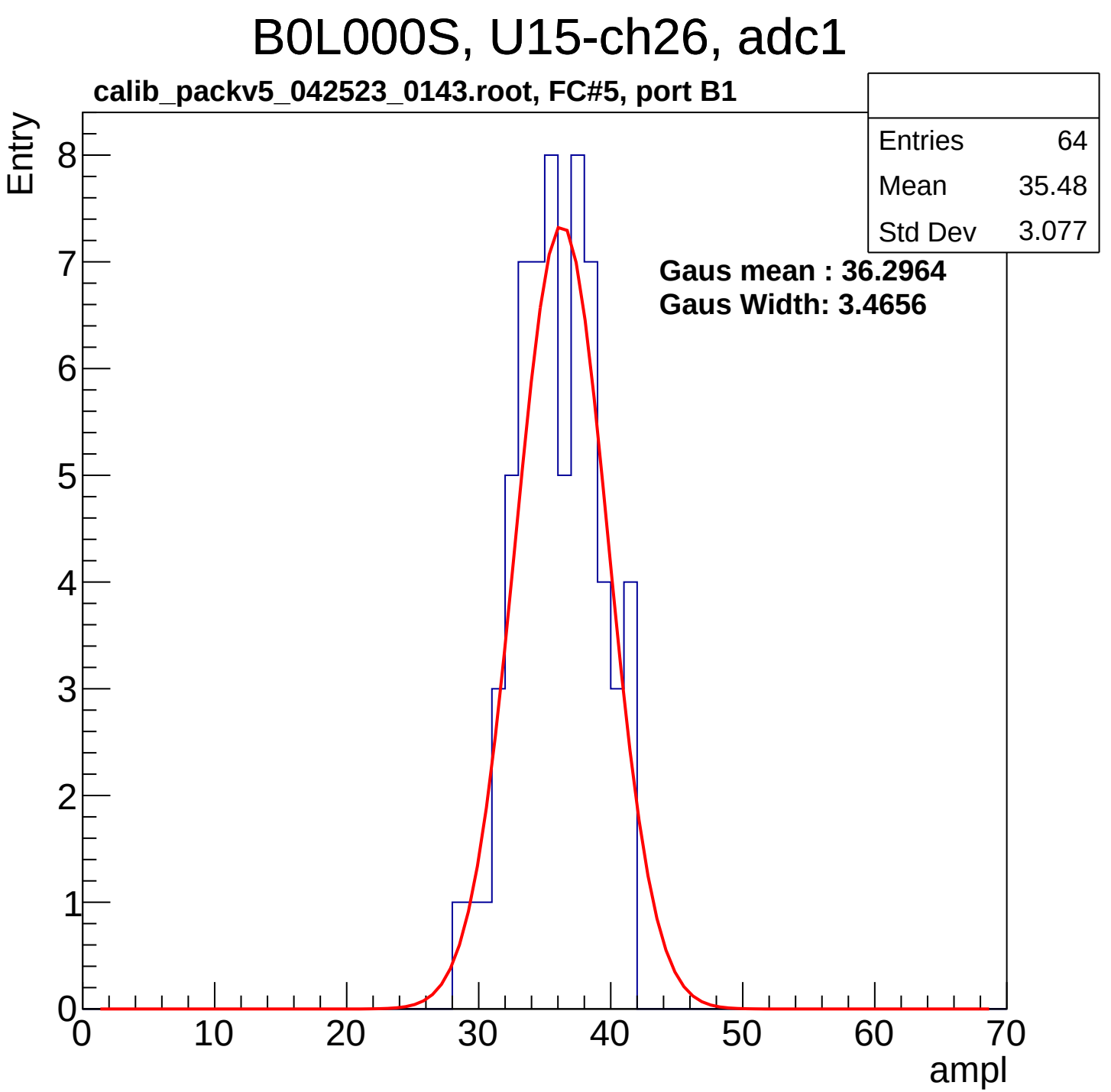
B0L000S, U15-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.48
Std Dev 3.077
Gaus mean : 36.2964
Gaus Width: 3.4656
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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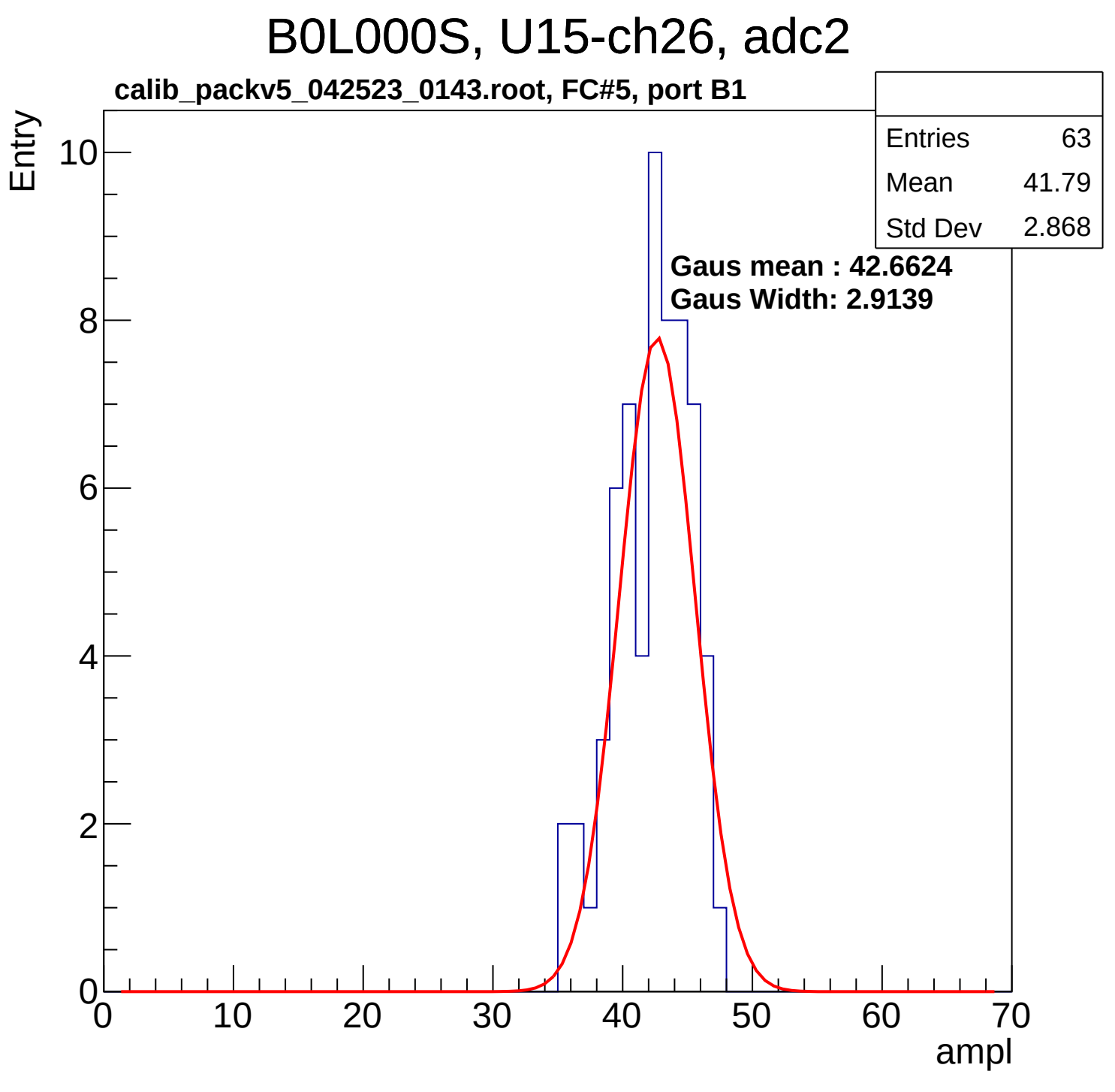
B0L000S, U15-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.79
Std Dev 2.868
Gaus mean : 42.6624
Gaus Width: 2.9139
Entry
ampl
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4
6
8
10
0
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50
60
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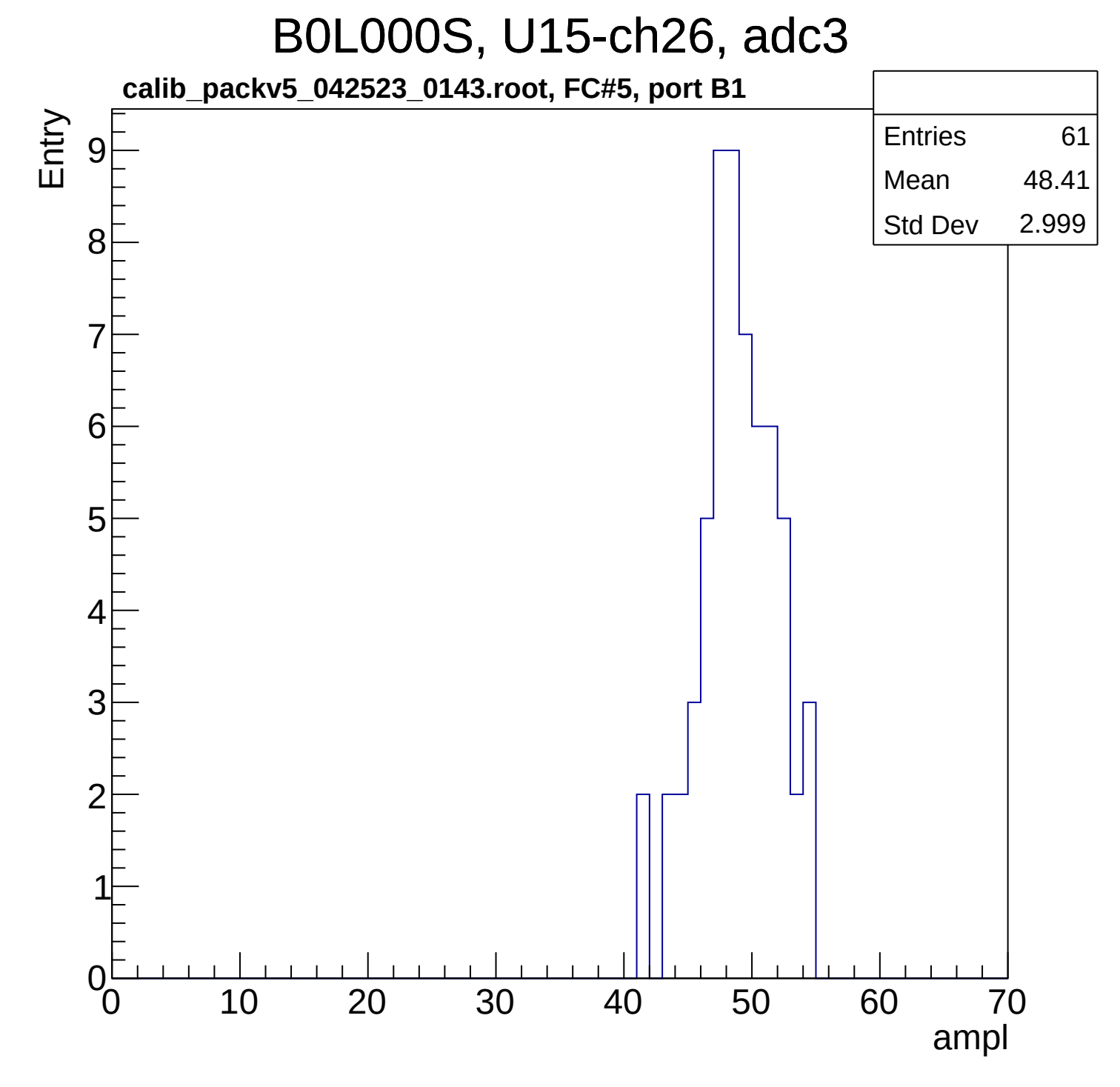
B0L000S, U15-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.41
Std Dev 2.999
Entry
ampl
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2
3
4
5
6
7
8
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0
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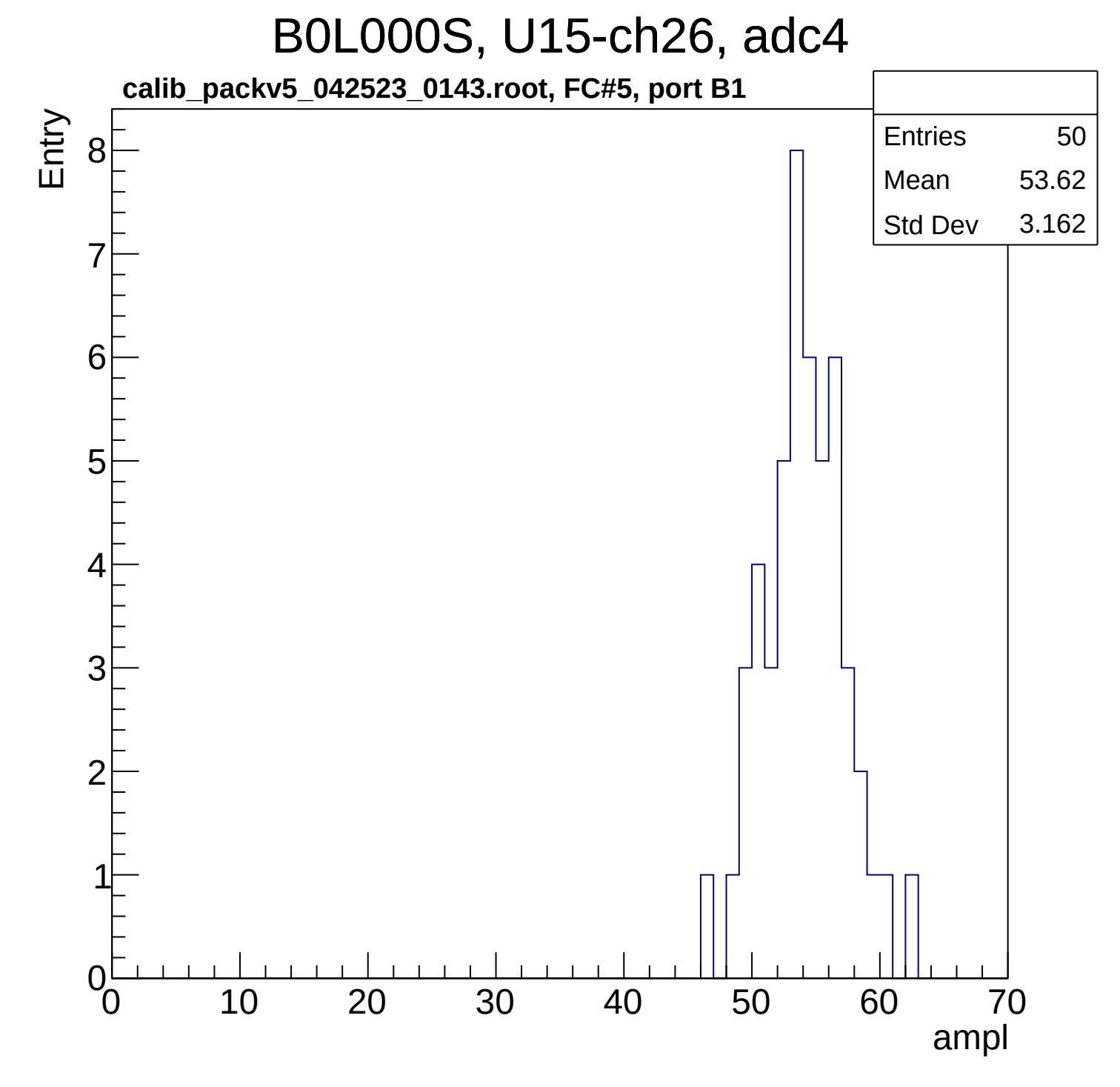
B0L000S, U15-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 53.62
Std Dev 3.162
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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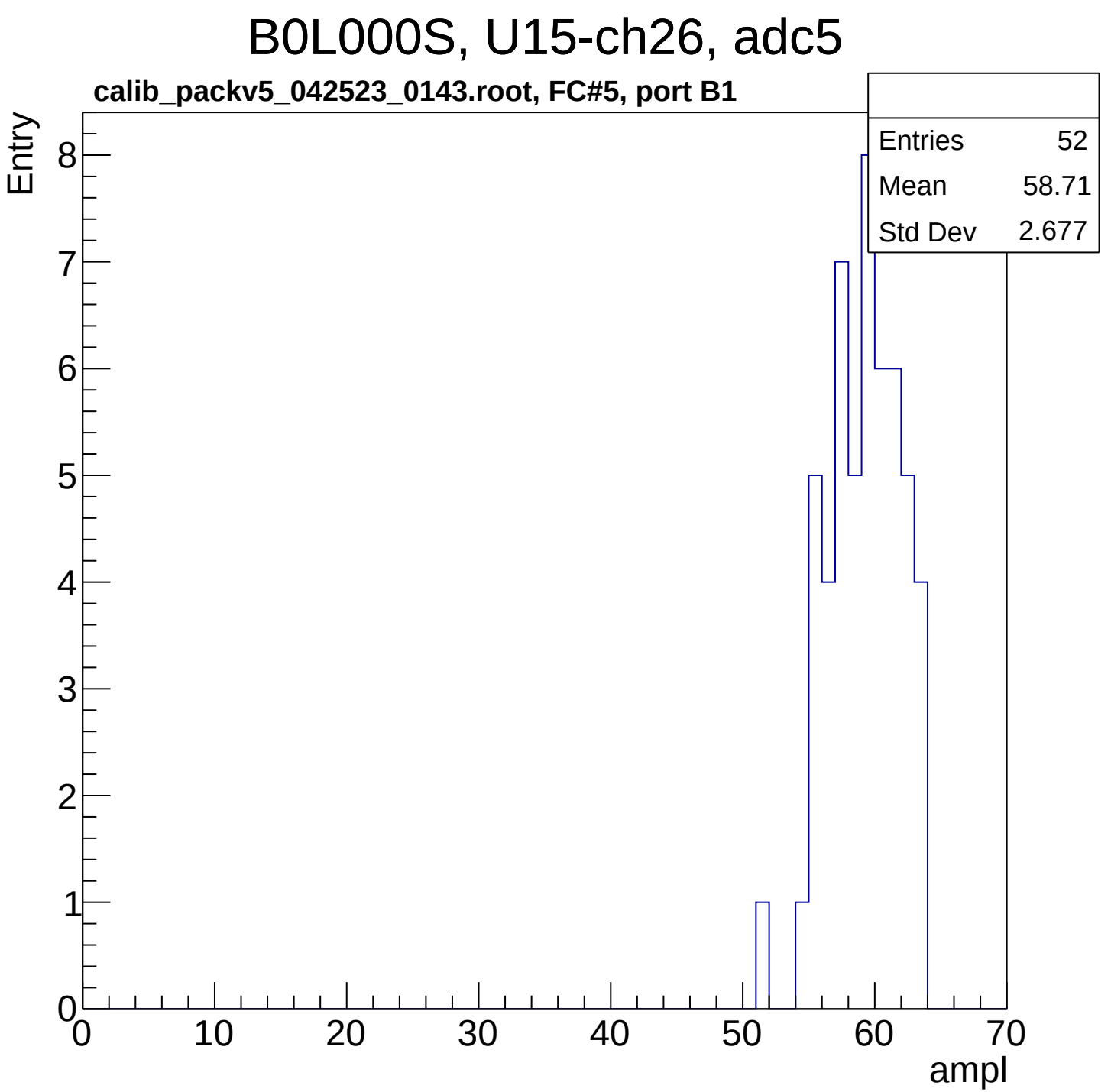
B0L000S, U15-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.71
Std Dev 2.677
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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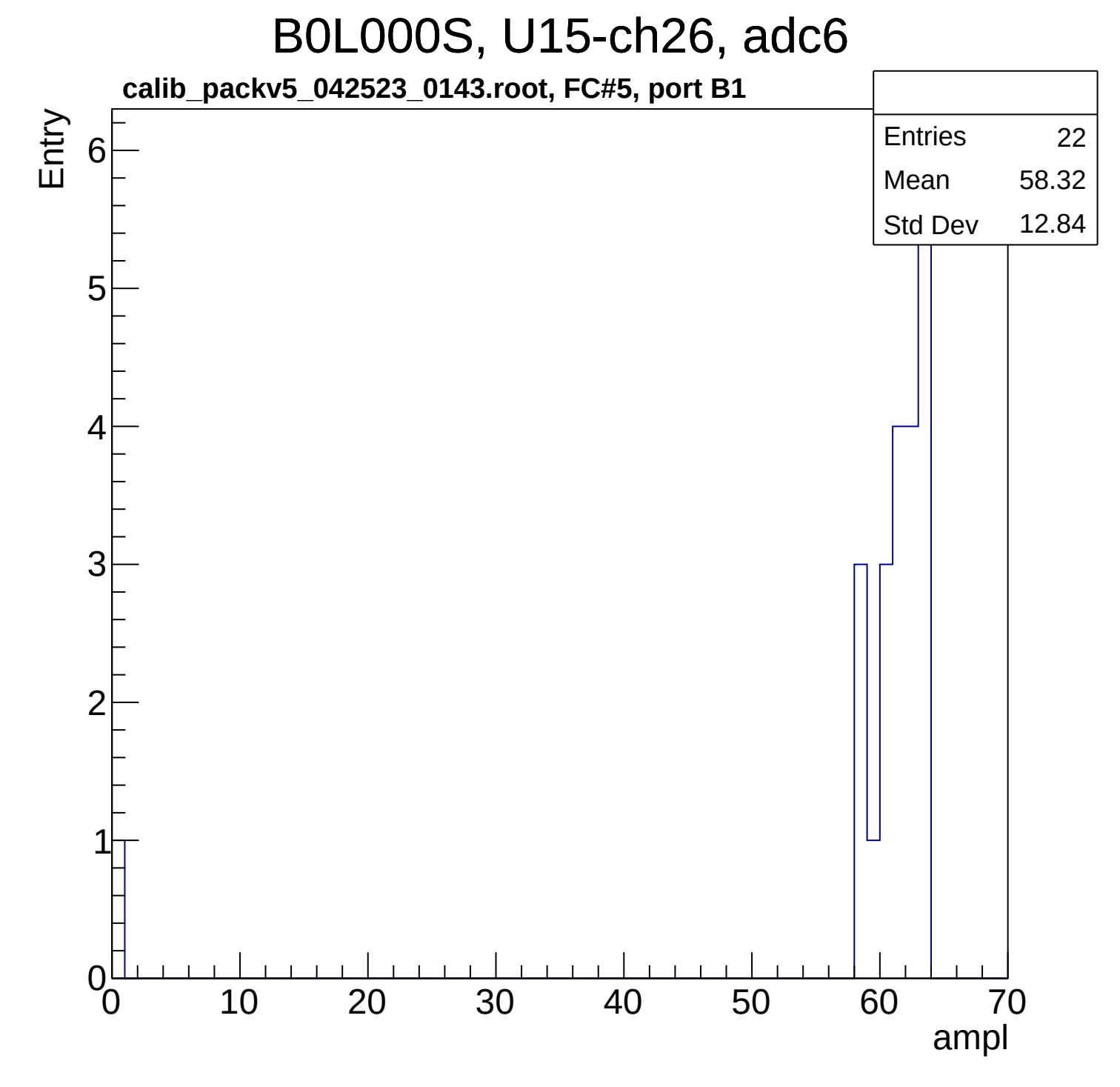
B0L000S, U15-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.32
Std Dev 12.84
Entry
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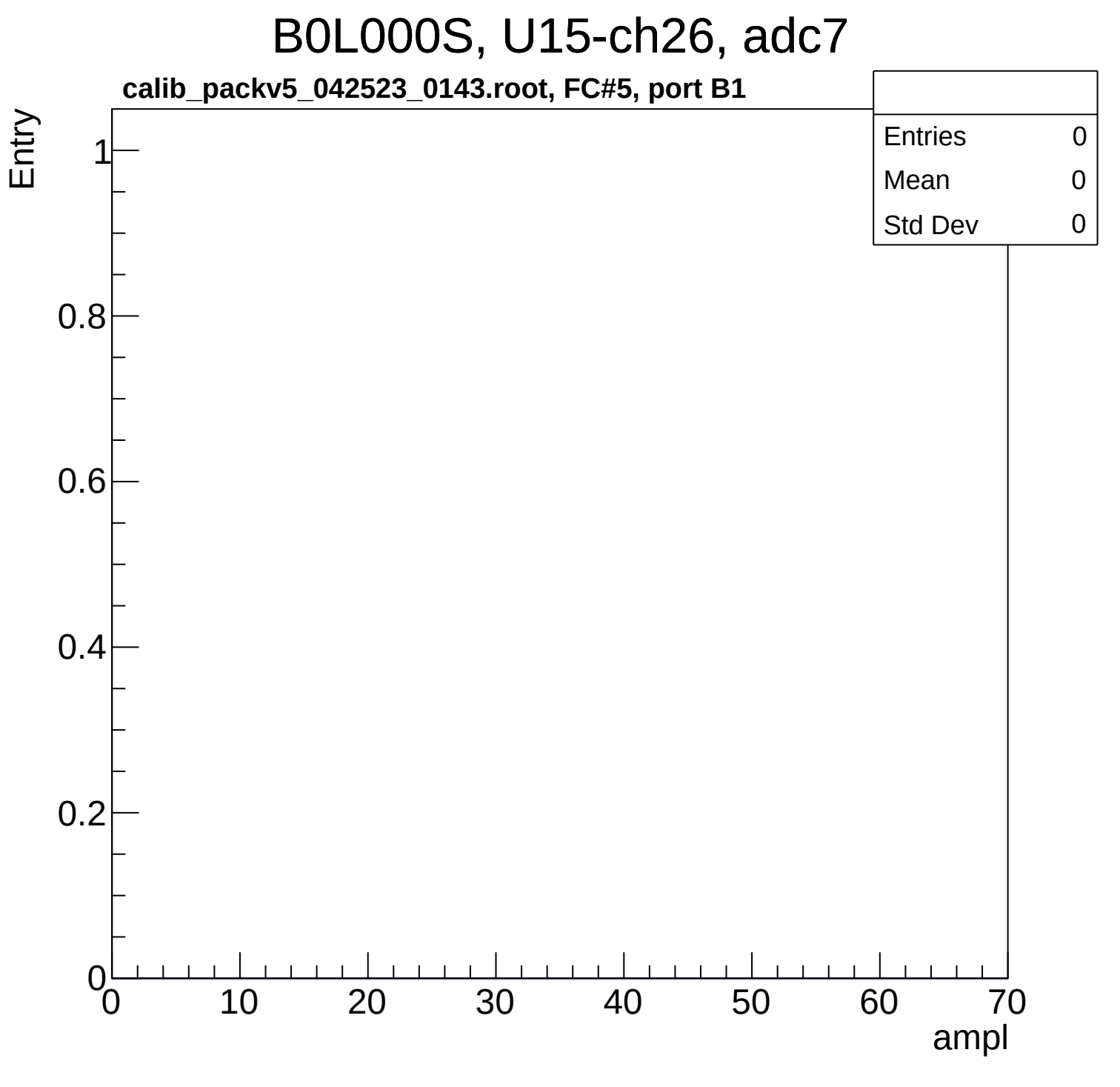
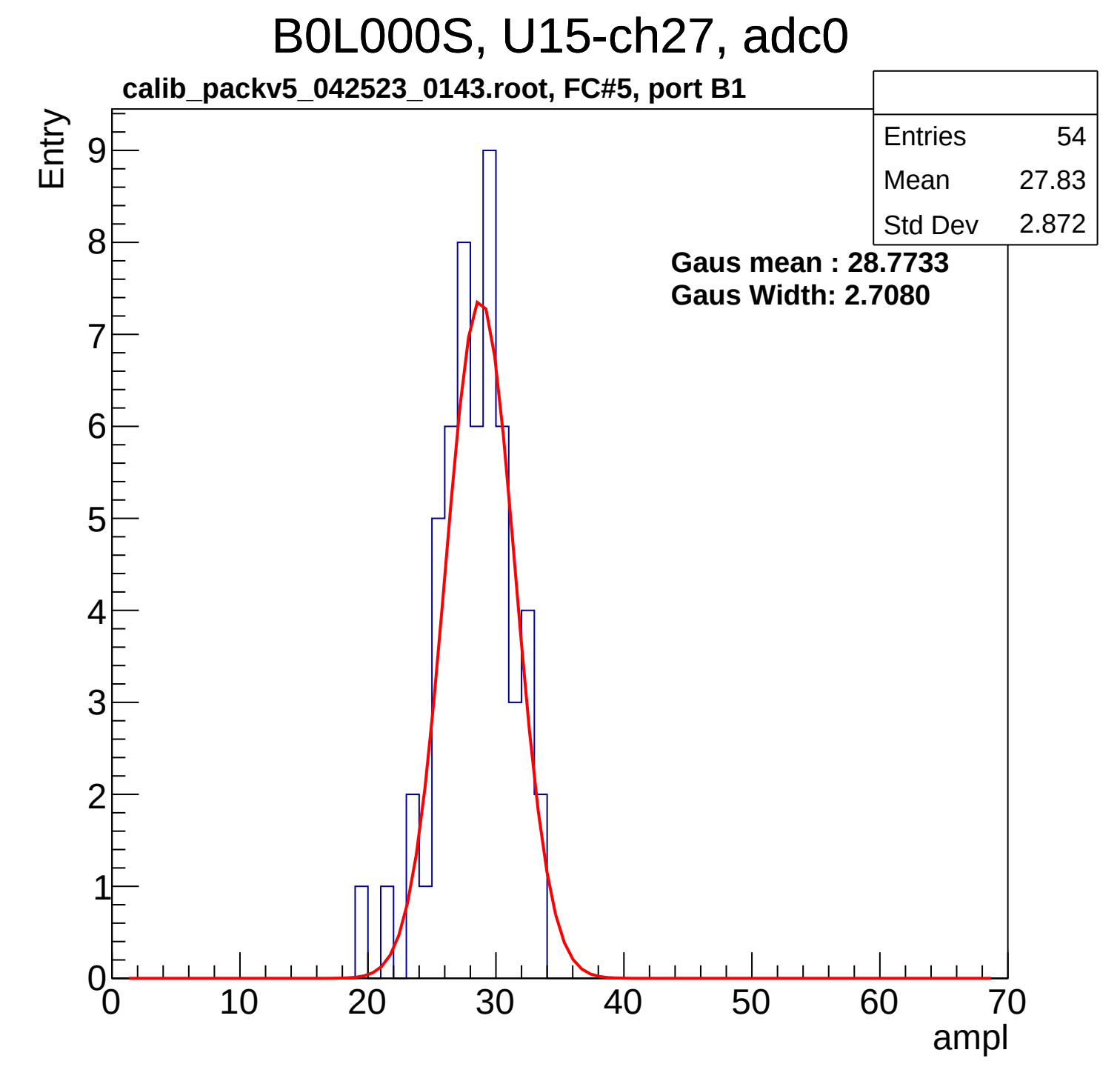
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 27.83
Std Dev 2.872
Gaus mean : 28.7733
Gaus Width: 2.7080
Entry
ampl
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2
3
4
5
6
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0
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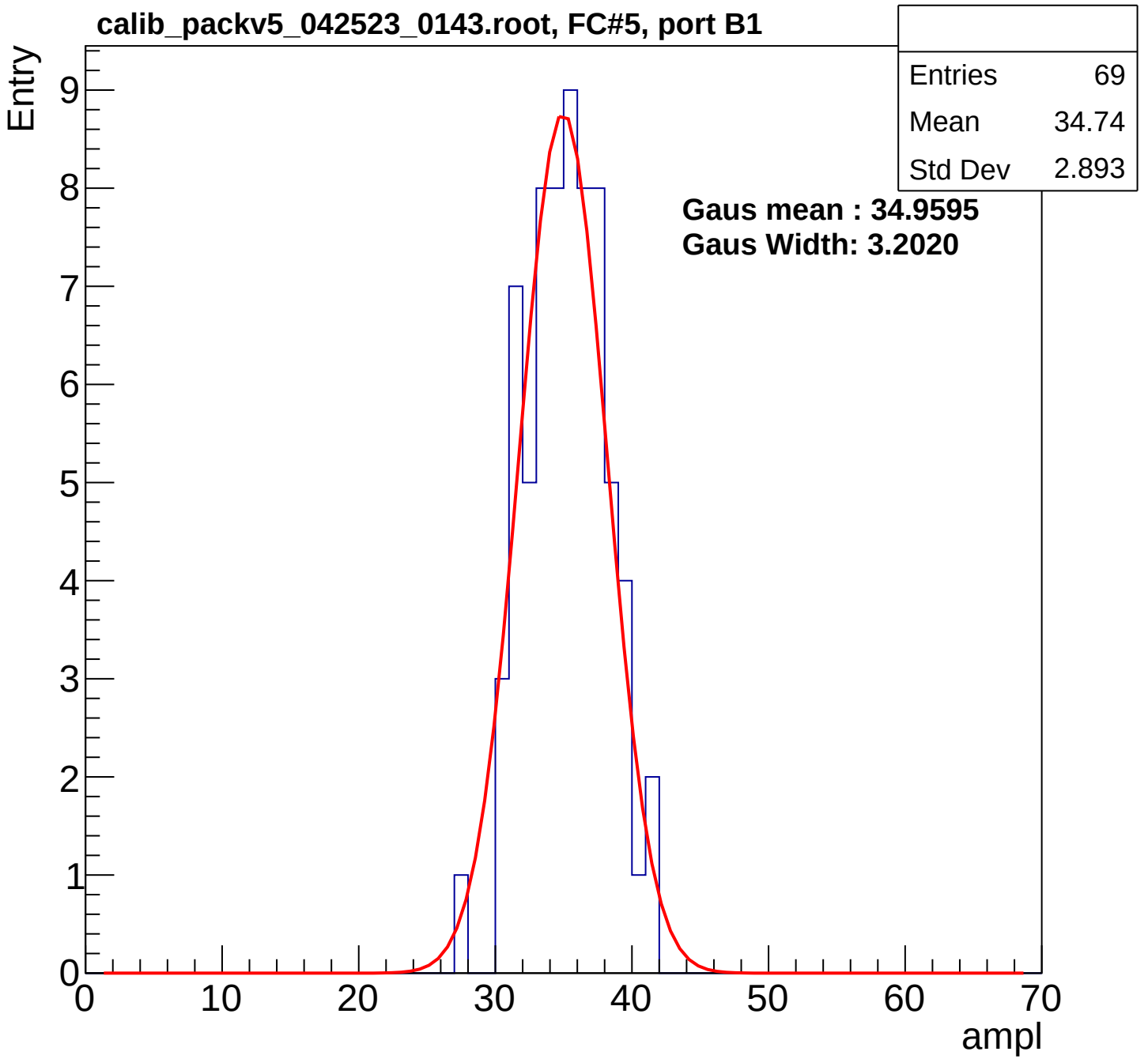
B0L000S, U15-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 34.74
Std Dev 2.893
Gaus mean : 34.9595
Gaus Width: 3.2020
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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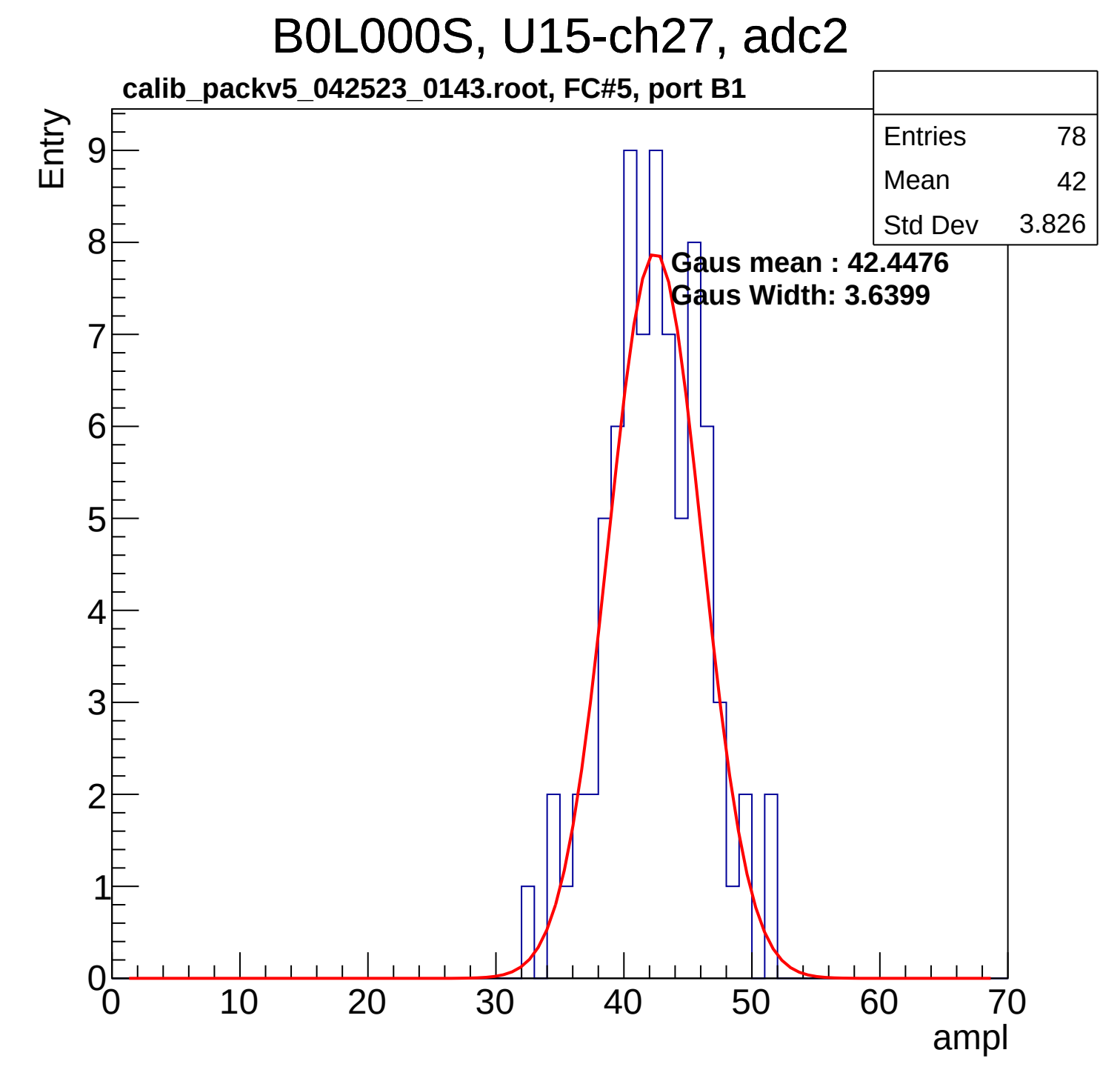
6
7
8
9


B0L000S, U15-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42
Std Dev 3.826
Gaus mean : 42.4476
Gaus Width: 3.6399
Entry
ampl
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2
3
4
5
6
7
8
9
0
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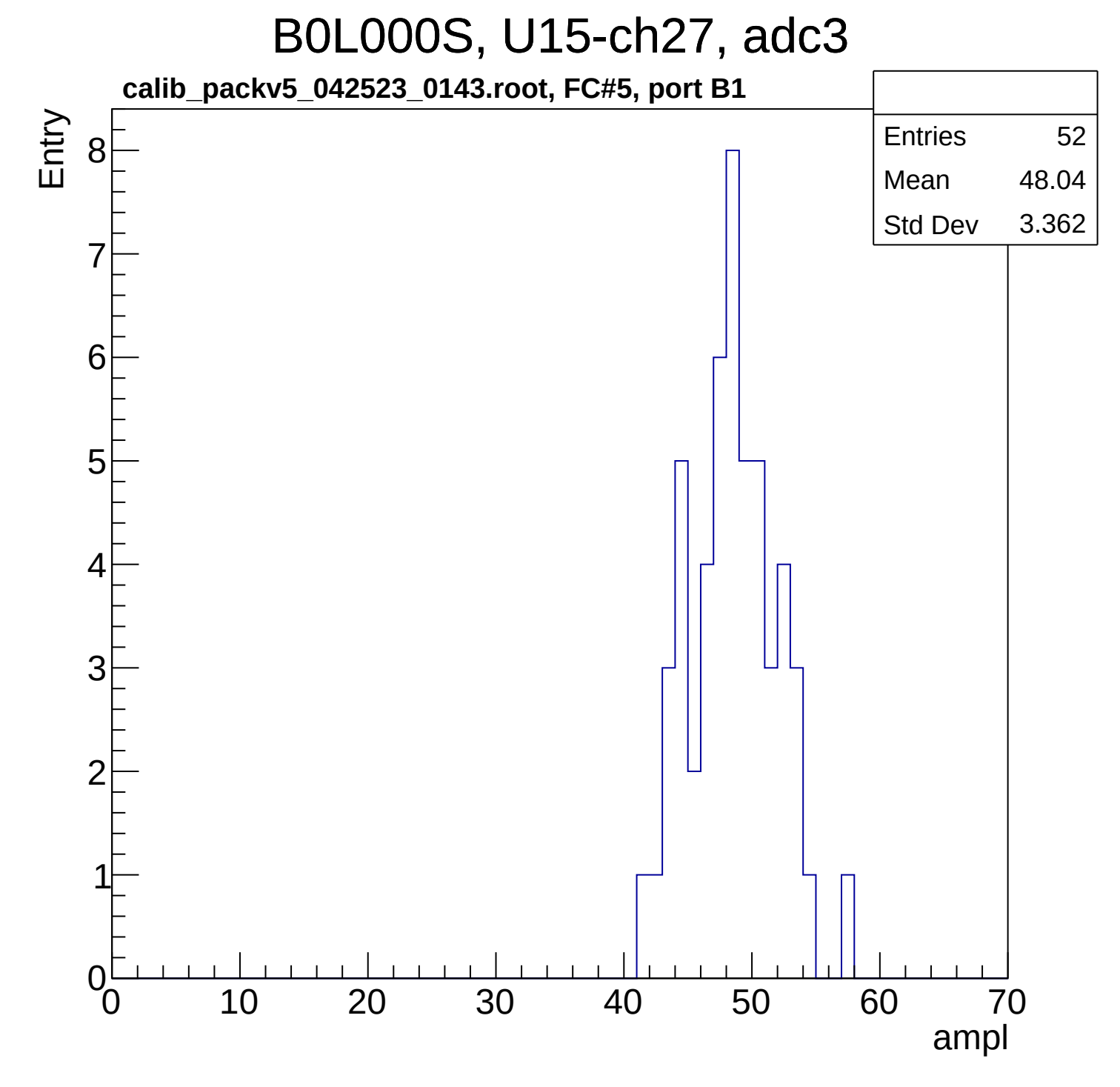
B0L000S, U15-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 48.04
Std Dev 3.362
Entry
ampl
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2
3
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5
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0
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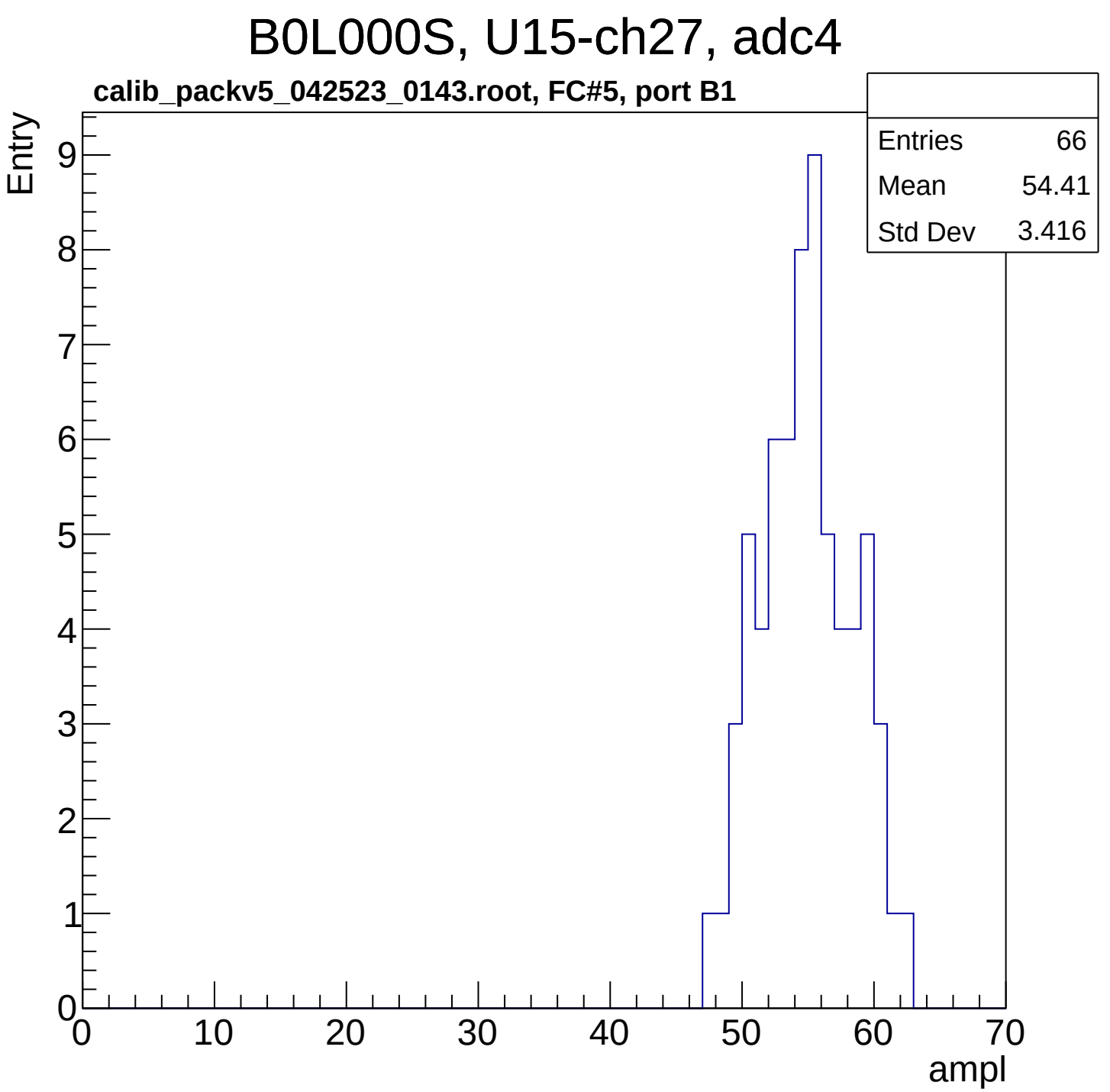
B0L000S, U15-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.41
Std Dev 3.416
Entry
ampl
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2
3
4
5
6
7
8
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0
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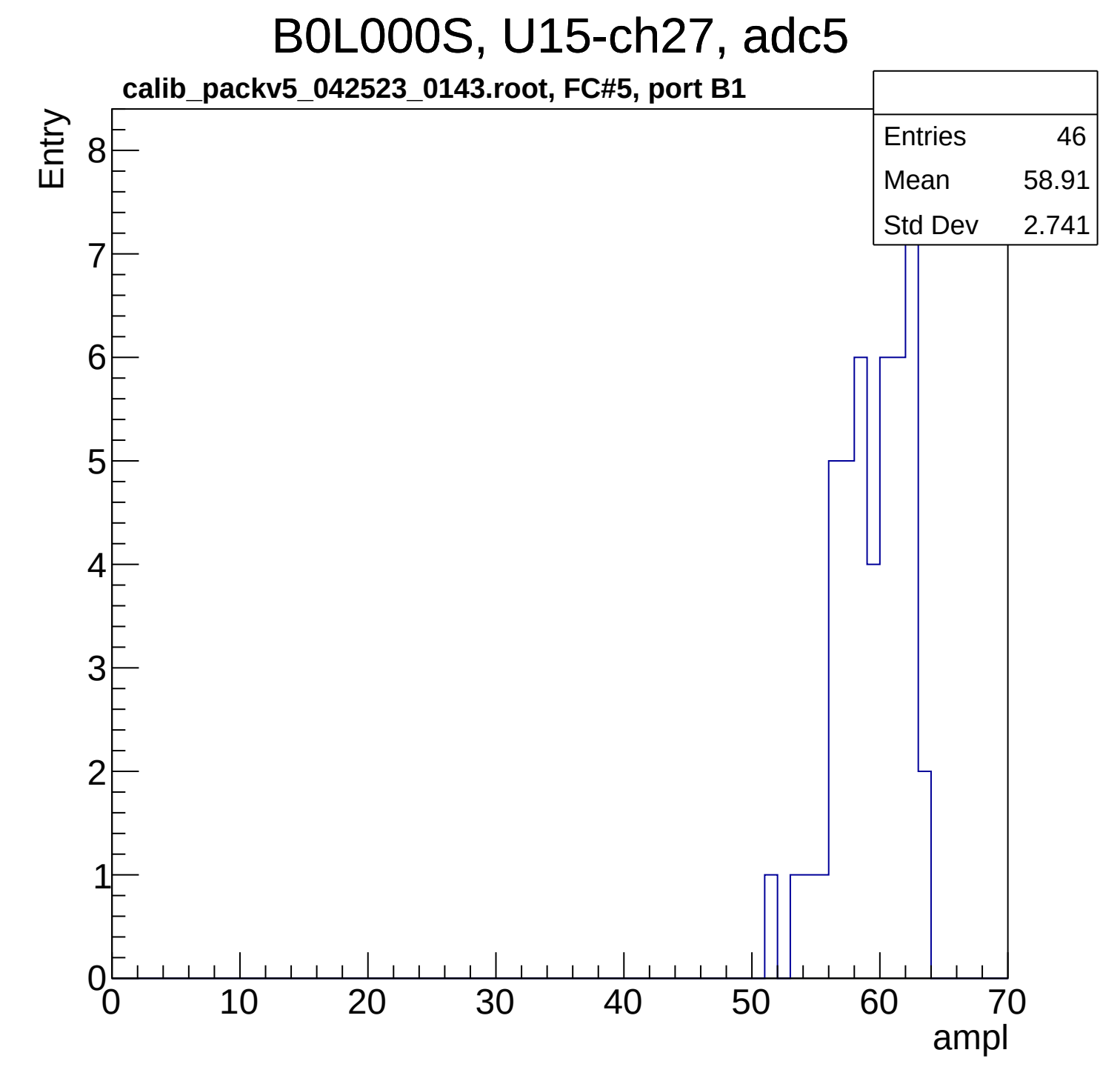
B0L000S, U15-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.91
Std Dev 2.741
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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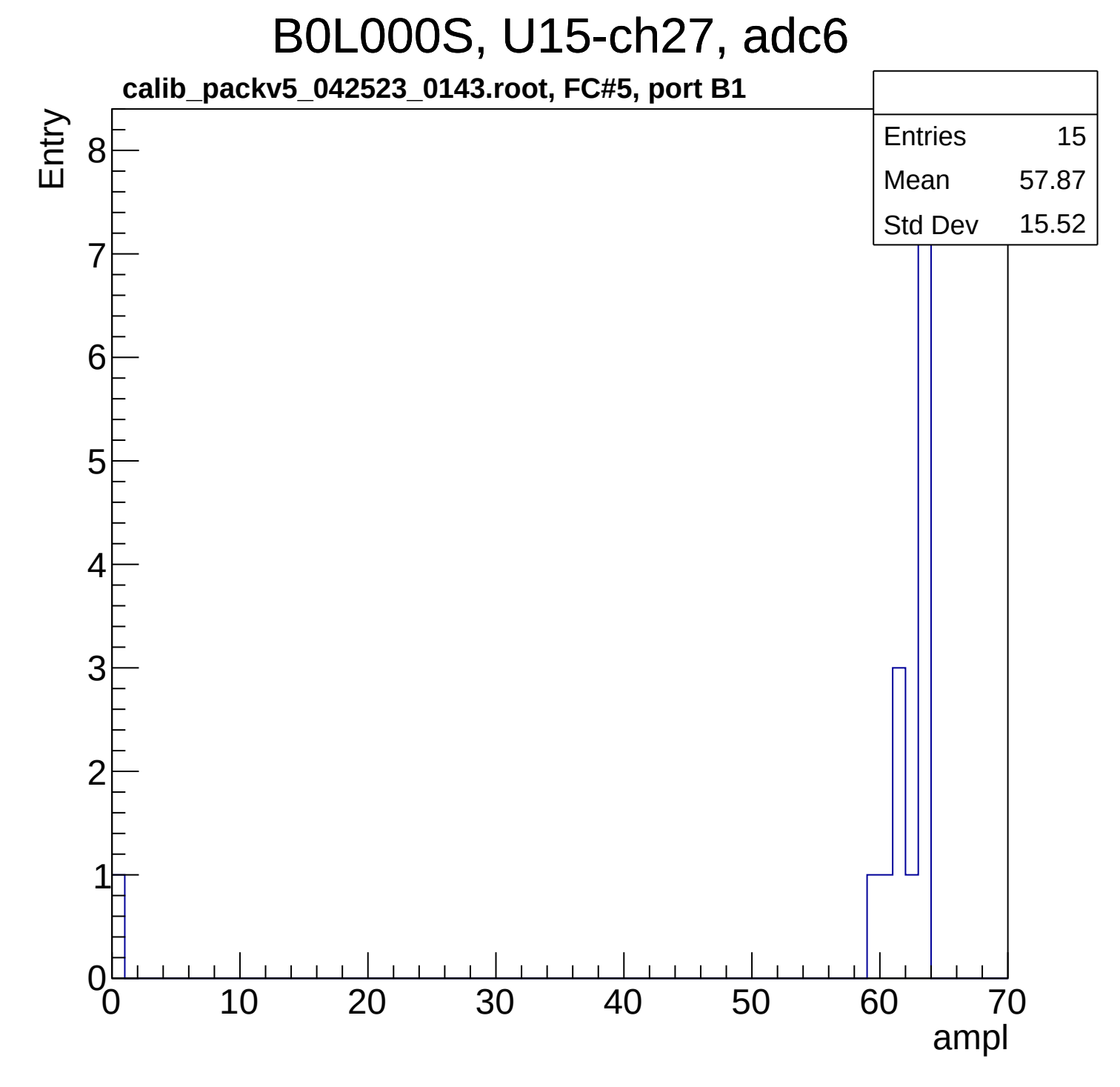
B0L000S, U15-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.87
Std Dev 15.52
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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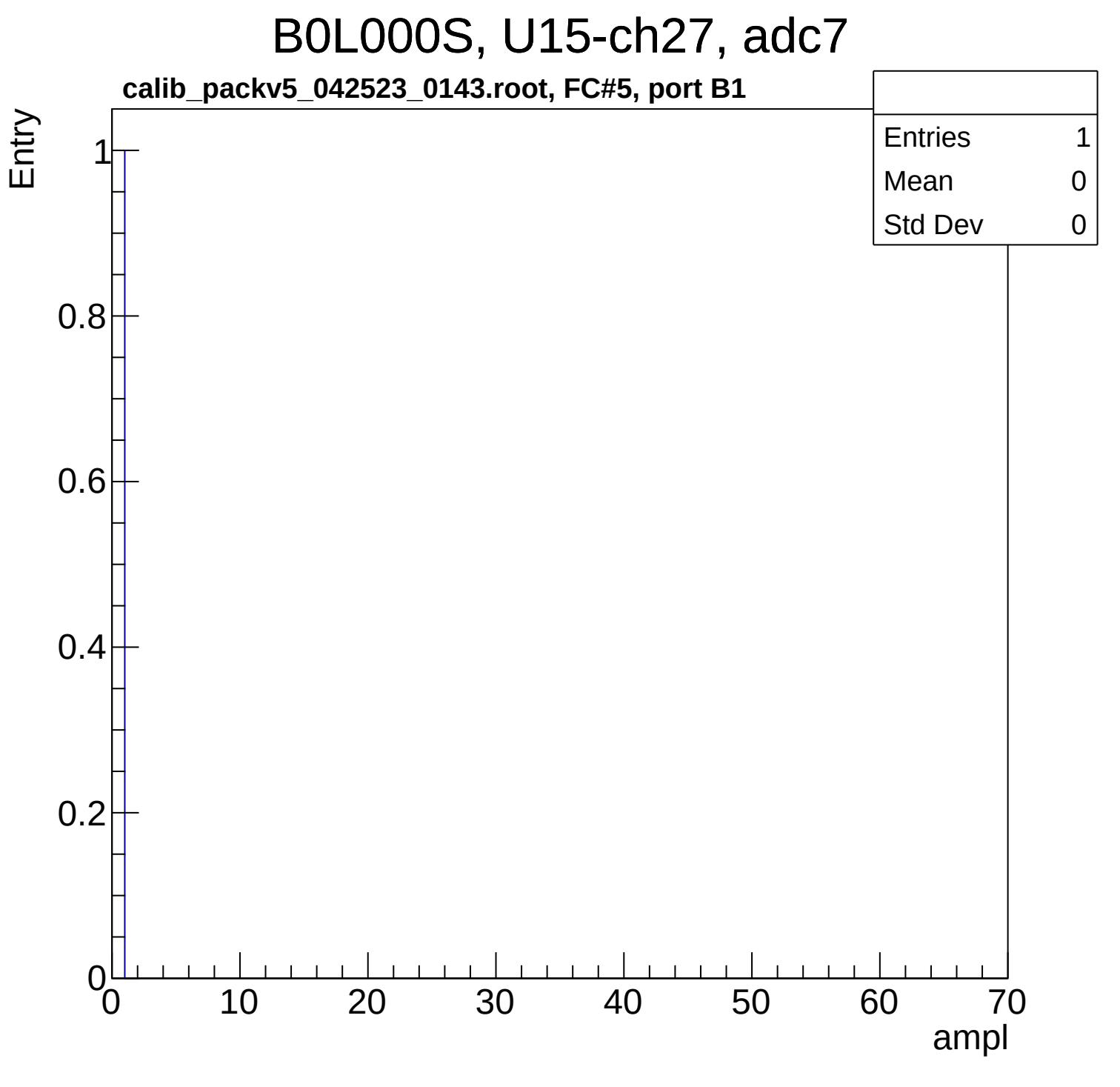
B0L000S, U15-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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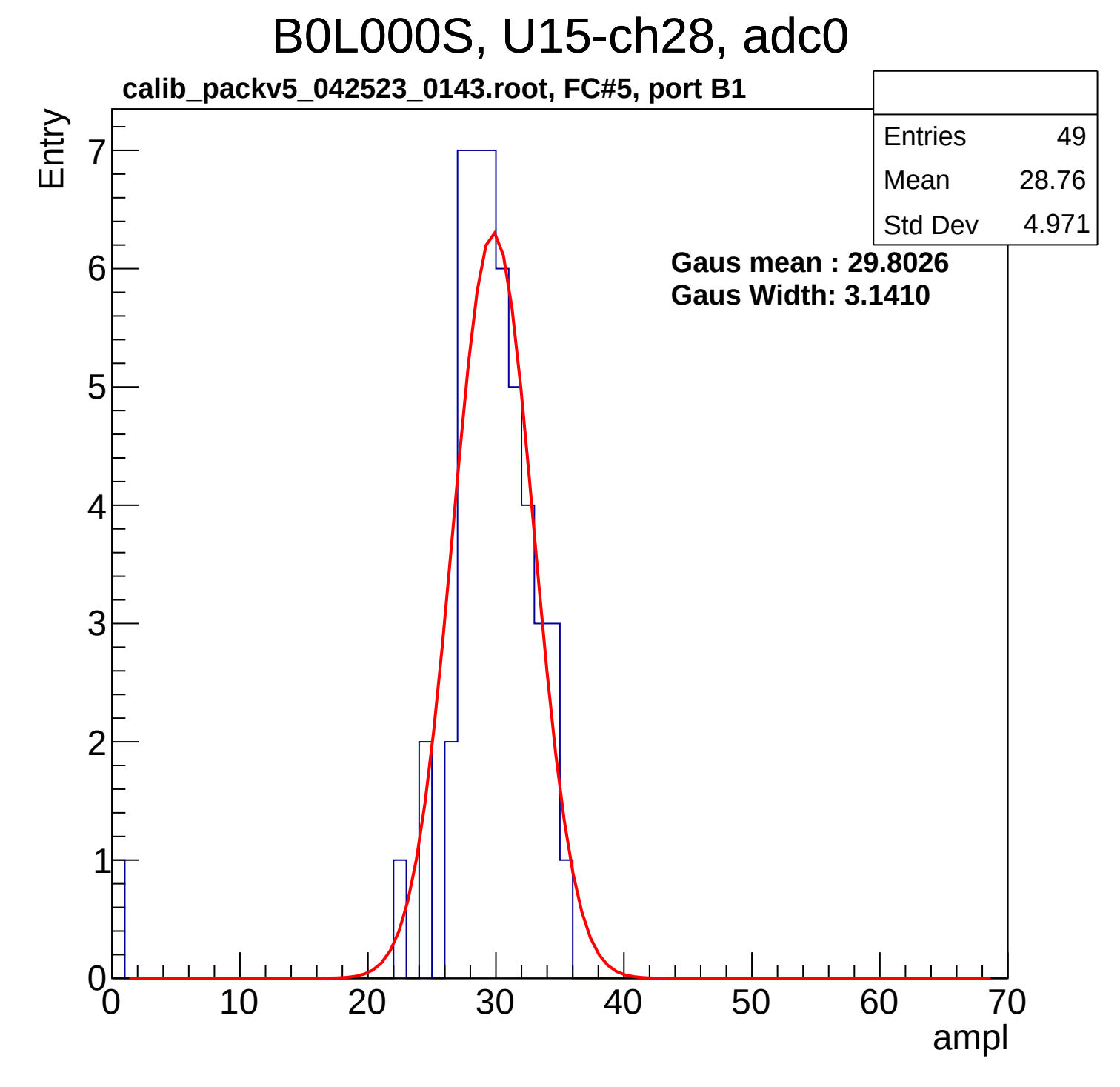
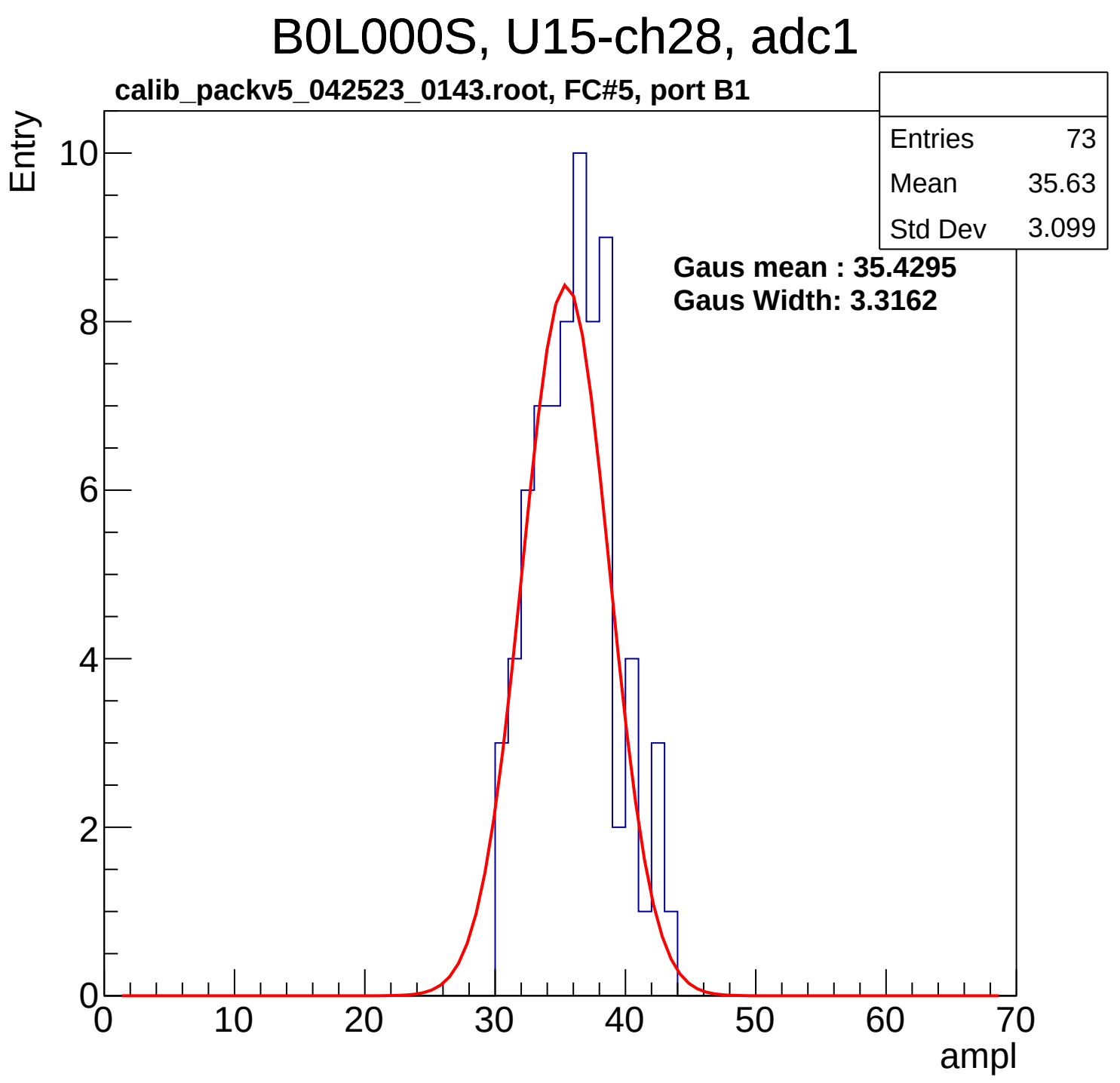
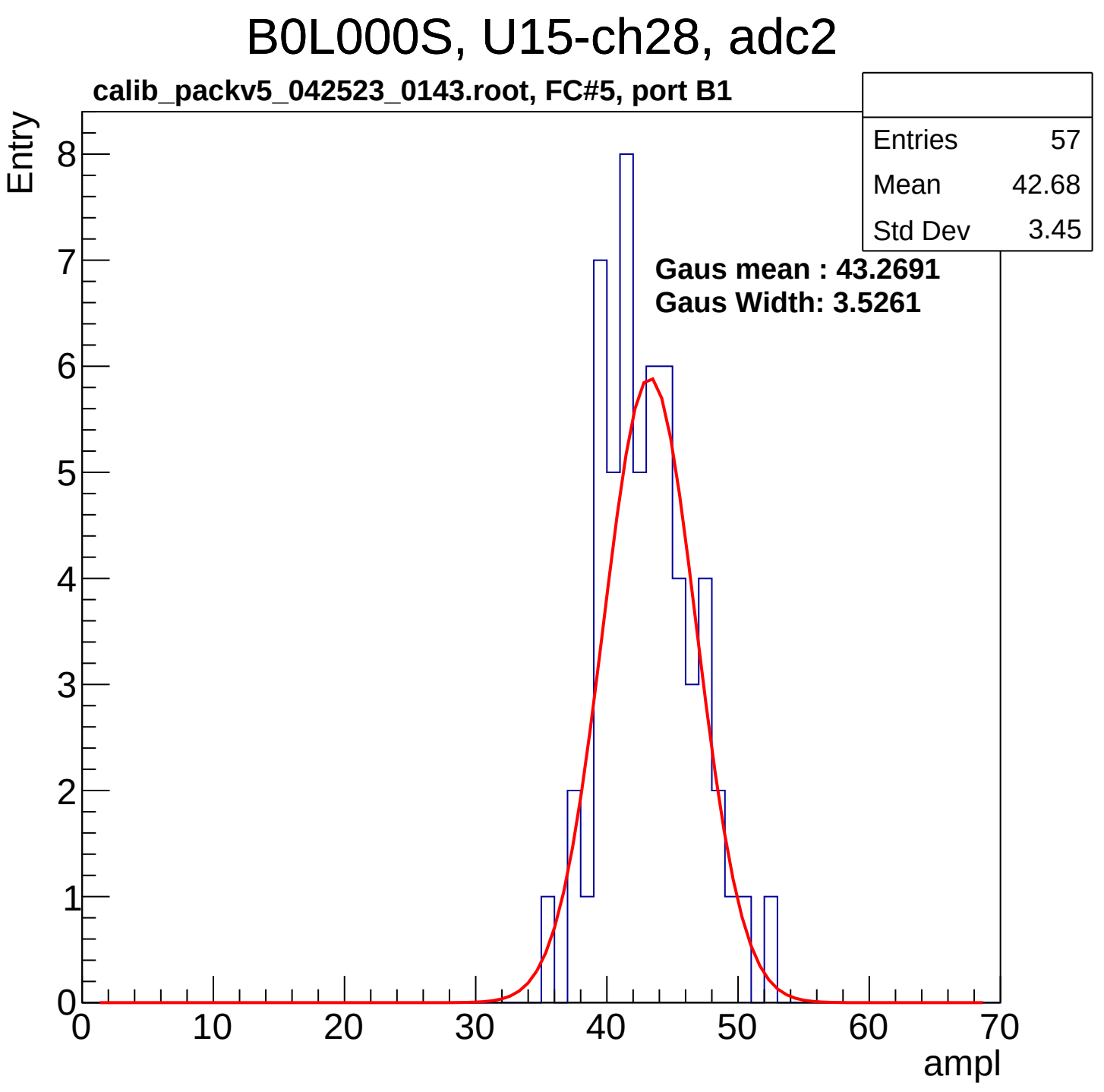
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 28.76
Std Dev 4.971
Gaus mean : 29.8026
Gaus Width: 3.1410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.63
Std Dev 3.099
Gaus mean : 35.4295
Gaus Width: 3.3162
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.68
Std Dev 3.45
Gaus mean : 43.2691
Gaus Width: 3.5261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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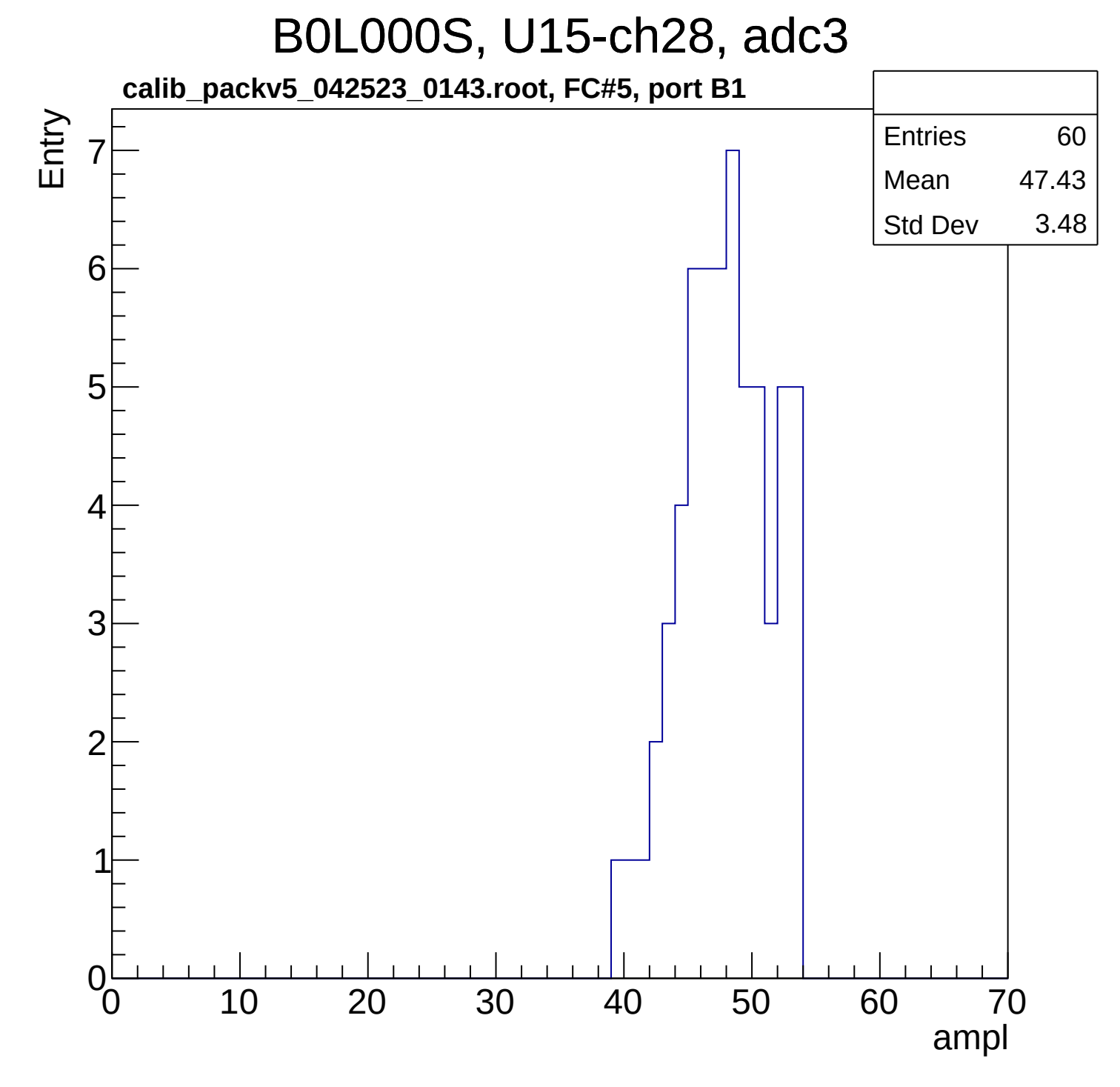
B0L000S, U15-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 47.43
Std Dev 3.48
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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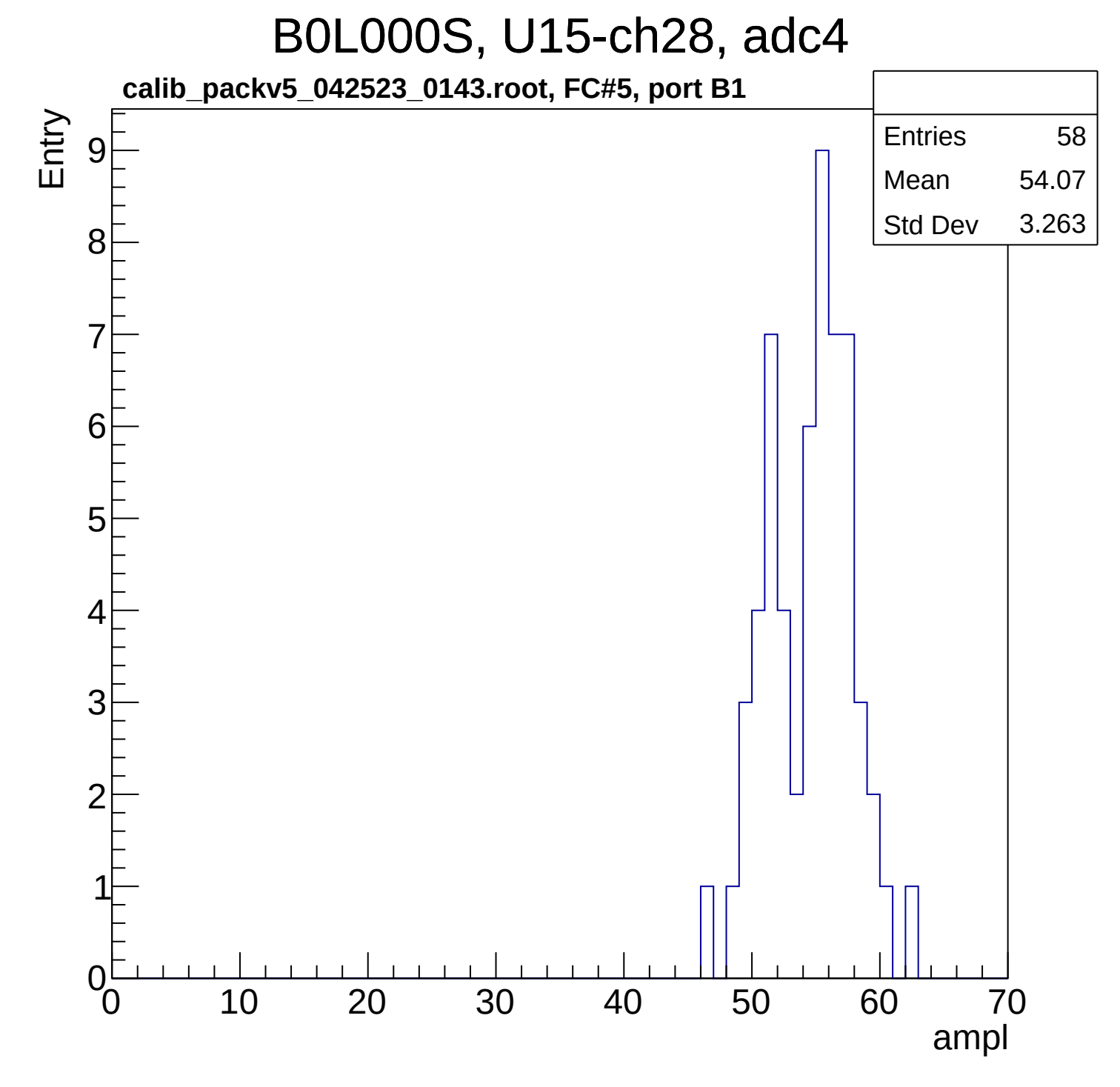
B0L000S, U15-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.07
Std Dev 3.263
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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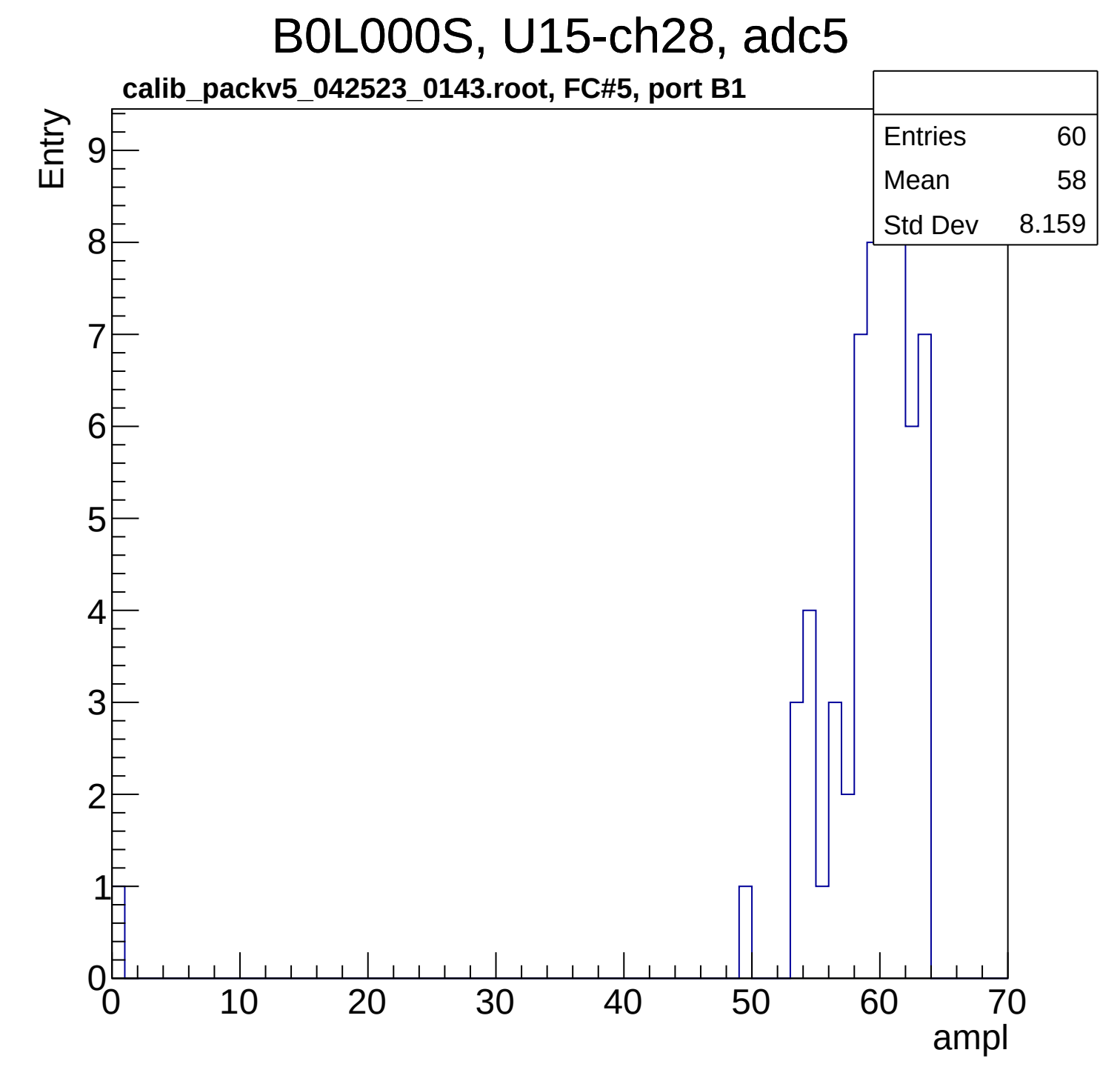
B0L000S, U15-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58
Std Dev 8.159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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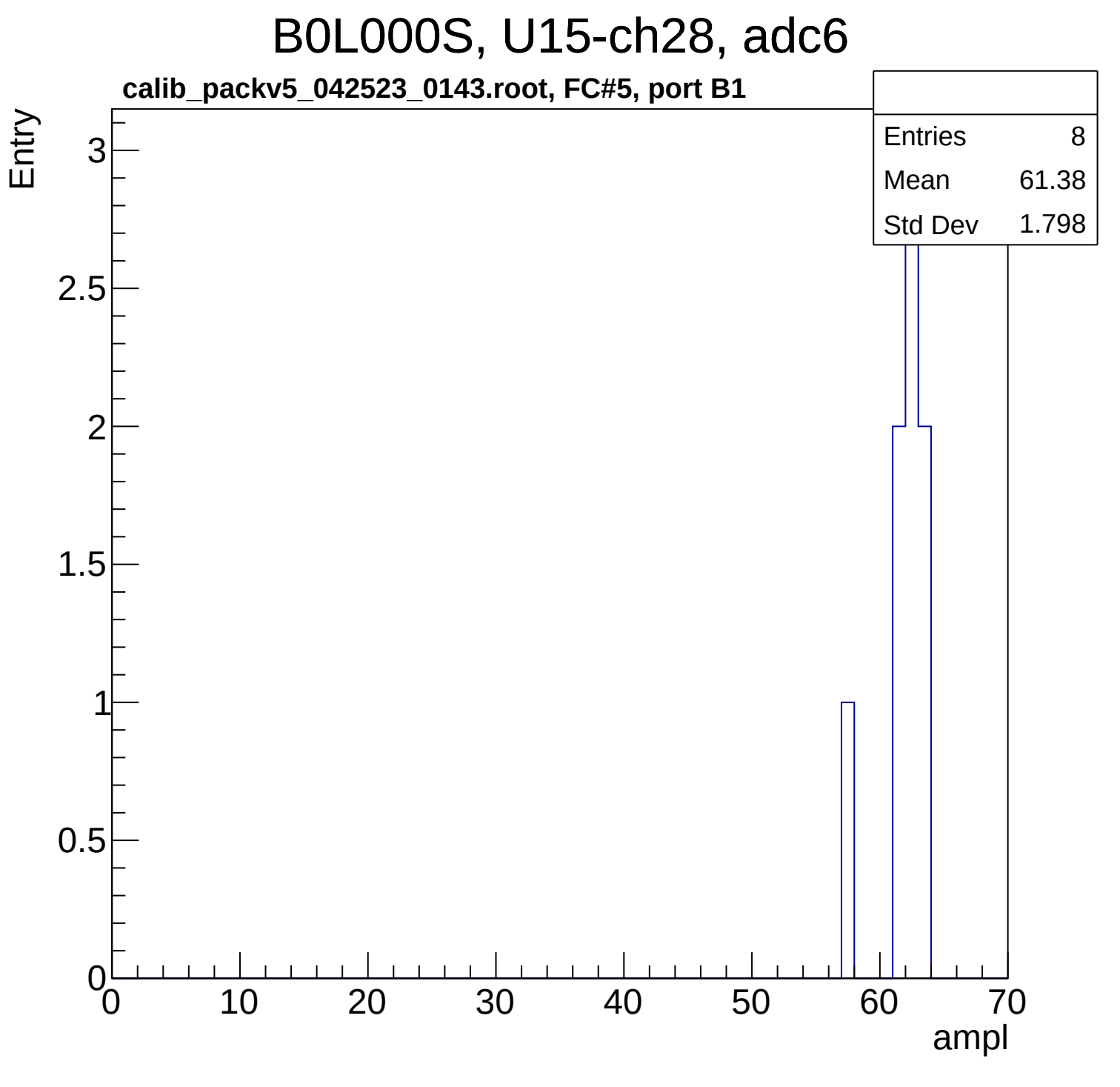
B0L000S, U15-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.38
Std Dev 1.798
Entry
ampl
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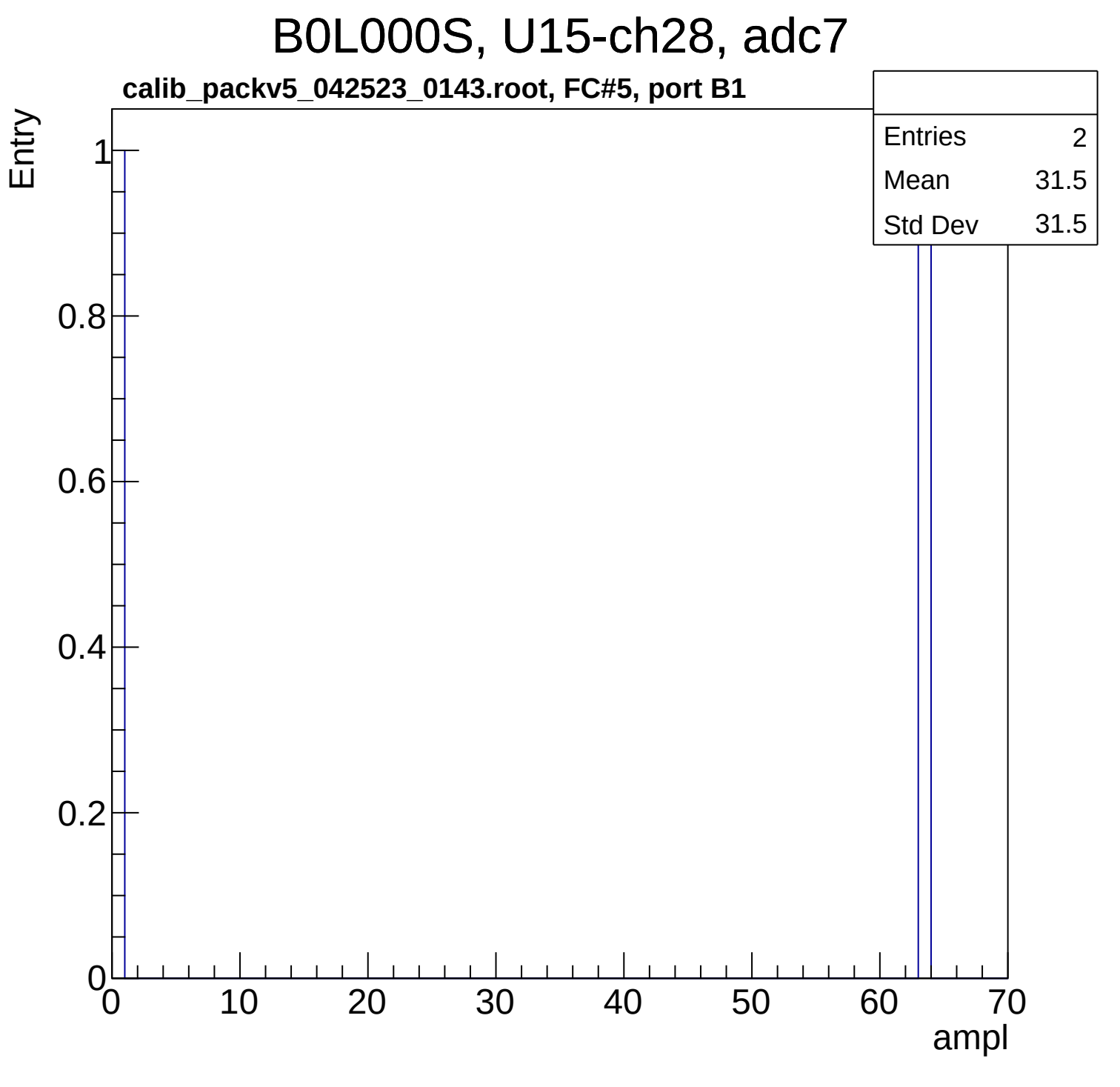
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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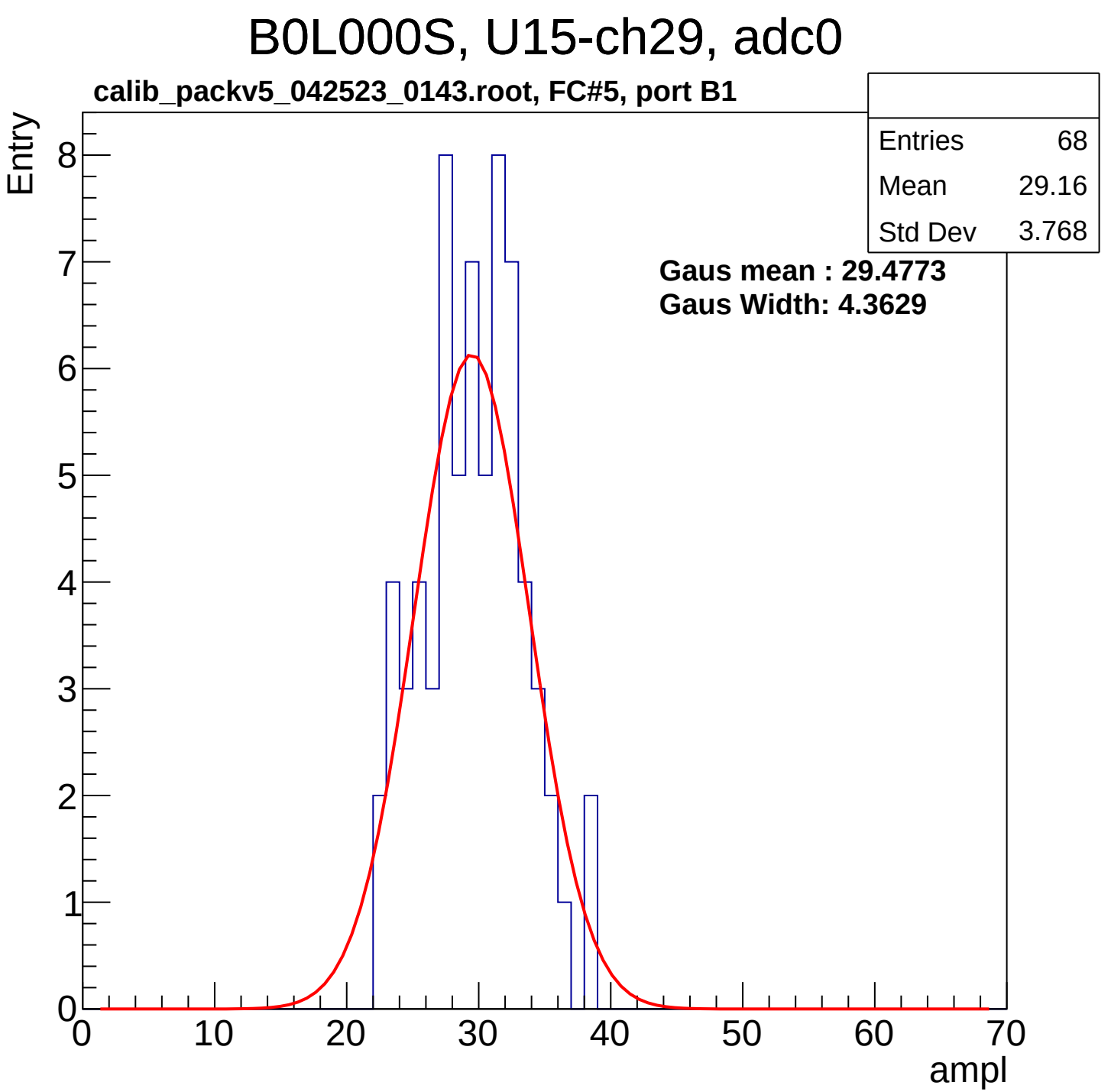
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.16
Std Dev 3.768
Gaus mean : 29.4773
Gaus Width: 4.3629
Entry
ampl
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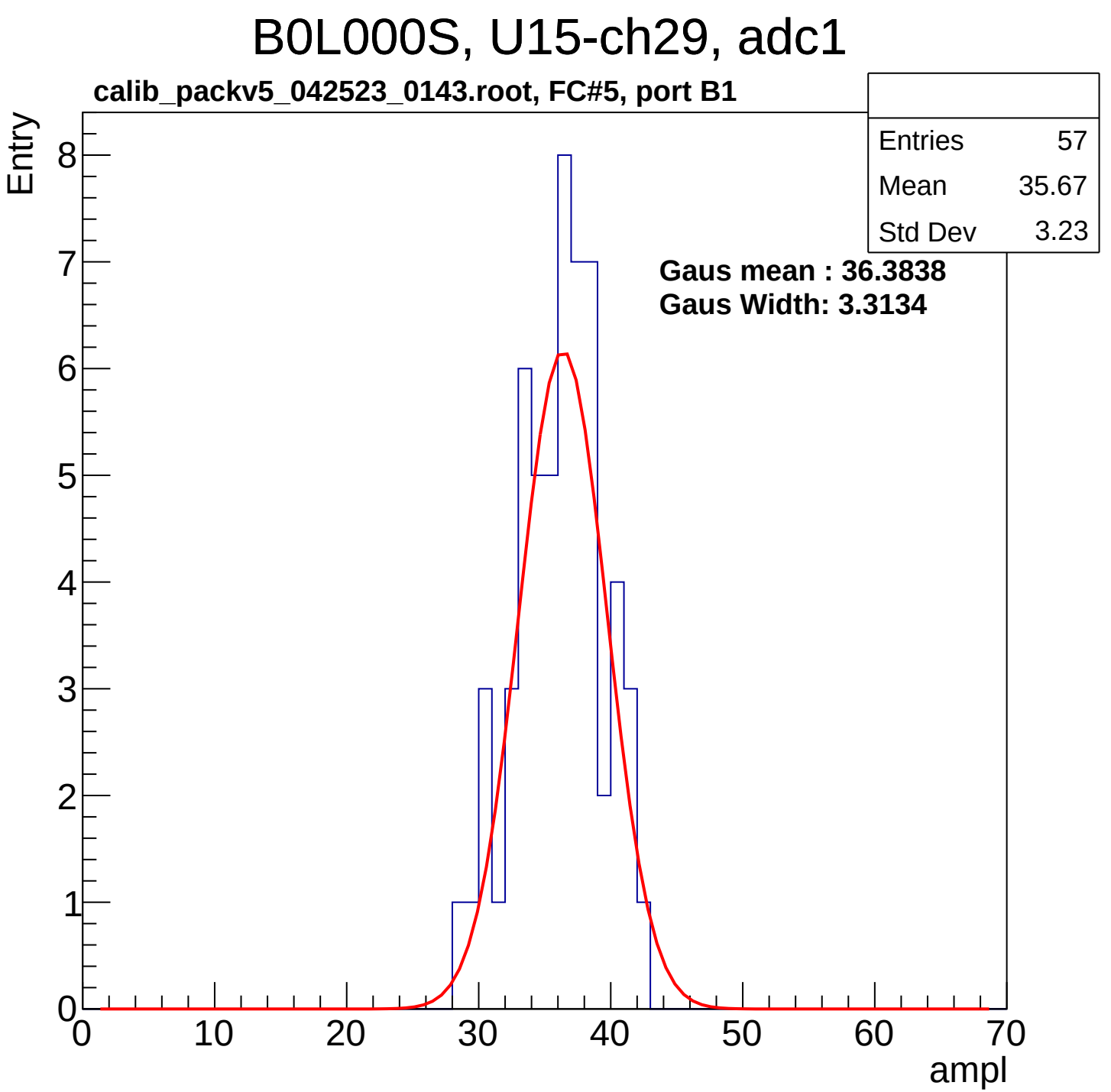
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.67
Std Dev 3.23
Gaus mean : 36.3838
Gaus Width: 3.3134
Entry
ampl
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1
2
3
4
5
6
7
8
0
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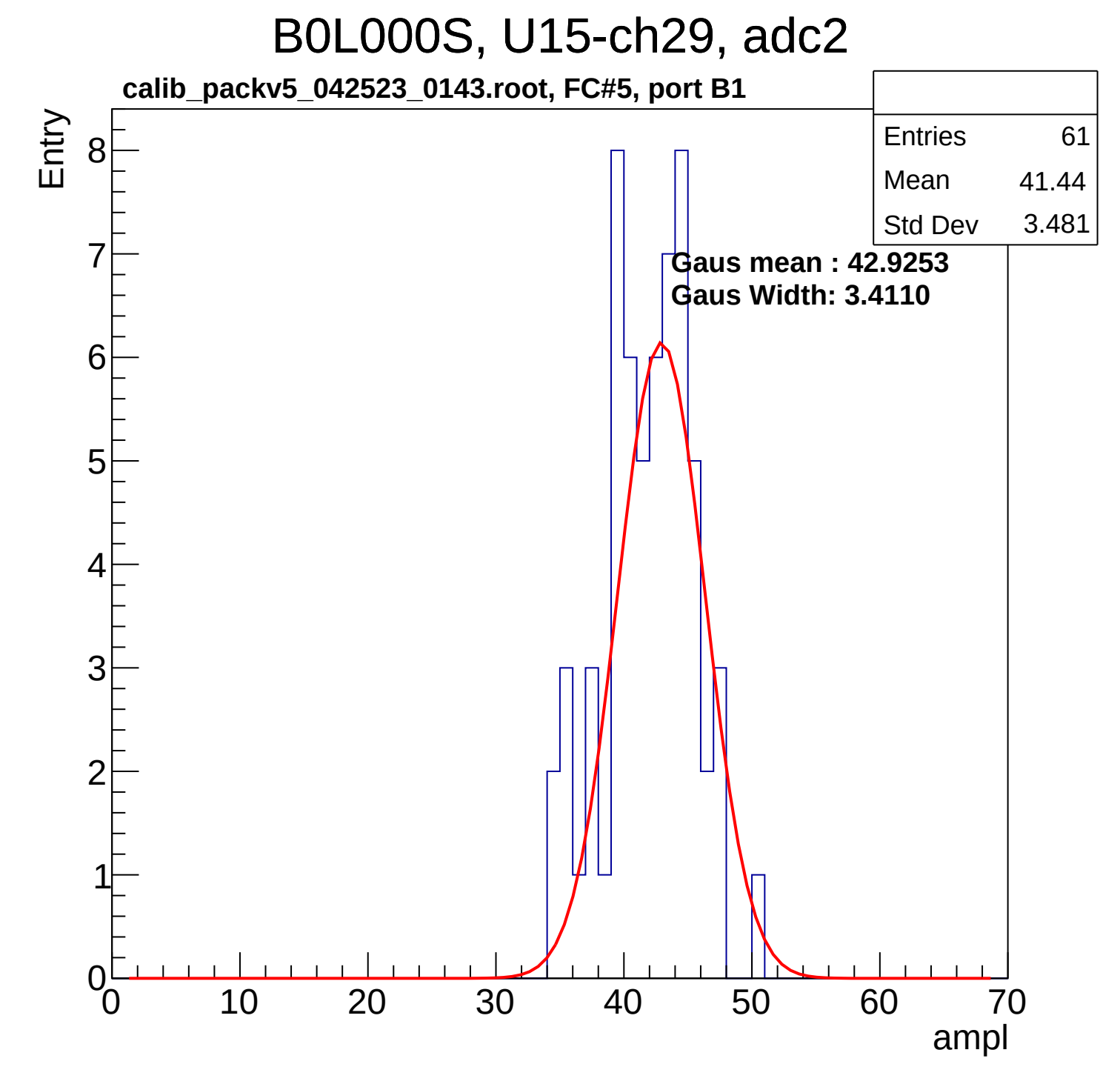
20
30
40
50
60
70


B0L000S, U15-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 41.44
Std Dev 3.481
Gaus mean : 42.9253
Gaus Width: 3.4110
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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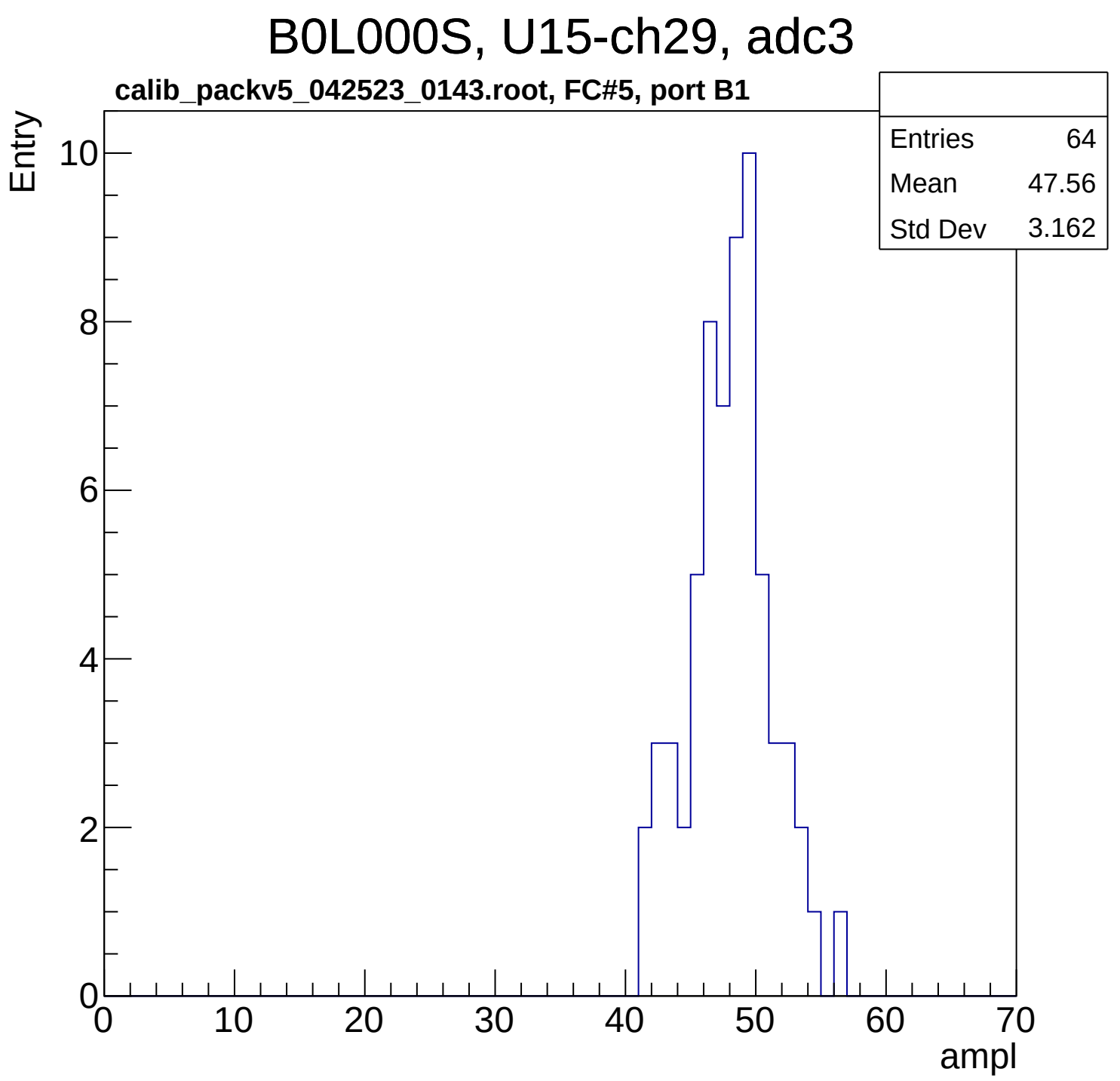
B0L000S, U15-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.56
Std Dev 3.162
Entry
ampl
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2
4
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8
10
0
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20
30
40
50
60
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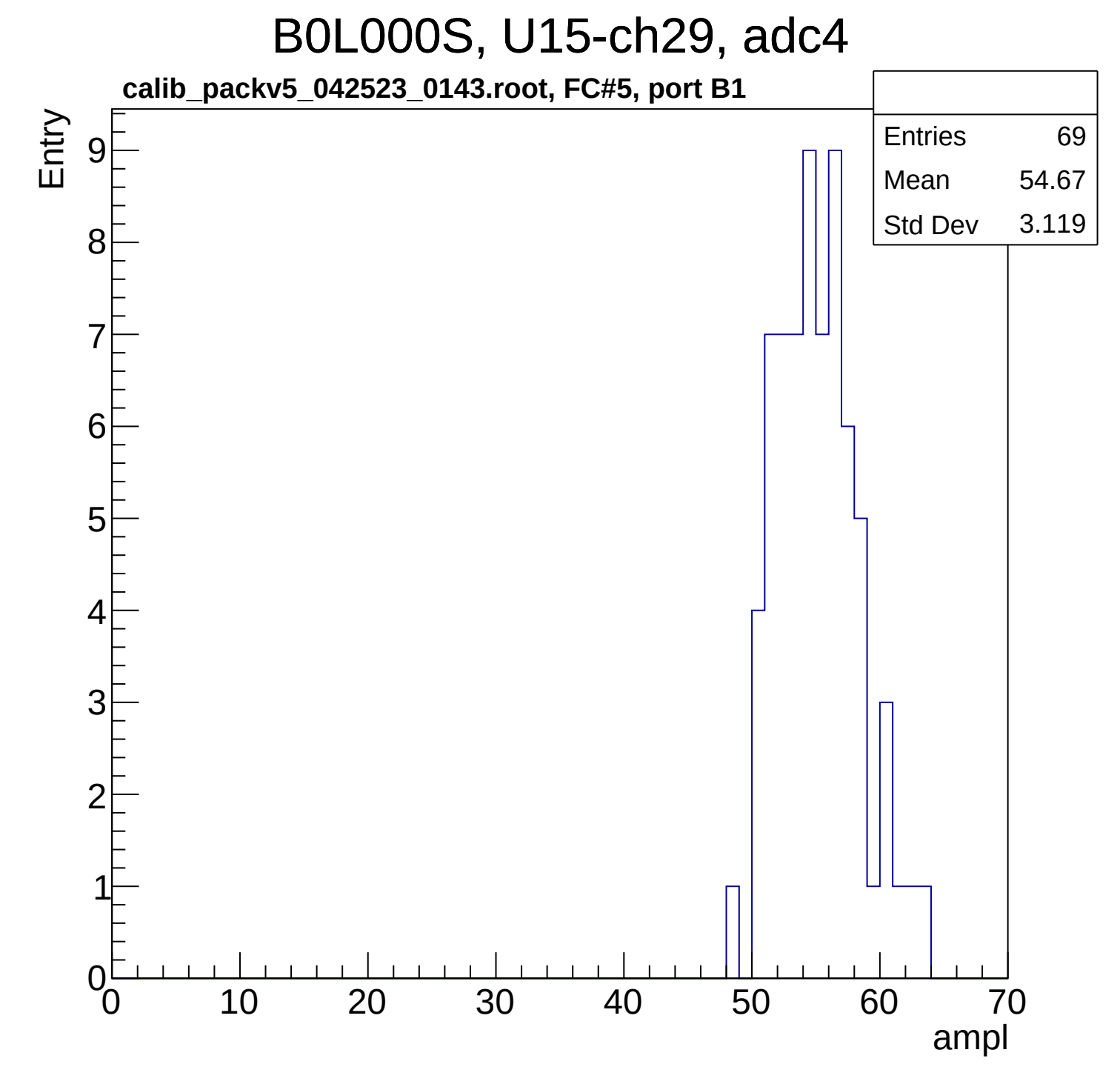
B0L000S, U15-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 54.67
Std Dev 3.119
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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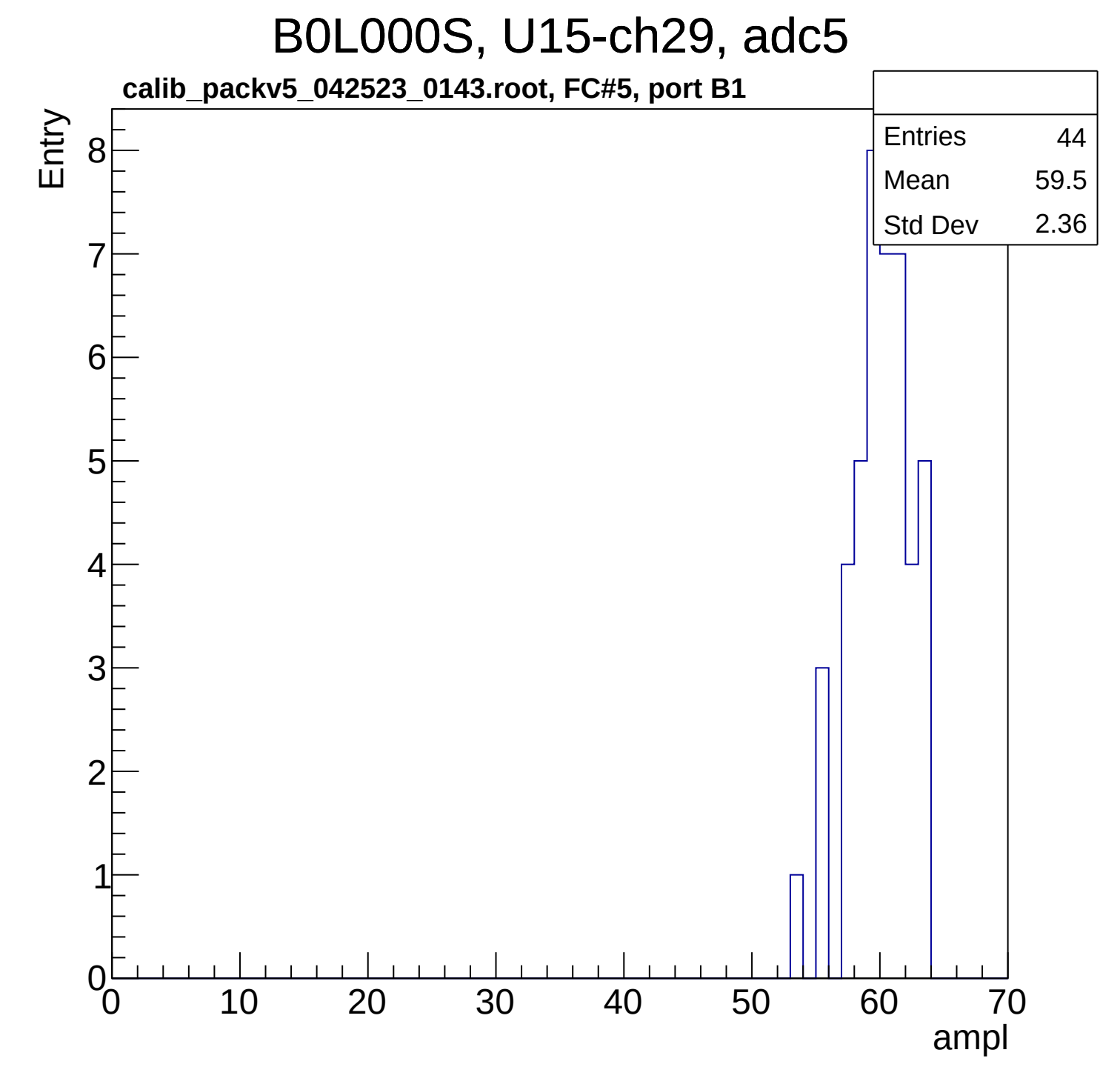
B0L000S, U15-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.5
Std Dev 2.36
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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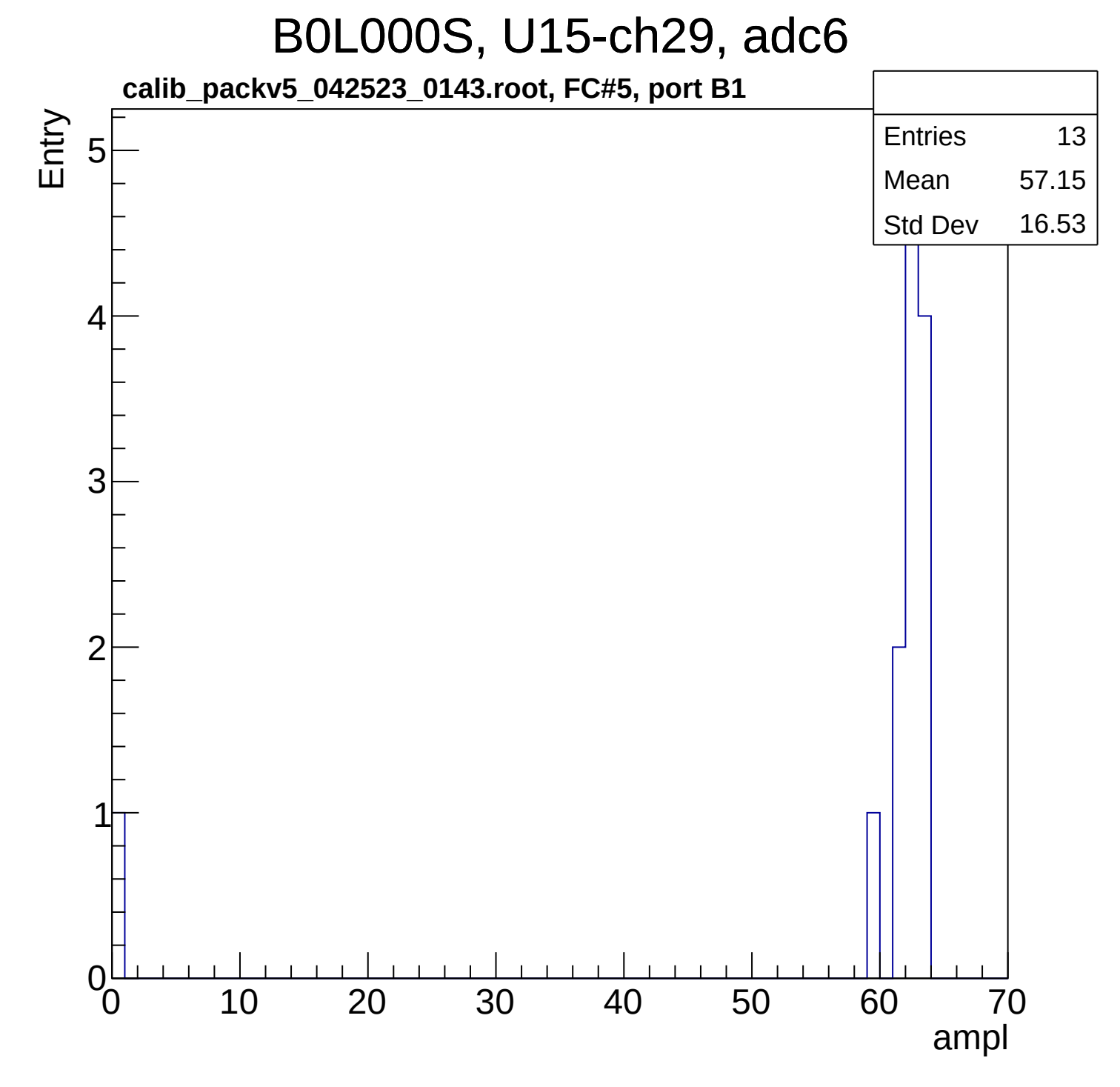
B0L000S, U15-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.15
Std Dev 16.53
Entry
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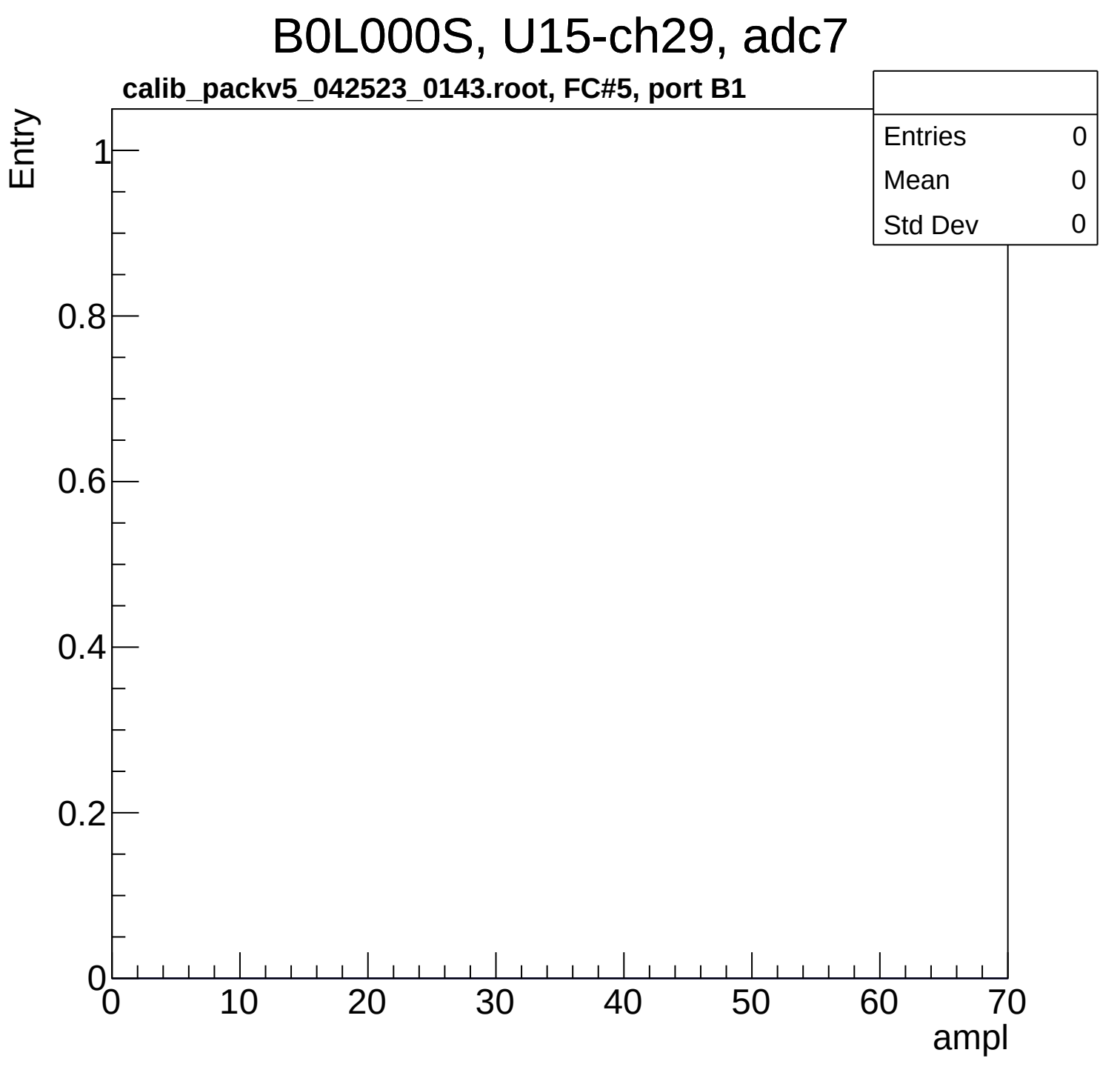
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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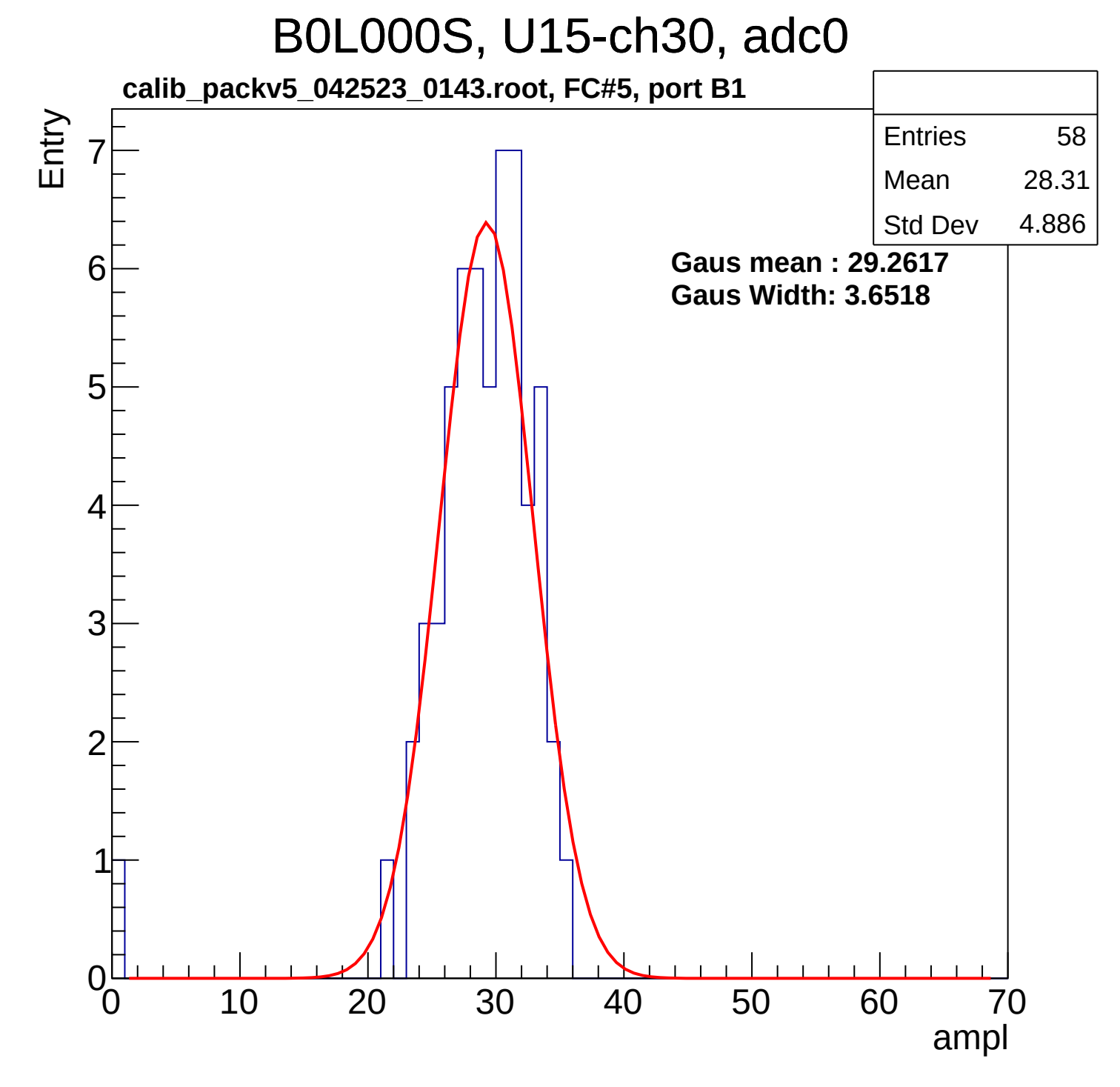
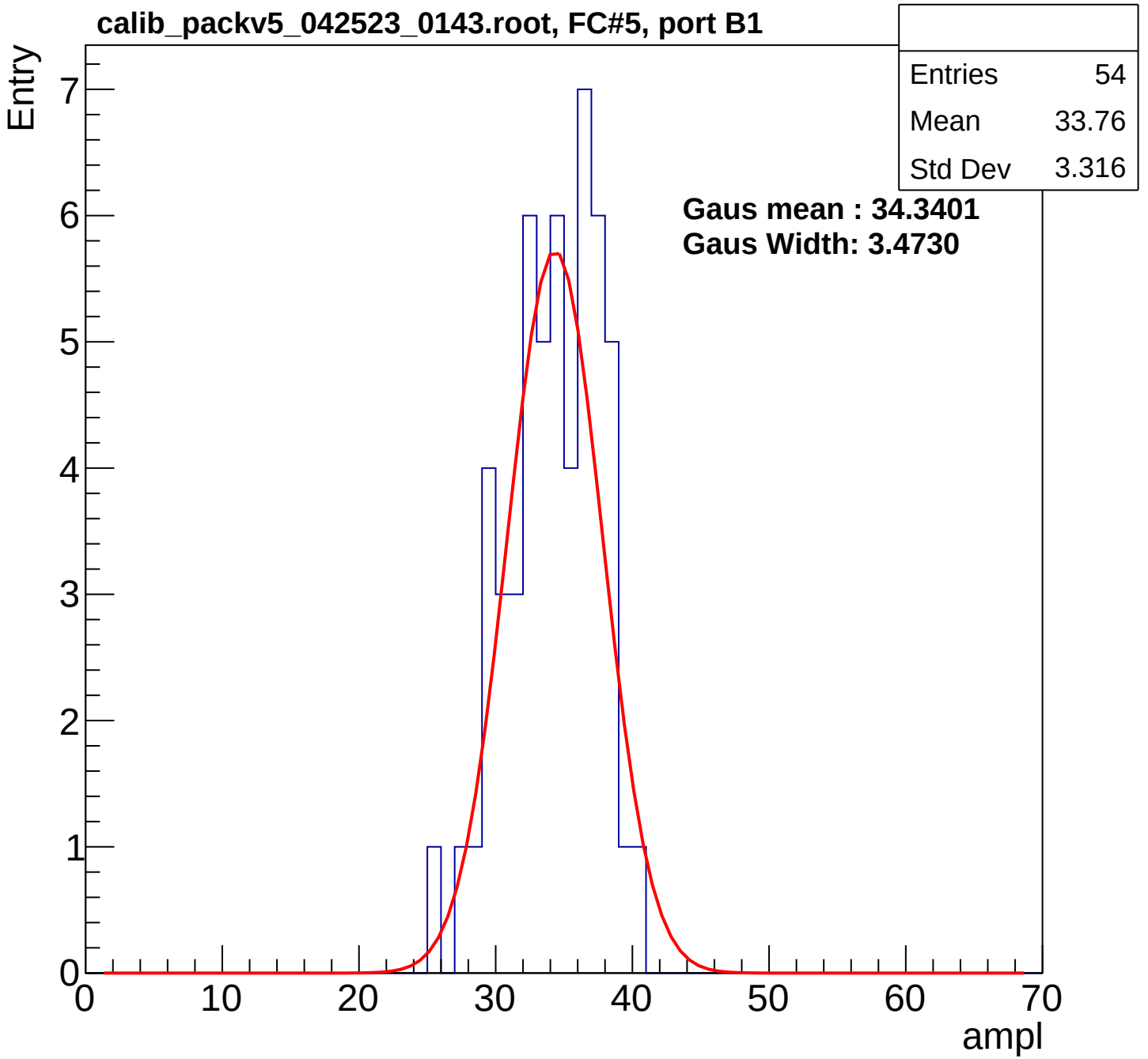
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 28.31
Std Dev 4.886
Gaus mean : 29.2617
Gaus Width: 3.6518
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 33.76
Std Dev 3.316
Gaus mean : 34.3401
Gaus Width: 3.4730
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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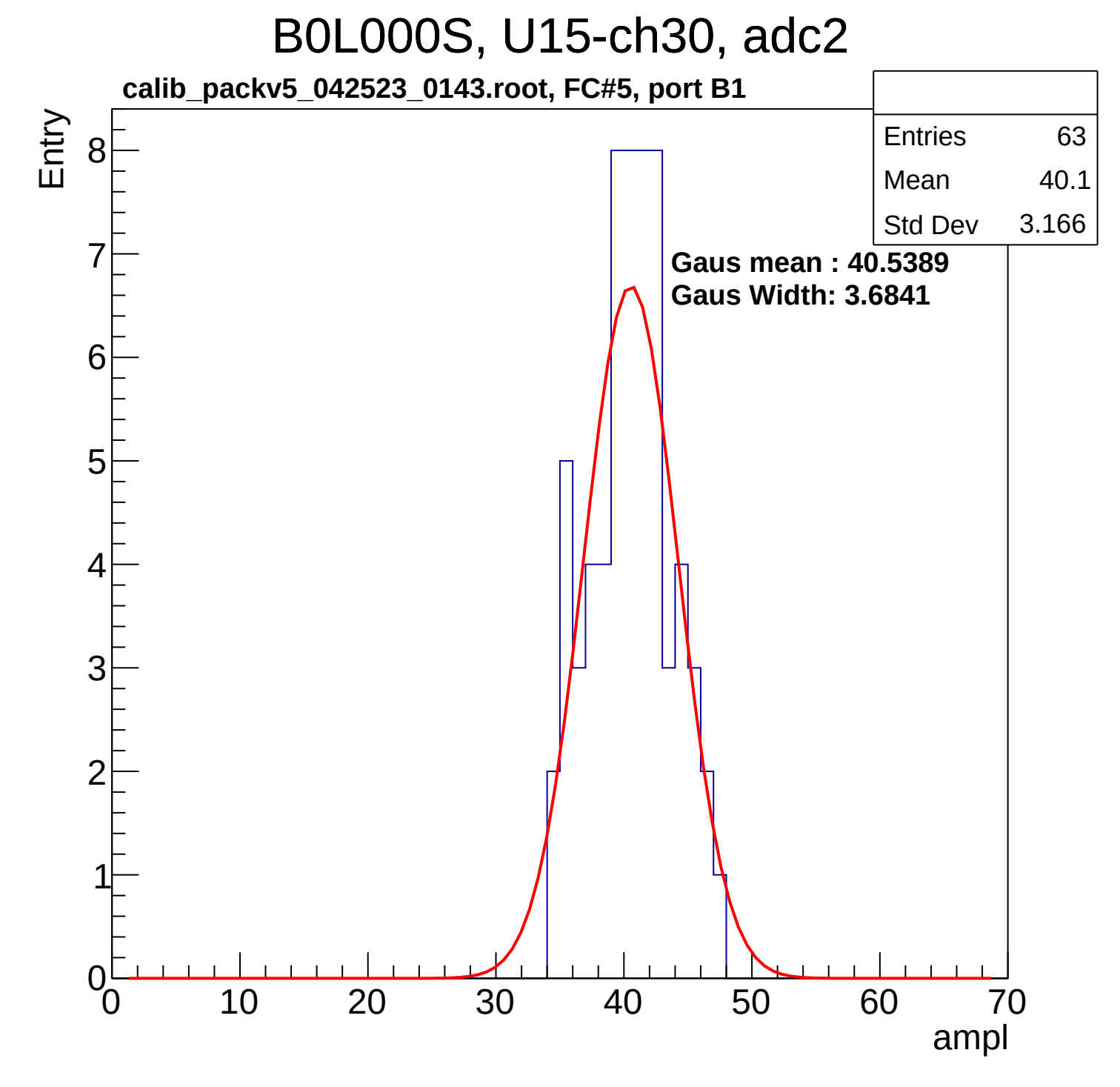
B0L000S, U15-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 40.1
Std Dev 3.166
Gaus mean : 40.5389
Gaus Width: 3.6841
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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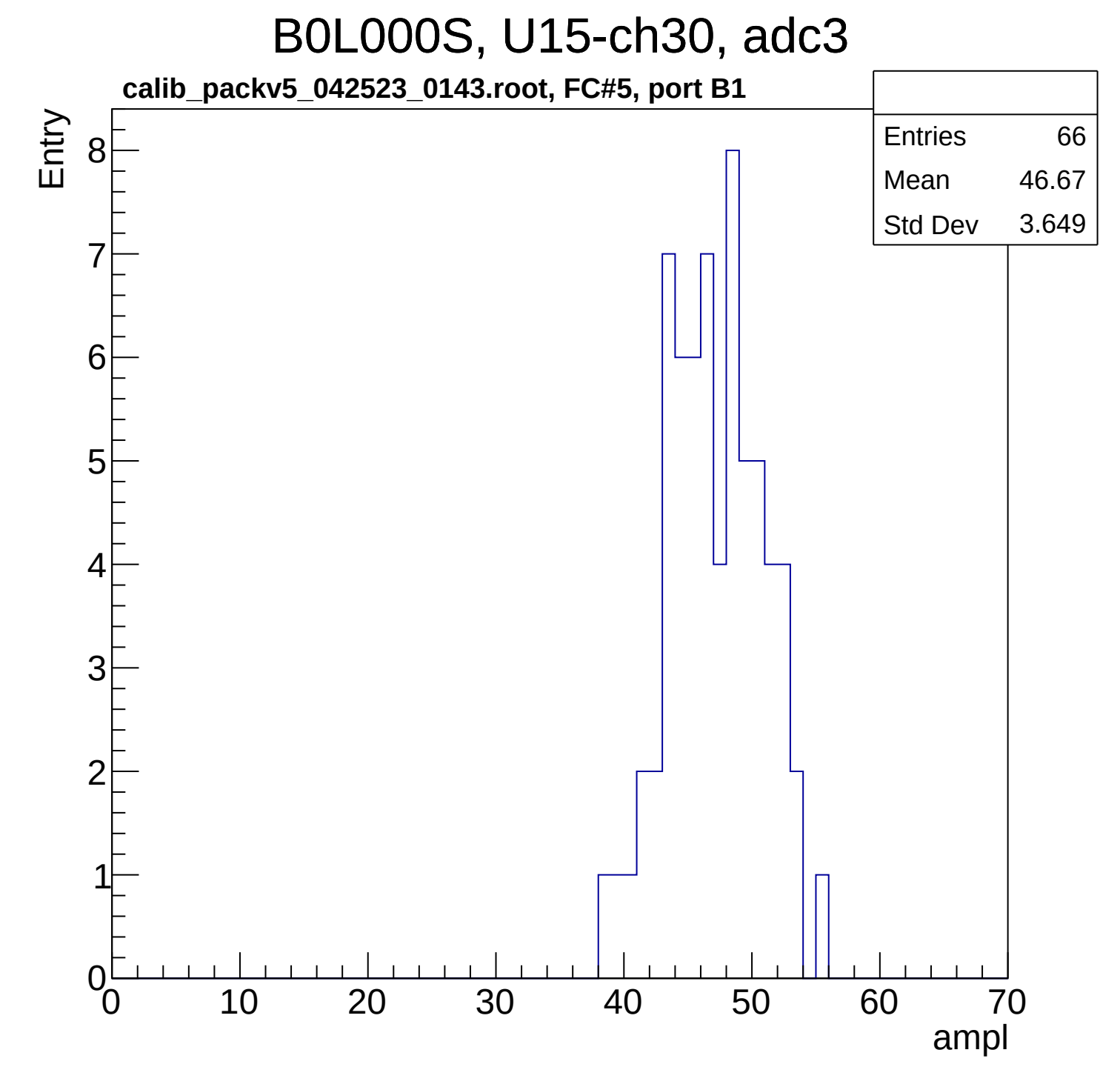
B0L000S, U15-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 46.67
Std Dev 3.649
Entry
ampl
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3
4
5
6
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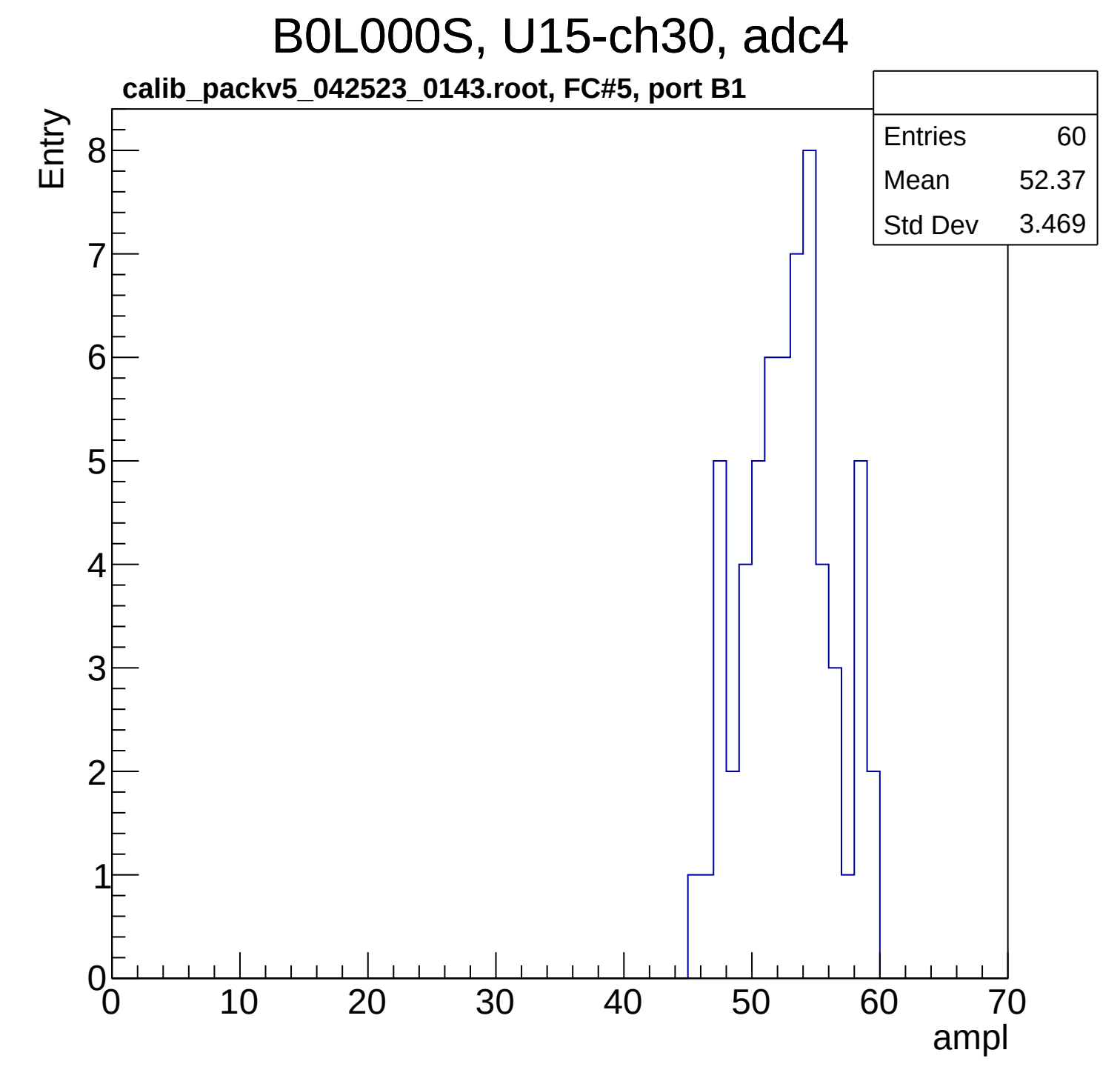
B0L000S, U15-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 52.37
Std Dev 3.469
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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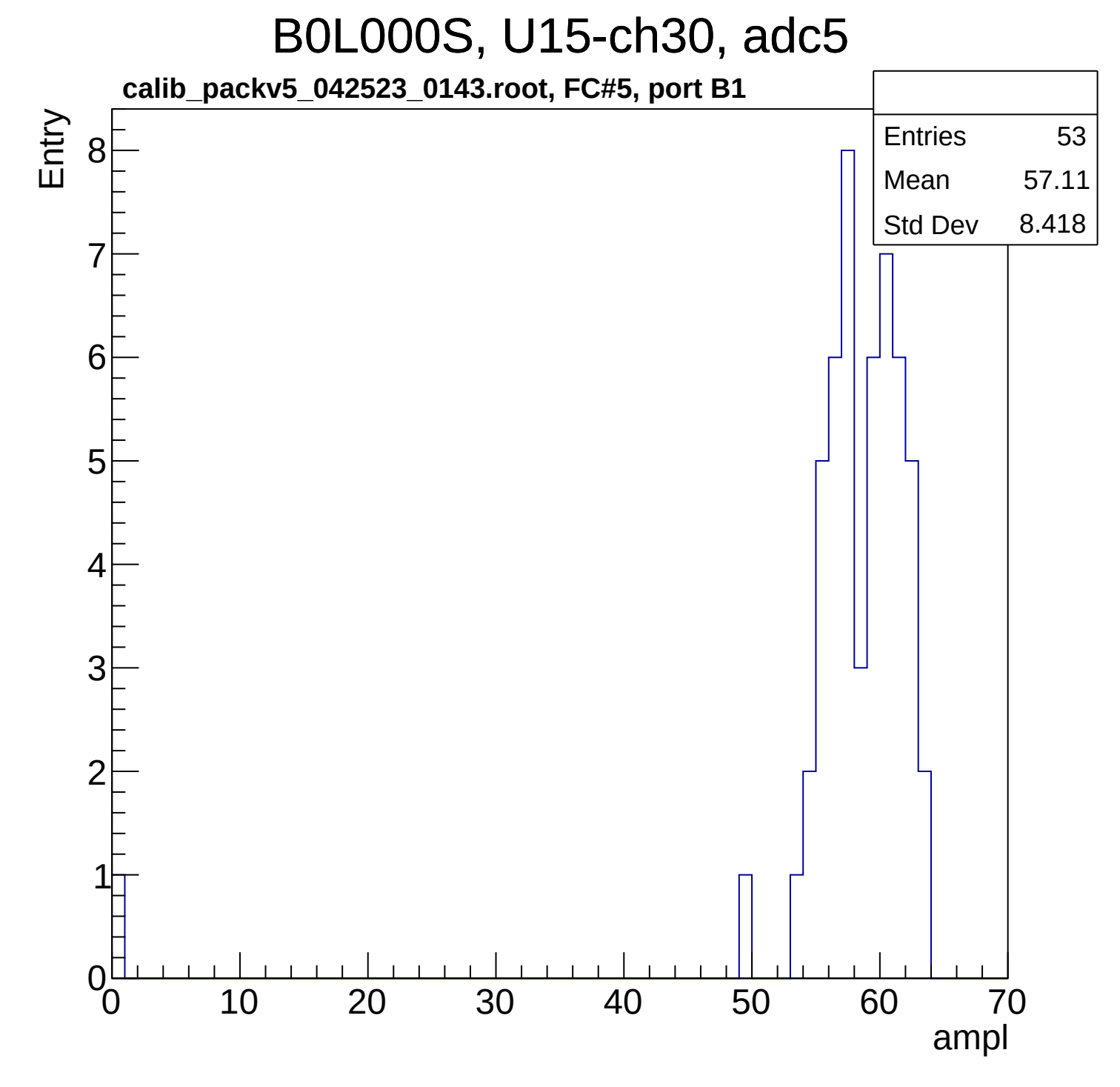
B0L000S, U15-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.11
Std Dev 8.418
Entry
ampl
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2
3
4
5
6
7
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0
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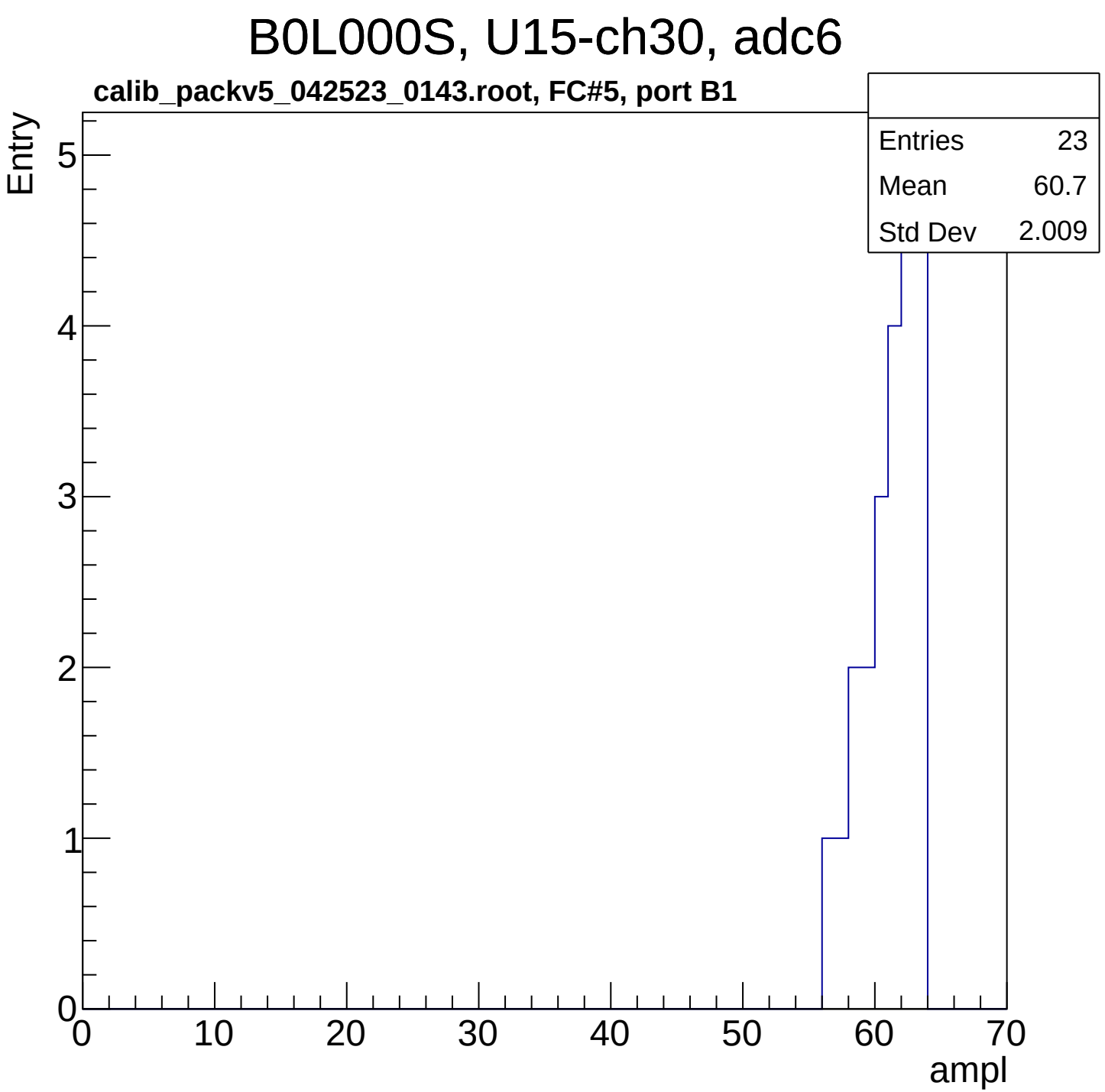
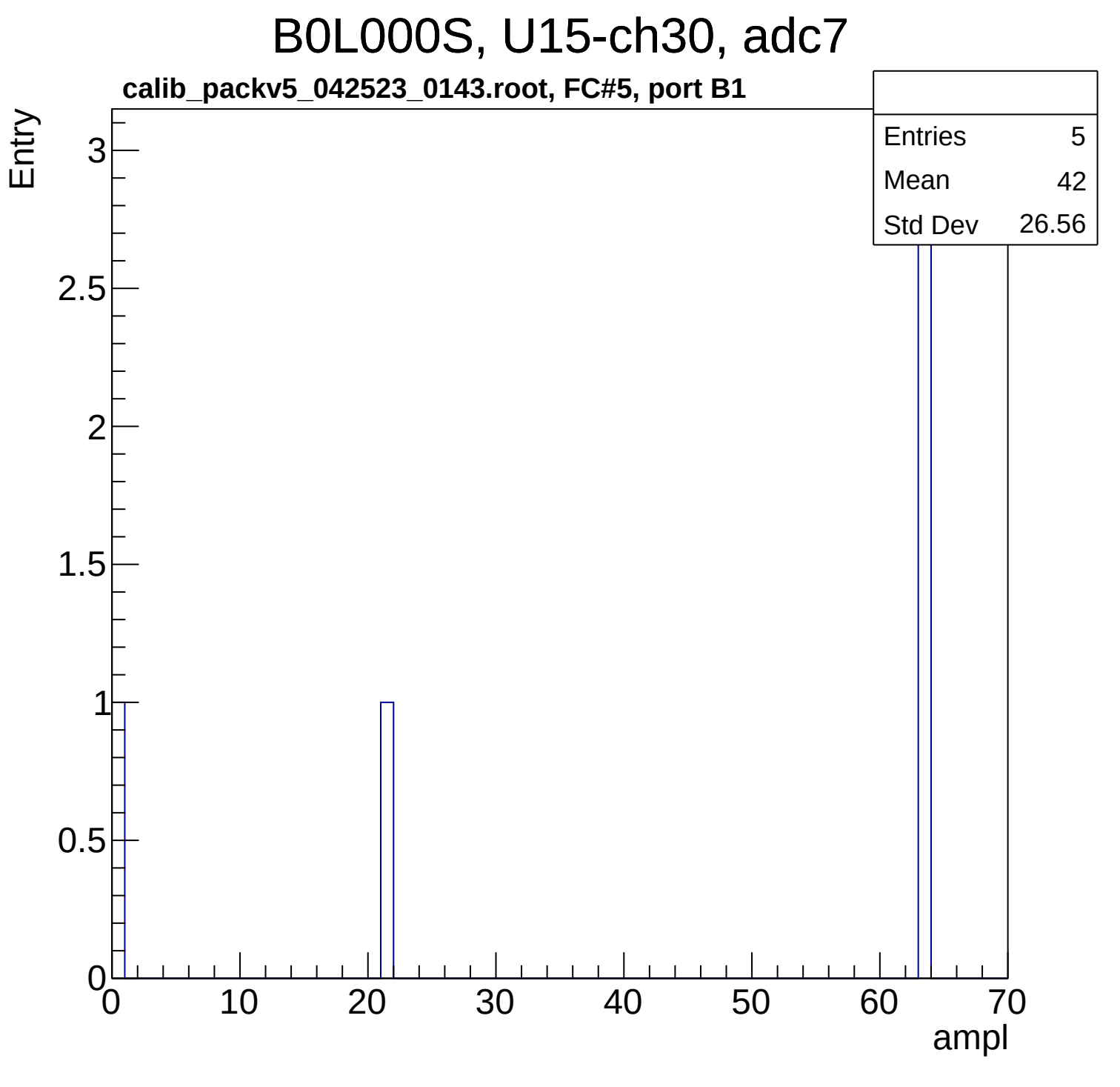

B0L000S, U15-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 60.7
Std Dev 2.009
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 42
Std Dev 26.56
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
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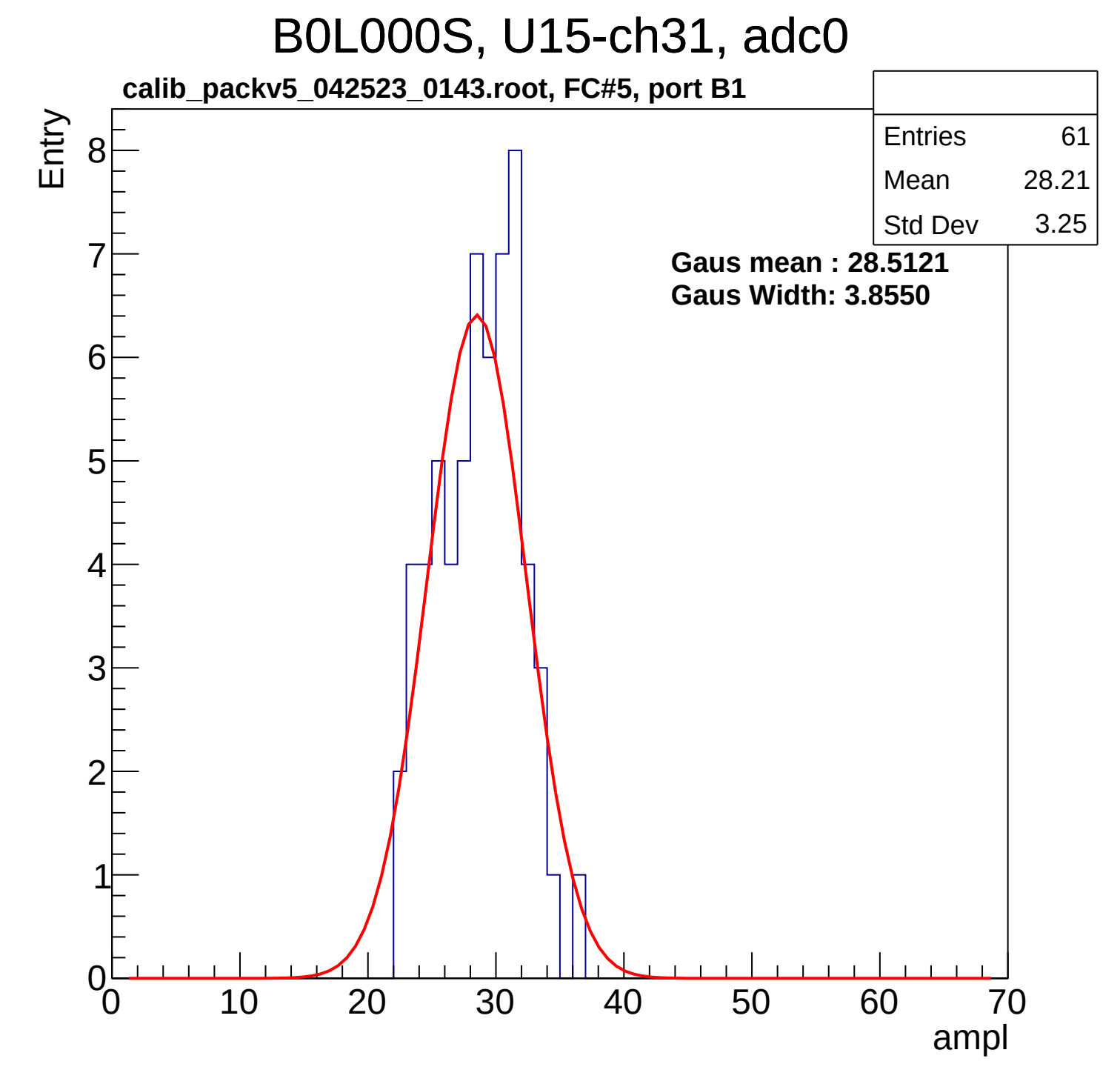
60
70


B0L000S, U15-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.21
Std Dev 3.25
Gaus mean : 28.5121
Gaus Width: 3.8550
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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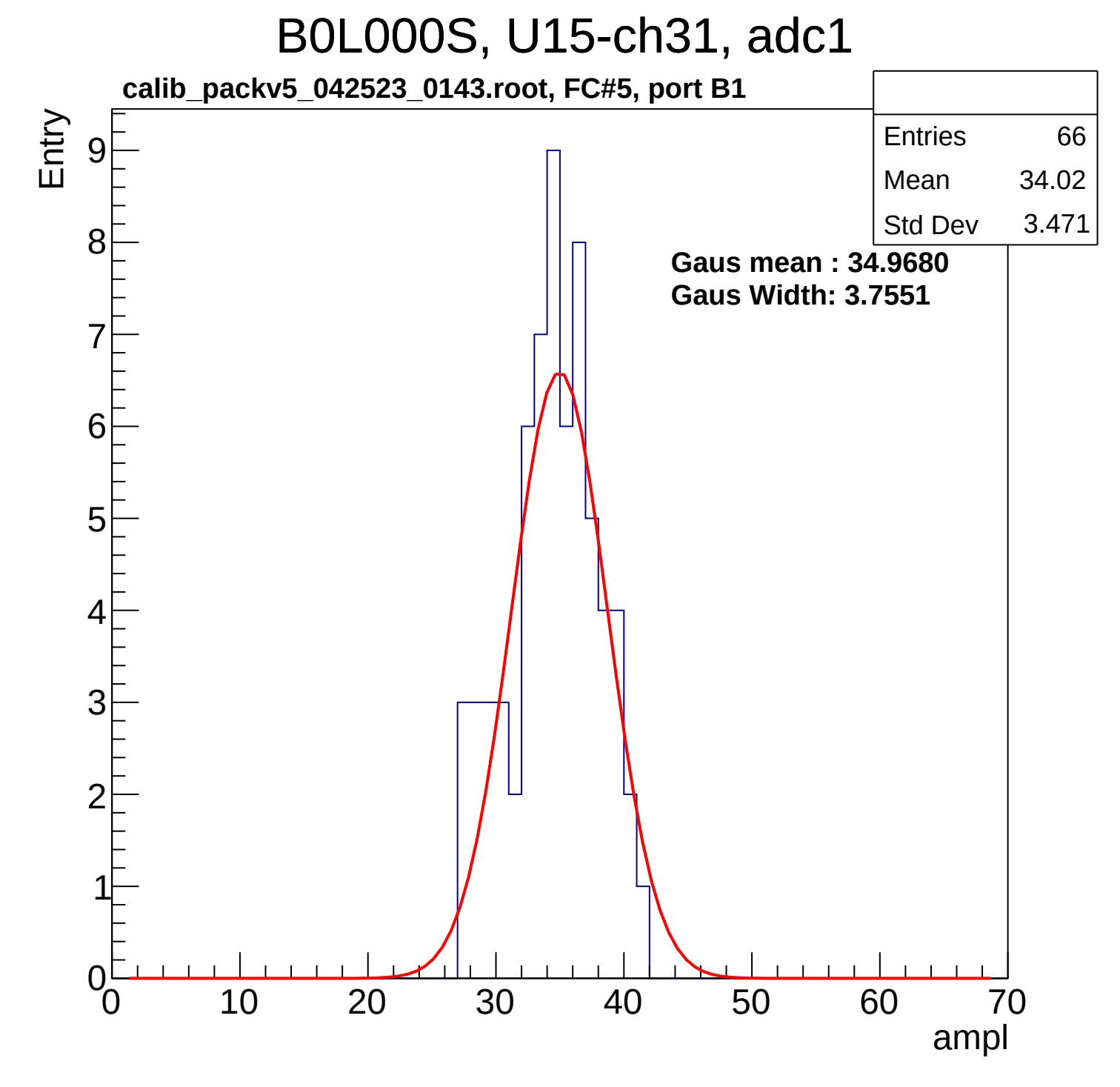
B0L000S, U15-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 34.02
Std Dev 3.471
Gaus mean : 34.9680
Gaus Width: 3.7551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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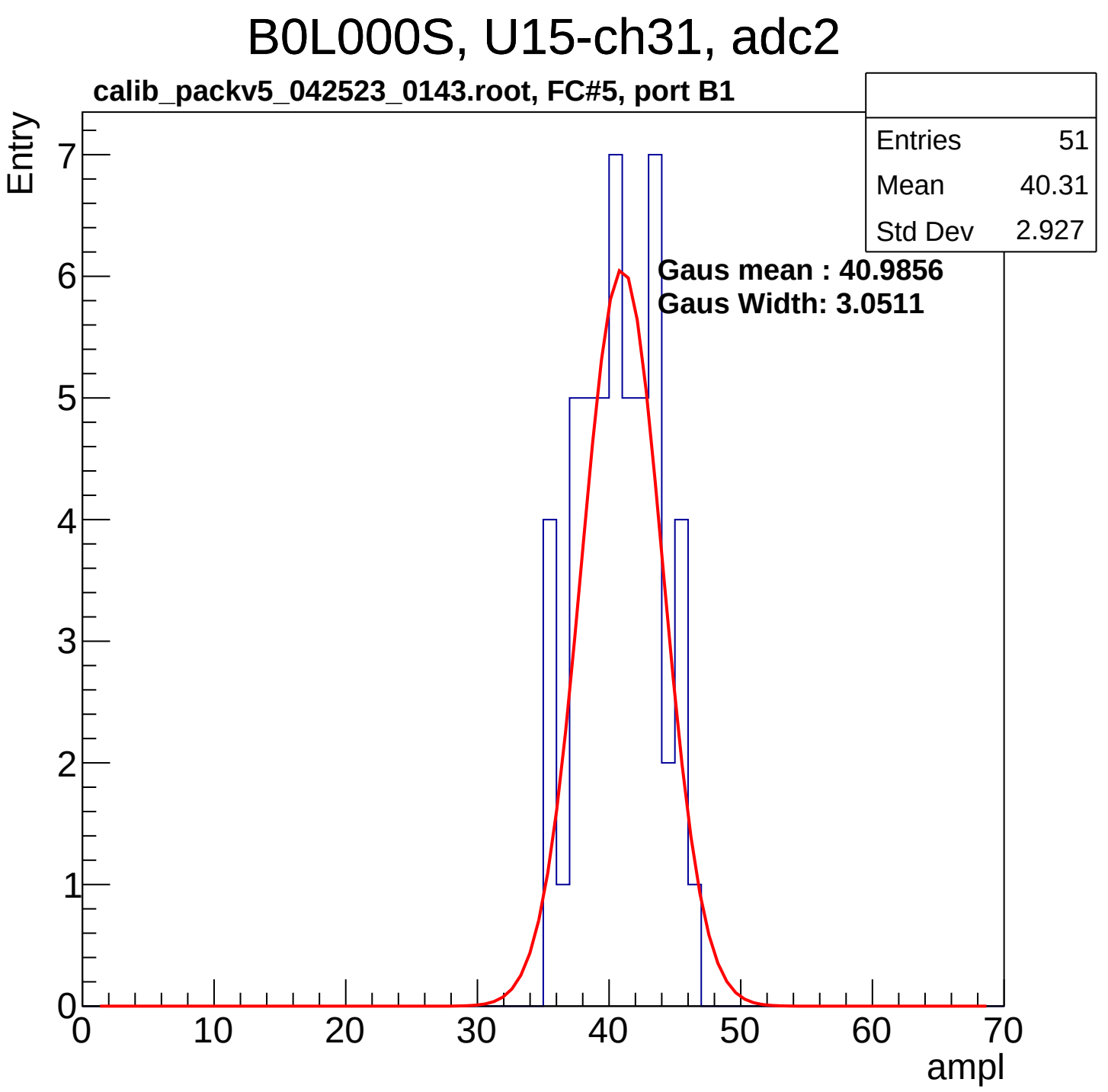
B0L000S, U15-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 40.31
Std Dev 2.927
Gaus mean : 40.9856
Gaus Width: 3.0511
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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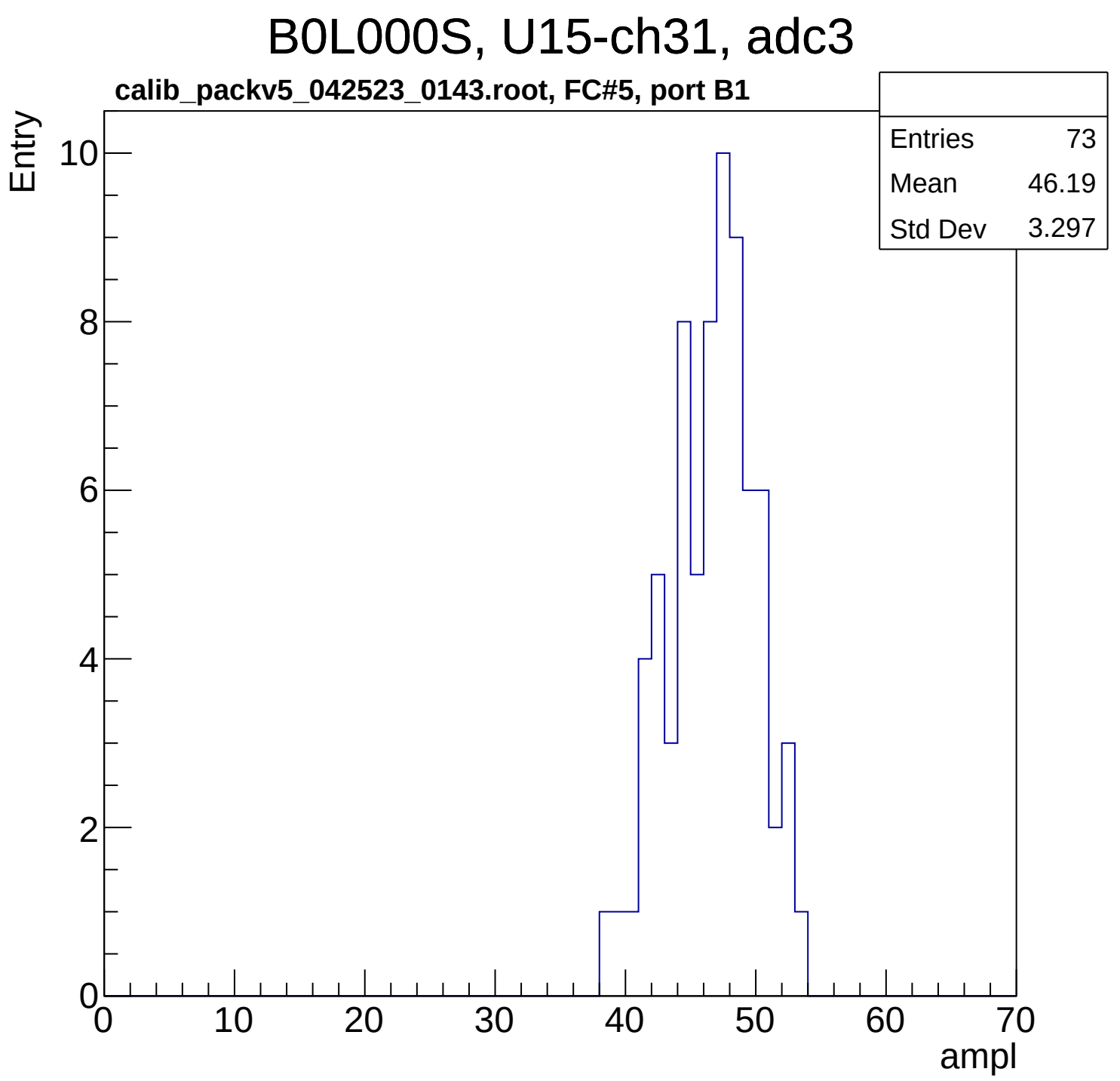
B0L000S, U15-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 46.19
Std Dev 3.297
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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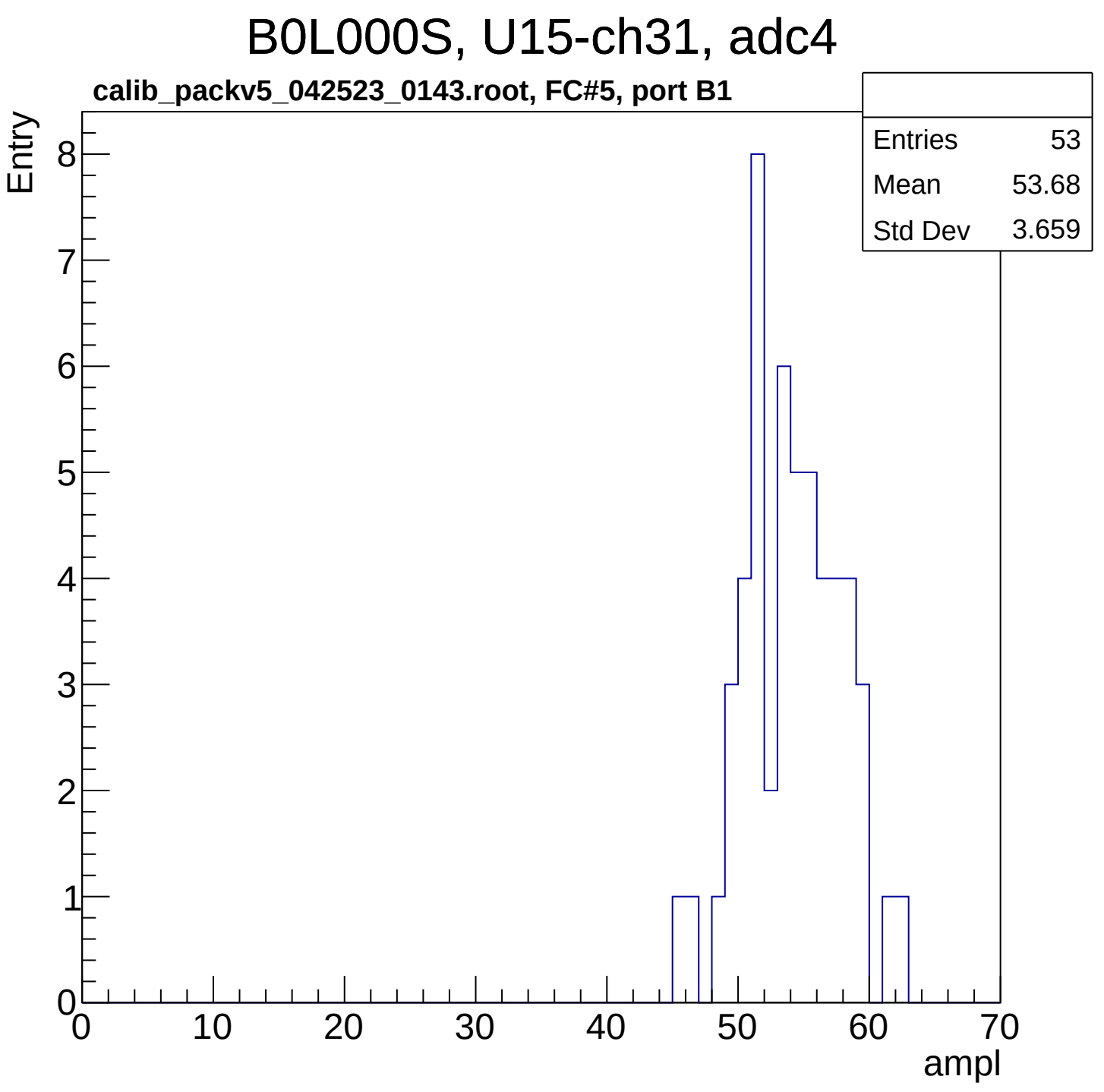
B0L000S, U15-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.68
Std Dev 3.659
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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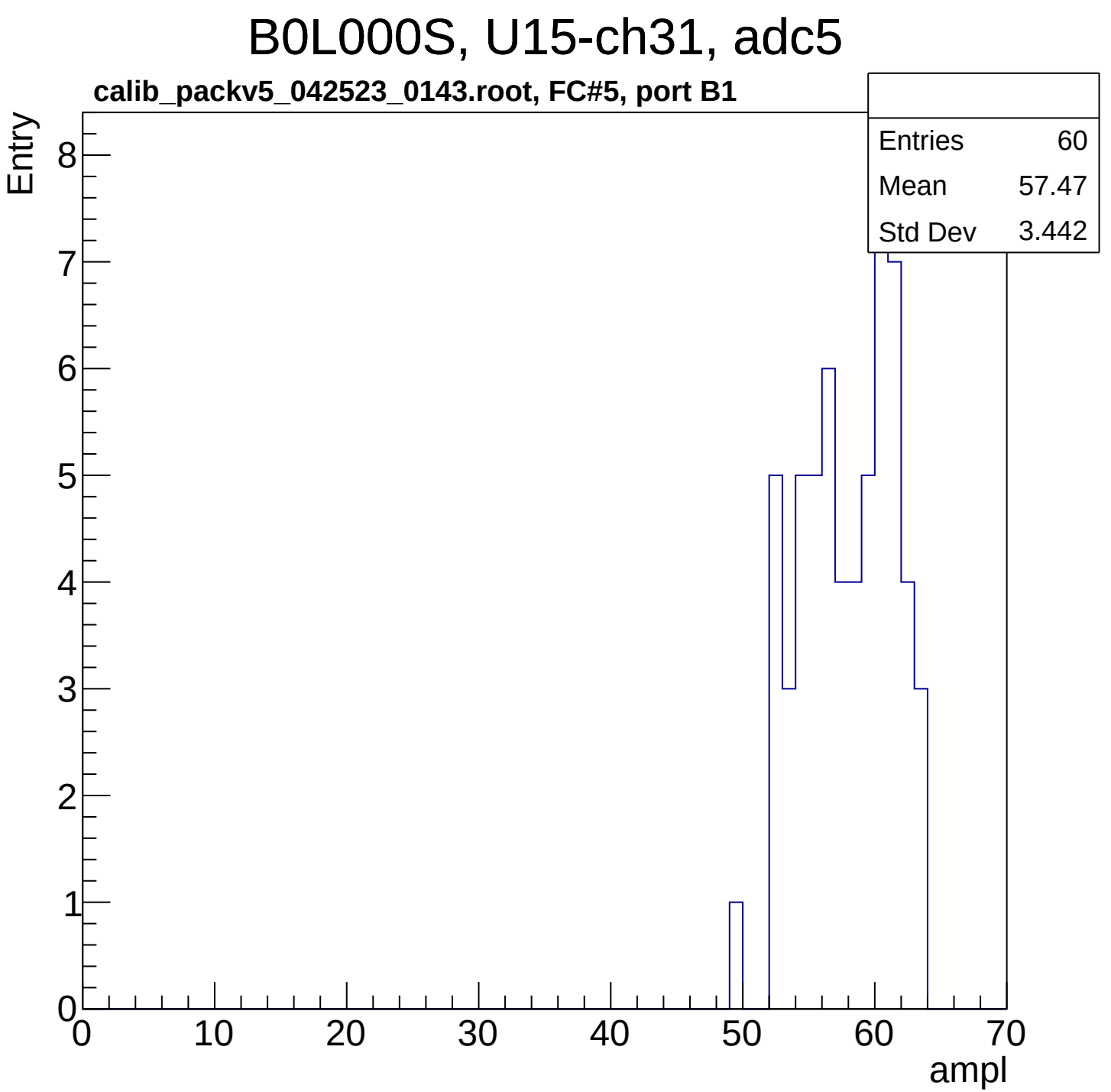
B0L000S, U15-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.47
Std Dev 3.442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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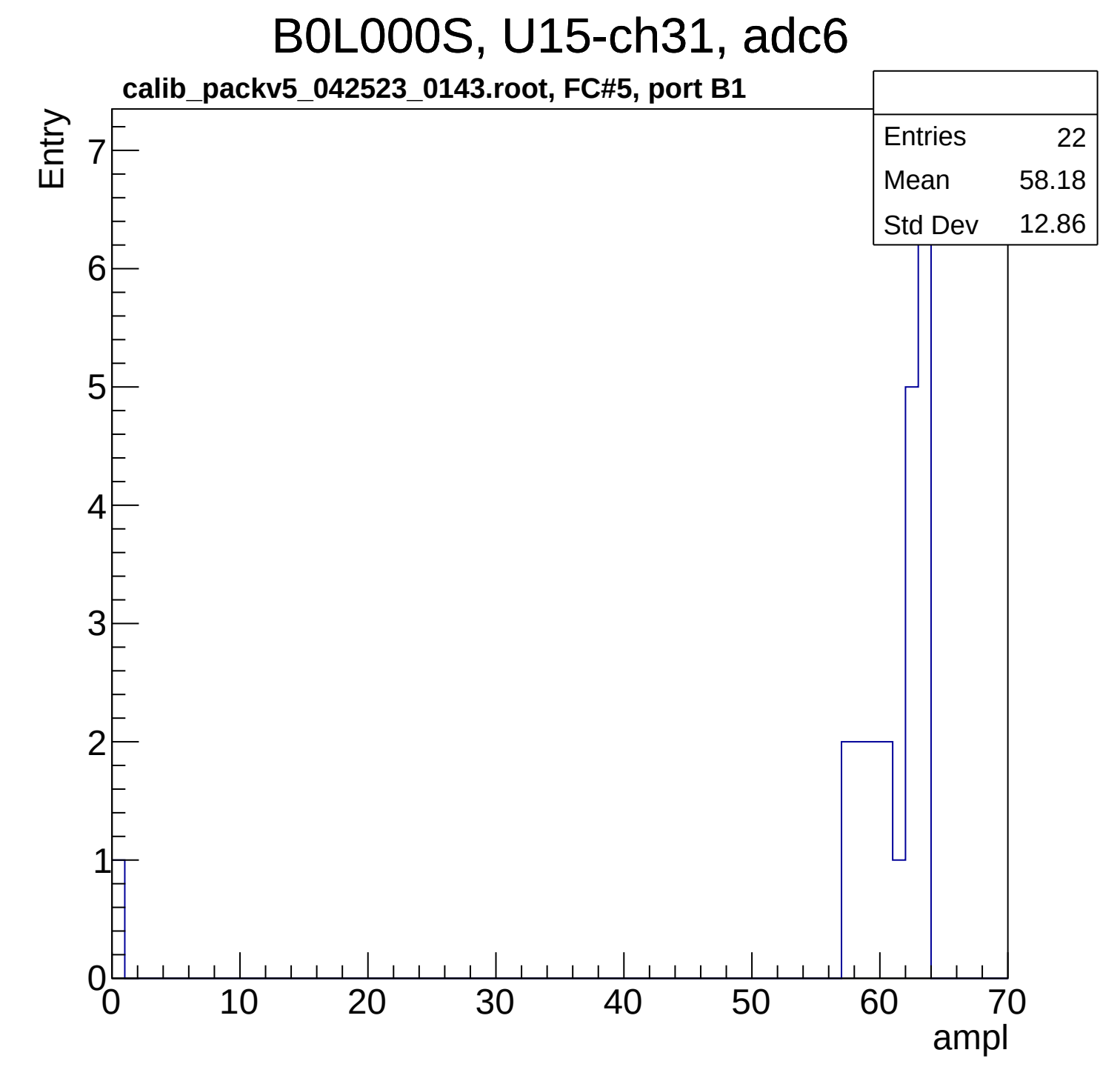
B0L000S, U15-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.18
Std Dev 12.86
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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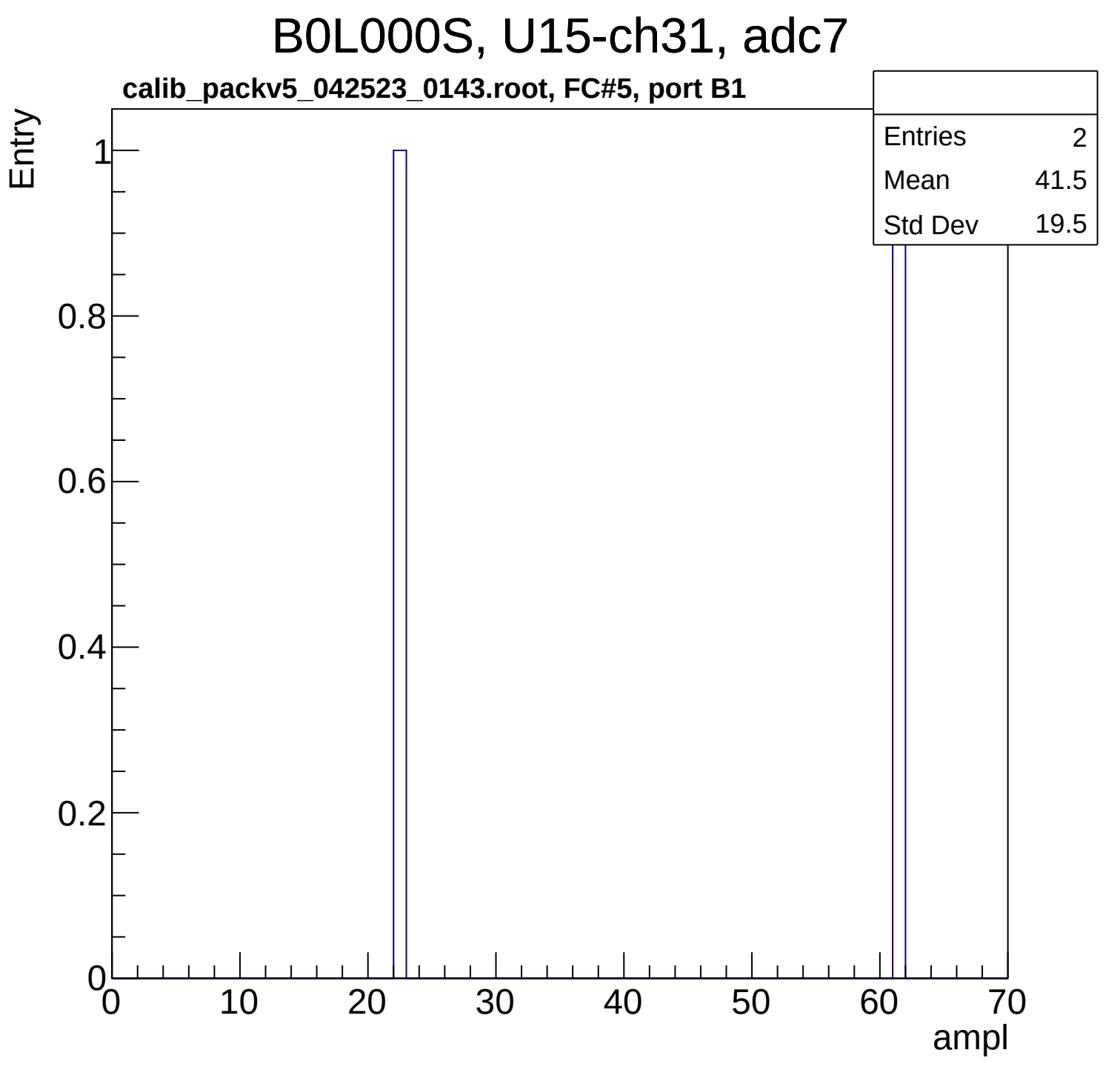
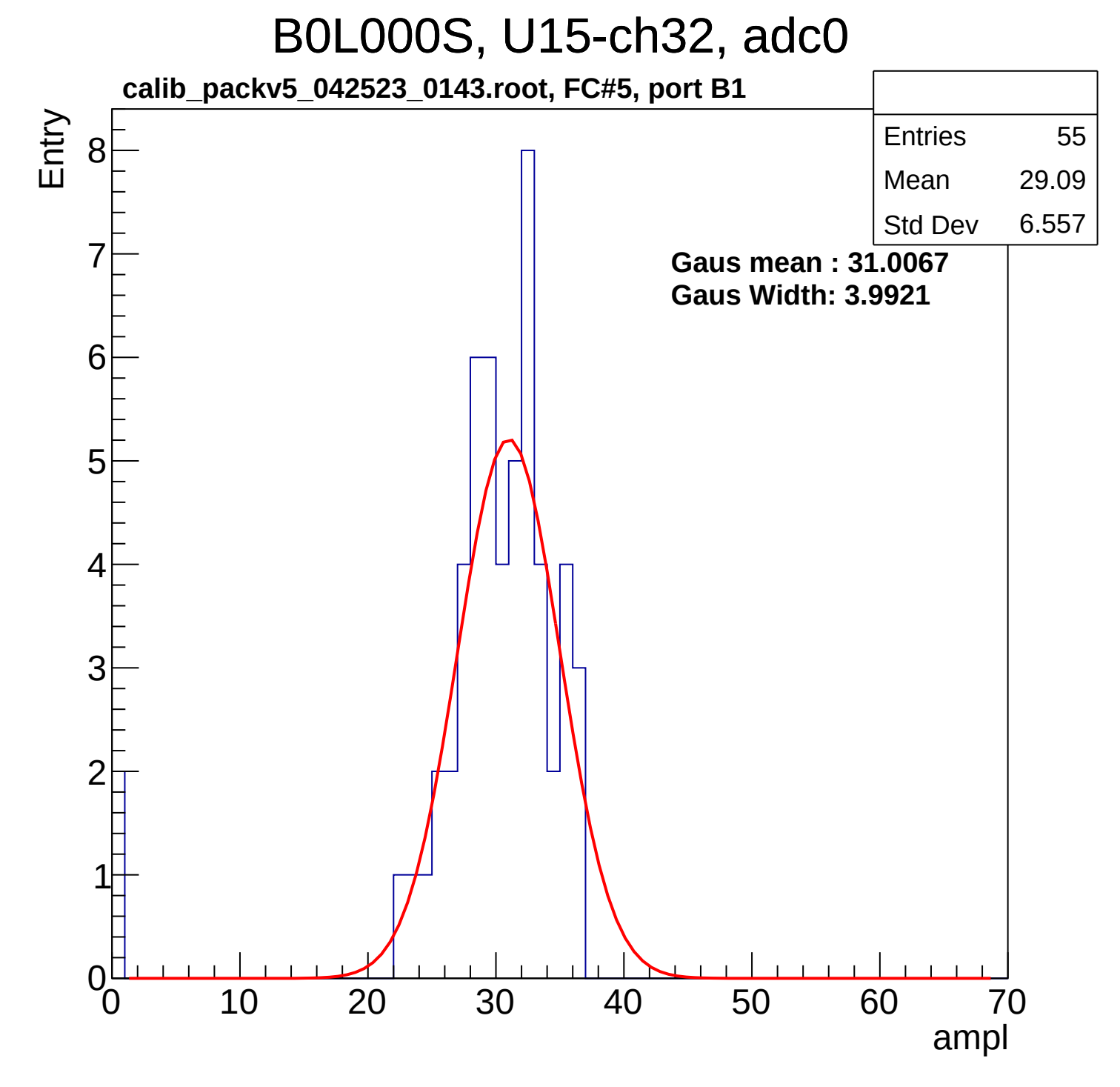

B0L000S, U15-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 41.5
Std Dev 19.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U15-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.09
Std Dev 6.557
Gaus mean : 31.0067
Gaus Width: 3.9921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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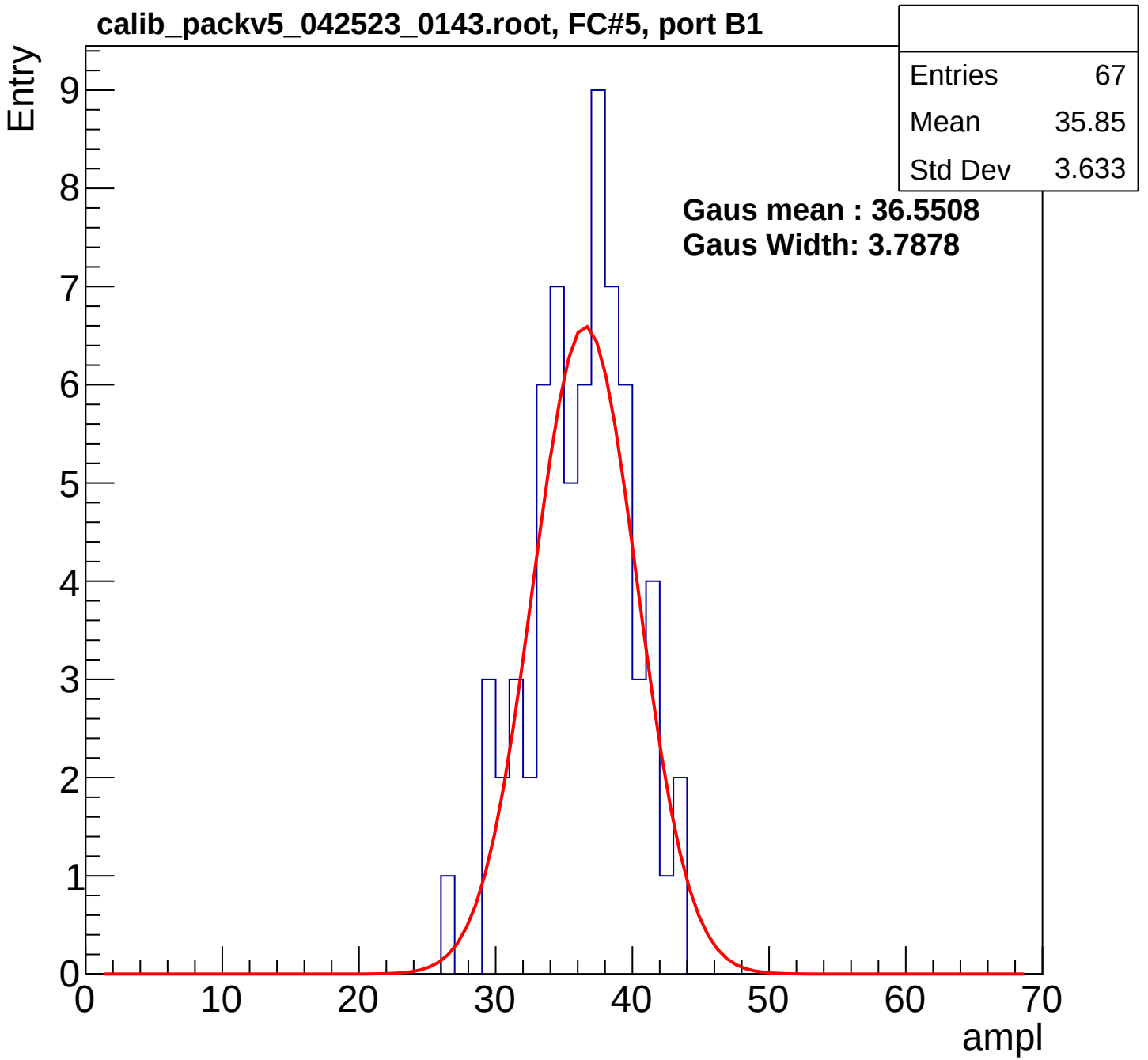
B0L000S, U15-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.85
Std Dev 3.633
Gaus mean : 36.5508
Gaus Width: 3.7878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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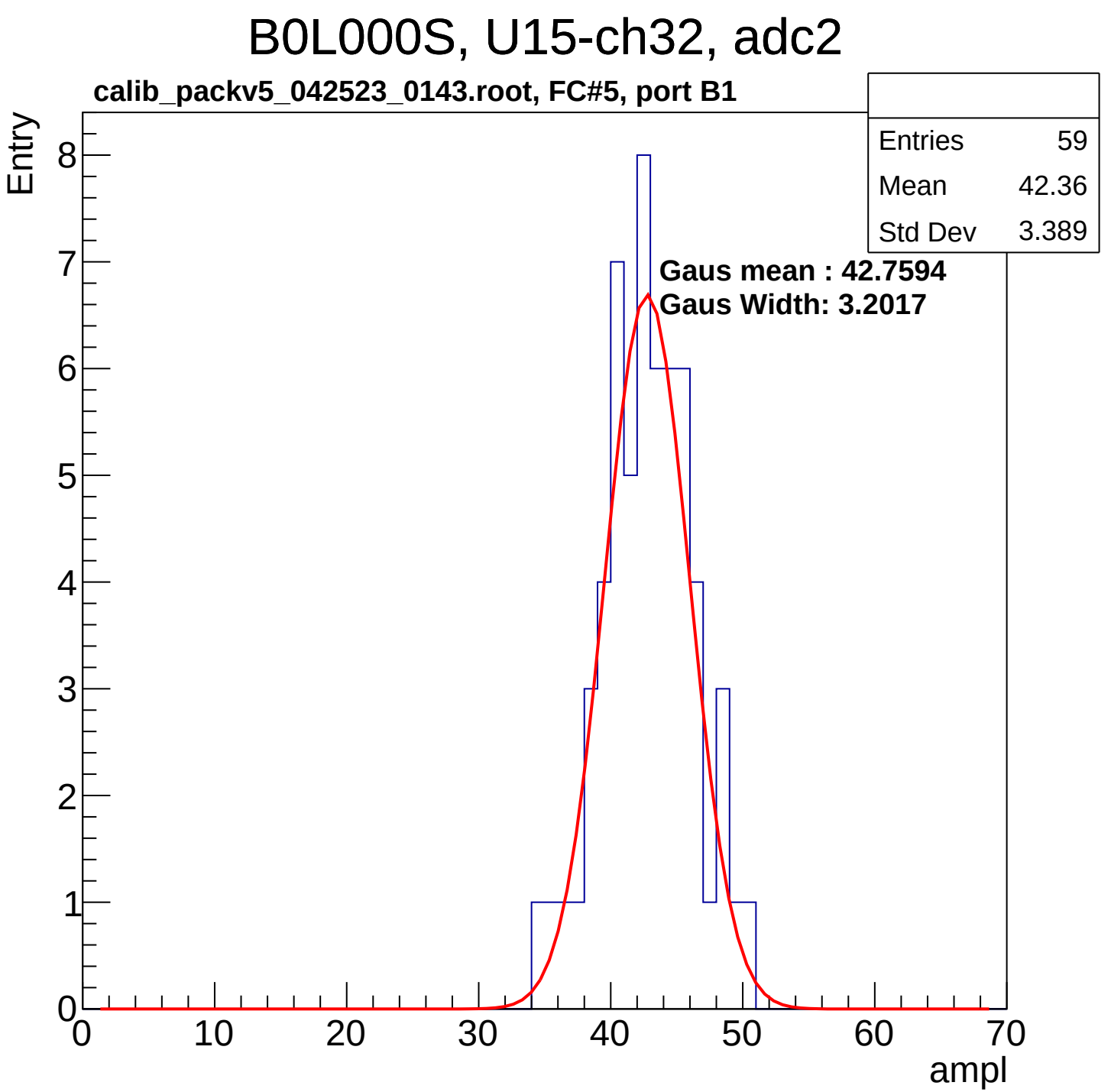
B0L000S, U15-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.36
Std Dev 3.389
Gaus mean : 42.7594
Gaus Width: 3.2017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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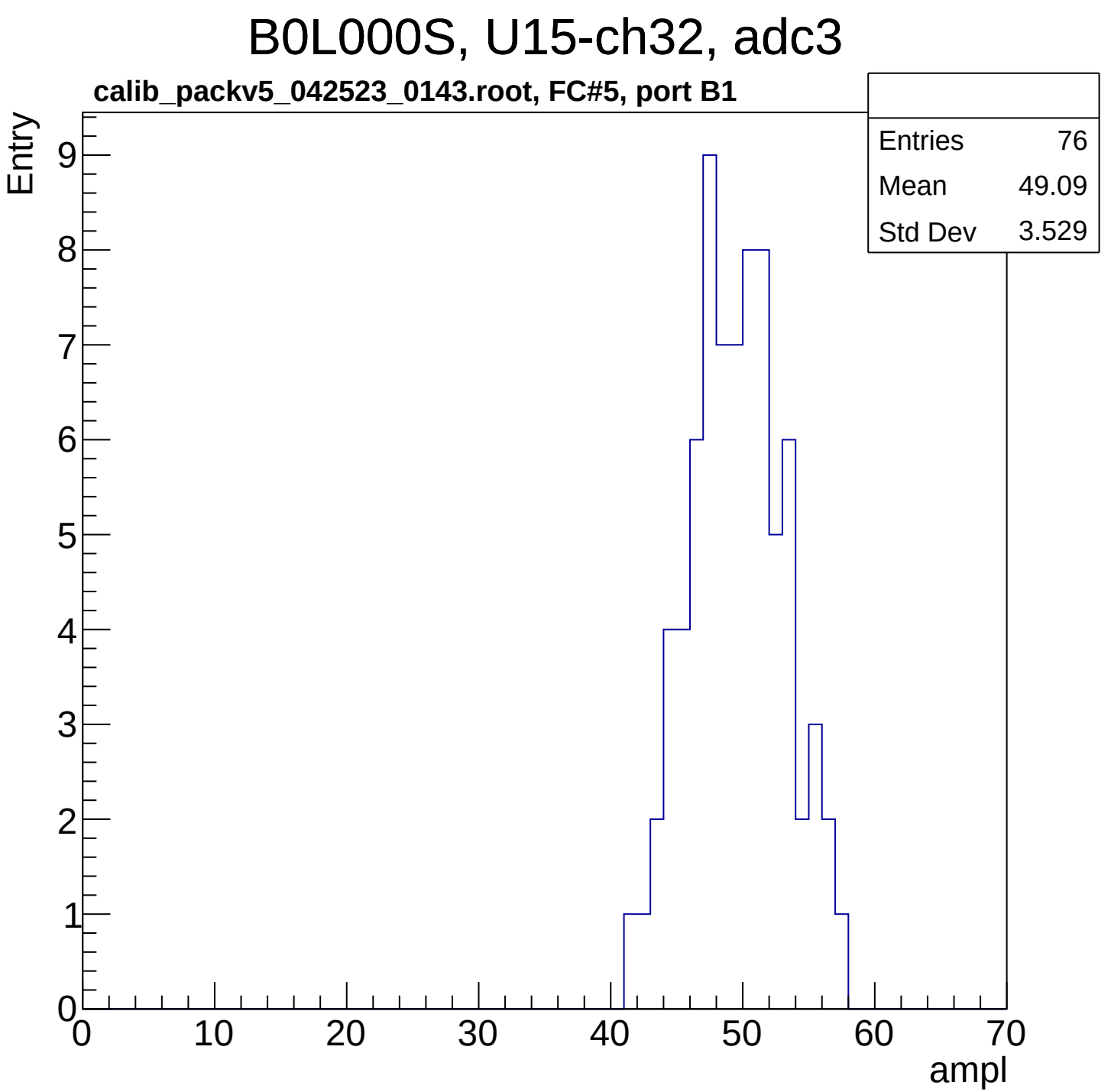
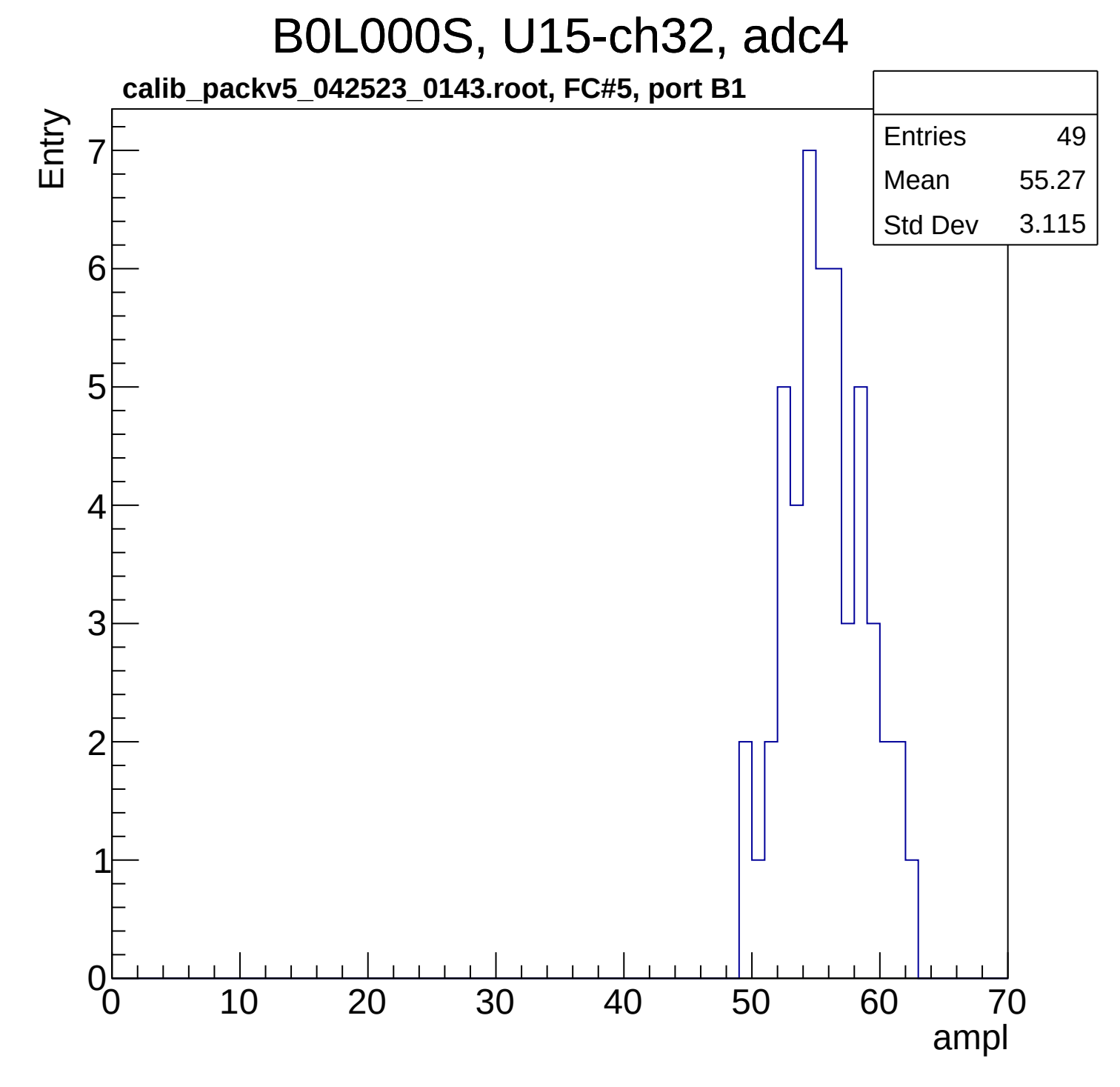
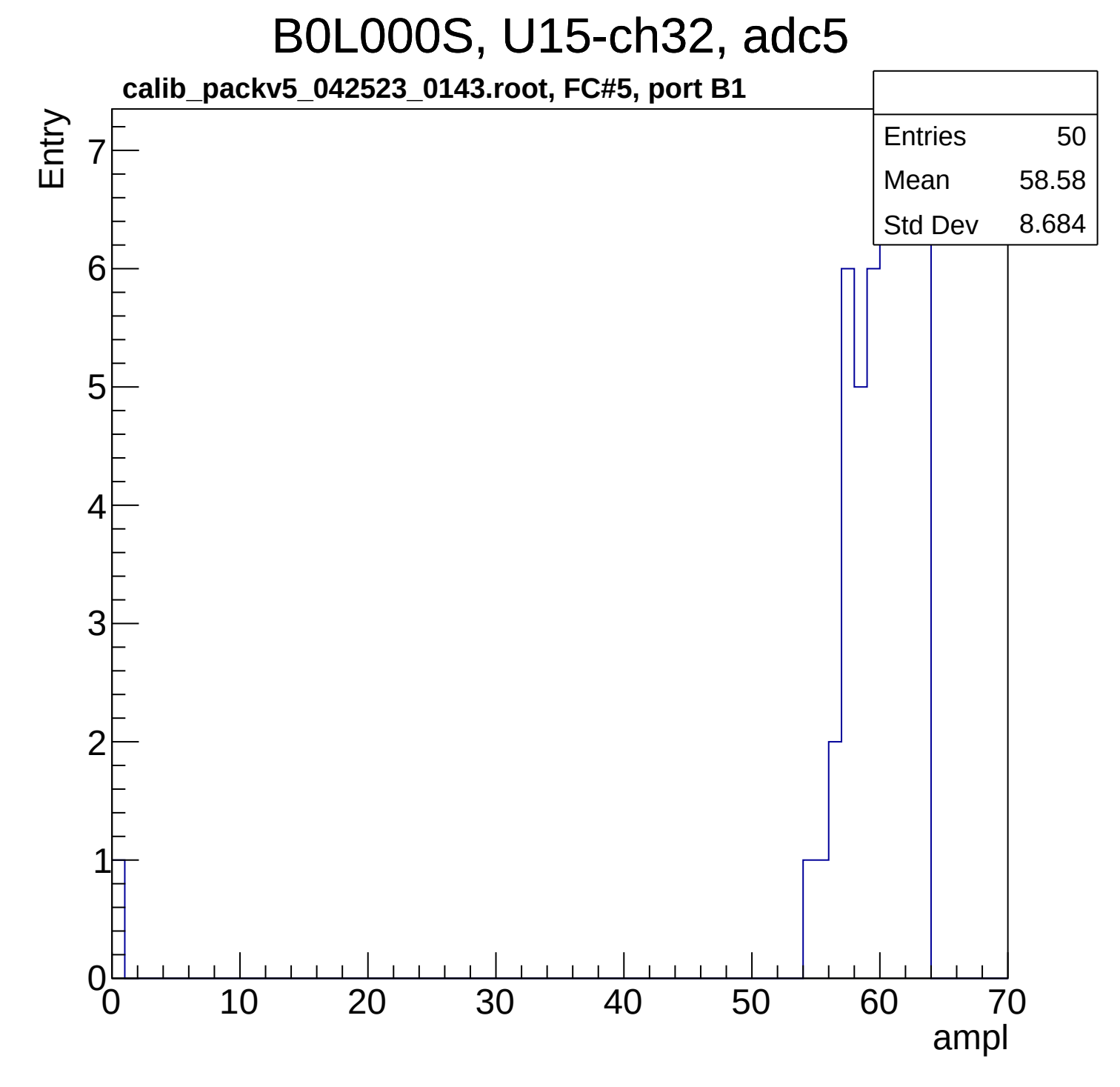
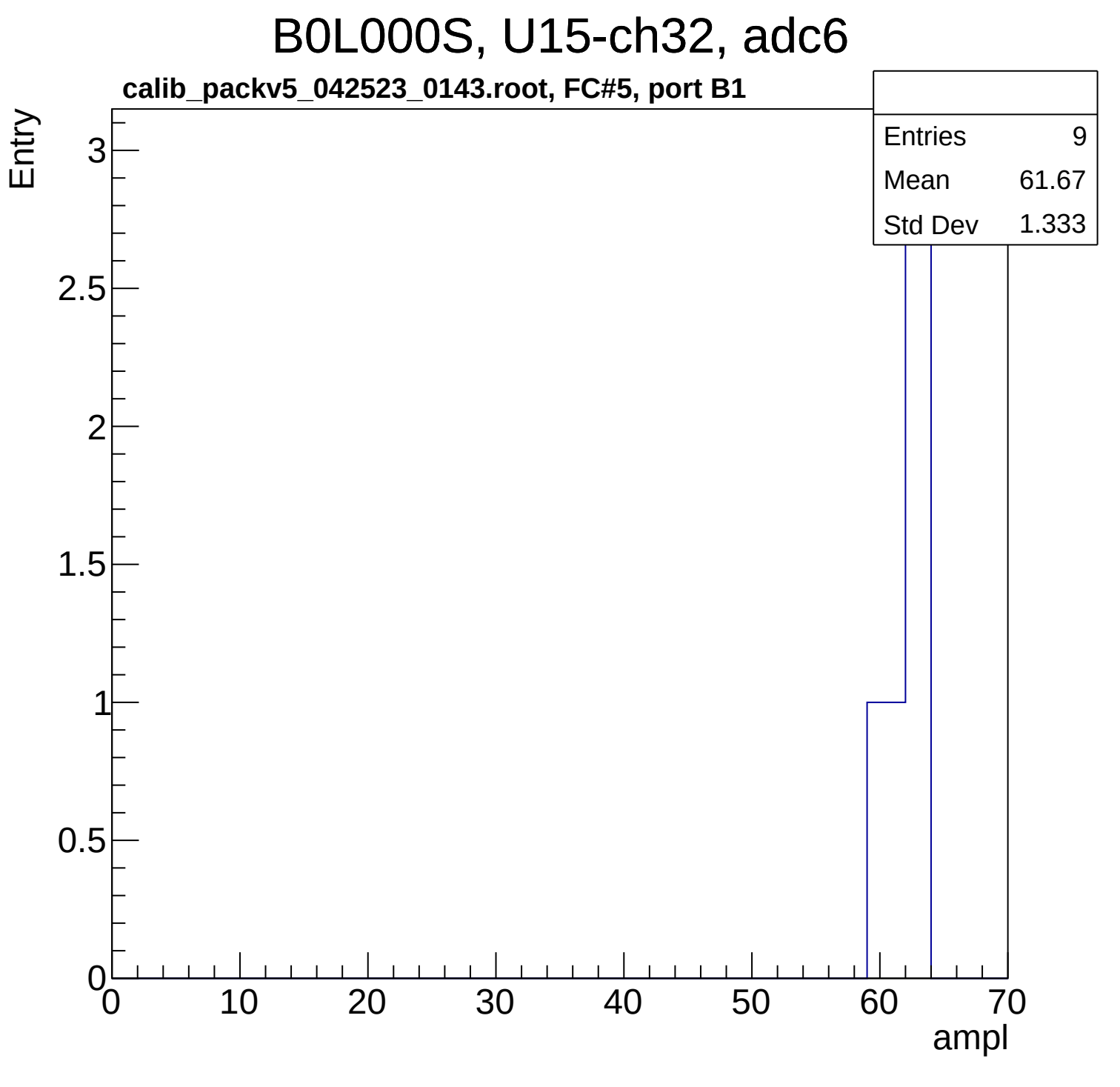

B0L000S, U15-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 49.09
Std Dev 3.529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.27
Std Dev 3.115
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.58
Std Dev 8.684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.67
Std Dev 1.333
Entry
ampl
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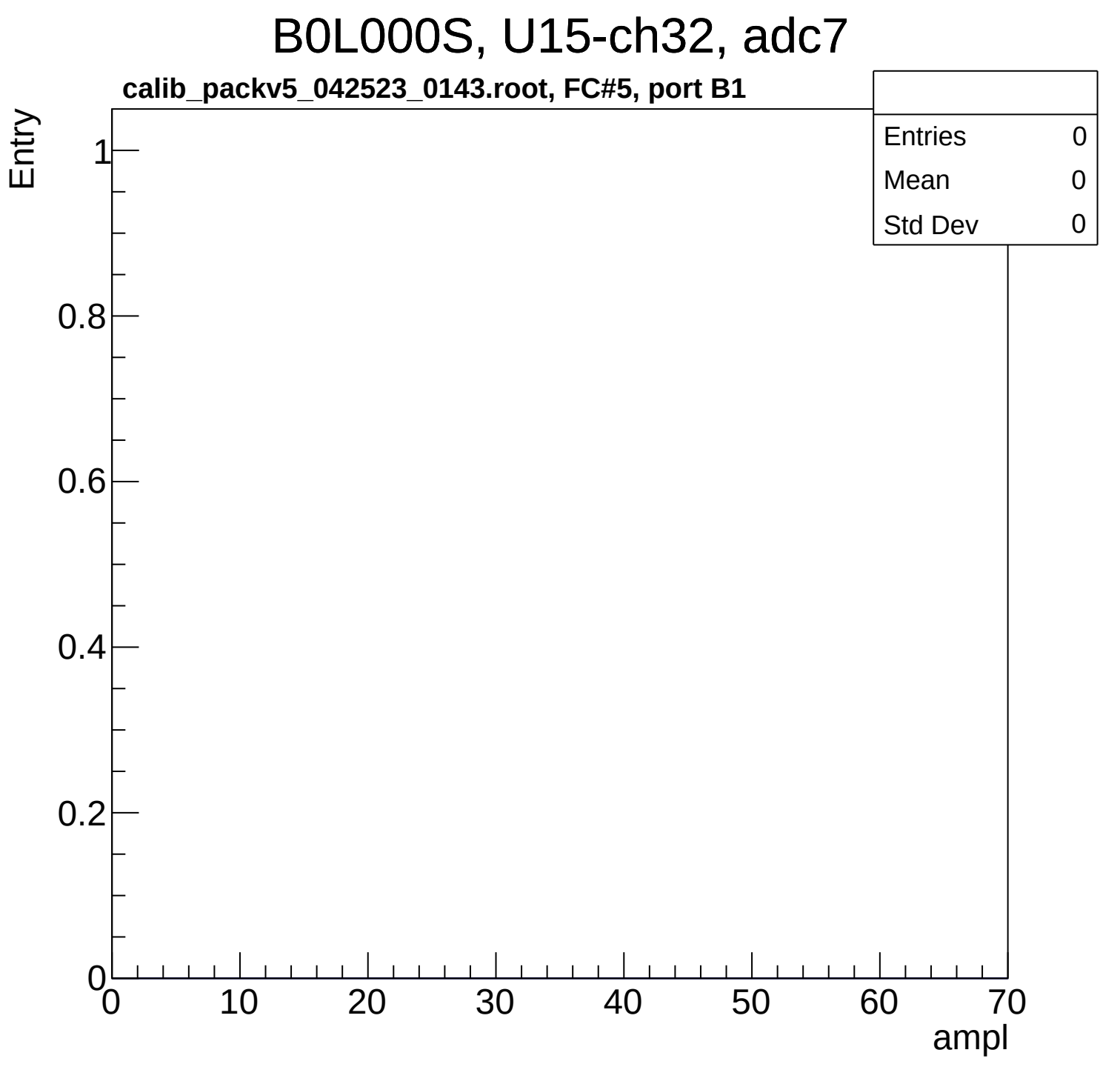
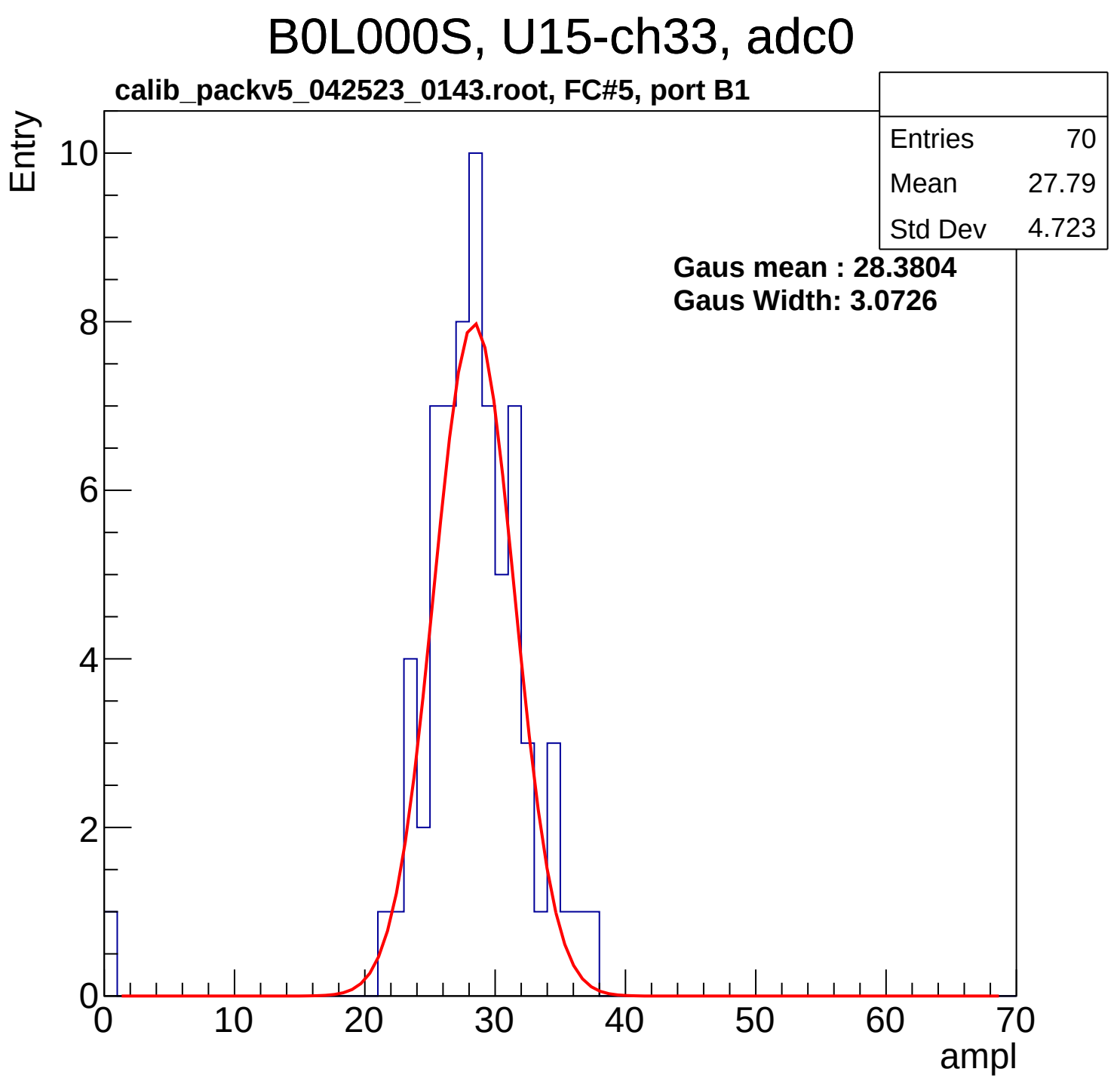
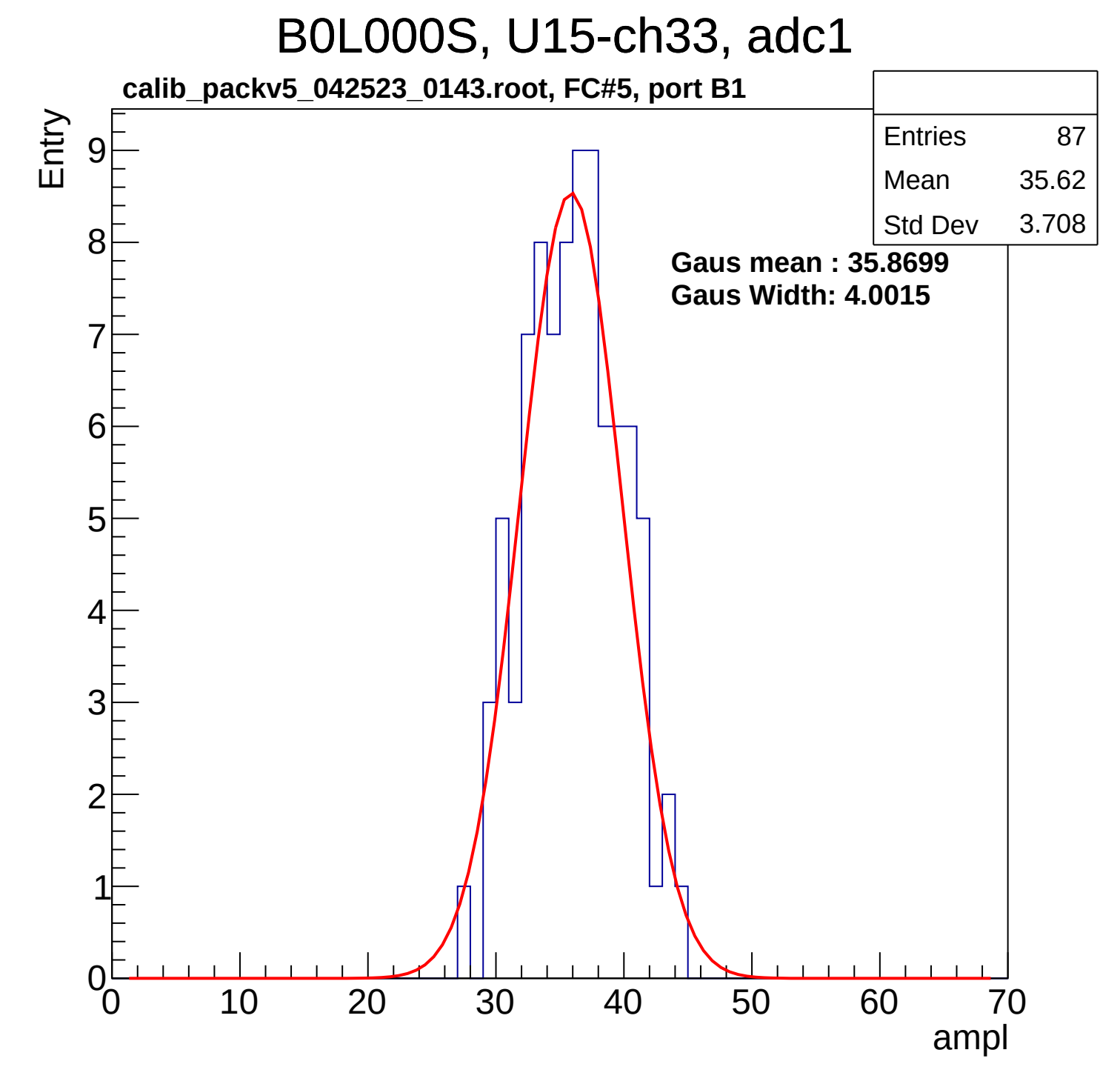
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.79
Std Dev 4.723
Gaus mean : 28.3804
Gaus Width: 3.0726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 35.62
Std Dev 3.708
Gaus mean : 35.8699
Gaus Width: 4.0015
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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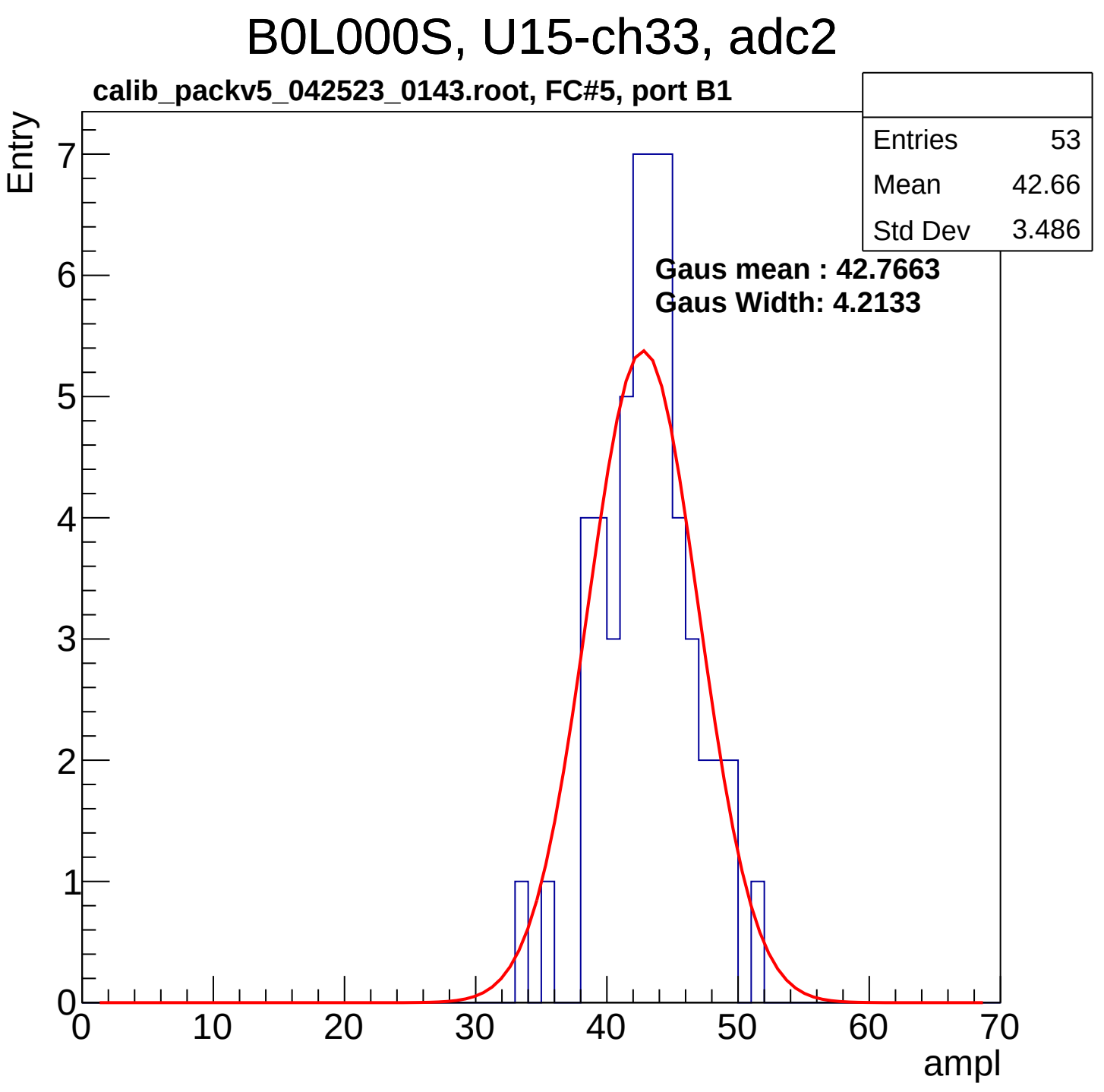
B0L000S, U15-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.66
Std Dev 3.486
Gaus mean : 42.7663
Gaus Width: 4.2133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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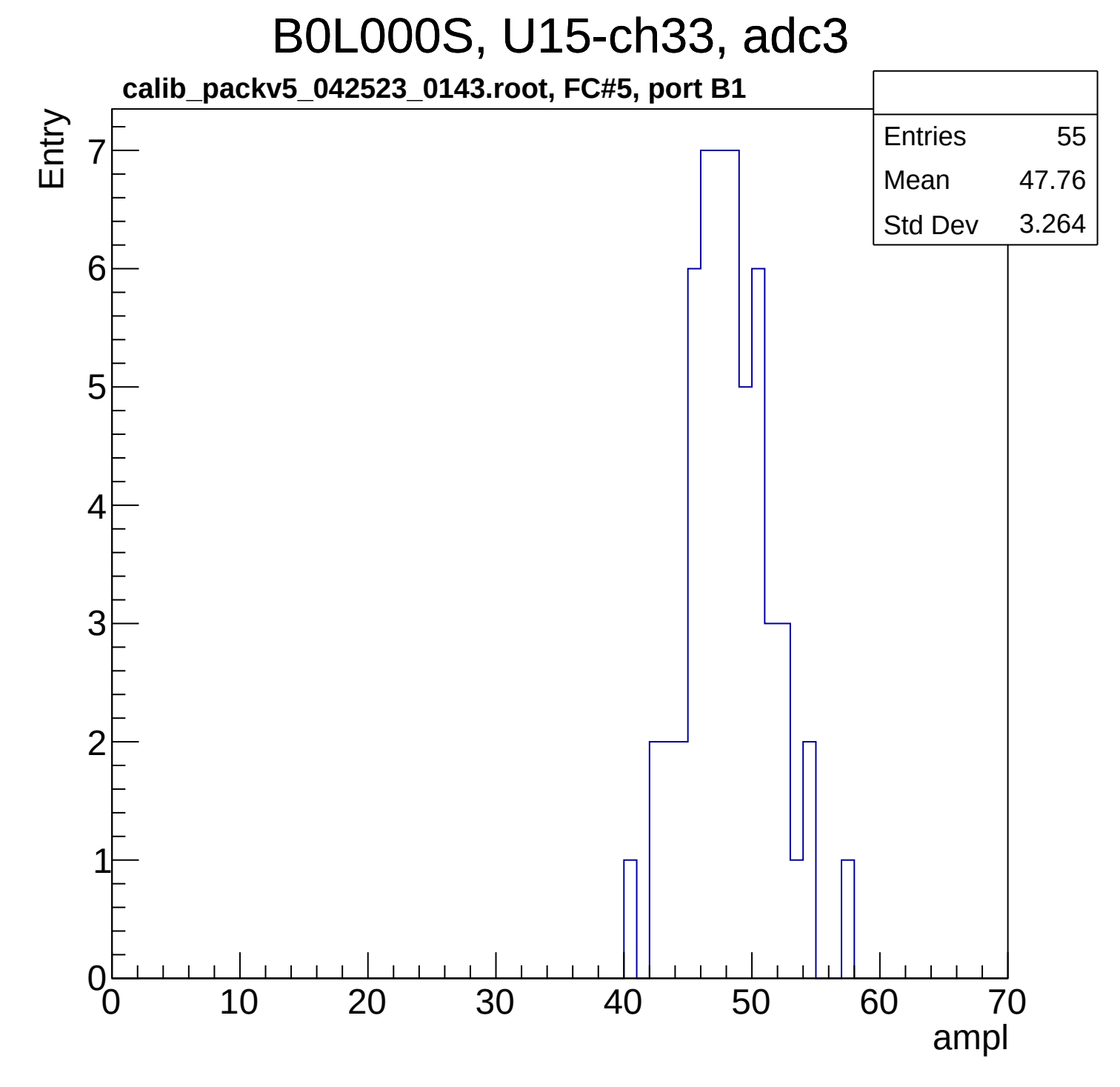
B0L000S, U15-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.76
Std Dev 3.264
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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50
60
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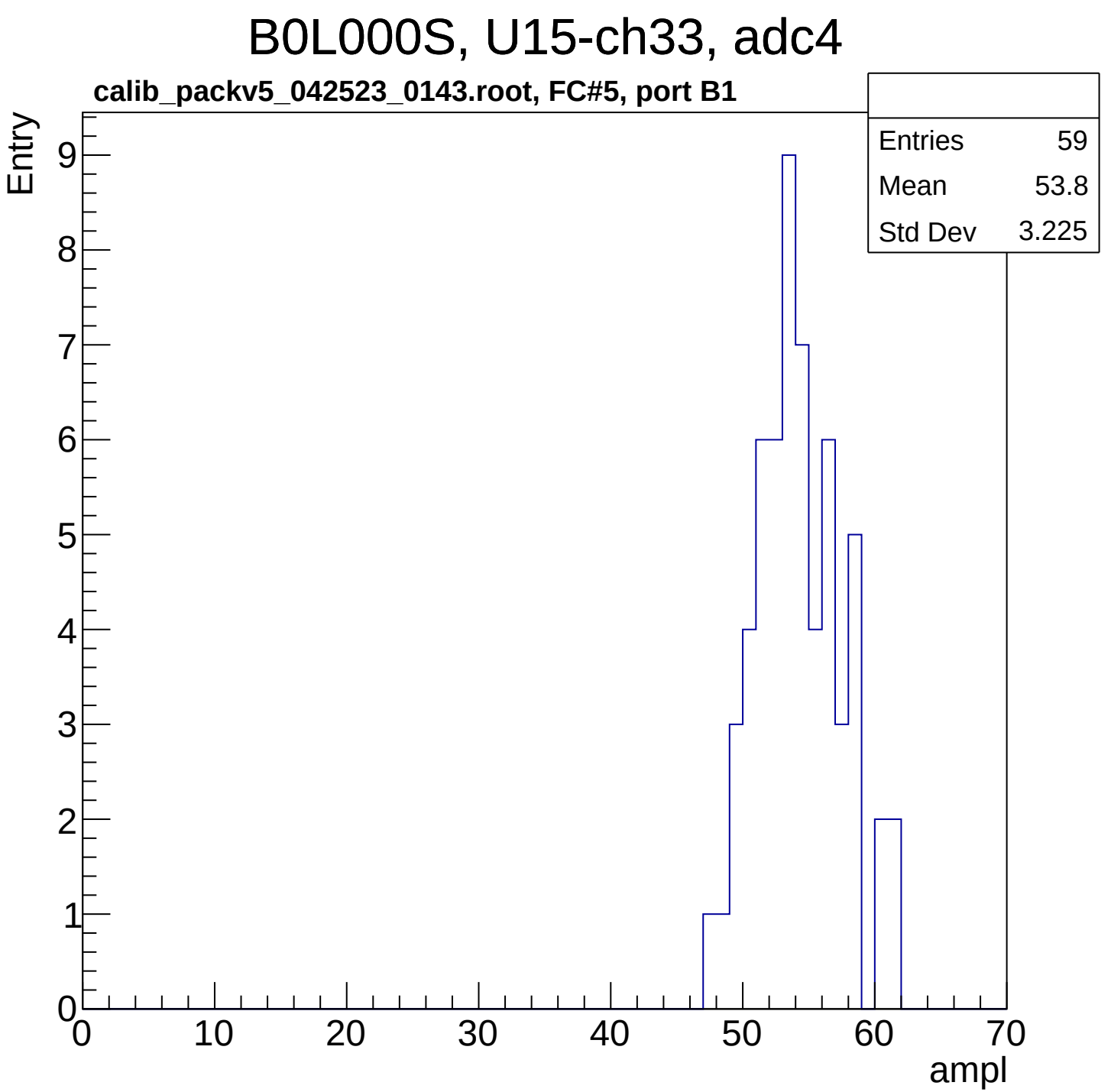
B0L000S, U15-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.8
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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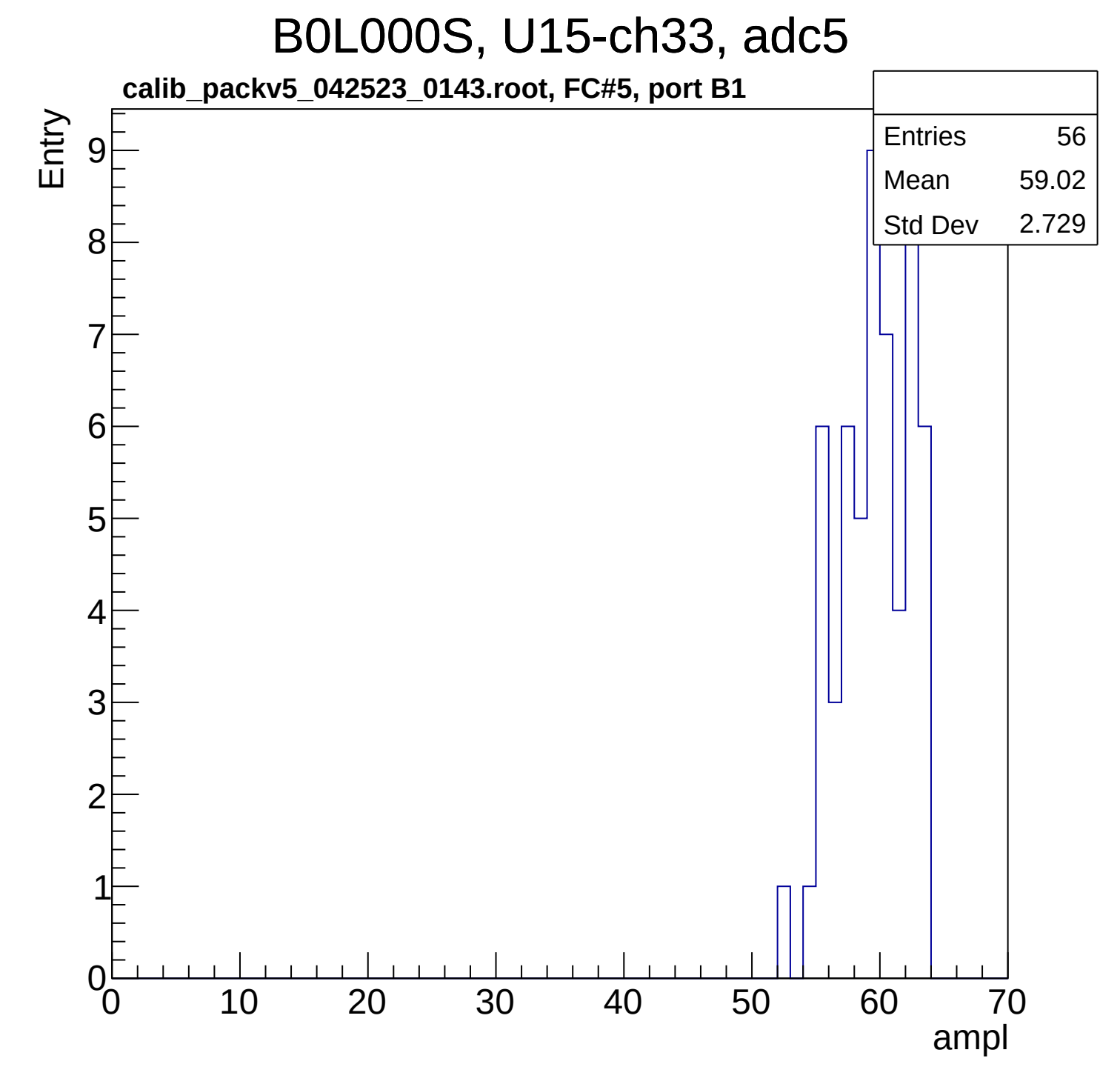
60
70


B0L000S, U15-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.02
Std Dev 2.729
Entry
ampl
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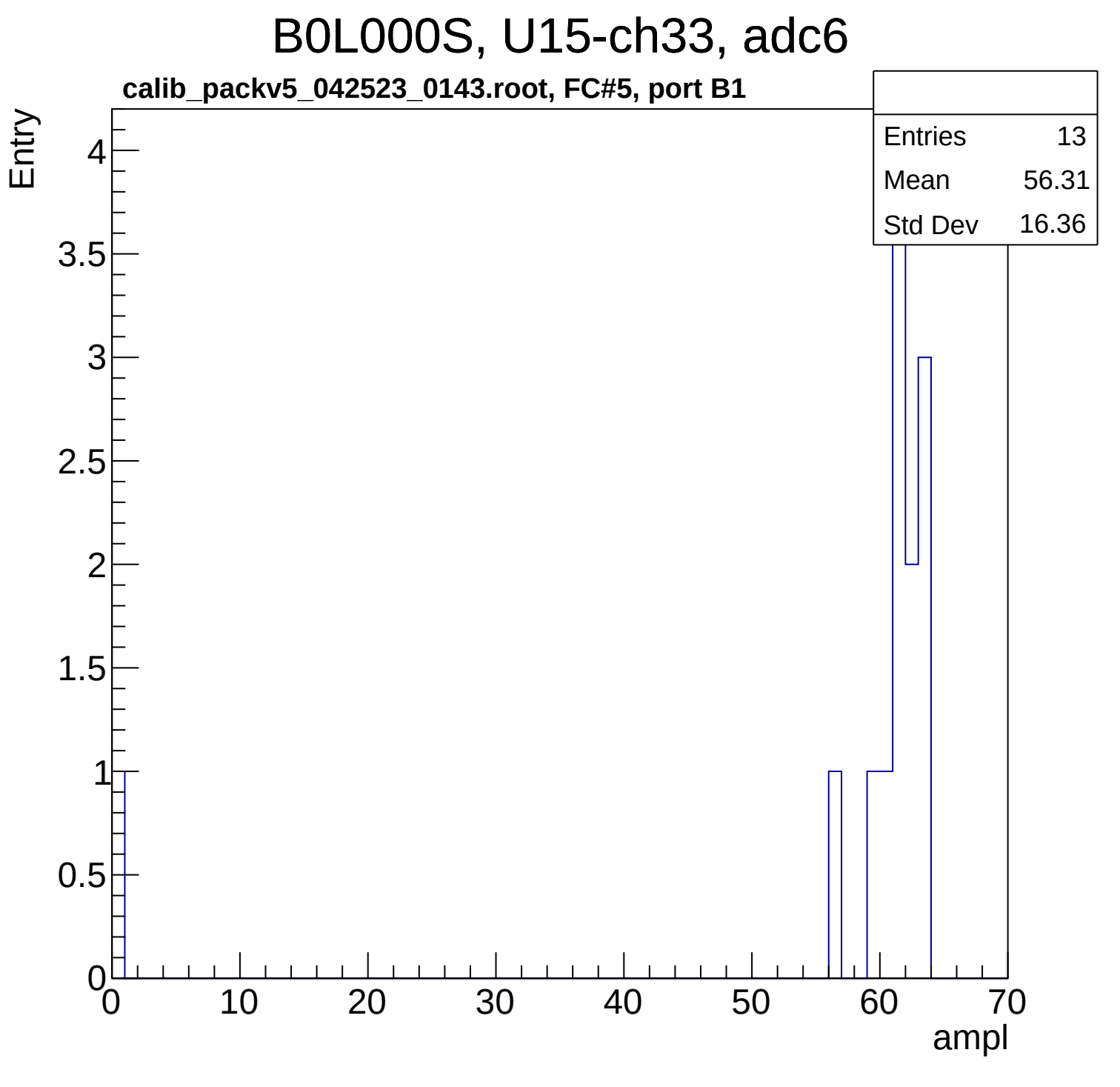
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.31
Std Dev 16.36
Entry
ampl
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0.5
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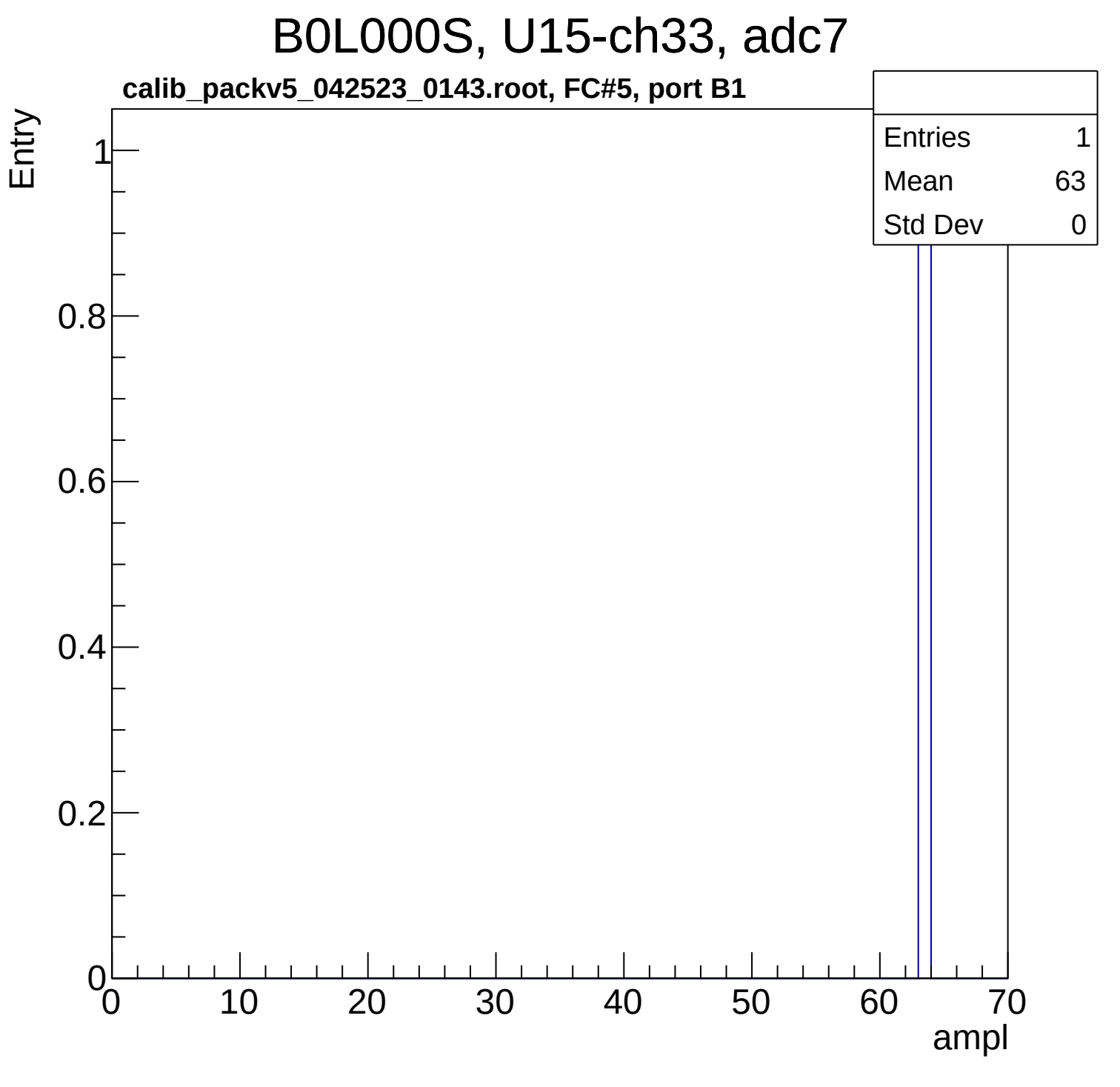
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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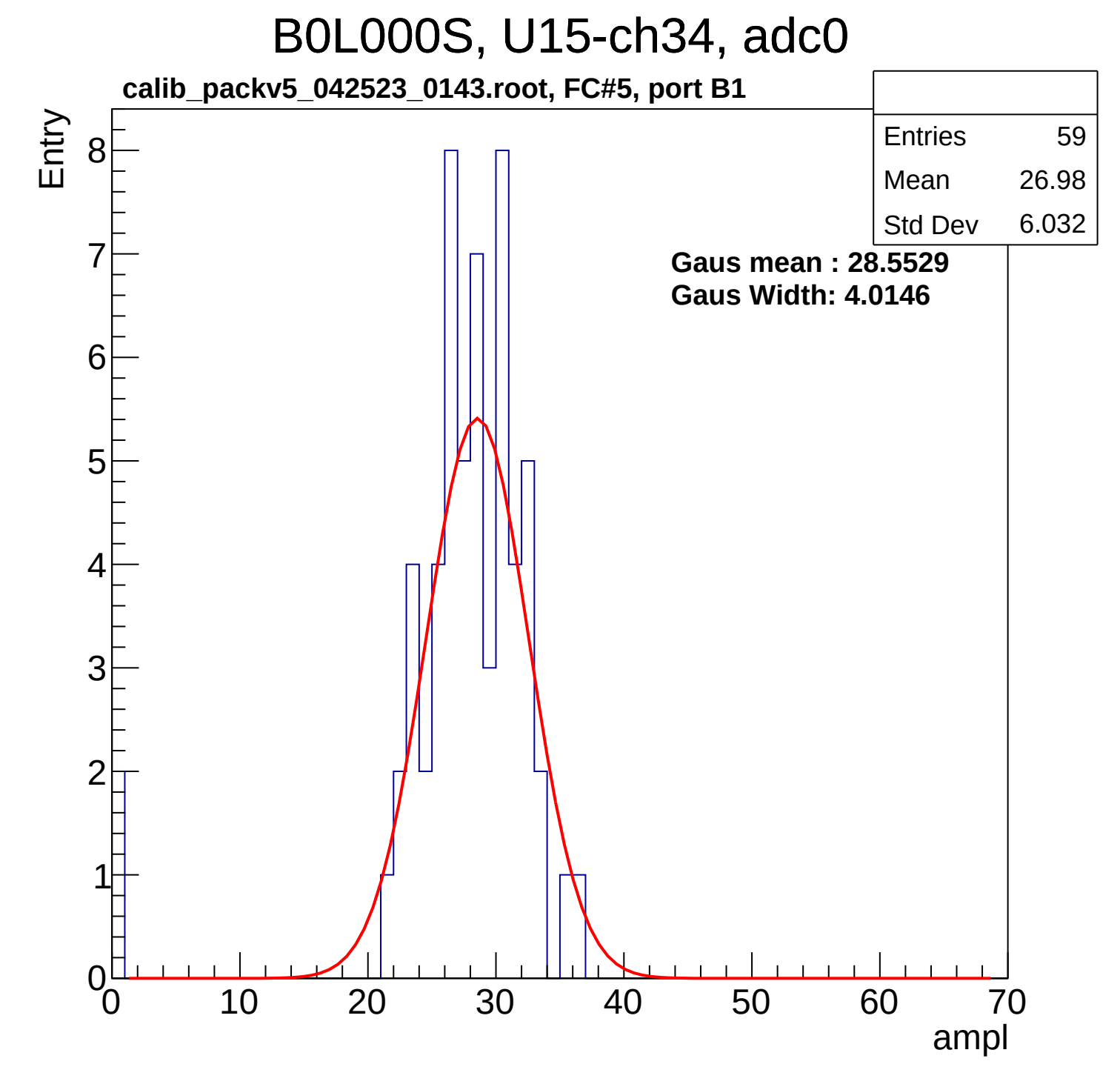
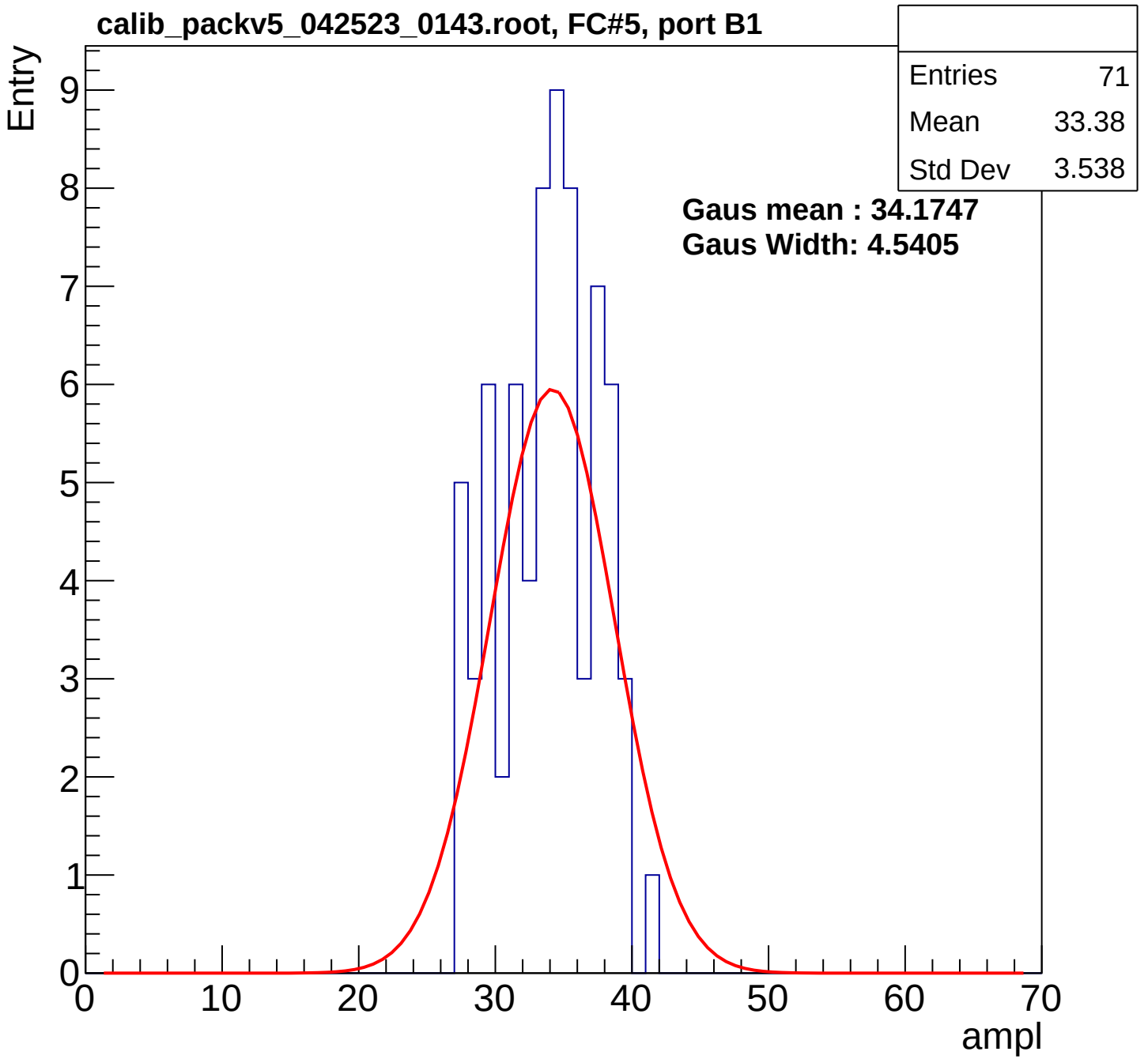
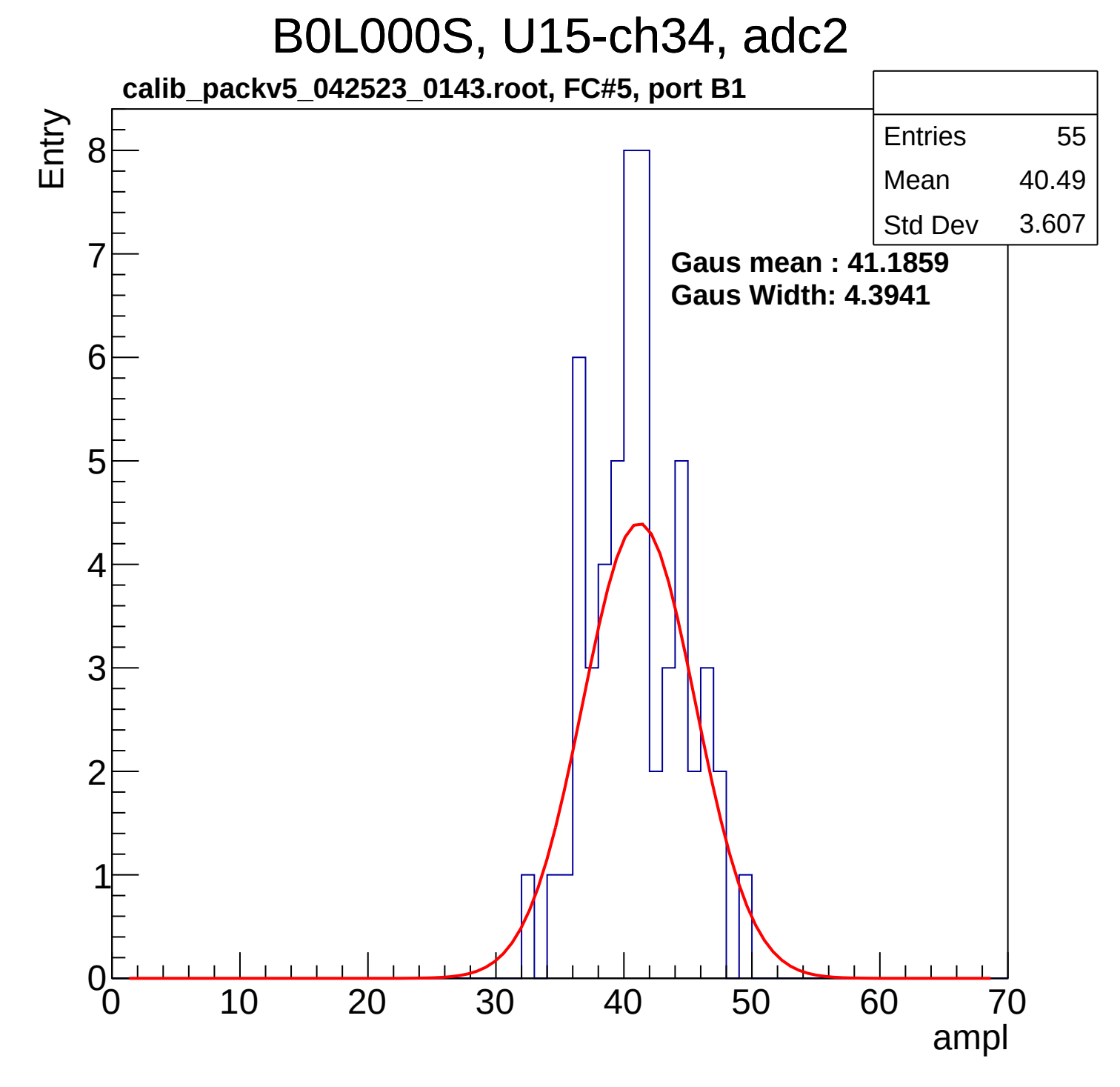
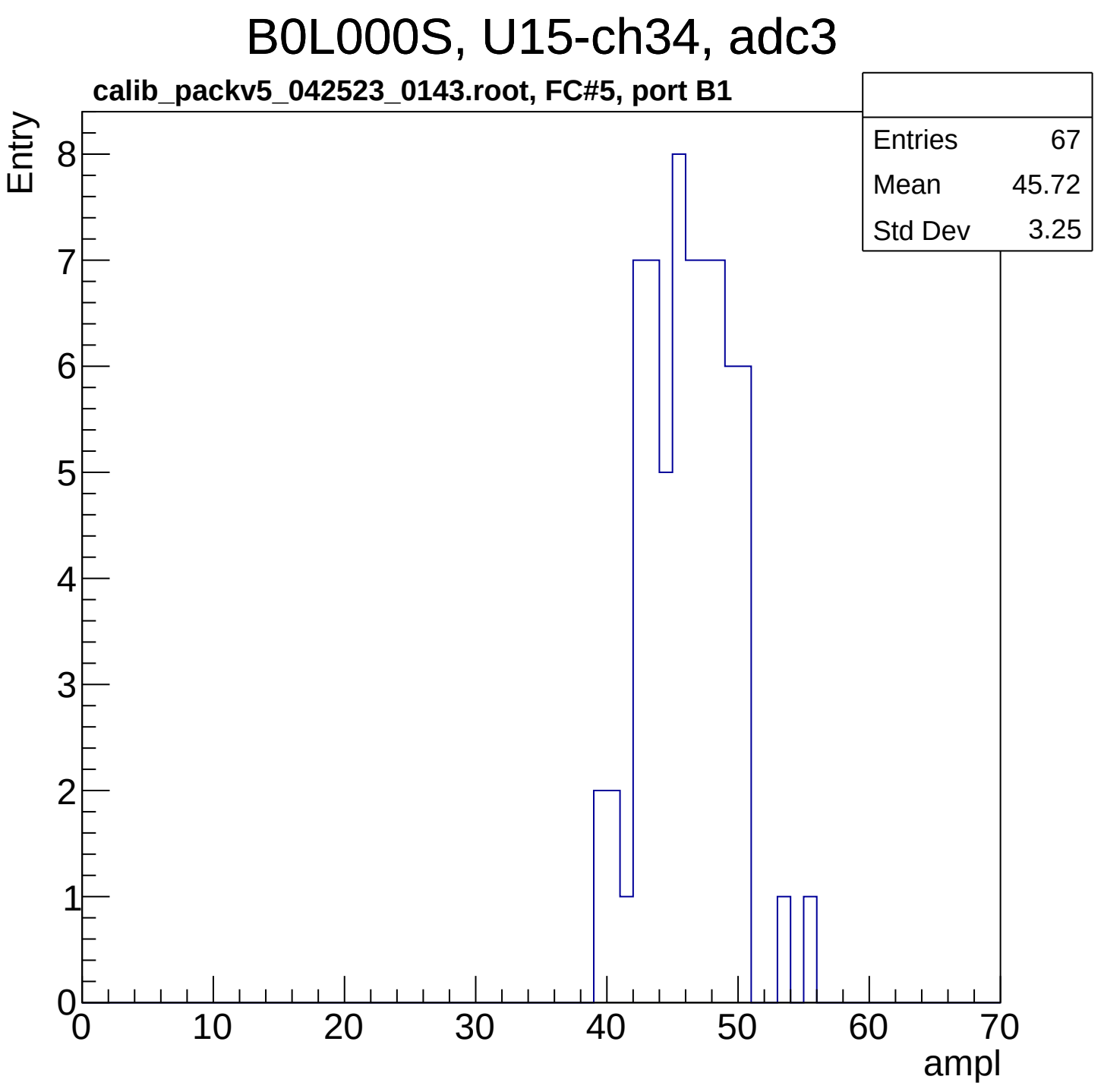
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 26.98
Std Dev 6.032
Gaus mean : 28.5529
Gaus Width: 4.0146
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 33.38
Std Dev 3.538
Gaus mean : 34.1747
Gaus Width: 4.5405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 40.49
Std Dev 3.607
Gaus mean : 41.1859
Gaus Width: 4.3941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 45.72
Std Dev 3.25
Entry
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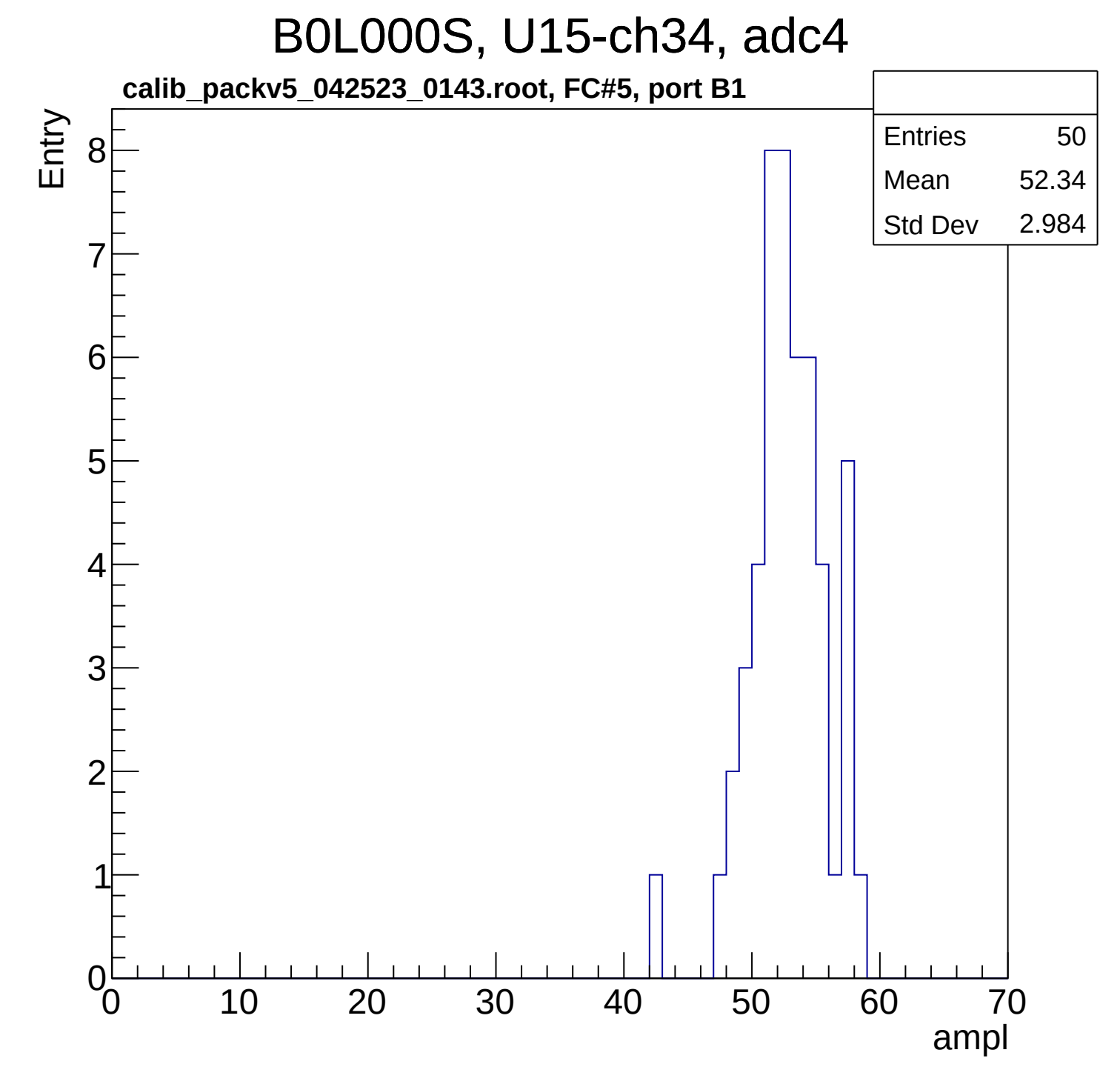
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 52.34
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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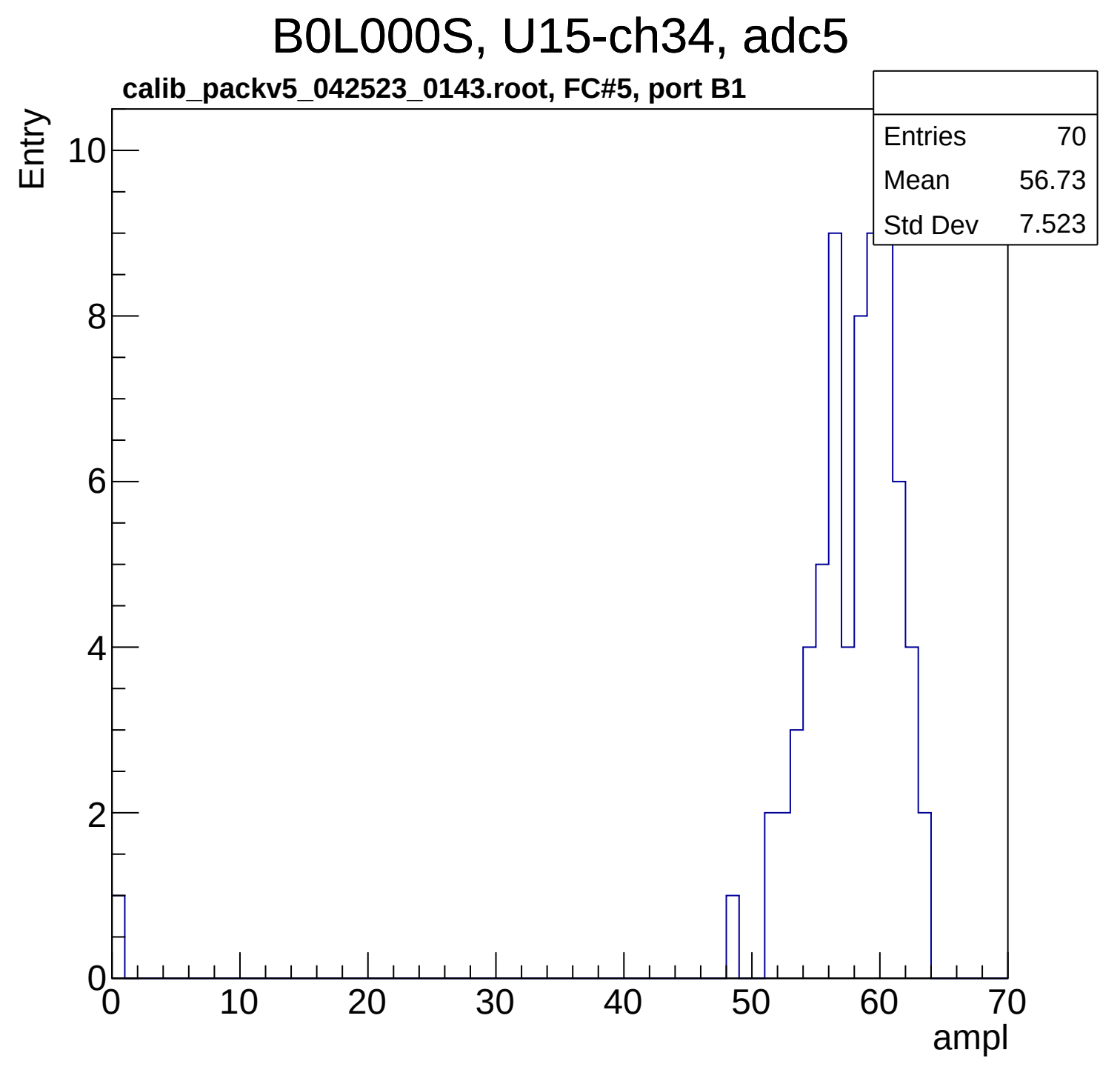
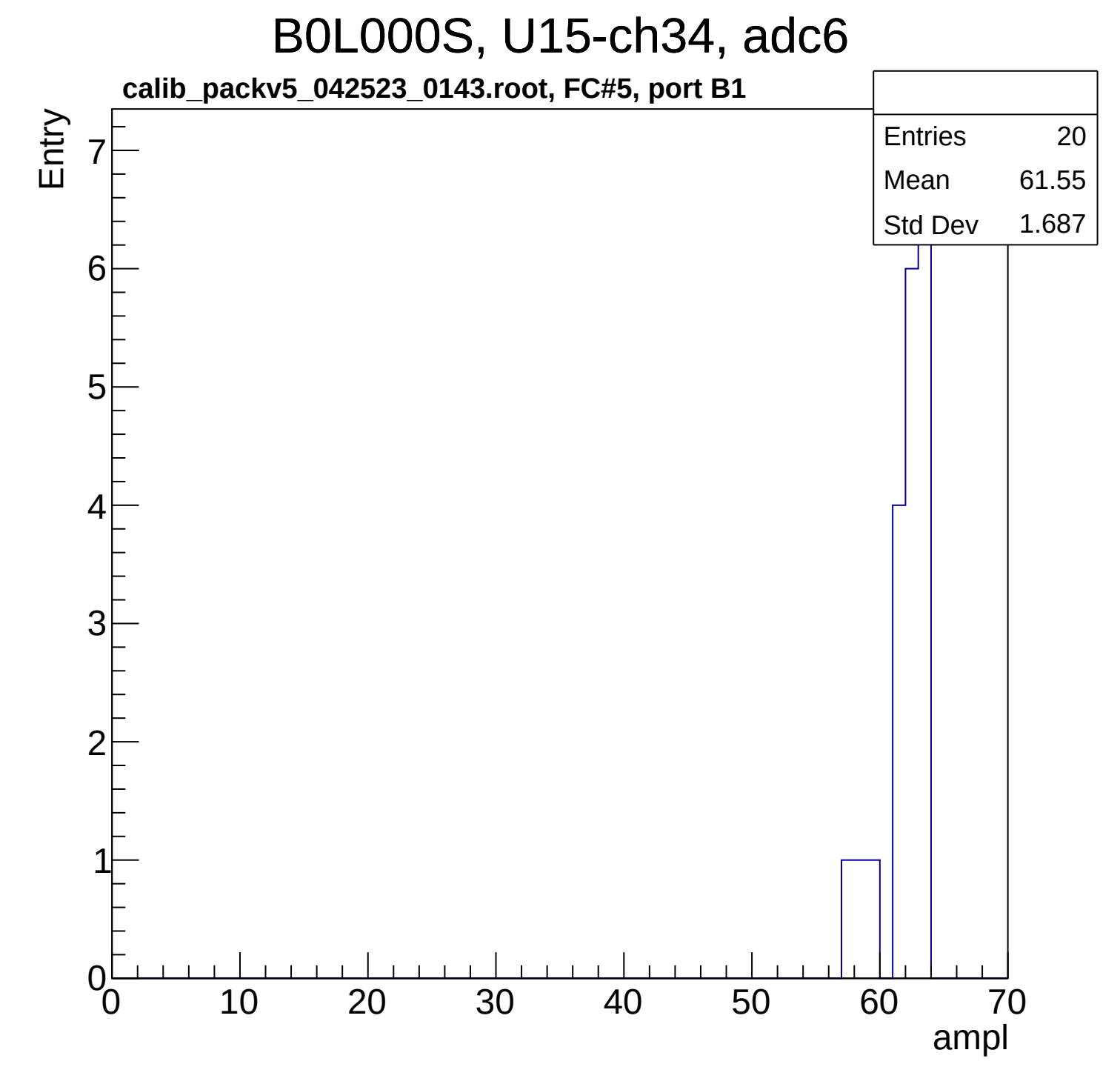
40
50
60
70


B0L000S, U15-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 56.73
Std Dev 7.523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 61.55
Std Dev 1.687
Entry
ampl
0
1
2
3
4
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6
7
0
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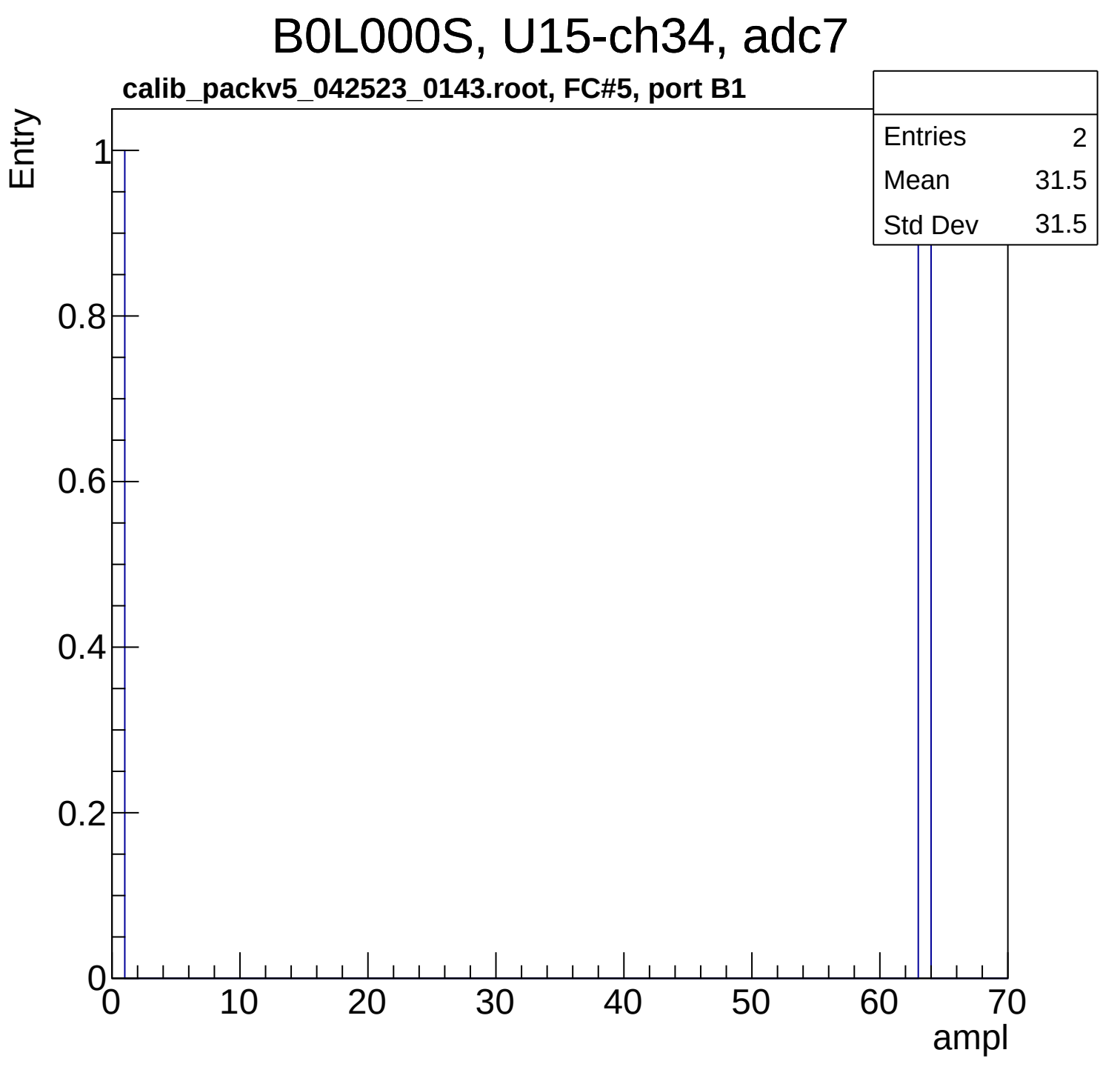
B0L000S, U15-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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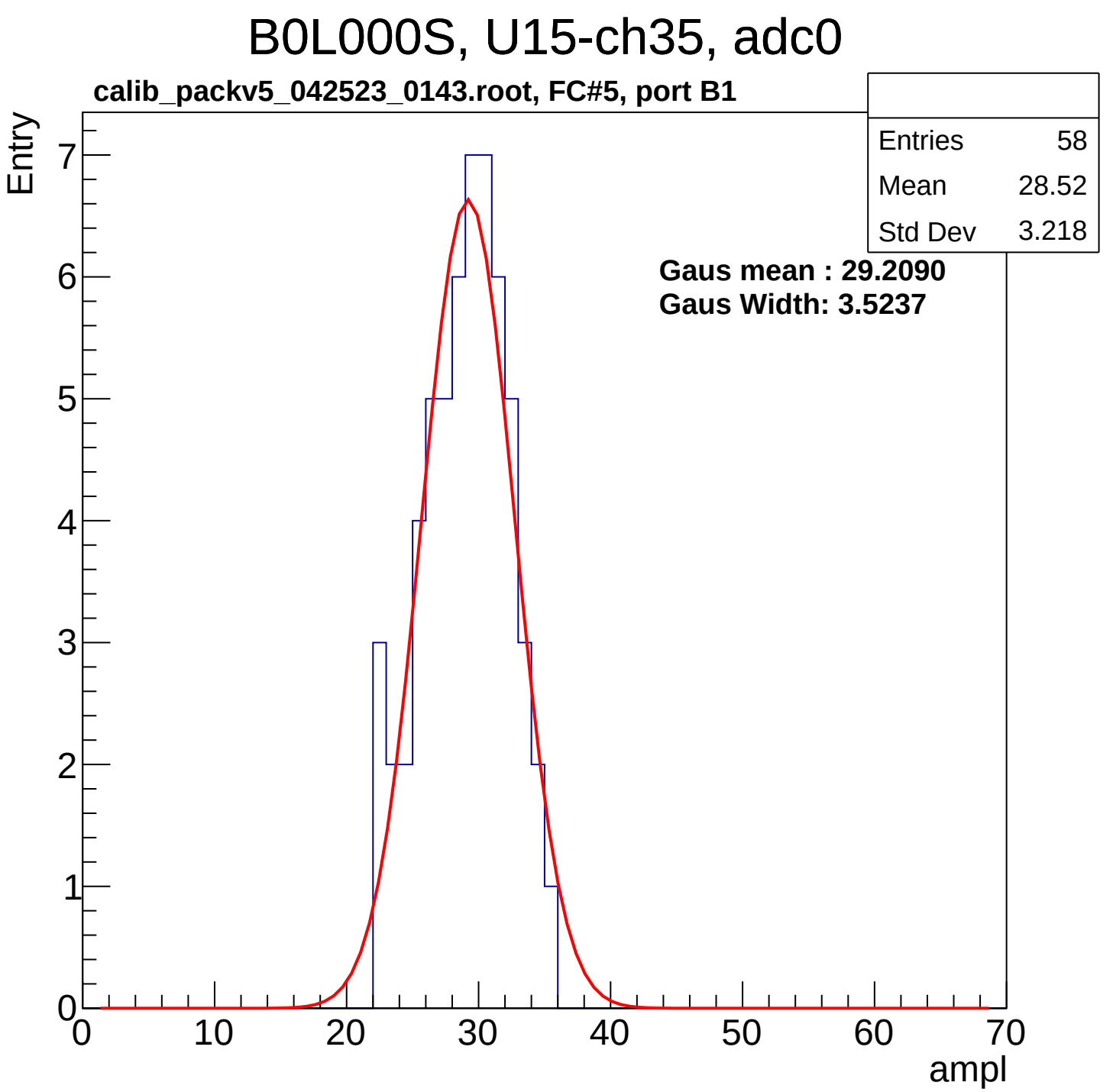
50
60
70
ampl


B0L000S, U15-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 28.52
Std Dev 3.218
Gaus mean : 29.2090
Gaus Width: 3.5237
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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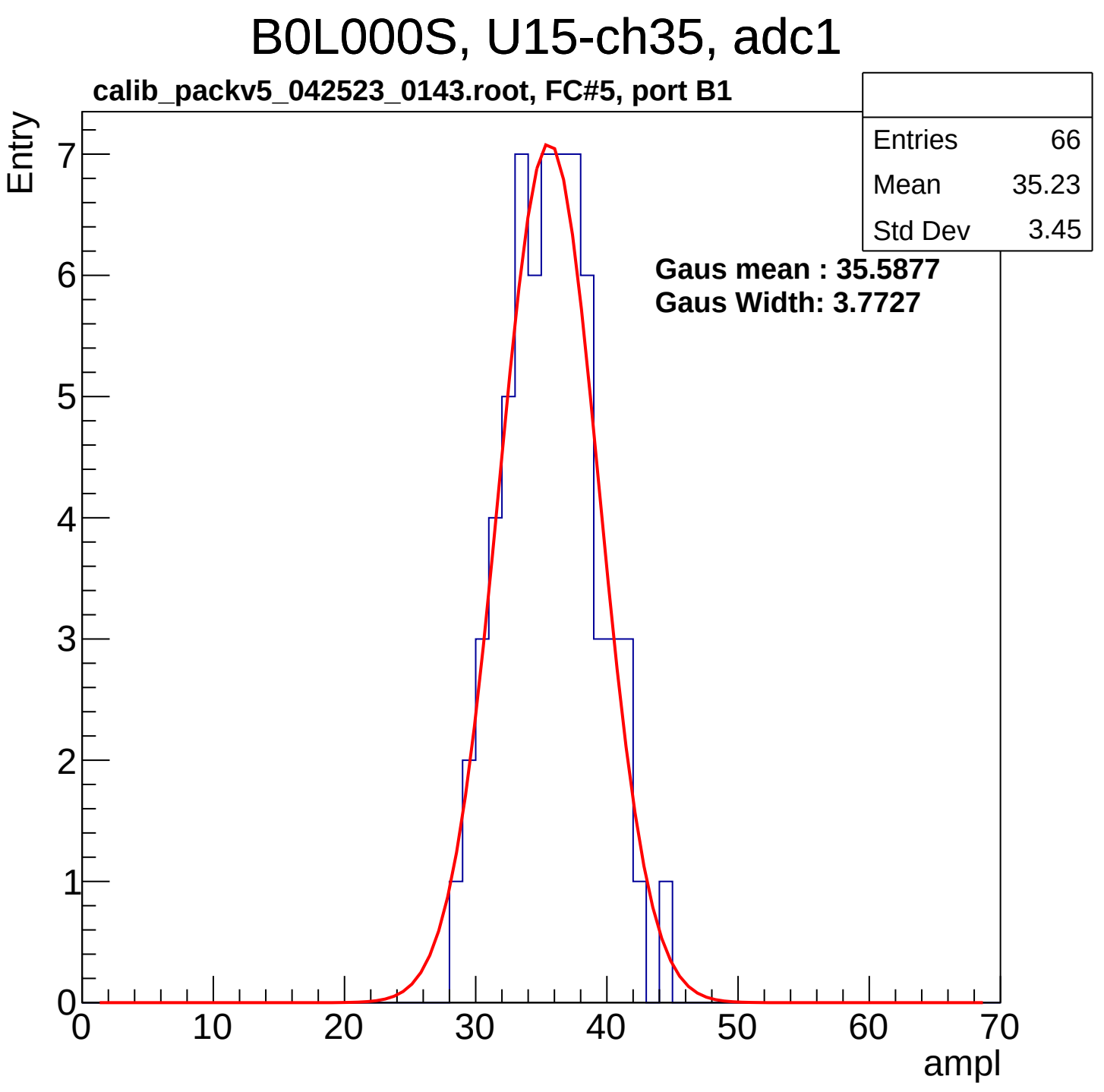
B0L000S, U15-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.23
Std Dev 3.45
Gaus mean : 35.5877
Gaus Width: 3.7727
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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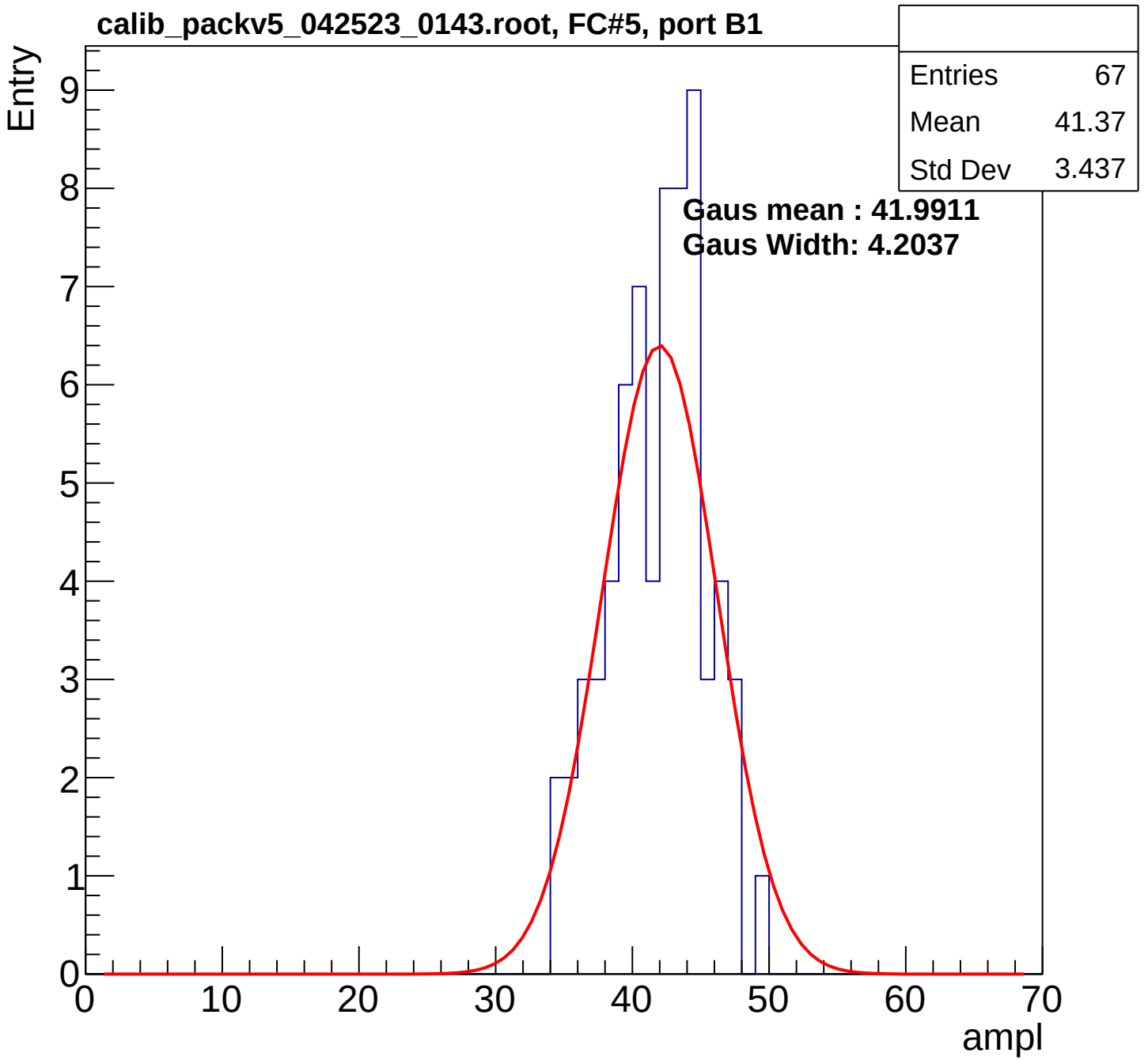
B0L000S, U15-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.37
Std Dev 3.437
Gaus mean : 41.9911
Gaus Width: 4.2037
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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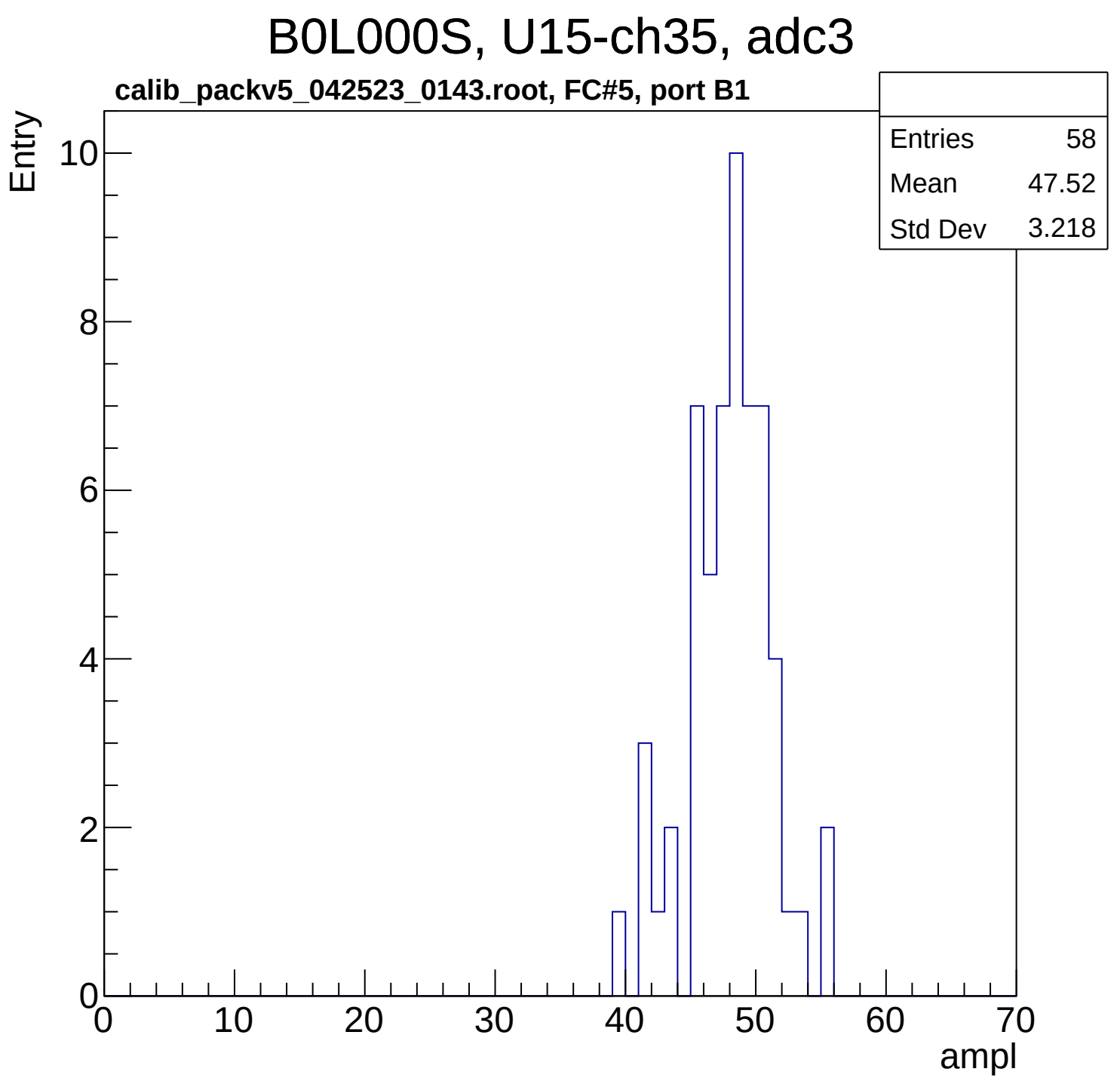
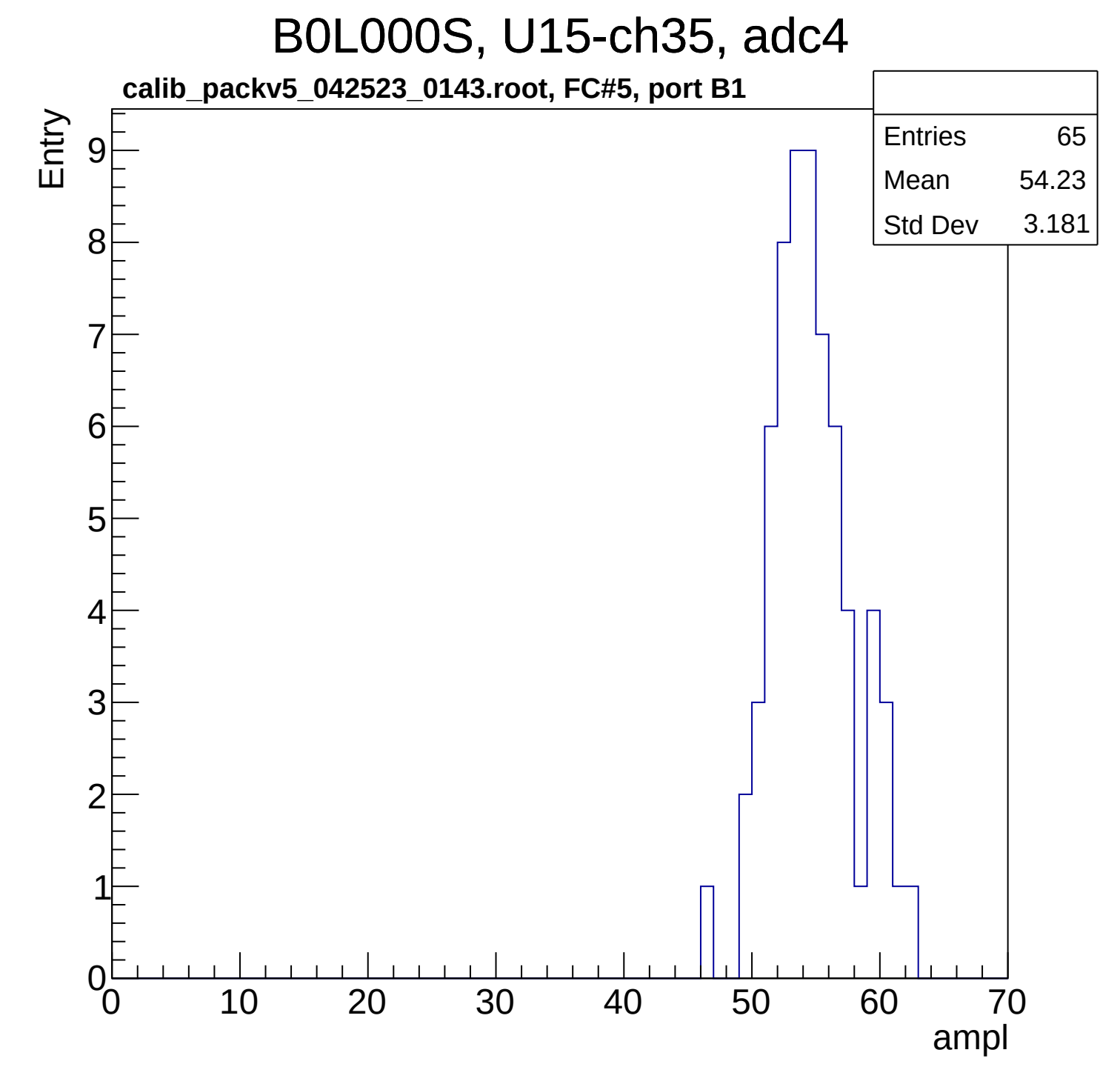

B0L000S, U15-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.52
Std Dev 3.218
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 54.23
Std Dev 3.181
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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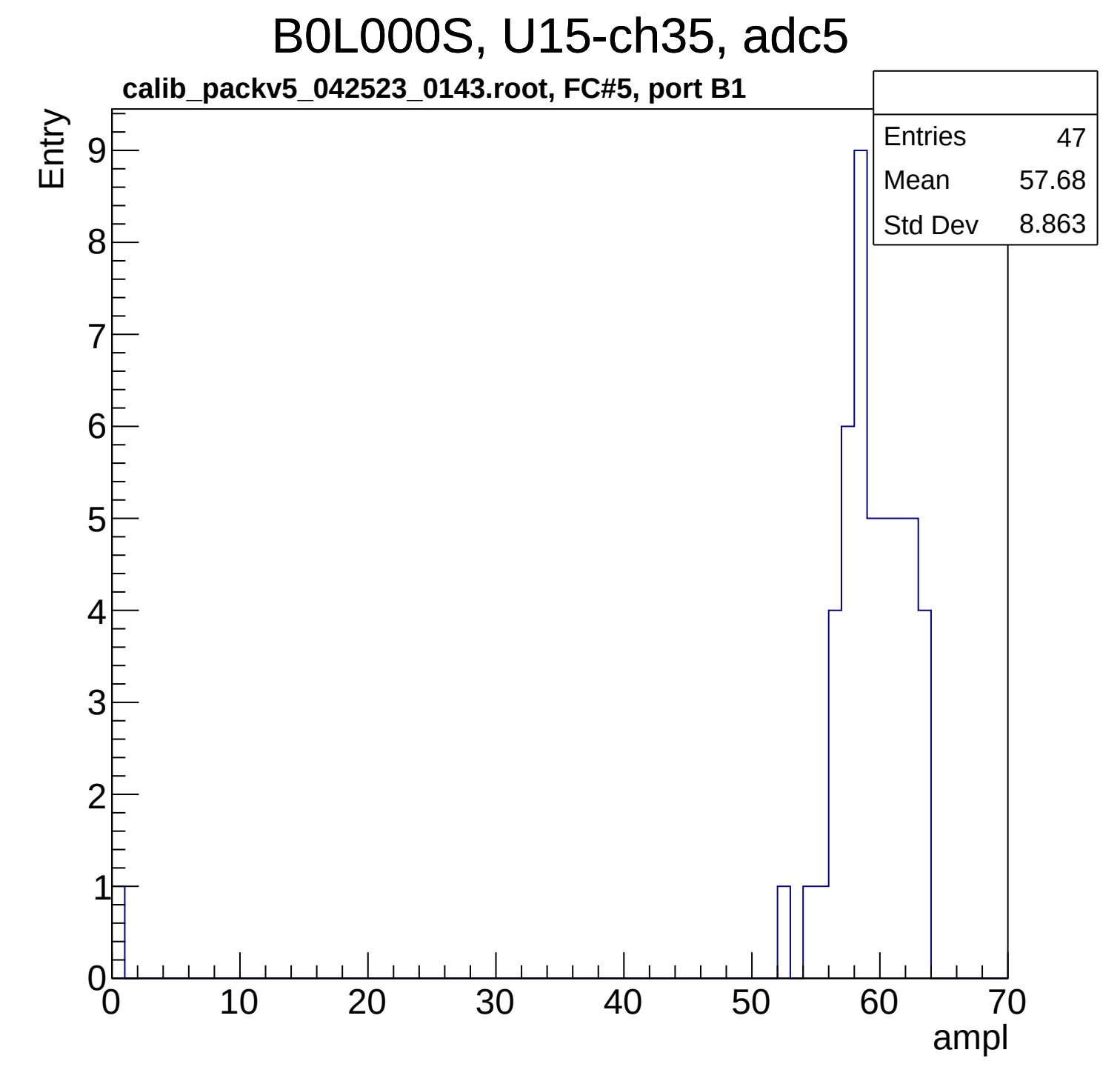
B0L000S, U15-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.68
Std Dev 8.863
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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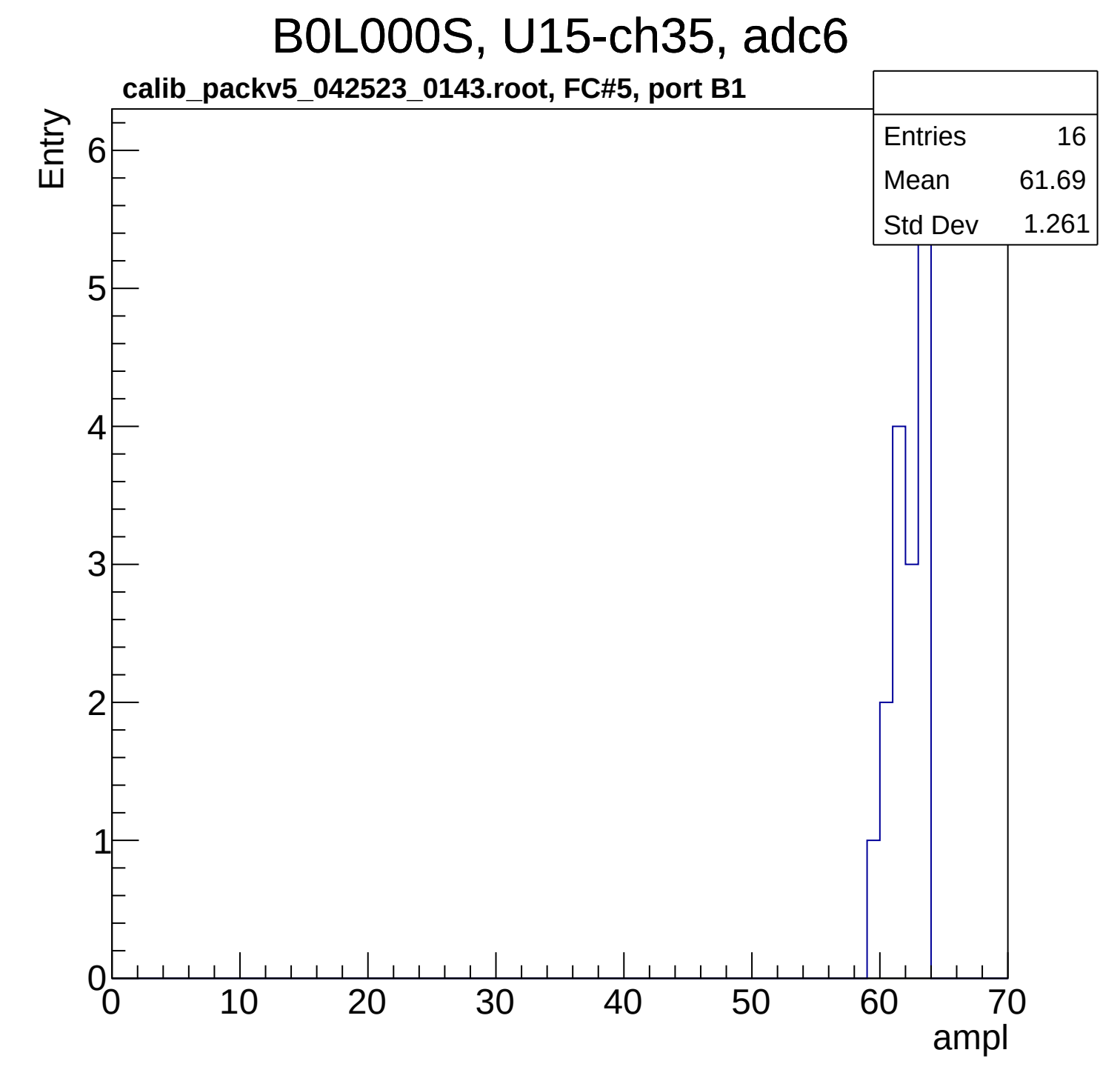
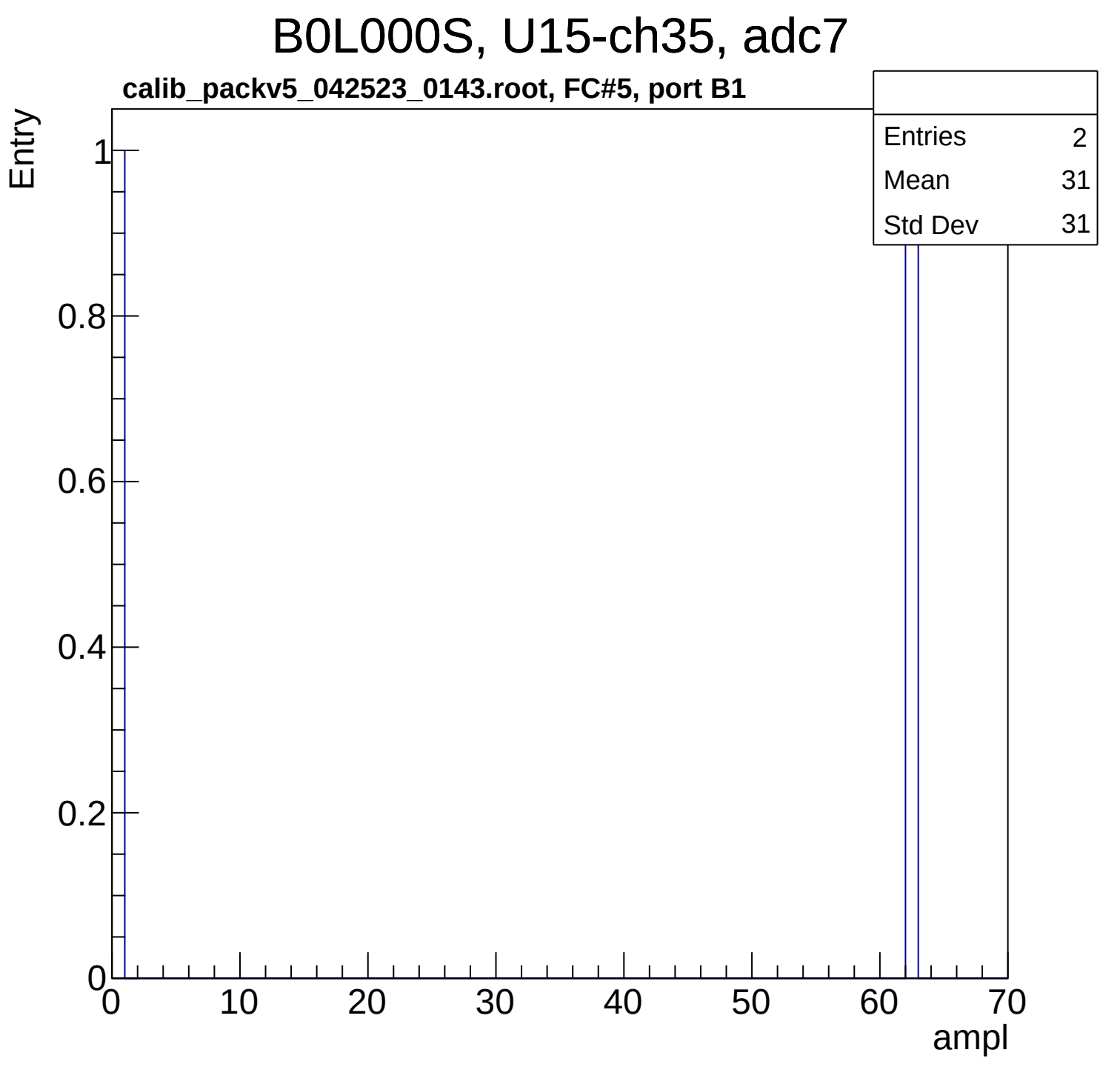
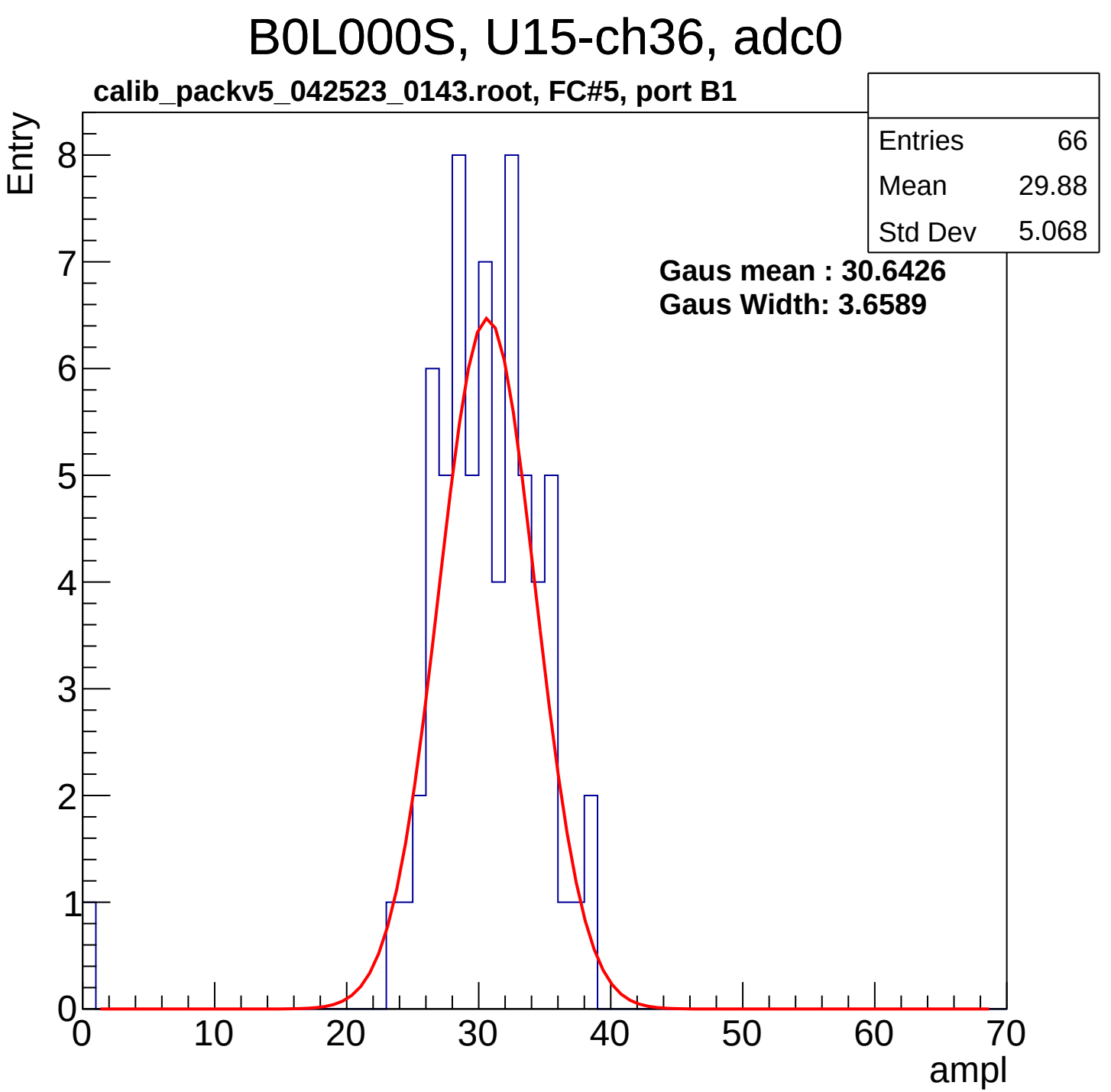
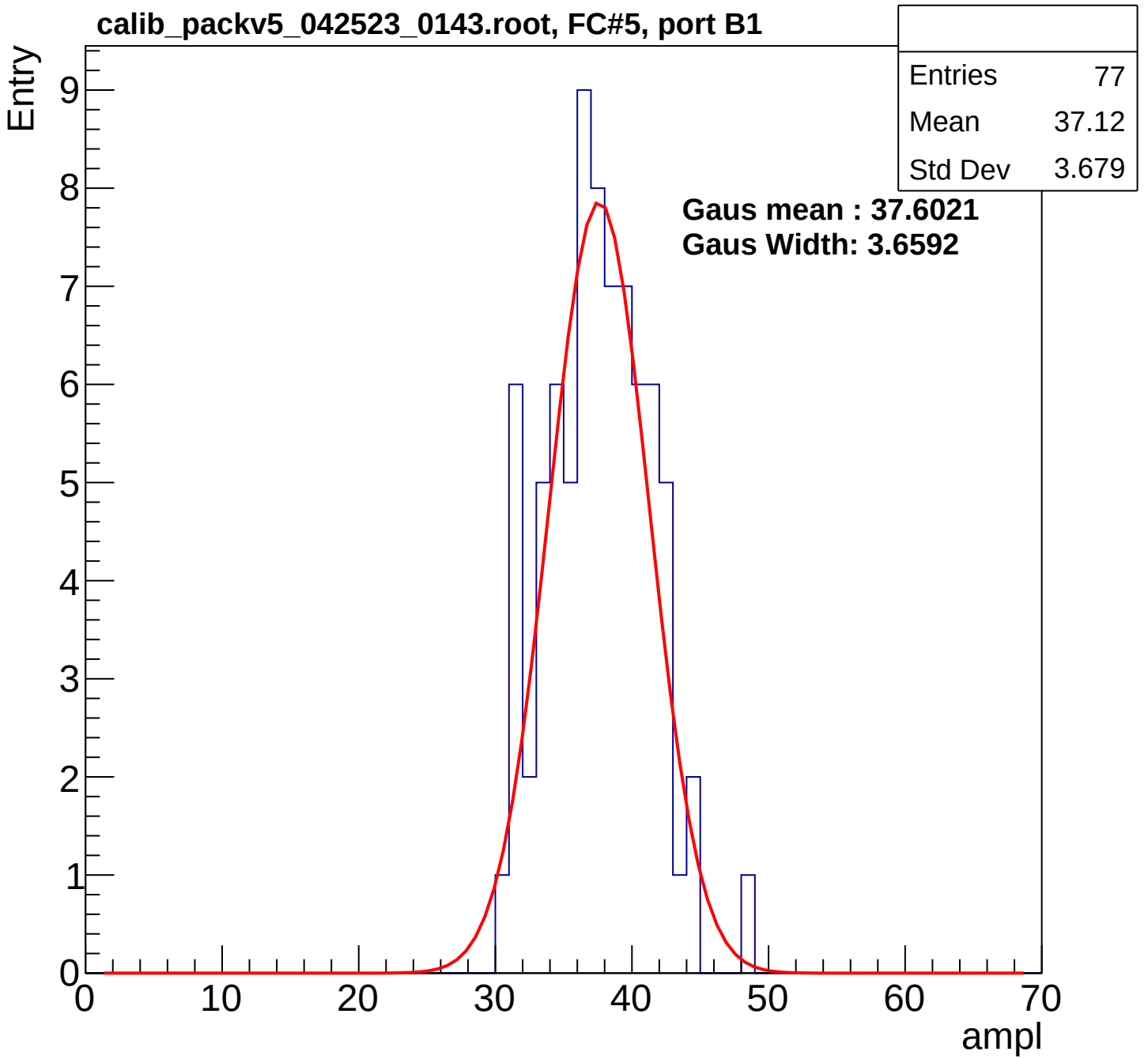

B0L000S, U15-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.69
Std Dev 1.261
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.88
Std Dev 5.068
Gaus mean : 30.6426
Gaus Width: 3.6589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 37.12
Std Dev 3.679
Gaus mean : 37.6021
Gaus Width: 3.6592
Entry
ampl
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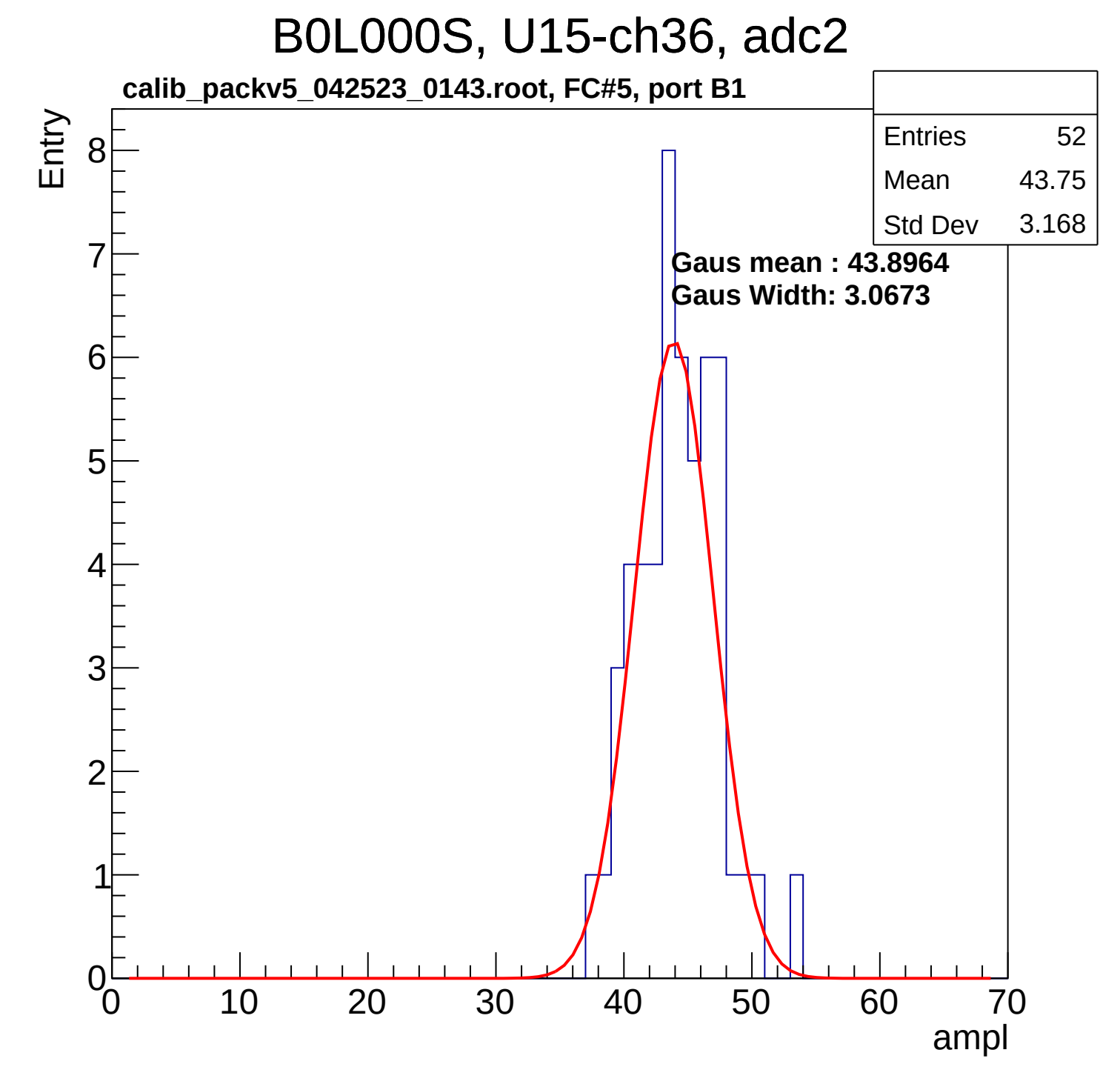
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 43.75
Std Dev 3.168
Gaus mean : 43.8964
Gaus Width: 3.0673
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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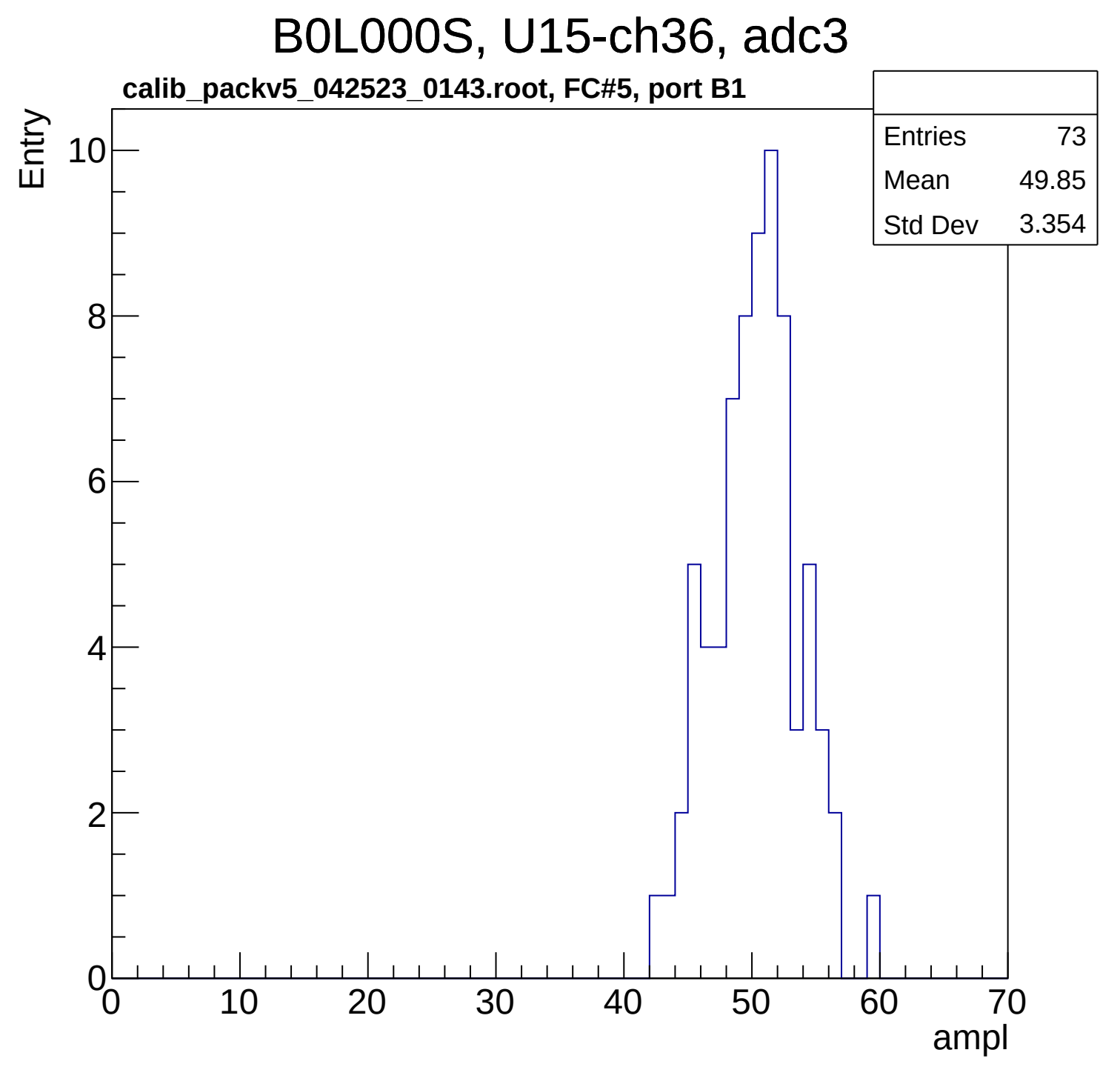
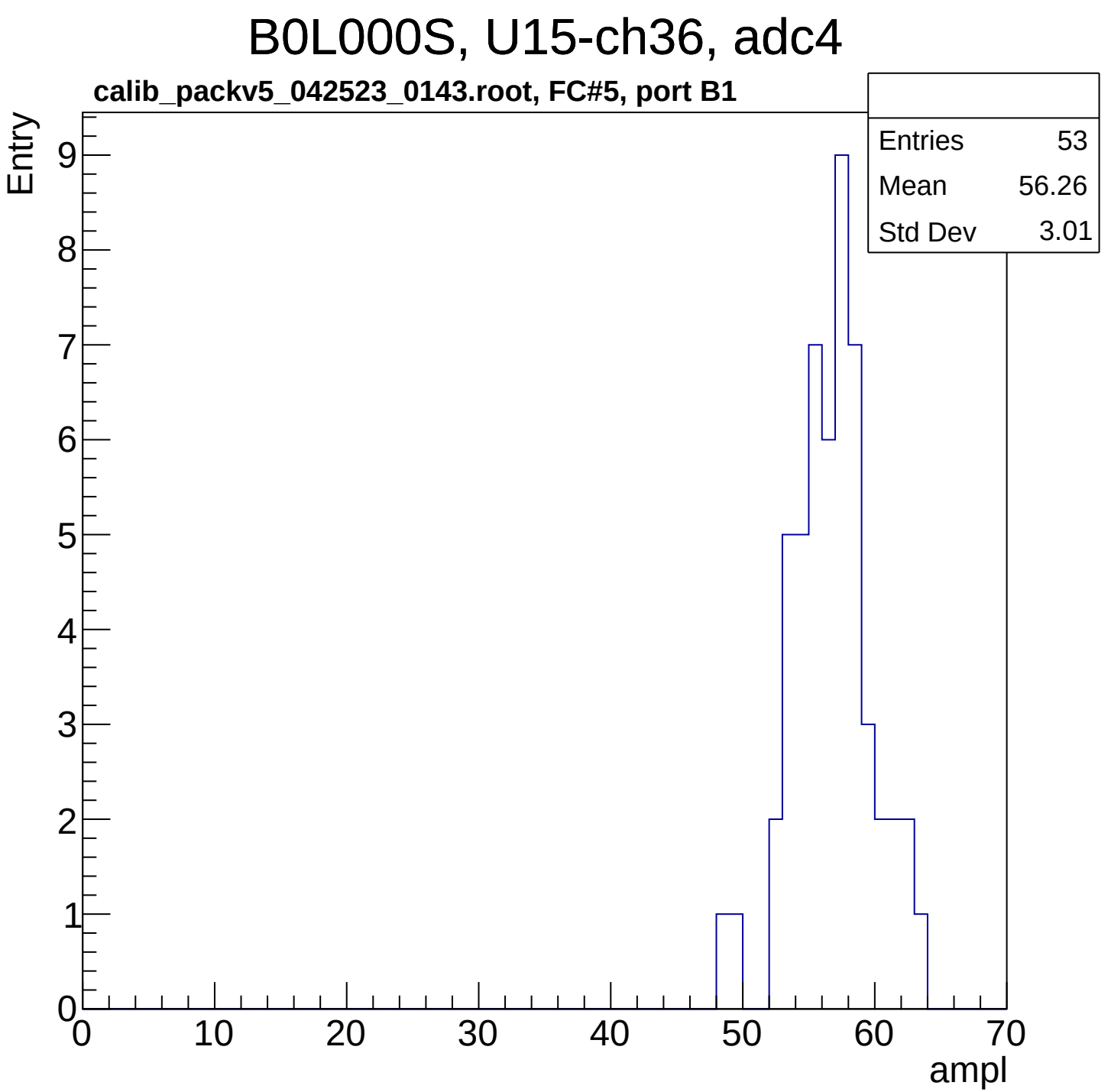

B0L000S, U15-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.85
Std Dev 3.354
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.26
Std Dev 3.01
Entry
ampl
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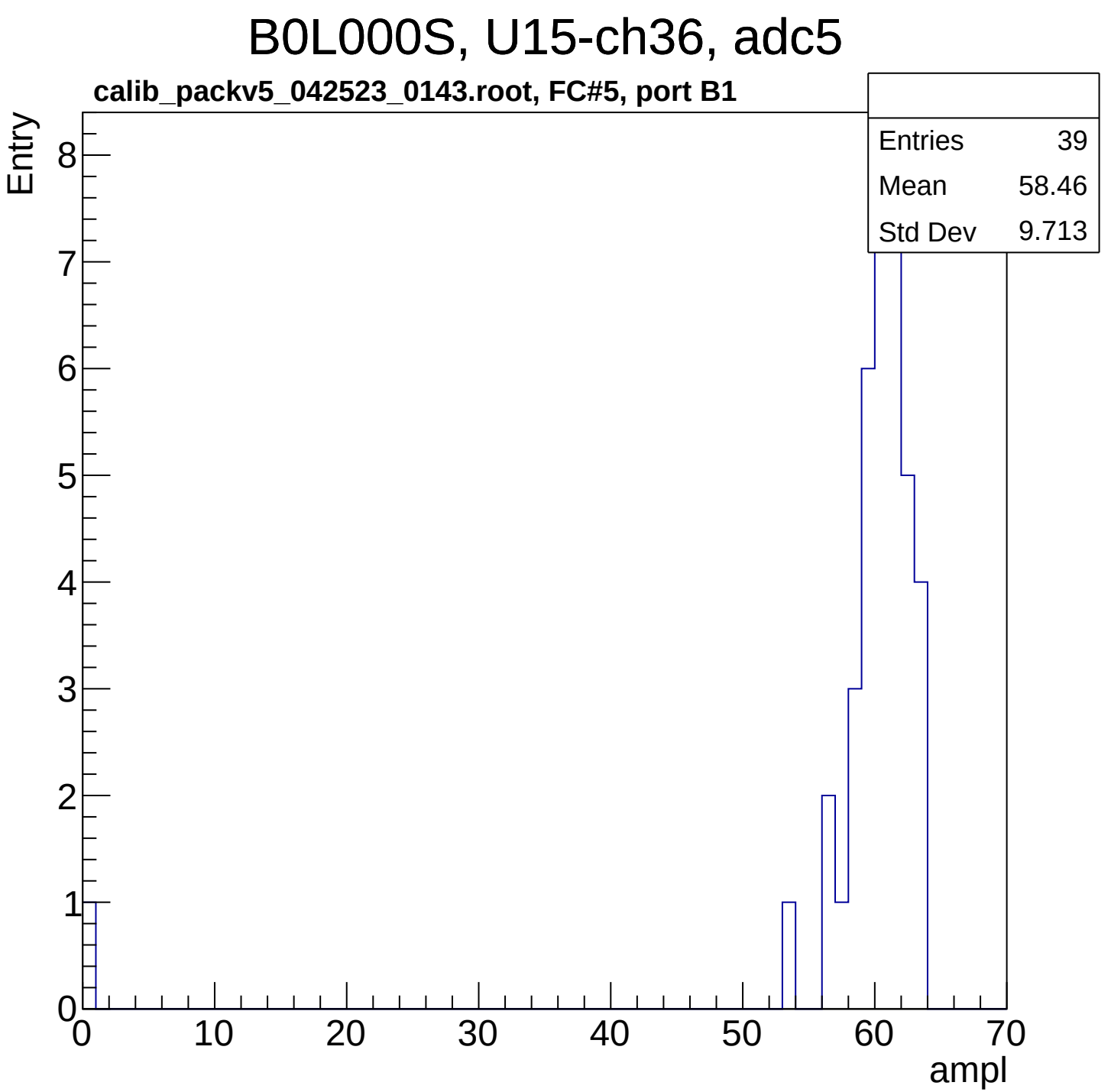
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.46
Std Dev 9.713
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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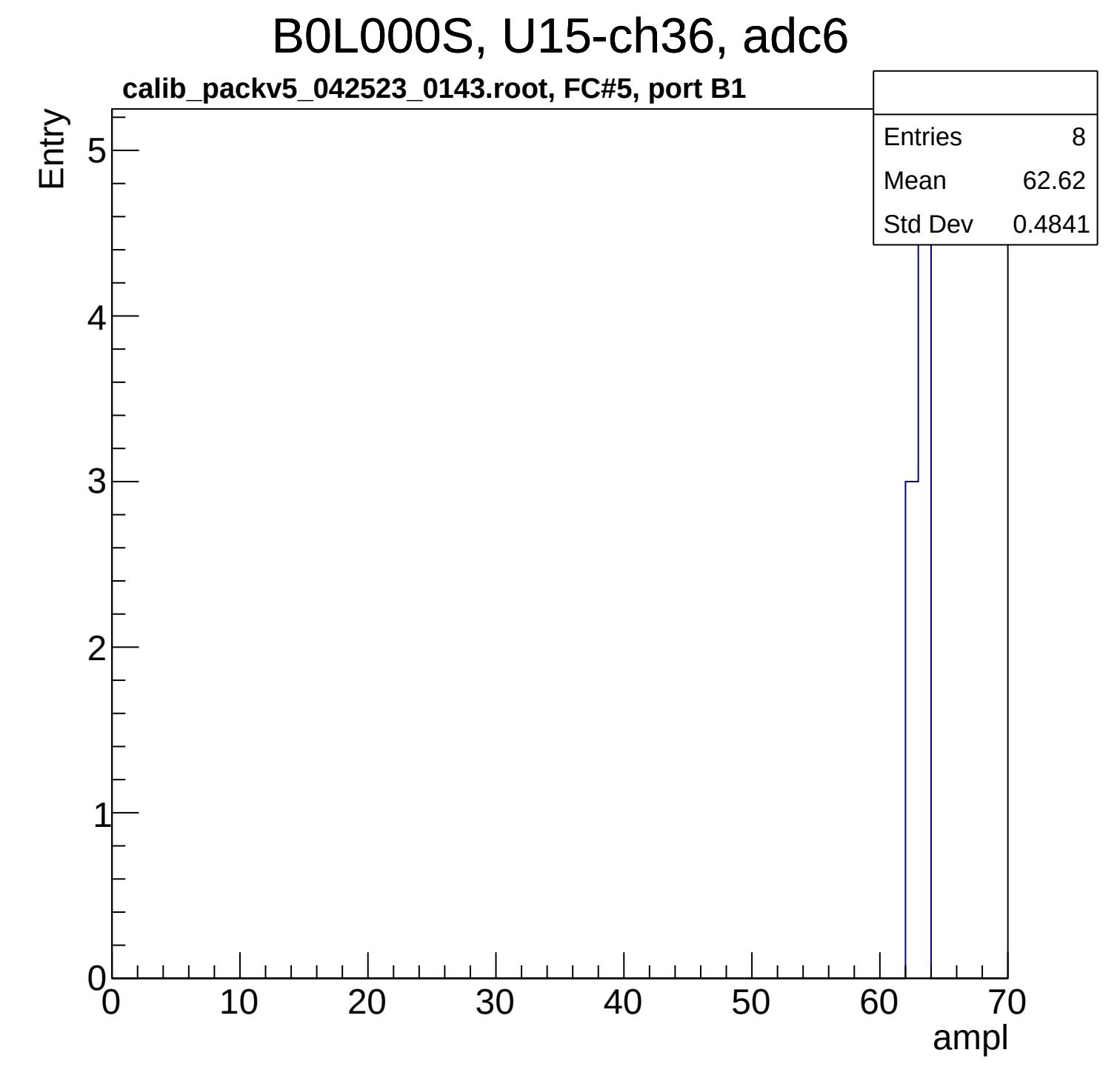
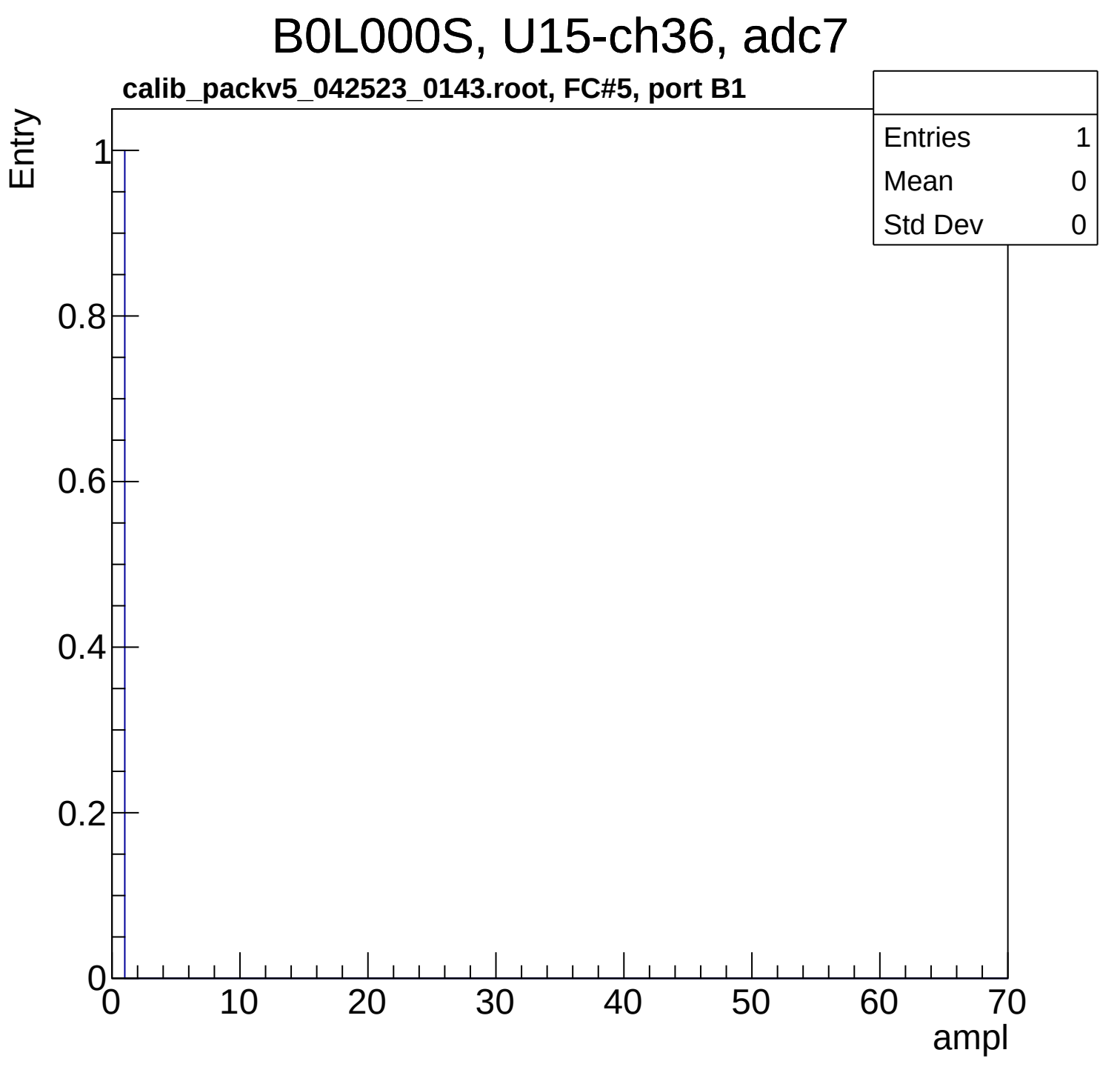

B0L000S, U15-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.62
Std Dev 0.4841
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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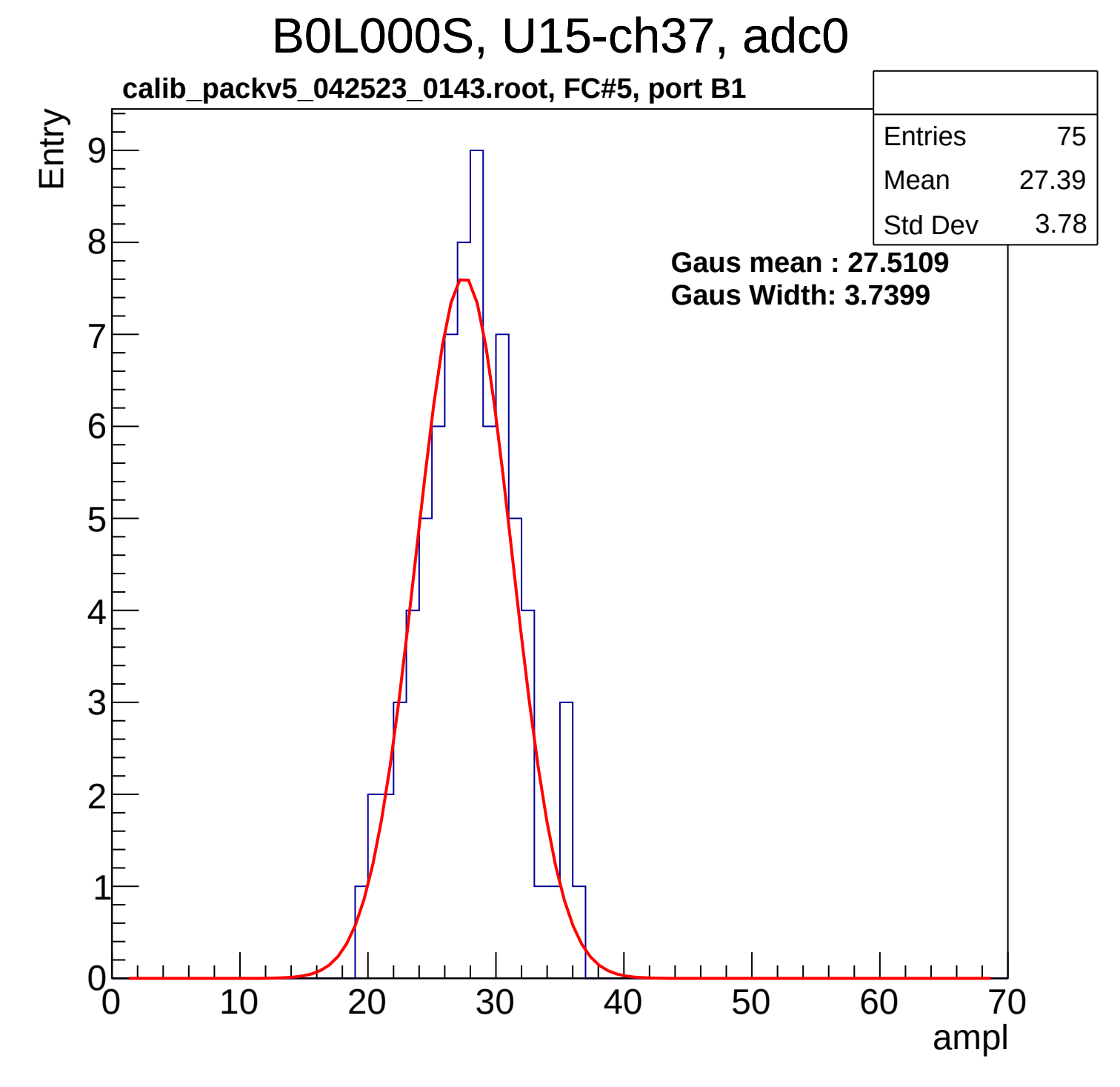
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.39
Std Dev 3.78
Gaus mean : 27.5109
Gaus Width: 3.7399
Entry
ampl
0
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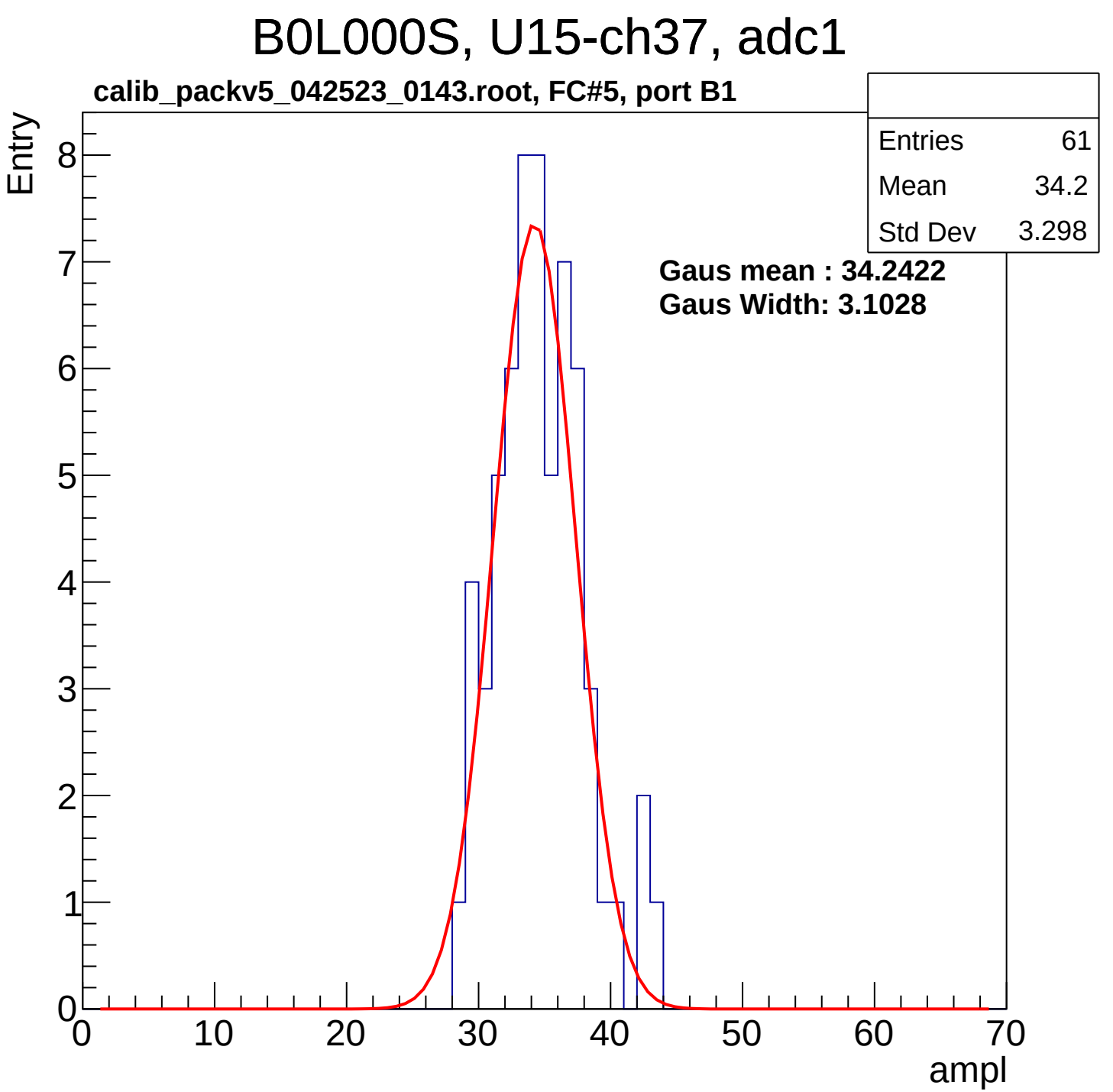
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.2
Std Dev 3.298
Gaus mean : 34.2422
Gaus Width: 3.1028
Entry
ampl
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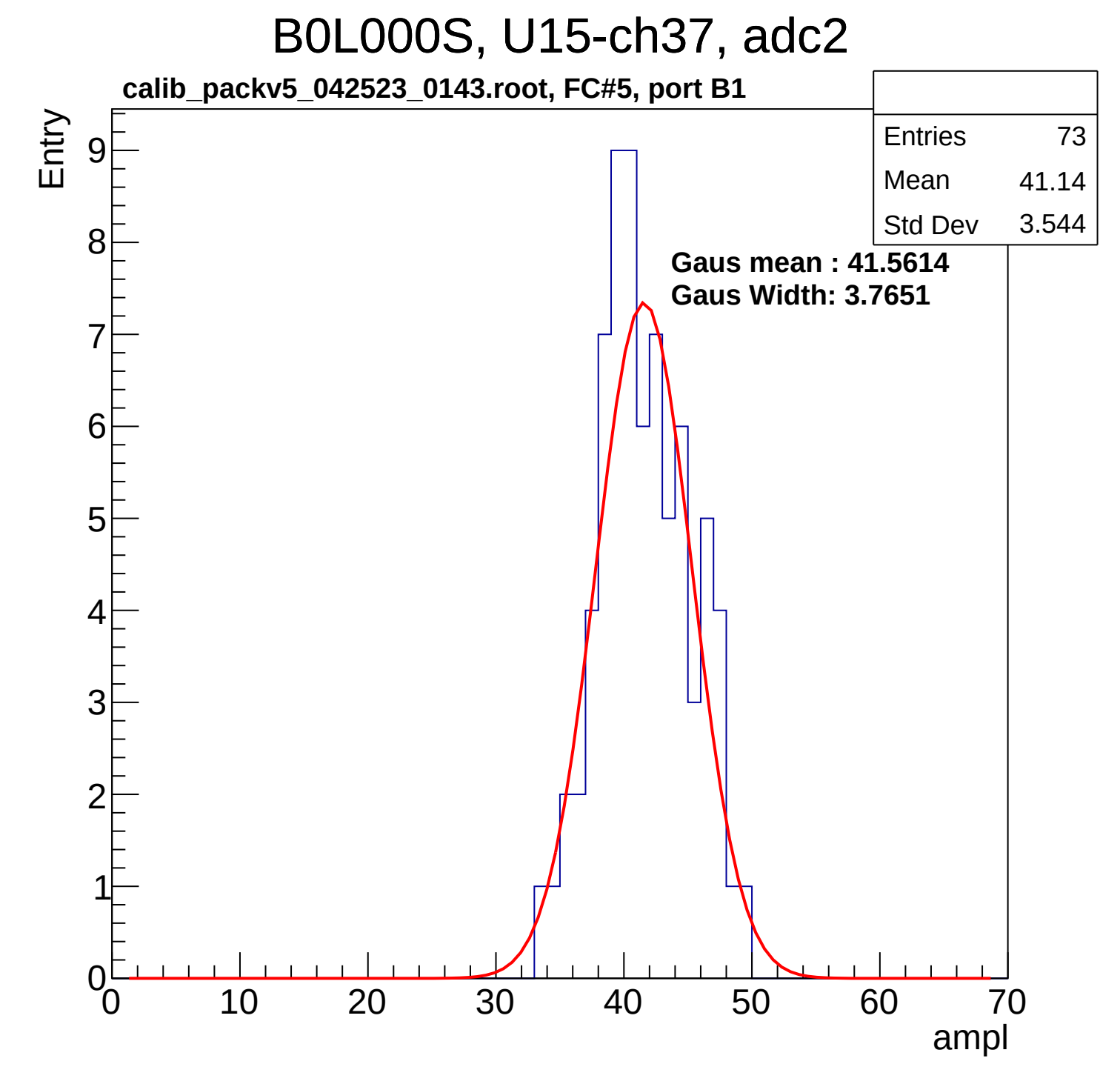
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 41.14
Std Dev 3.544
Gaus mean : 41.5614
Gaus Width: 3.7651
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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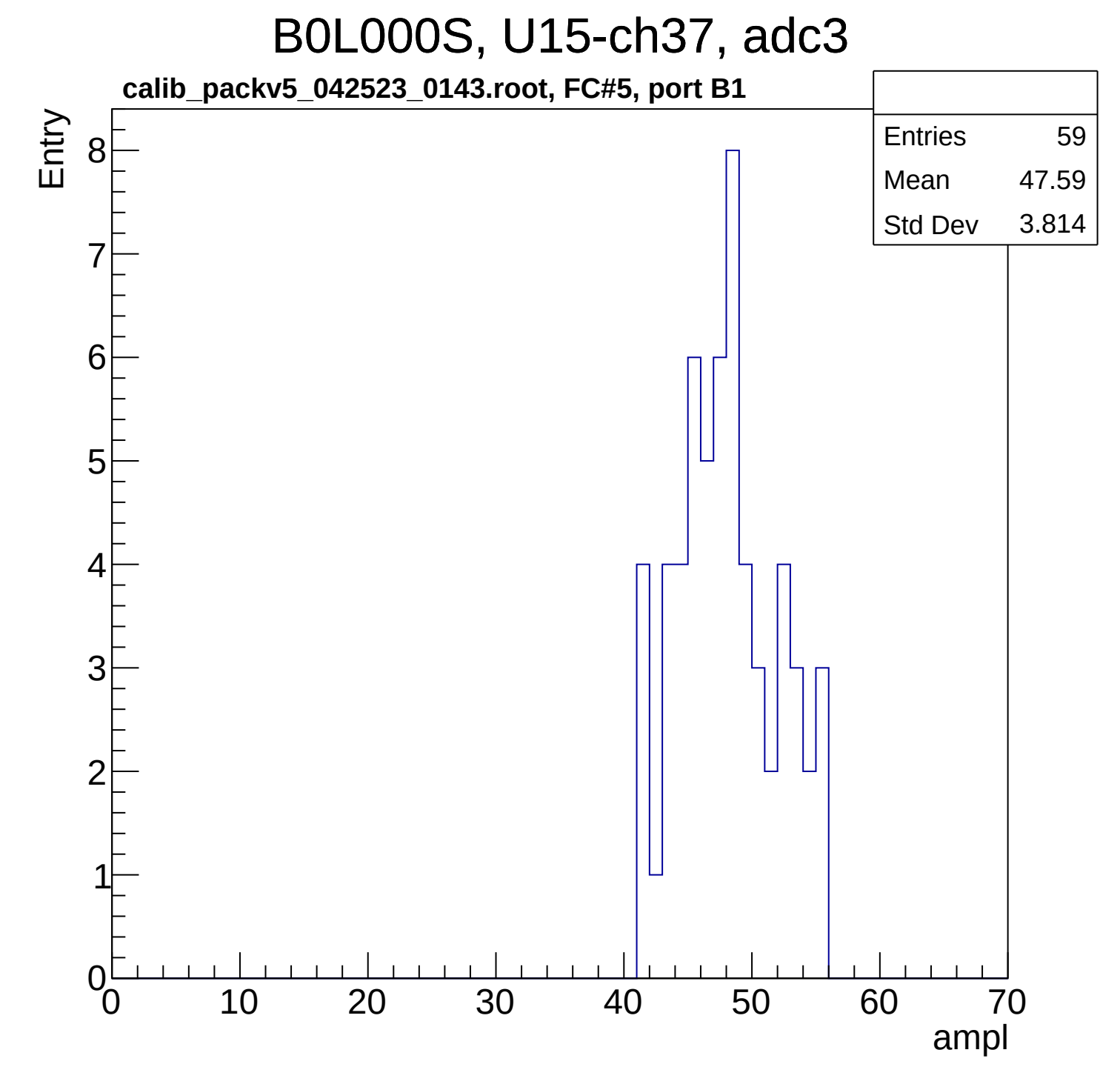
B0L000S, U15-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 47.59
Std Dev 3.814
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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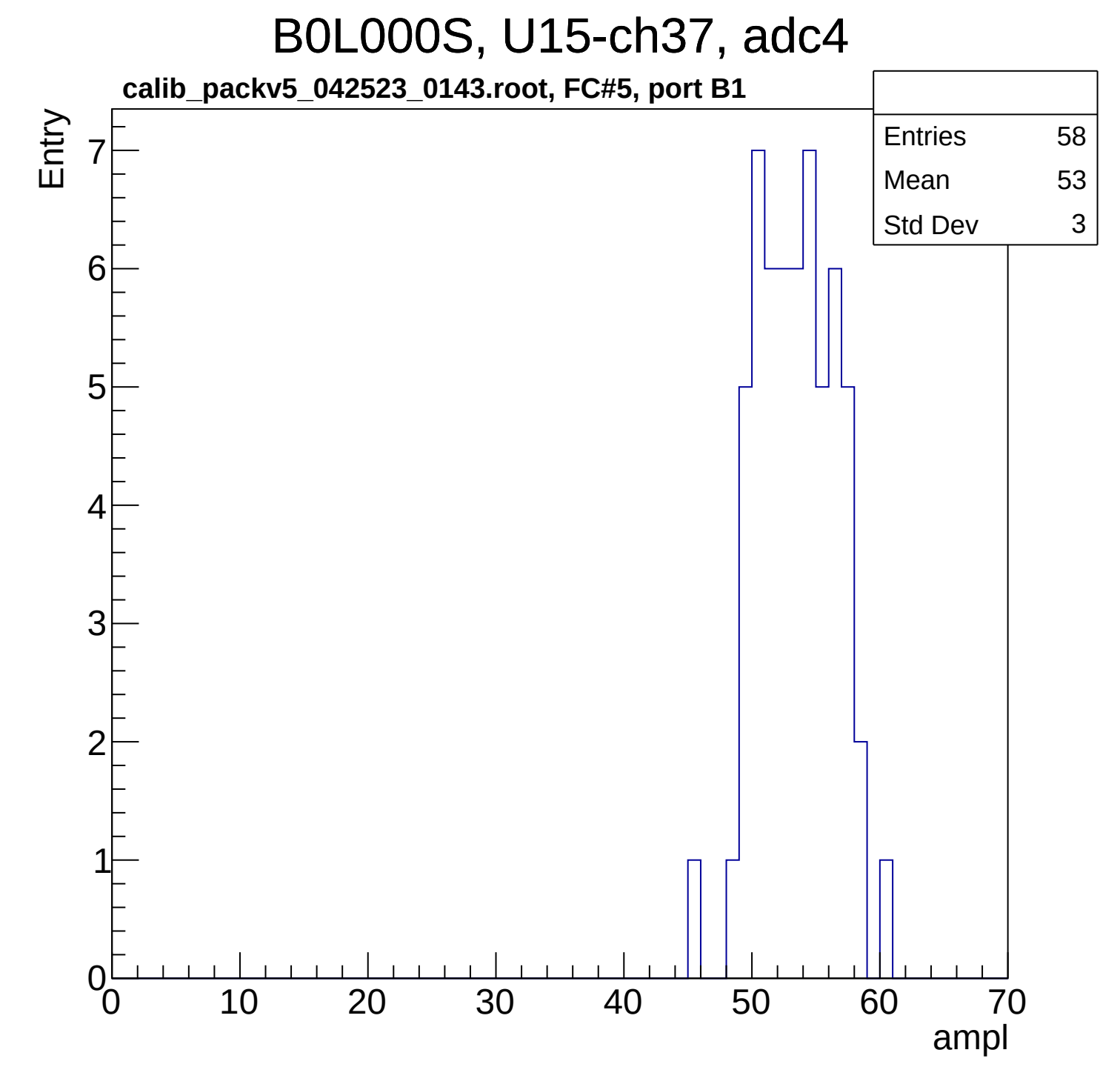
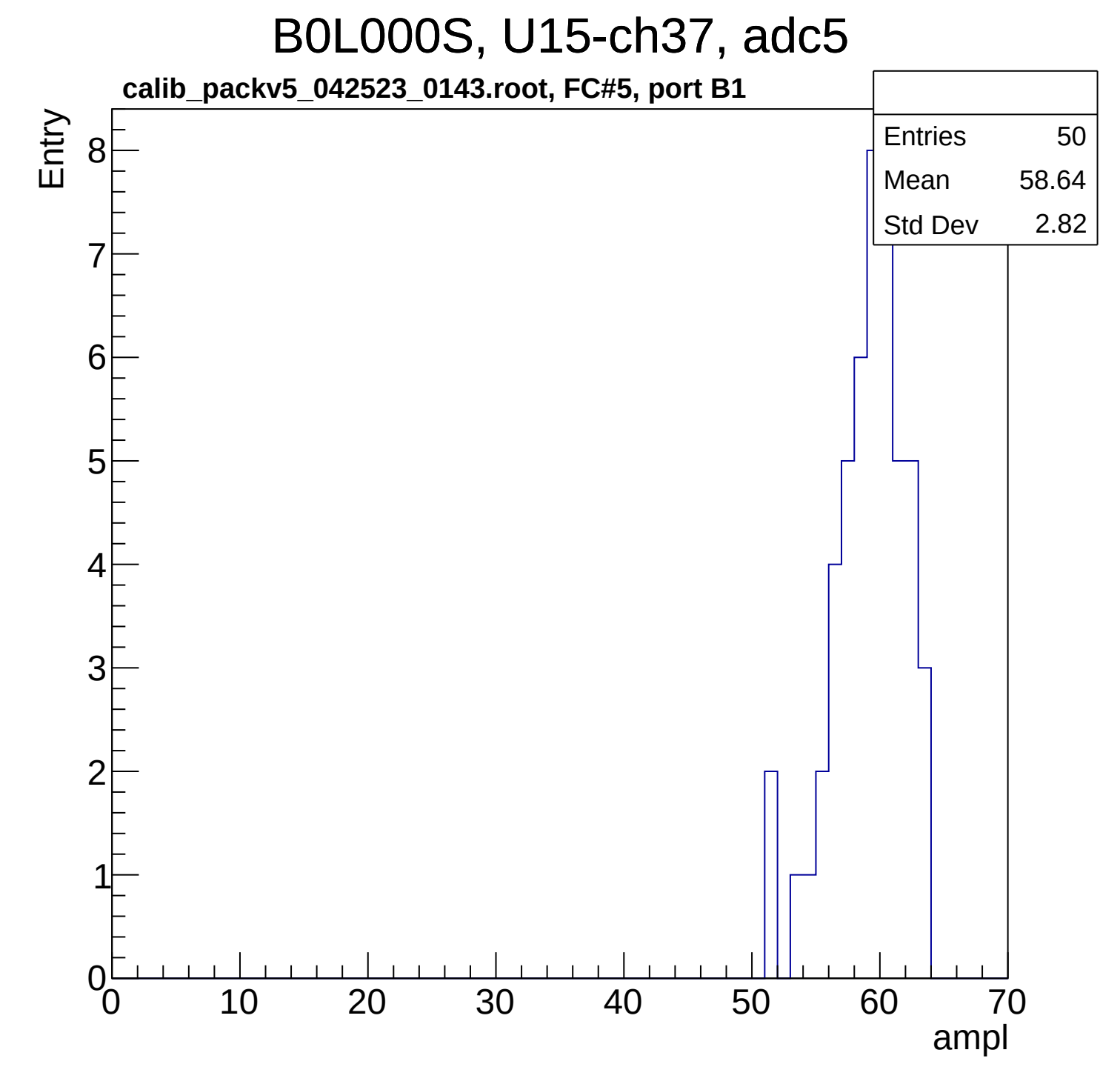
40
50
60
70


B0L000S, U15-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53
Std Dev 3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.64
Std Dev 2.82
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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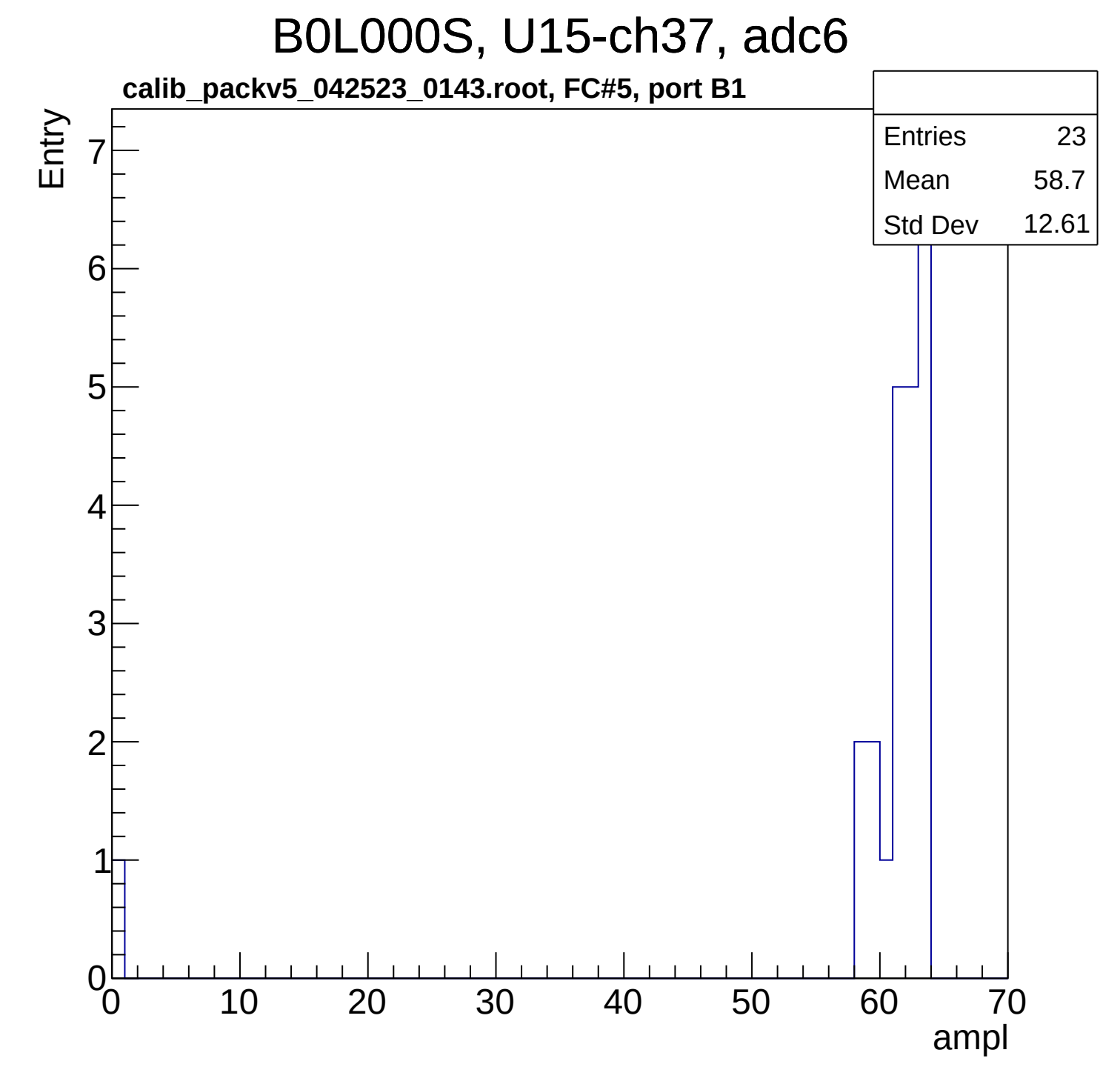
40
50
60
70


B0L000S, U15-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.7
Std Dev 12.61
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
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60
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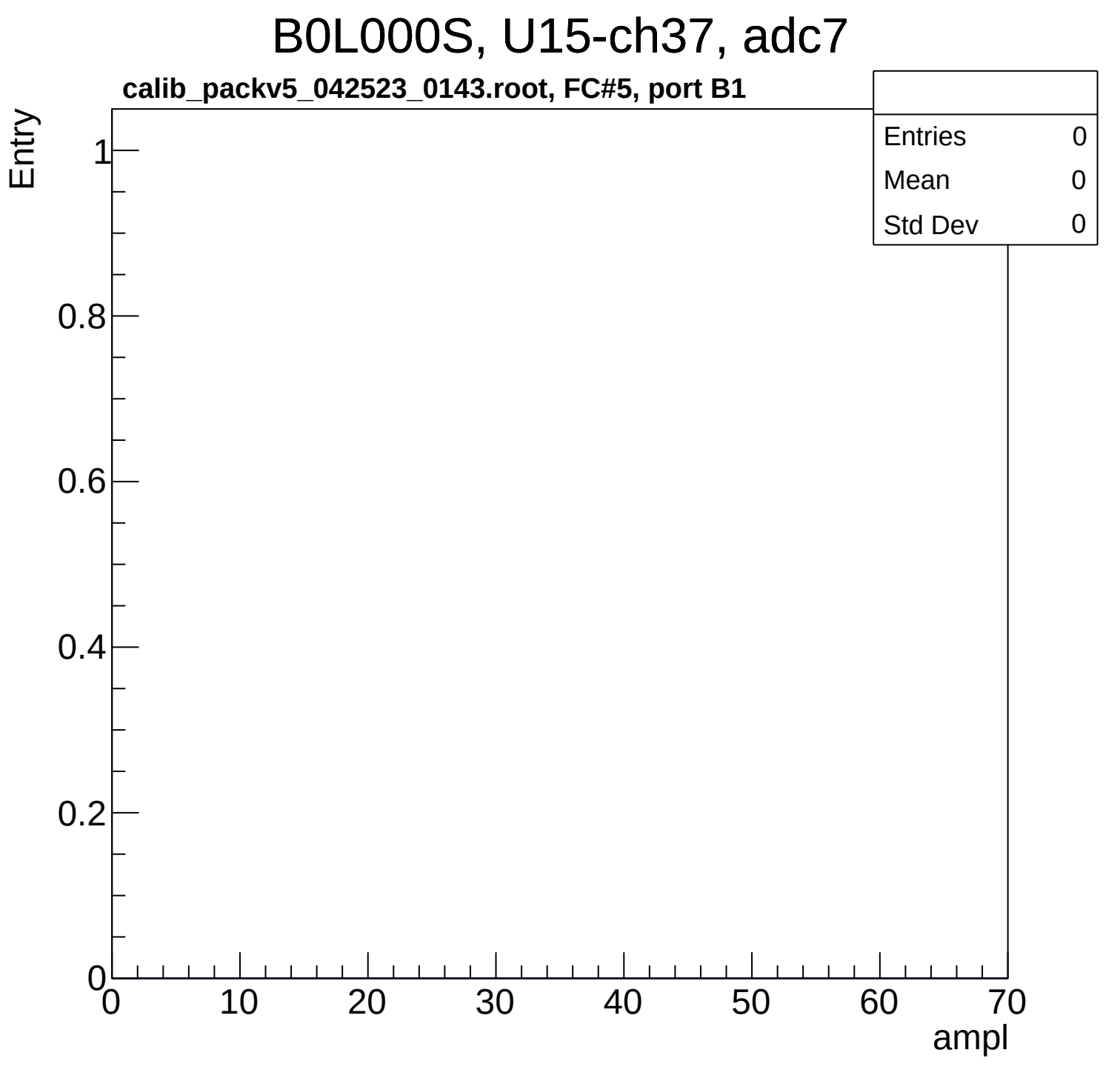
B0L000S, U15-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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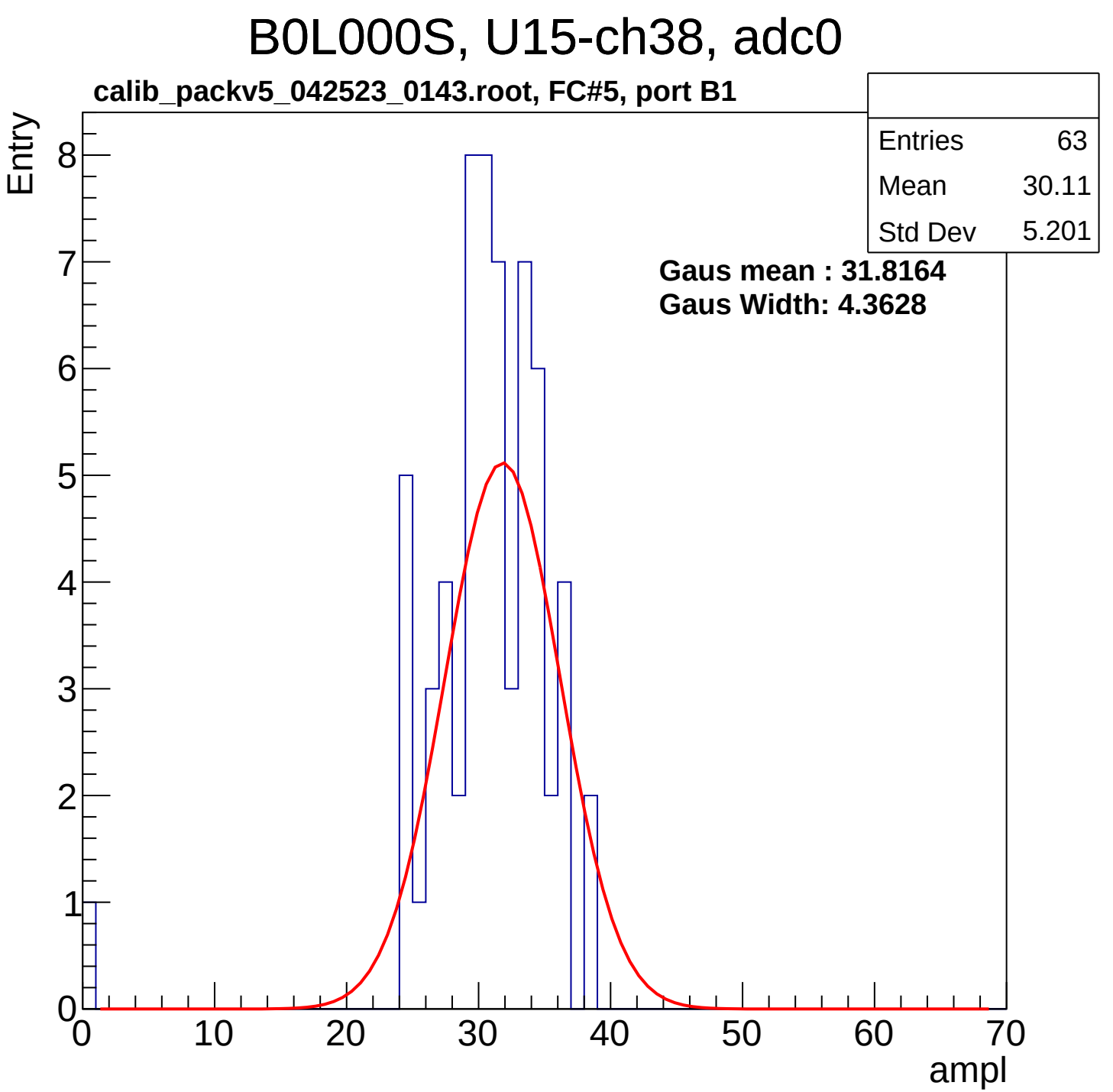
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.11
Std Dev 5.201
Gaus mean : 31.8164
Gaus Width: 4.3628
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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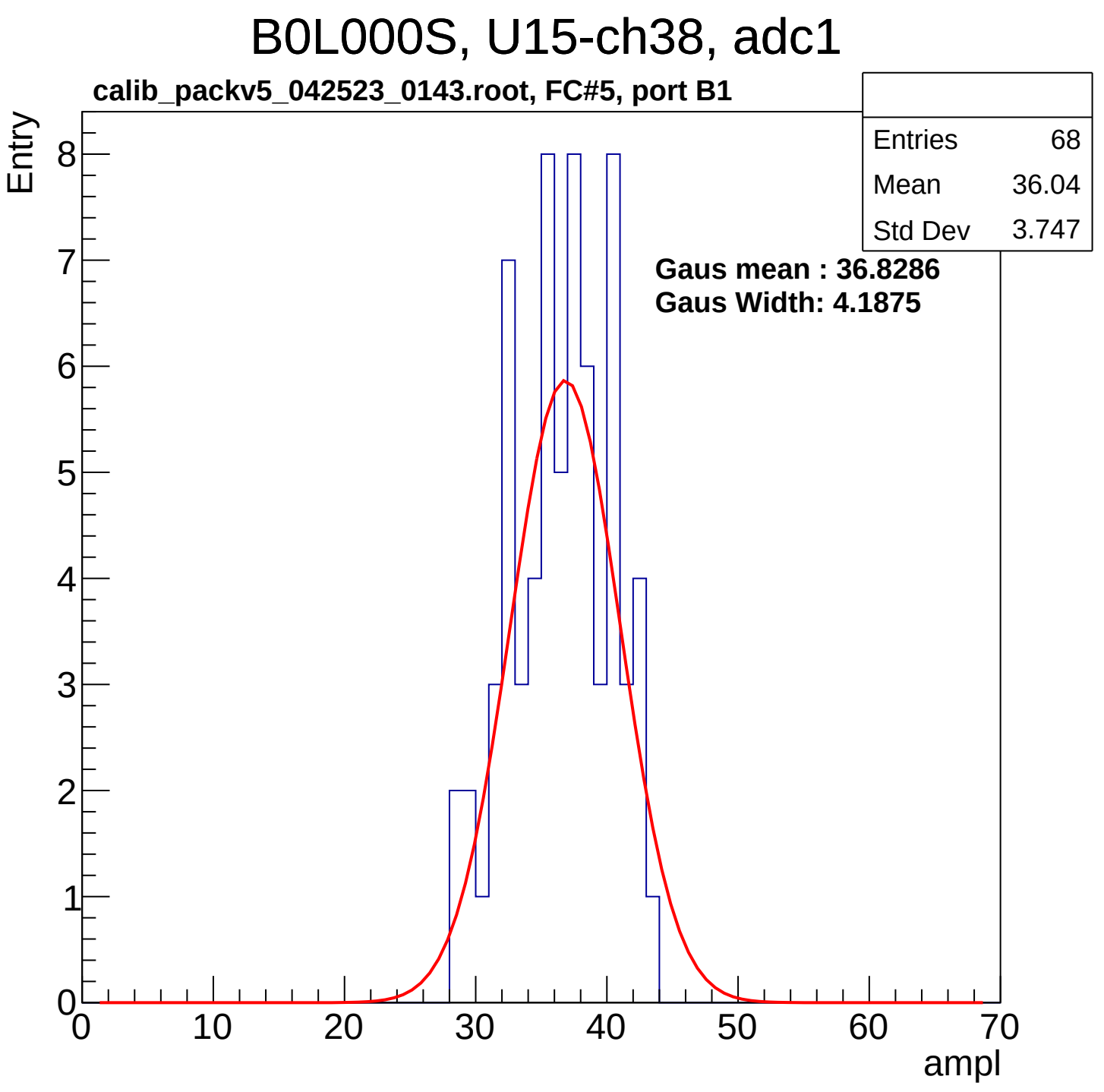
B0L000S, U15-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.04
Std Dev 3.747
Gaus mean : 36.8286
Gaus Width: 4.1875
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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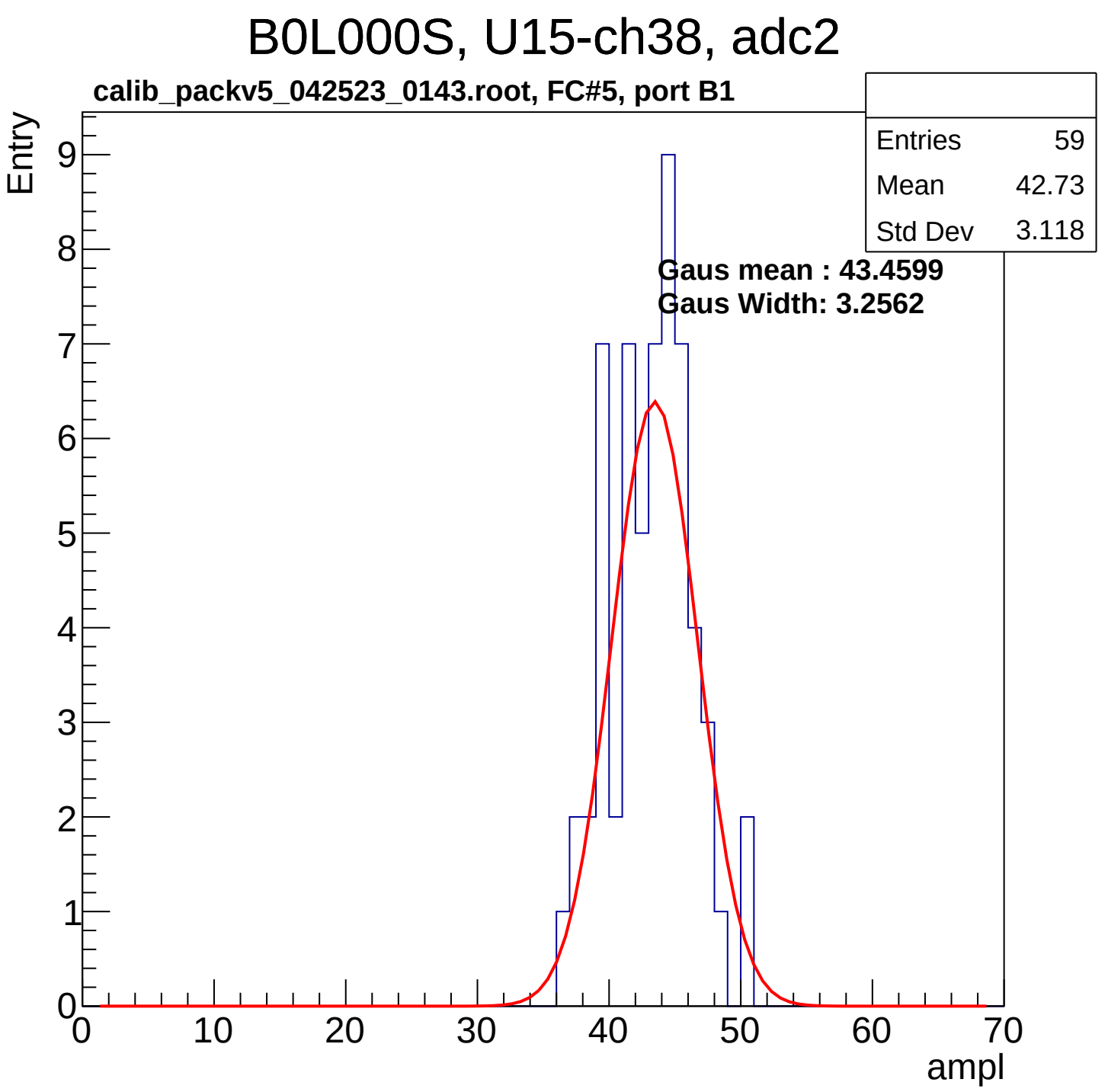
B0L000S, U15-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.73
Std Dev 3.118
Gaus mean : 43.4599
Gaus Width: 3.2562
Entry
ampl
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2
3
4
5
6
7
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9
0
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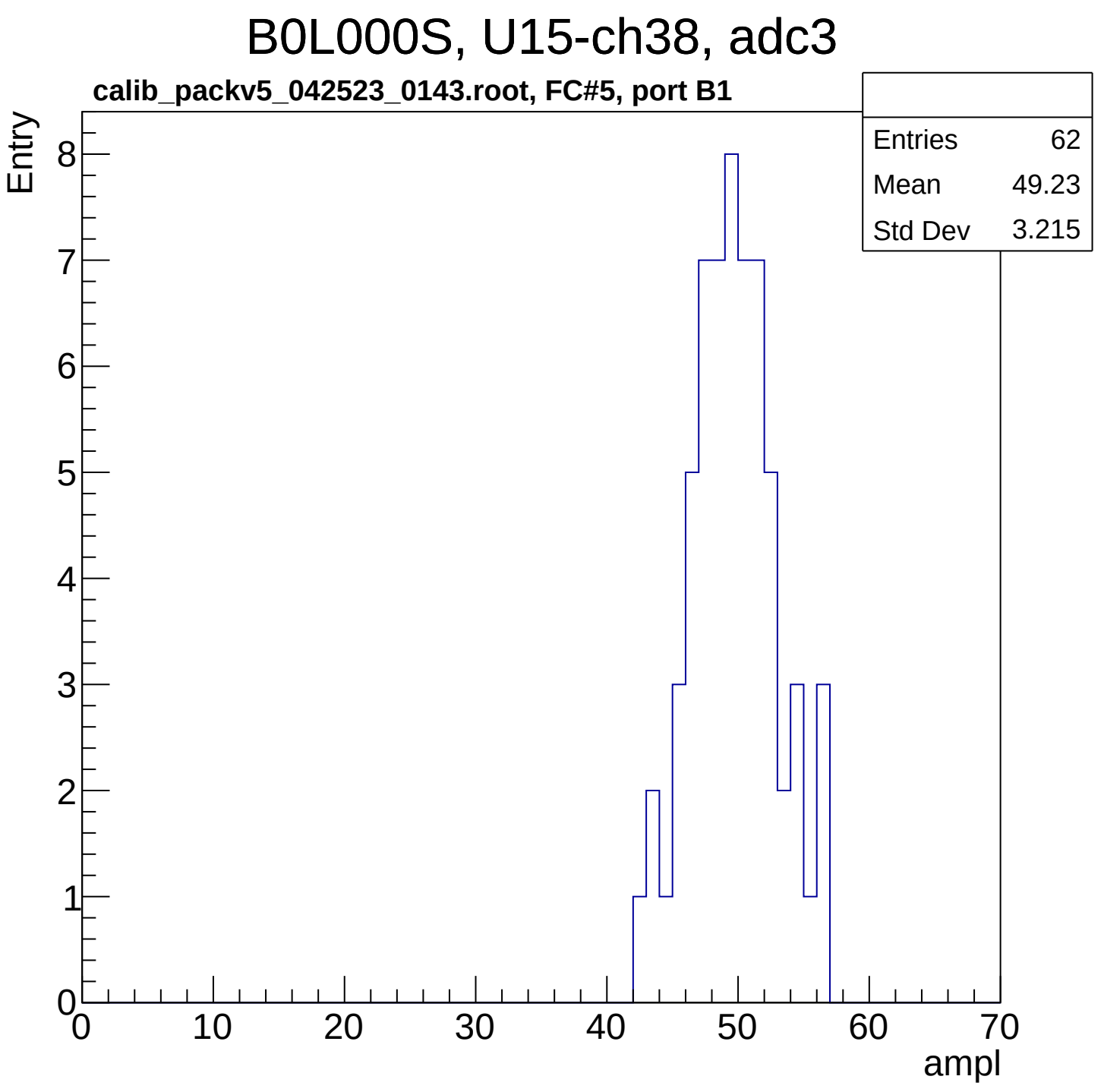
B0L000S, U15-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.23
Std Dev 3.215
Entry
ampl
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3
4
5
6
7
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0
10
20
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60
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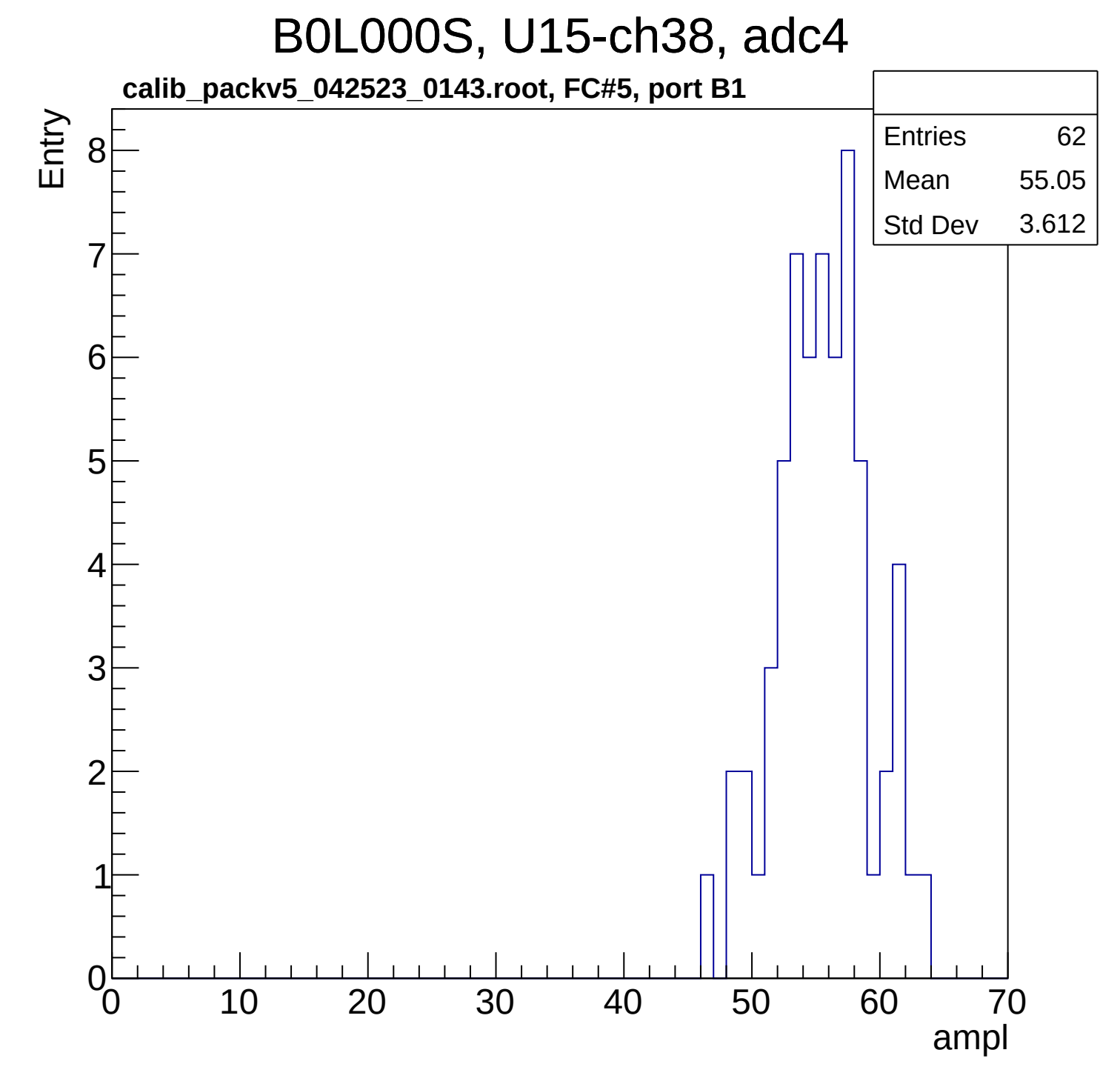
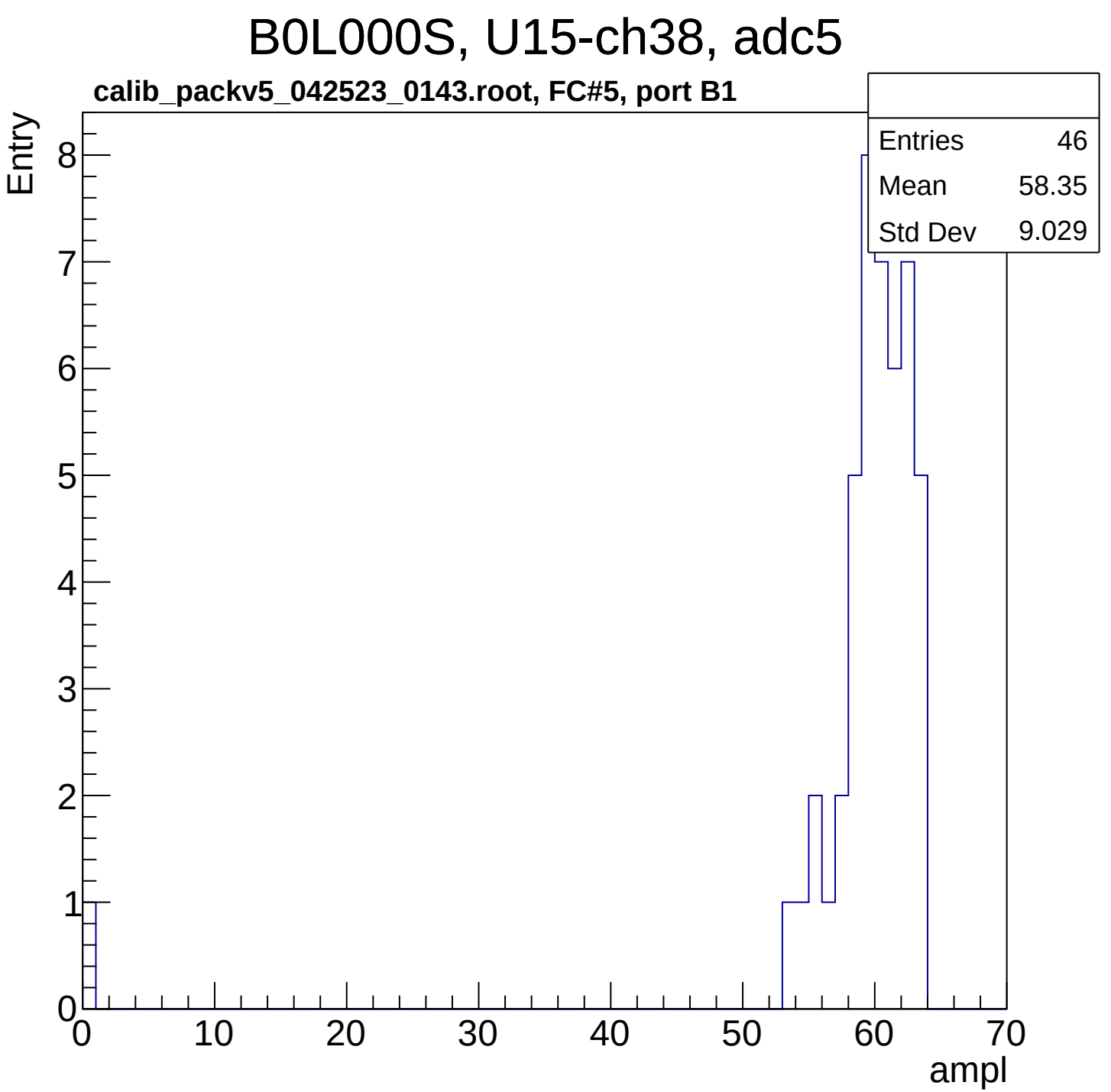

B0L000S, U15-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.05
Std Dev 3.612
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.35
Std Dev 9.029
Entry
ampl
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2
3
4
5
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7
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0
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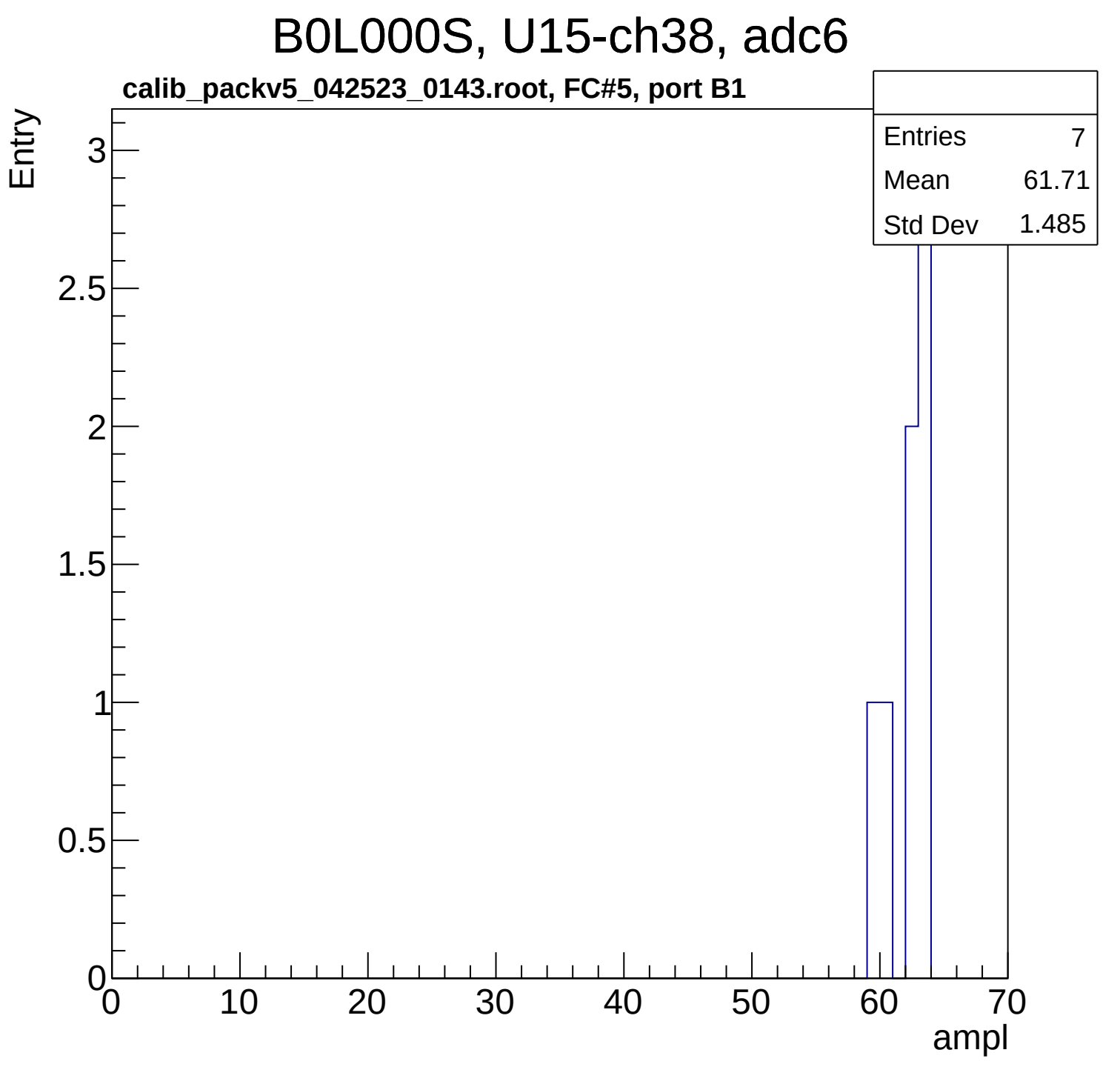
B0L000S, U15-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 1.485
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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40
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60
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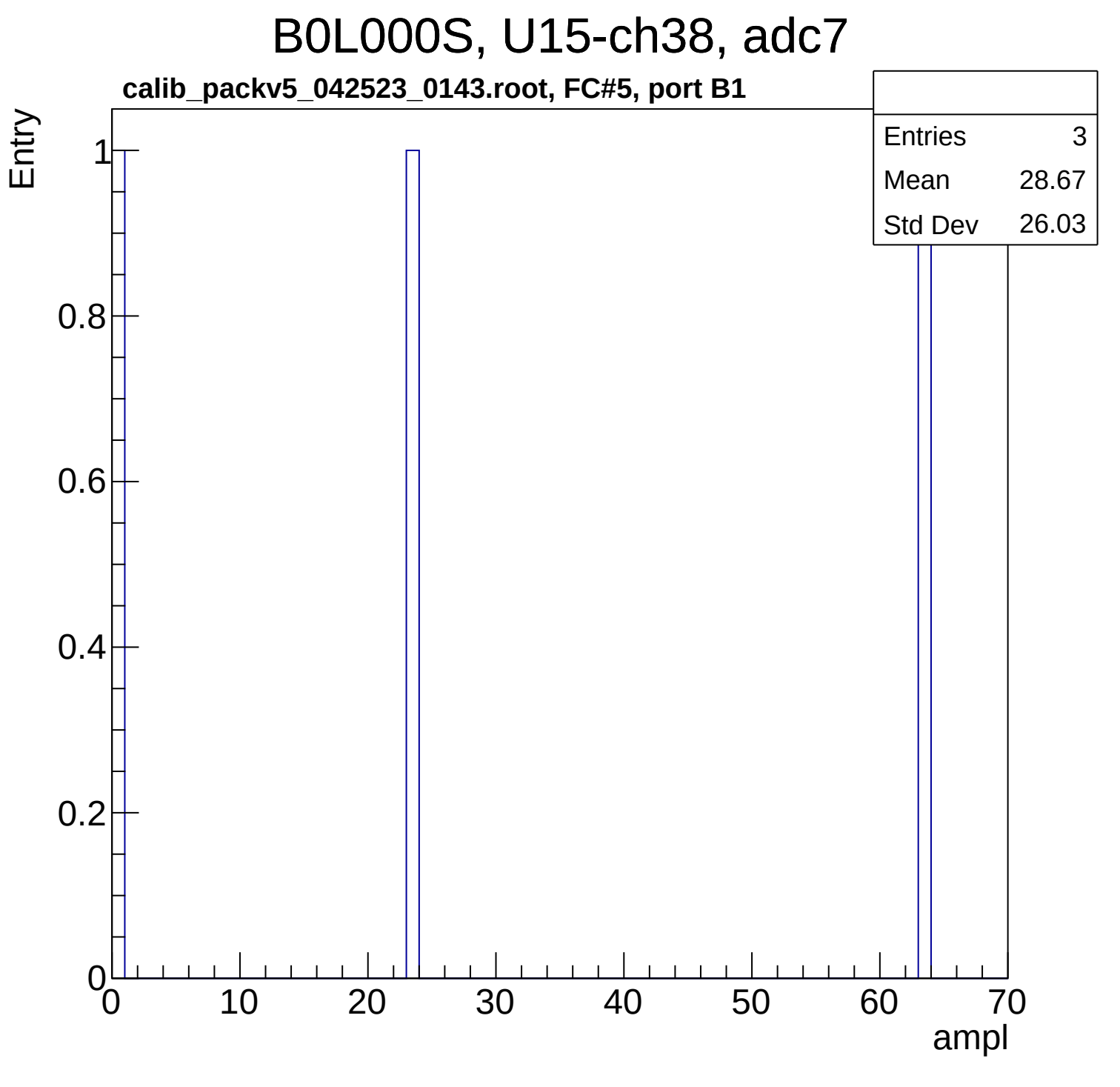
B0L000S, U15-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28.67
Std Dev 26.03
Entry
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0.6
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50
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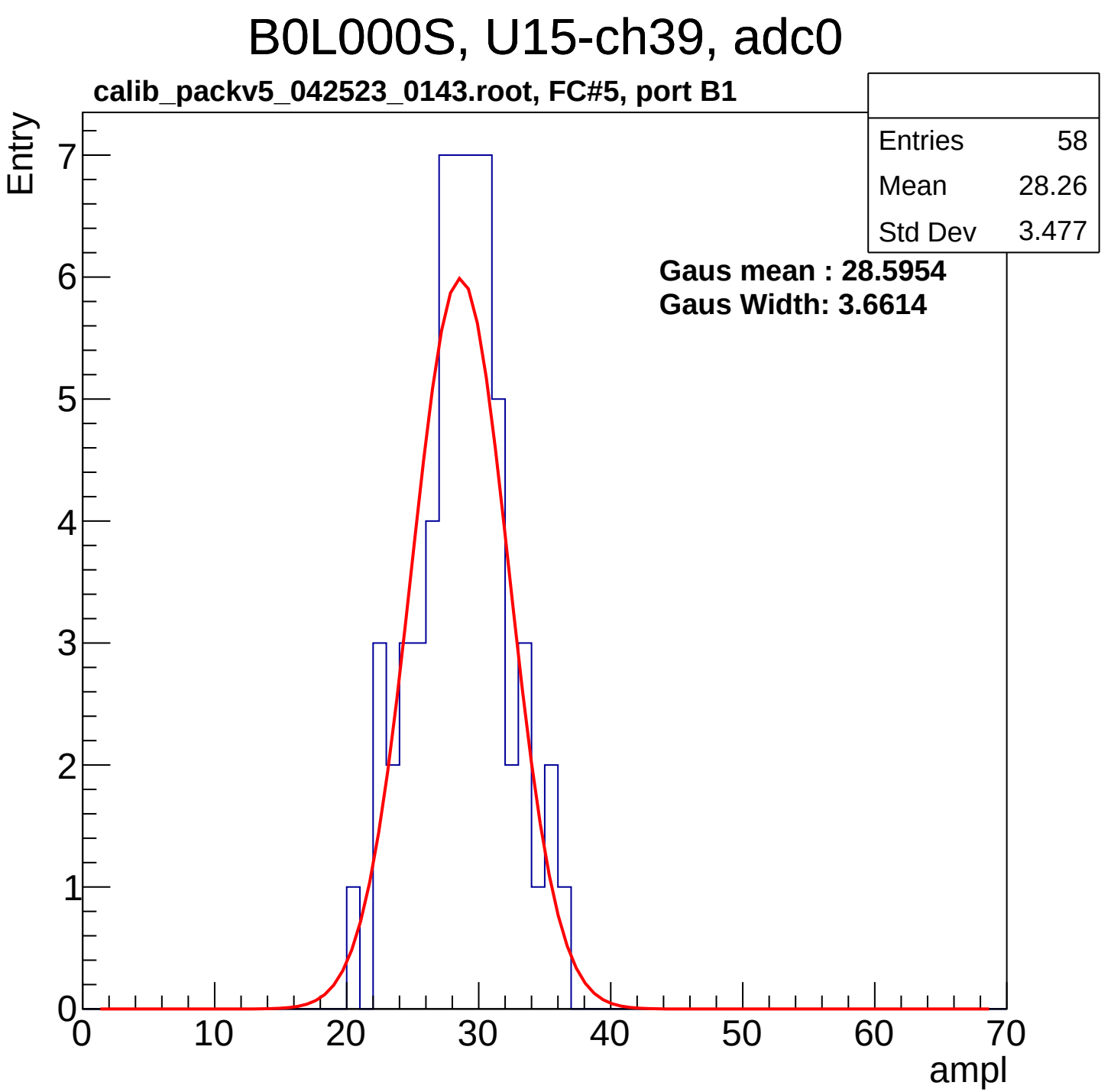
70
ampl


B0L000S, U15-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 28.26
Std Dev 3.477
Gaus mean : 28.5954
Gaus Width: 3.6614
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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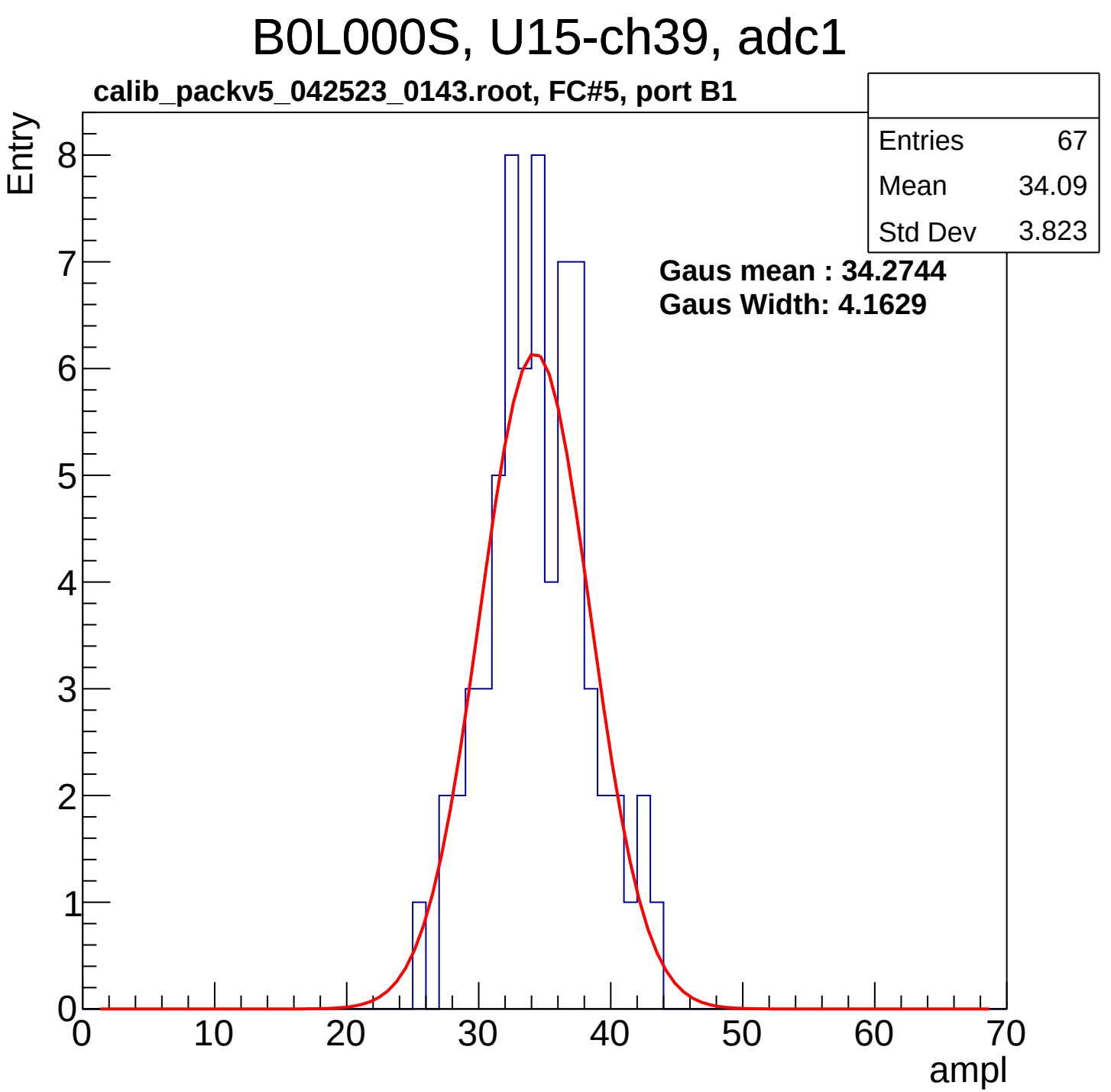
B0L000S, U15-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 34.09
Std Dev 3.823
Gaus mean : 34.2744
Gaus Width: 4.1629
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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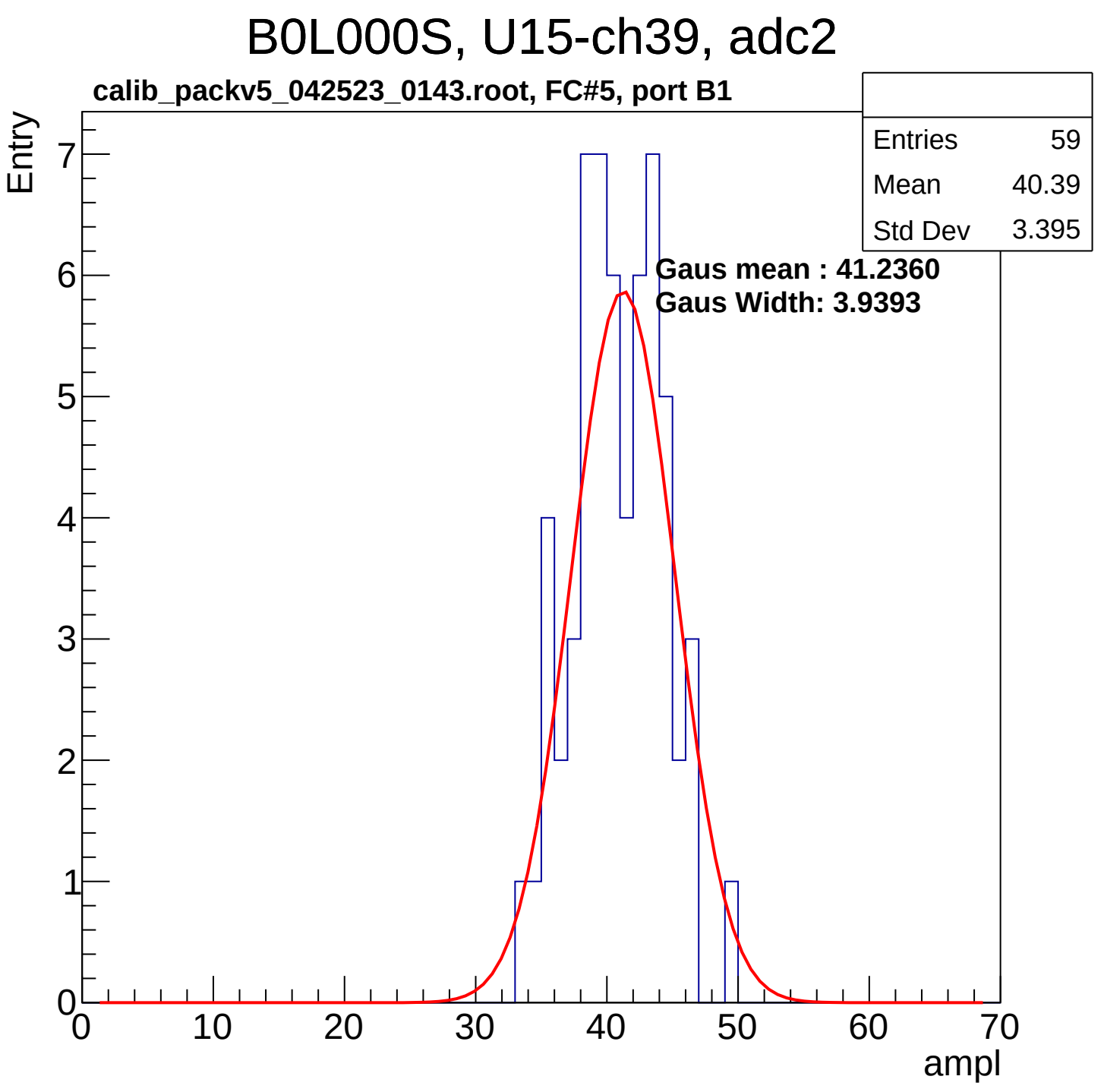
B0L000S, U15-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 40.39
Std Dev 3.395
Gaus mean : 41.2360
Gaus Width: 3.9393
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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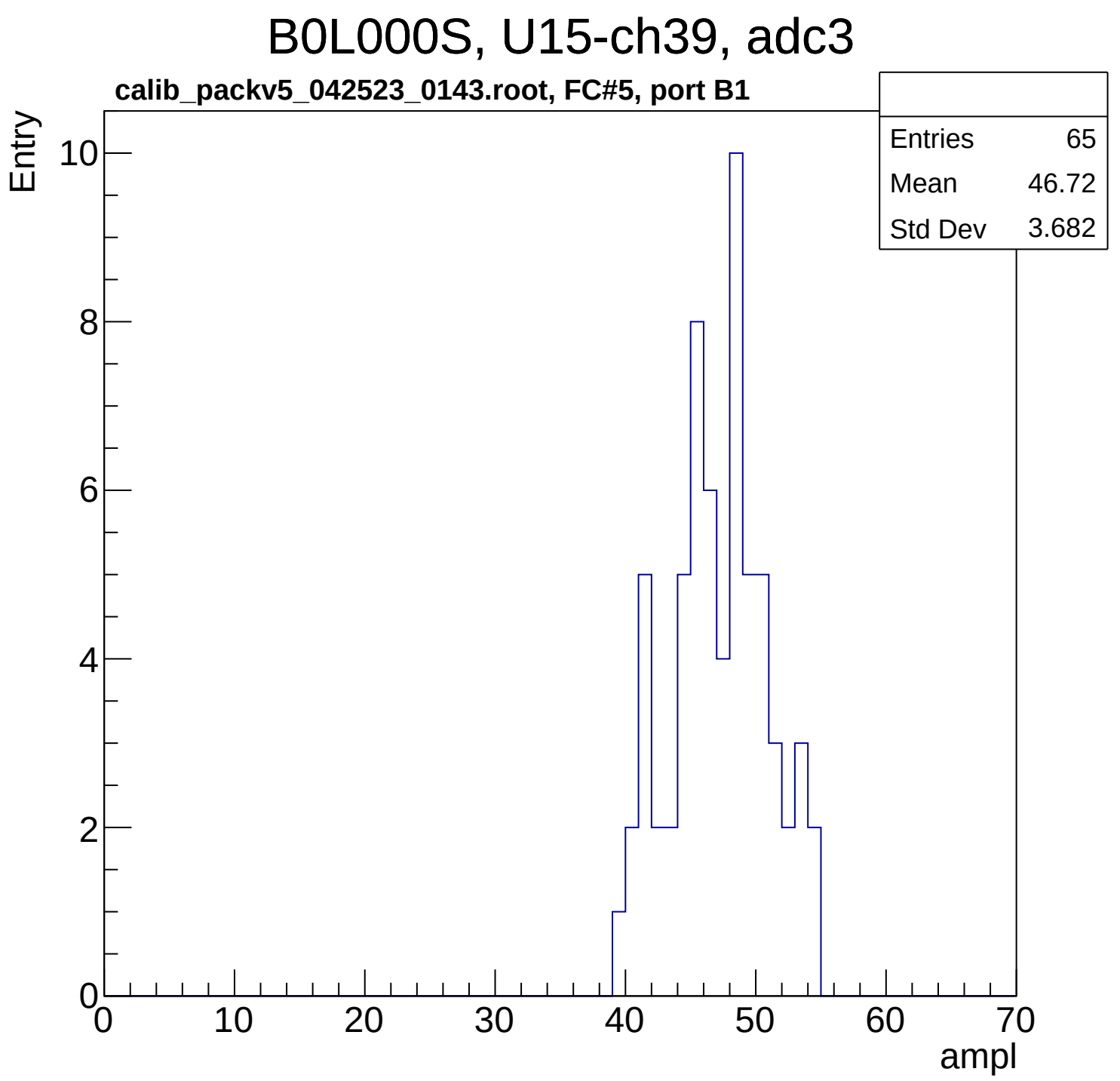
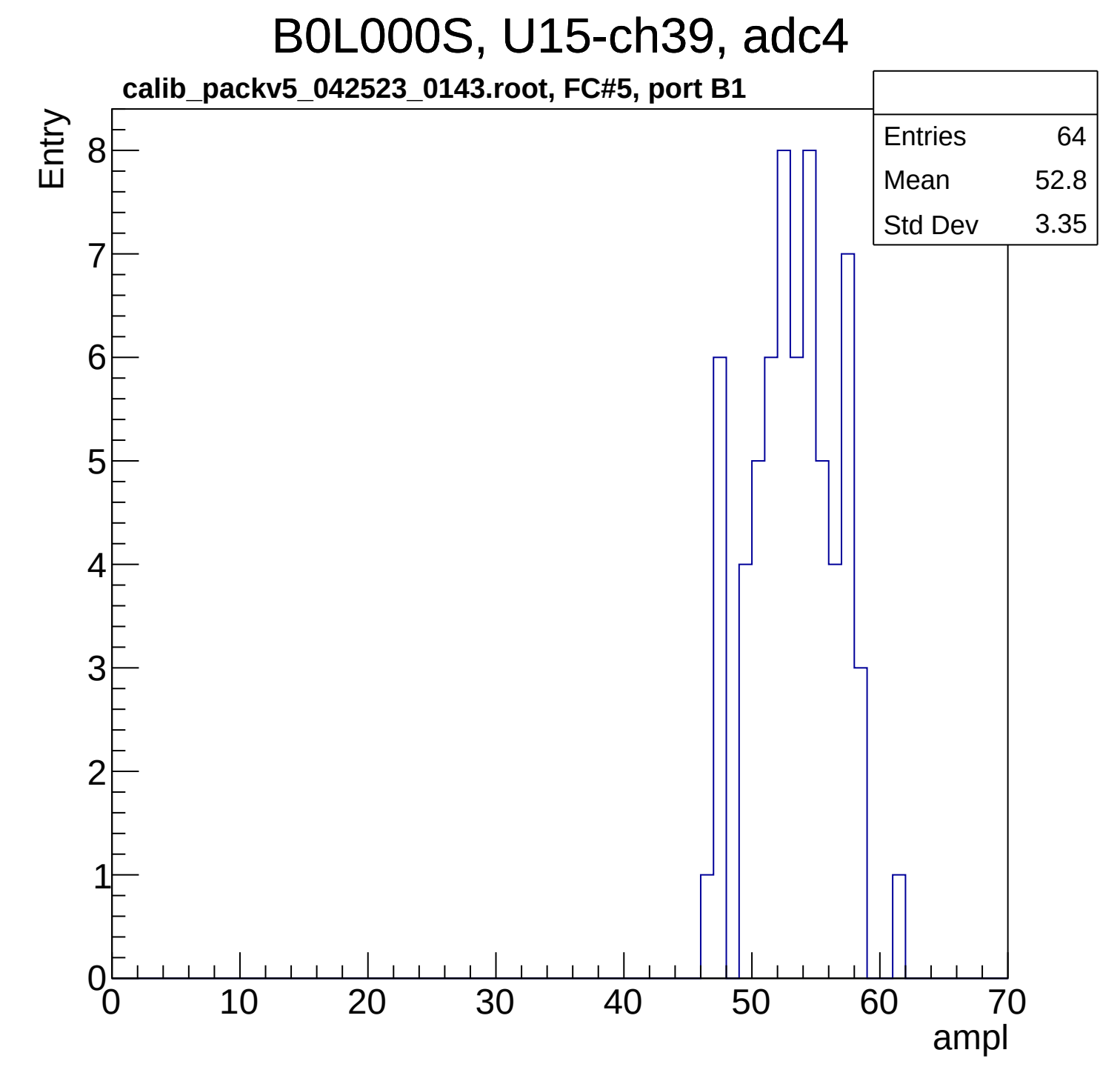

B0L000S, U15-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 46.72
Std Dev 3.682
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 52.8
Std Dev 3.35
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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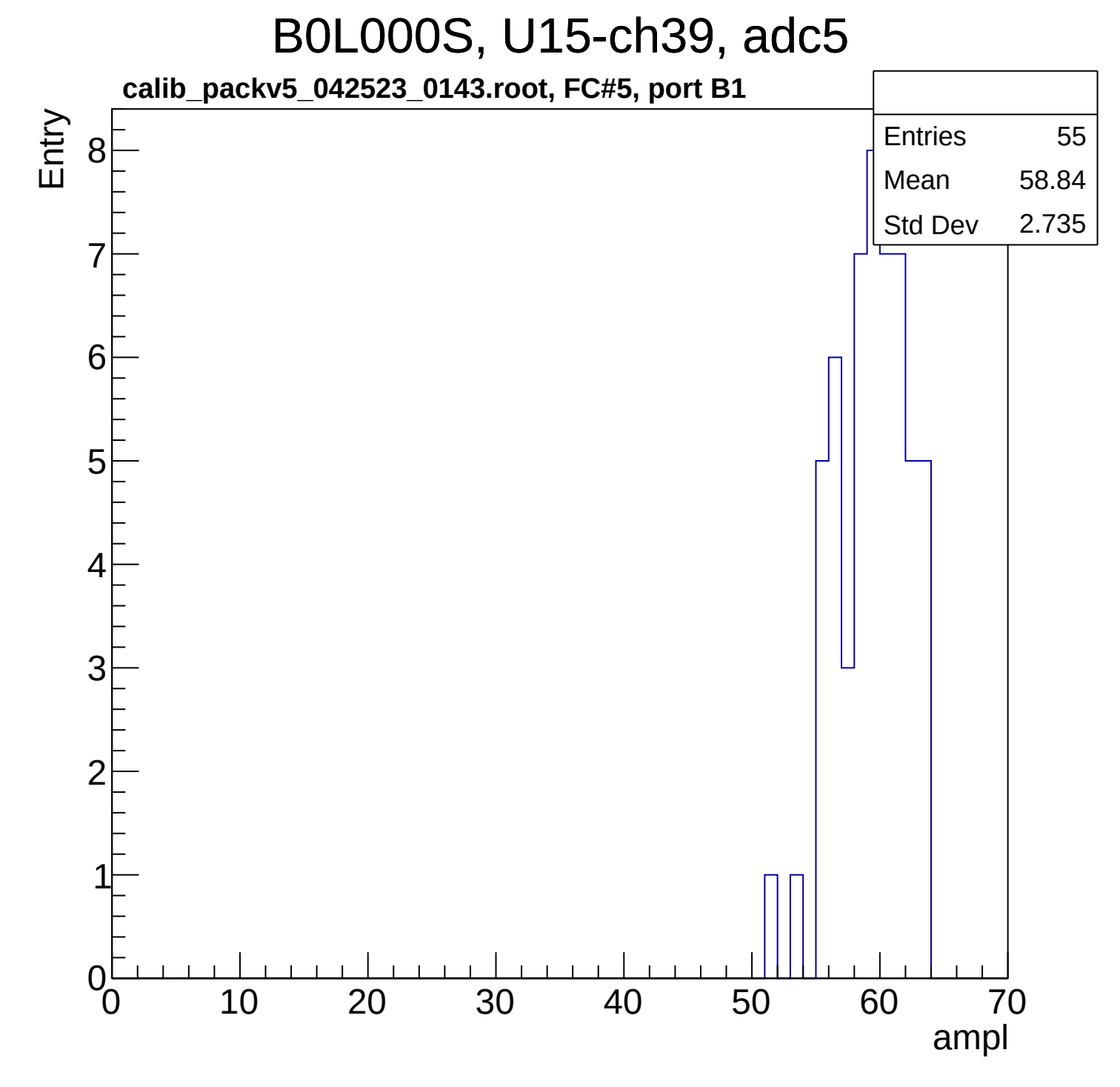
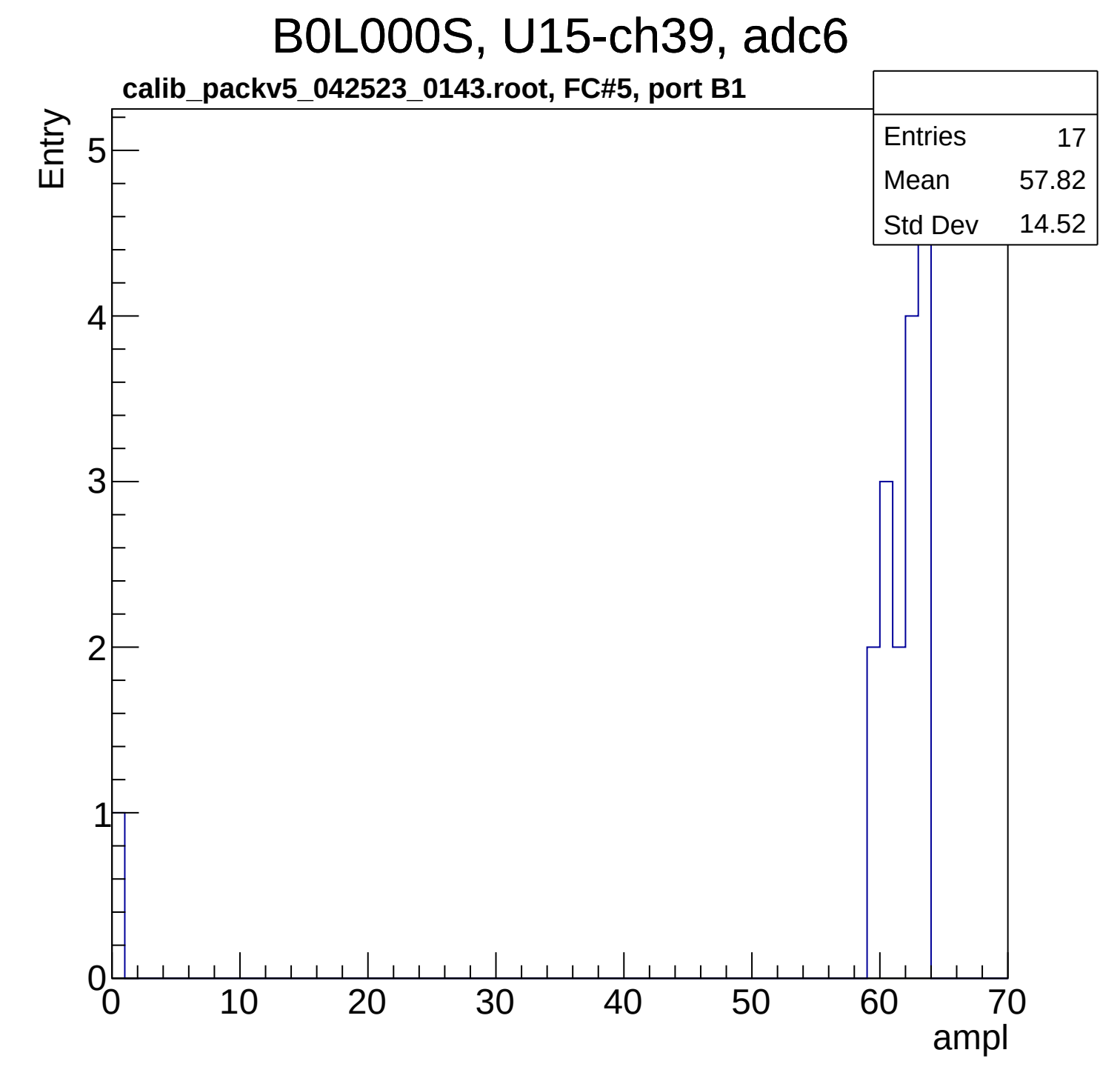
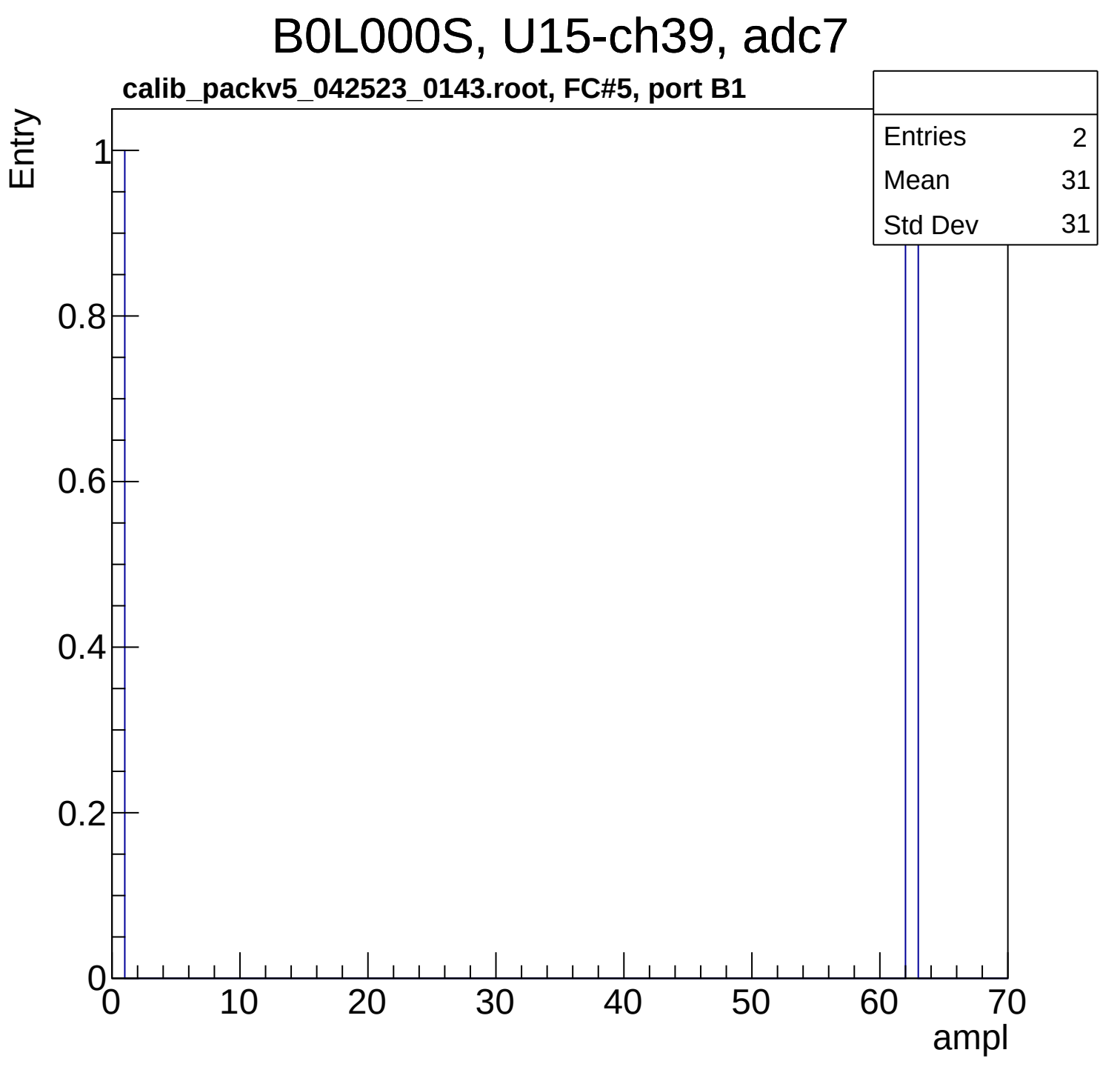

B0L000S, U15-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.84
Std Dev 2.735
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.82
Std Dev 14.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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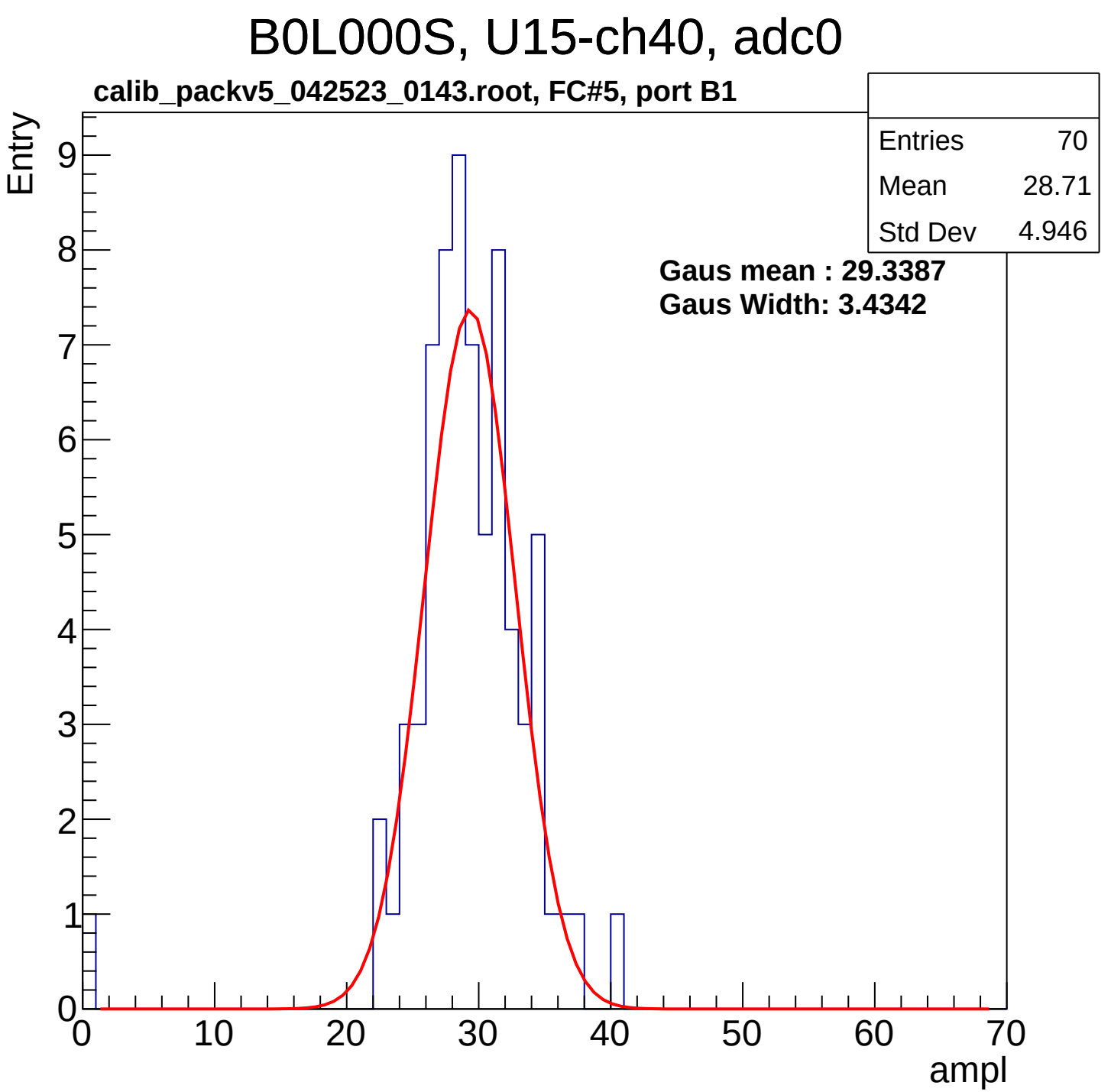
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.71
Std Dev 4.946
Gaus mean : 29.3387
Gaus Width: 3.4342
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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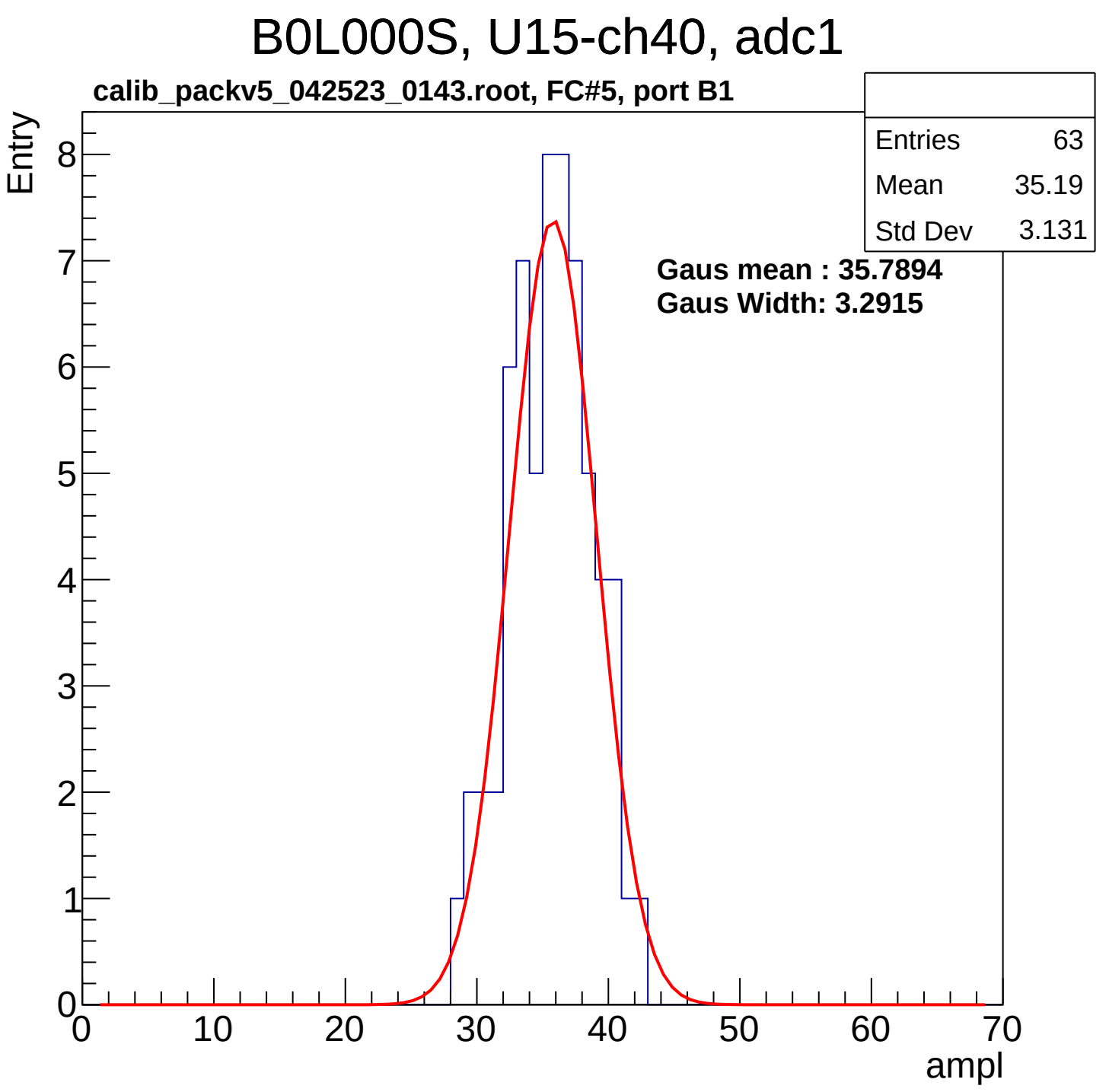
B0L000S, U15-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.19
Std Dev 3.131
Gaus mean : 35.7894
Gaus Width: 3.2915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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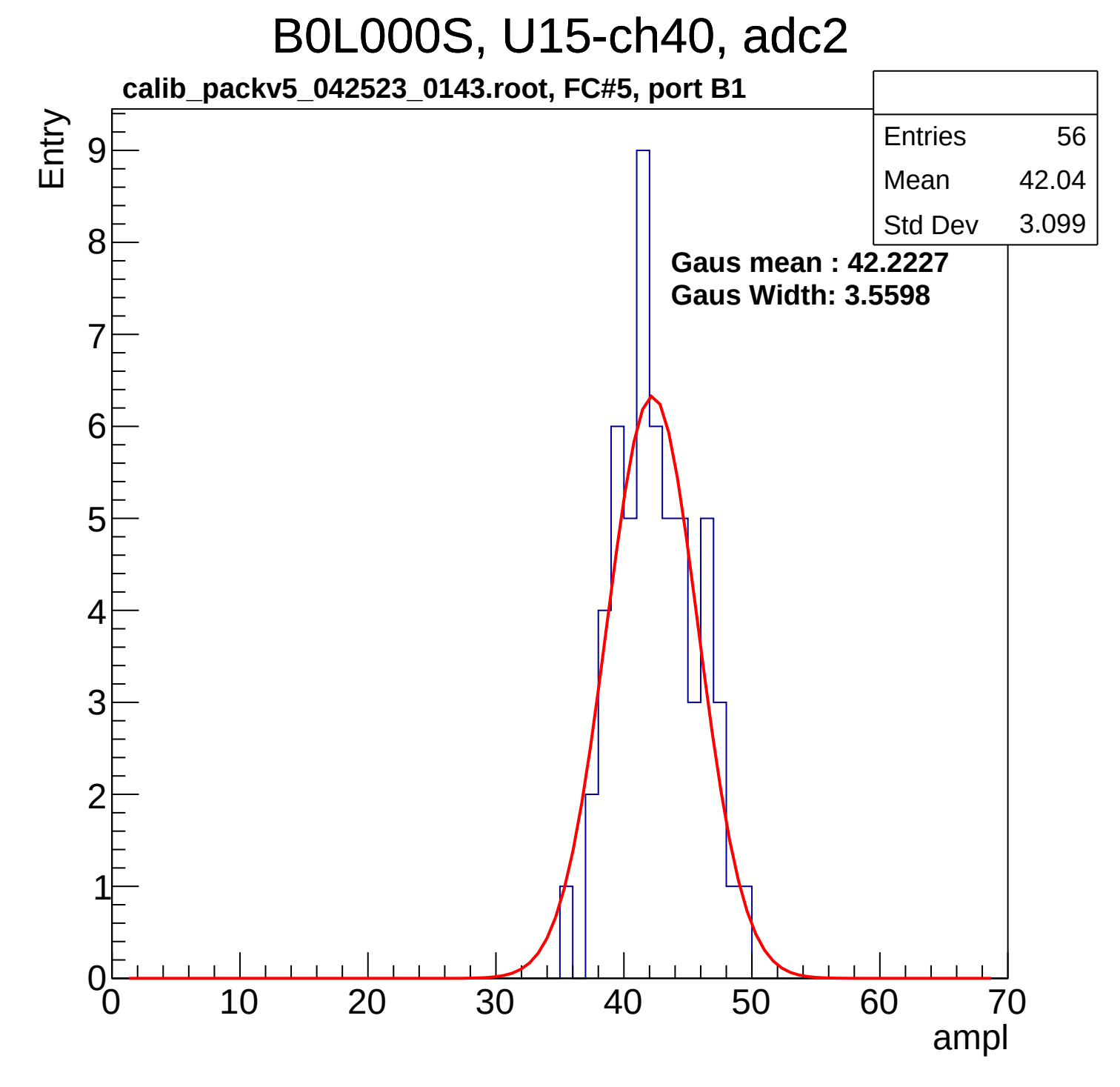
B0L000S, U15-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.04
Std Dev 3.099
Gaus mean : 42.2227
Gaus Width: 3.5598
Entry
ampl
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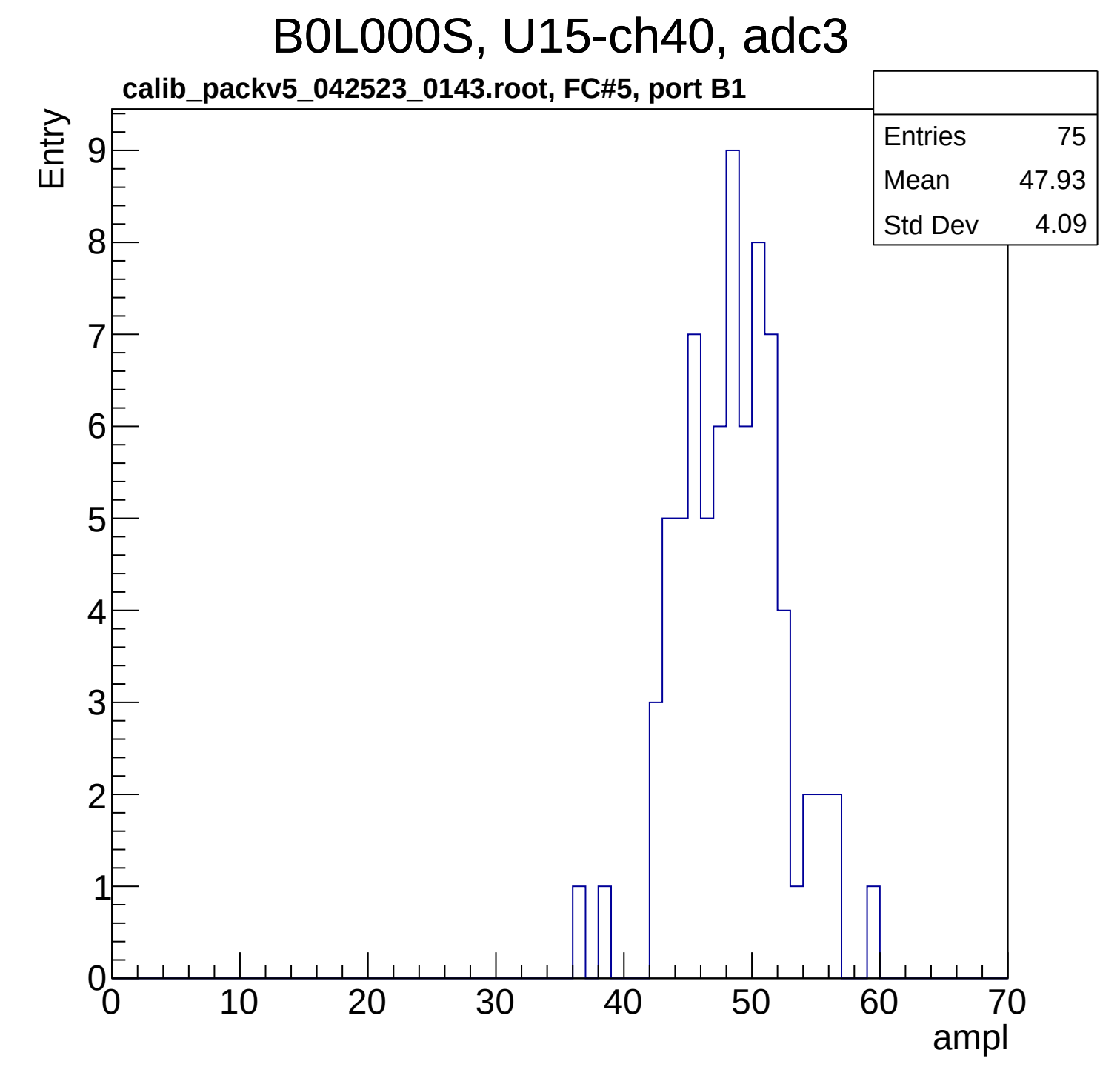
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 47.93
Std Dev 4.09
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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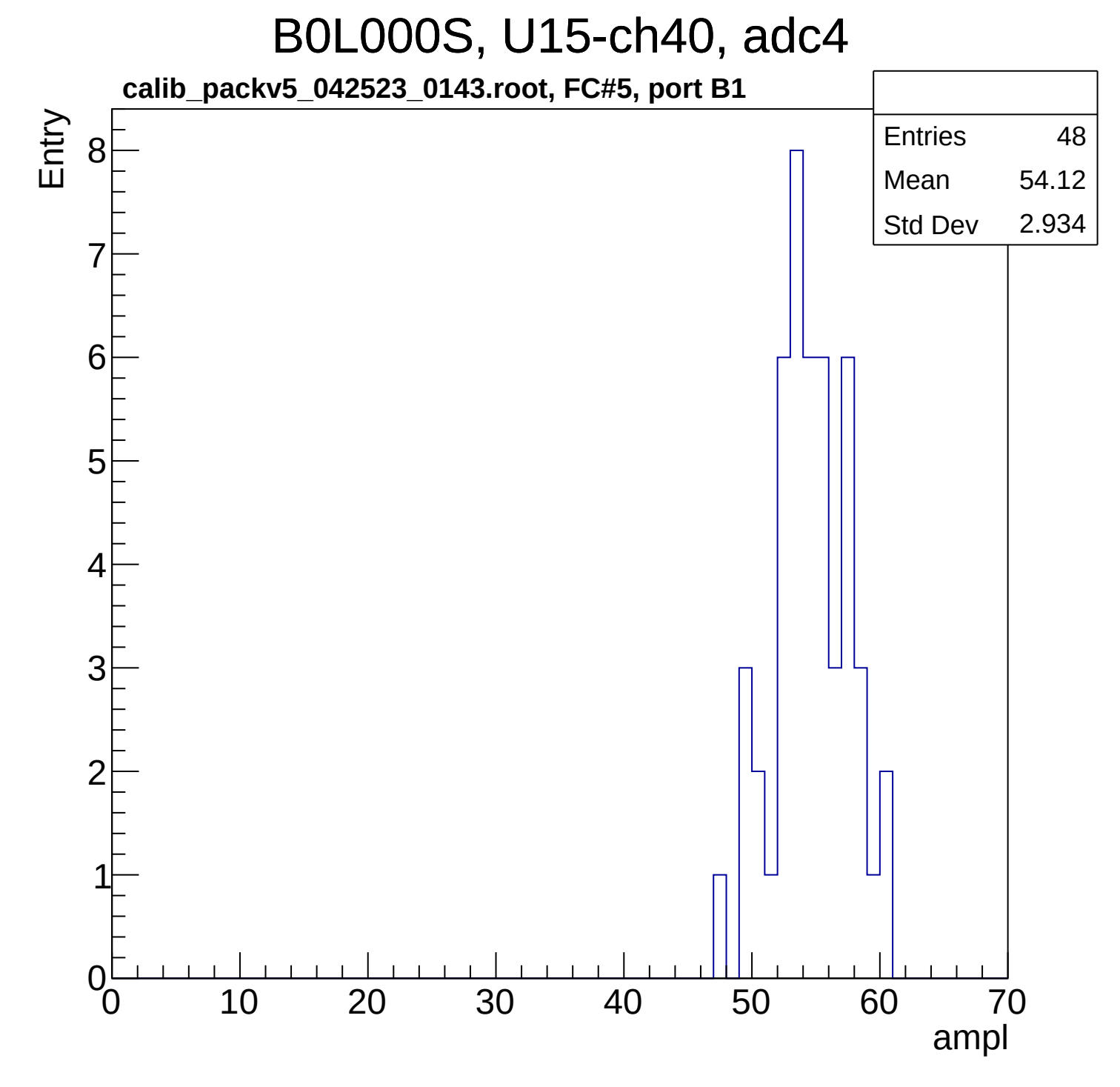
B0L000S, U15-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.12
Std Dev 2.934
Entry
ampl
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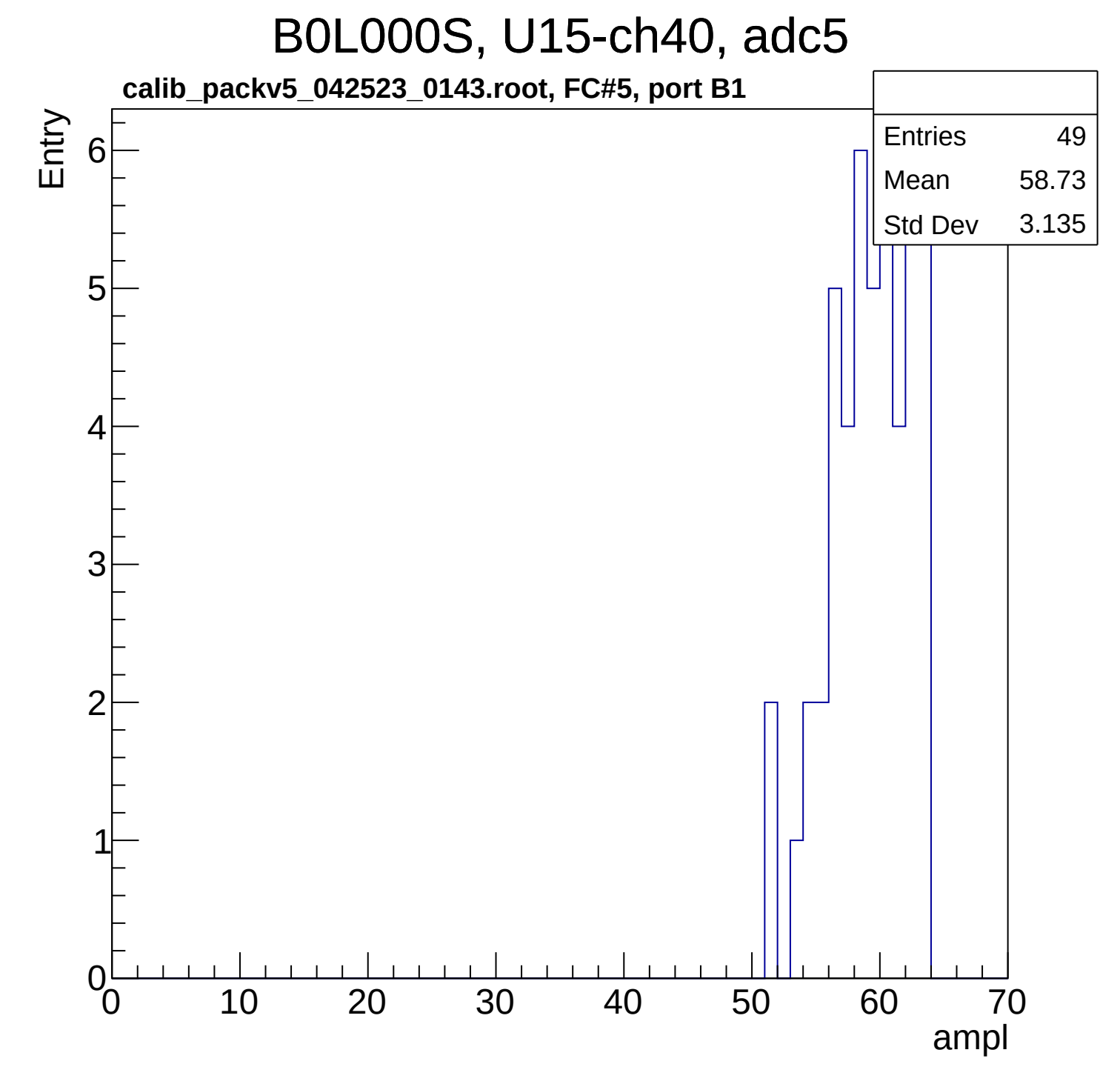
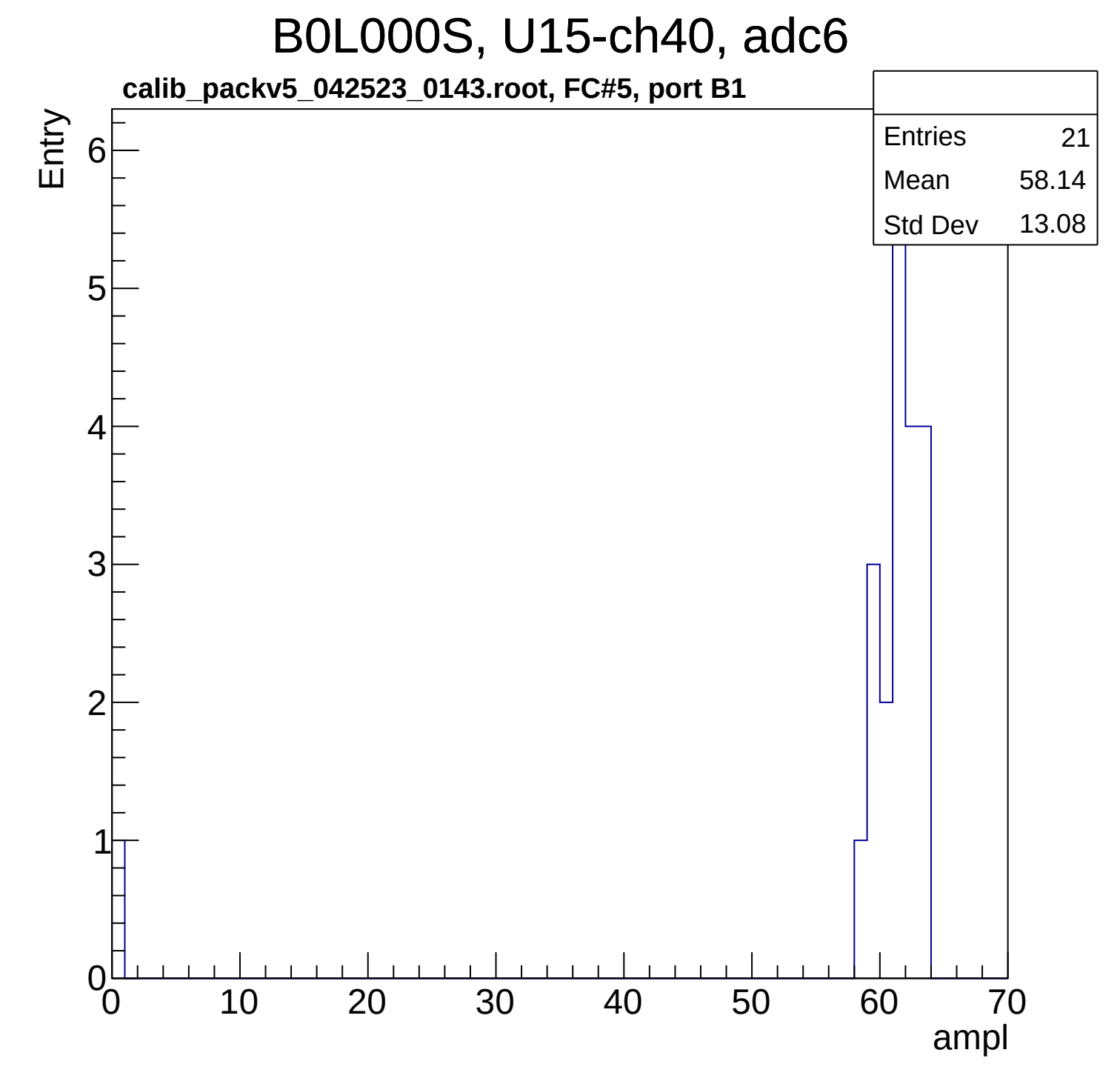
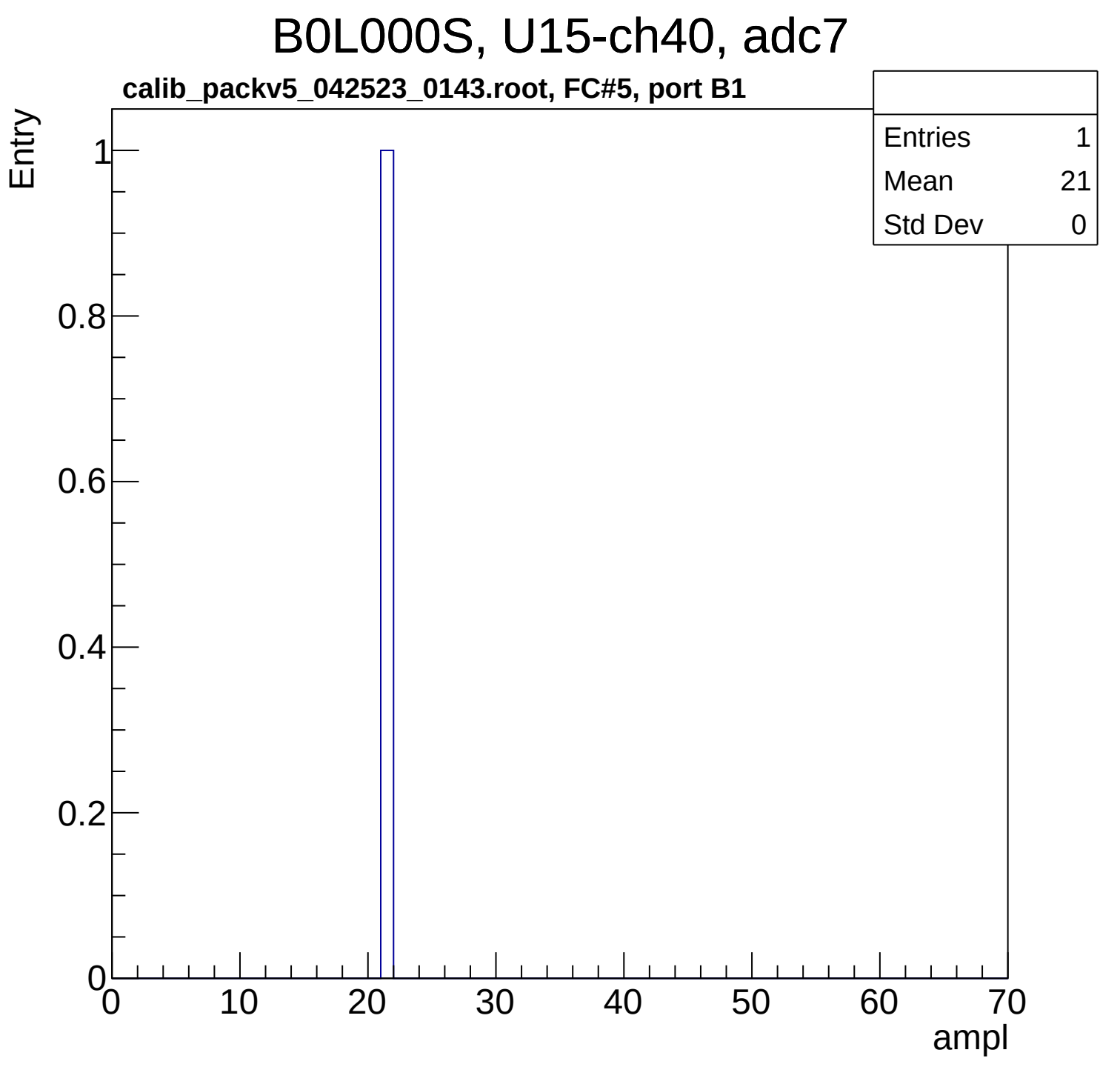
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.73
Std Dev 3.135
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.14
Std Dev 13.08
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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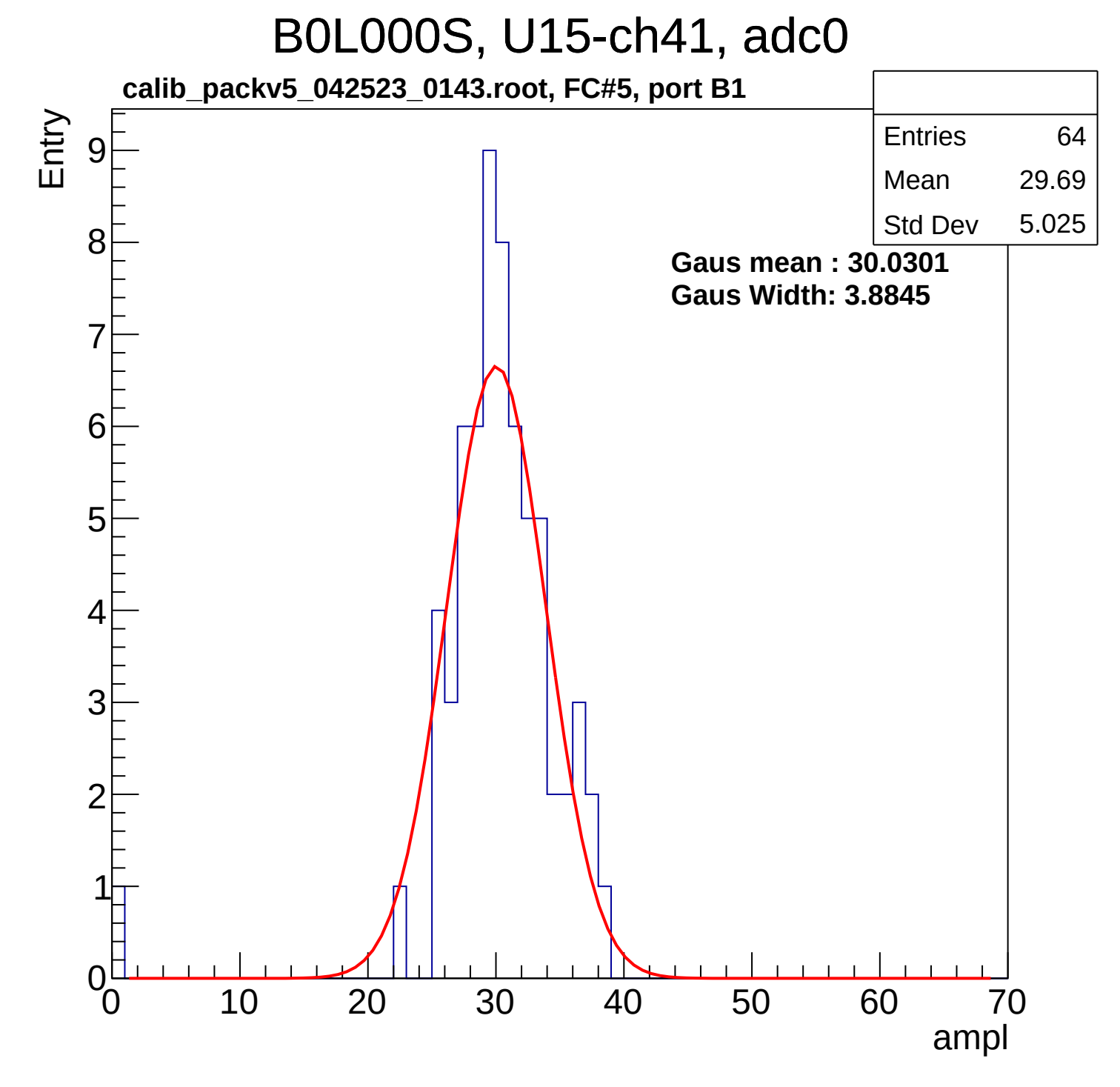
50
60
70
ampl


B0L000S, U15-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.69
Std Dev 5.025
Gaus mean : 30.0301
Gaus Width: 3.8845
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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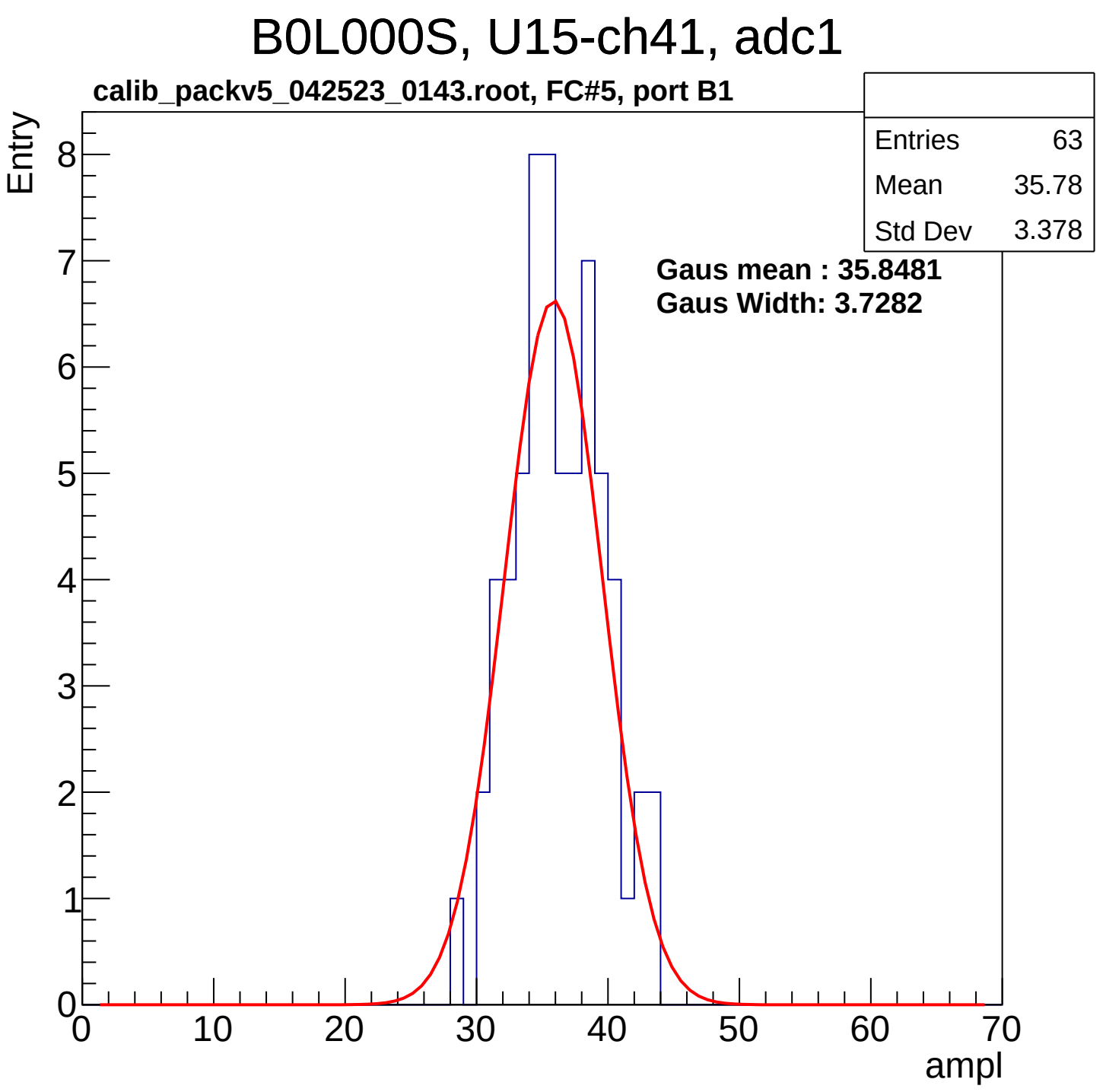
B0L000S, U15-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.78
Std Dev 3.378
Gaus mean : 35.8481
Gaus Width: 3.7282
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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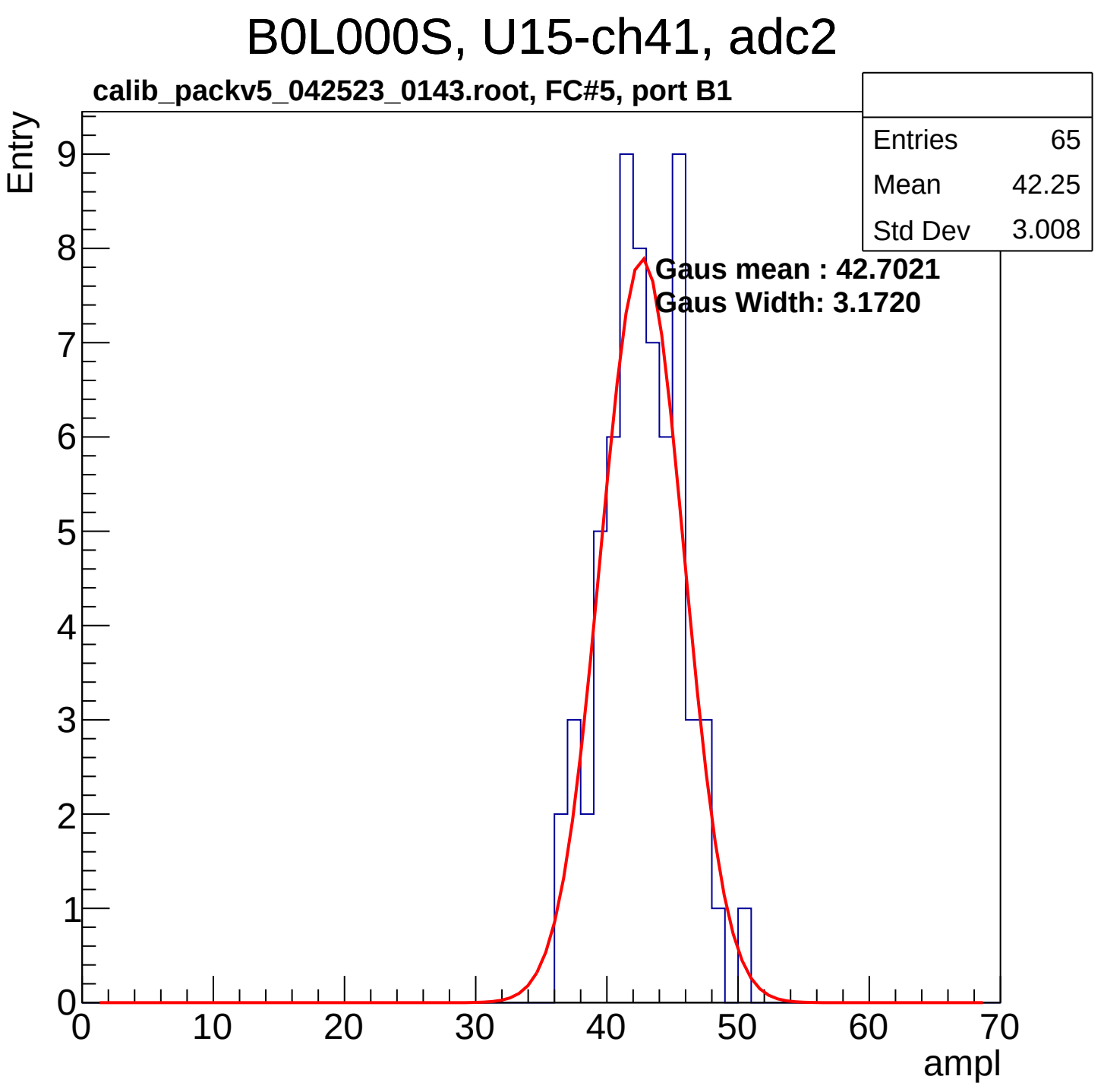
B0L000S, U15-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.25
Std Dev 3.008
Gaus mean : 42.7021
Gaus Width: 3.1720
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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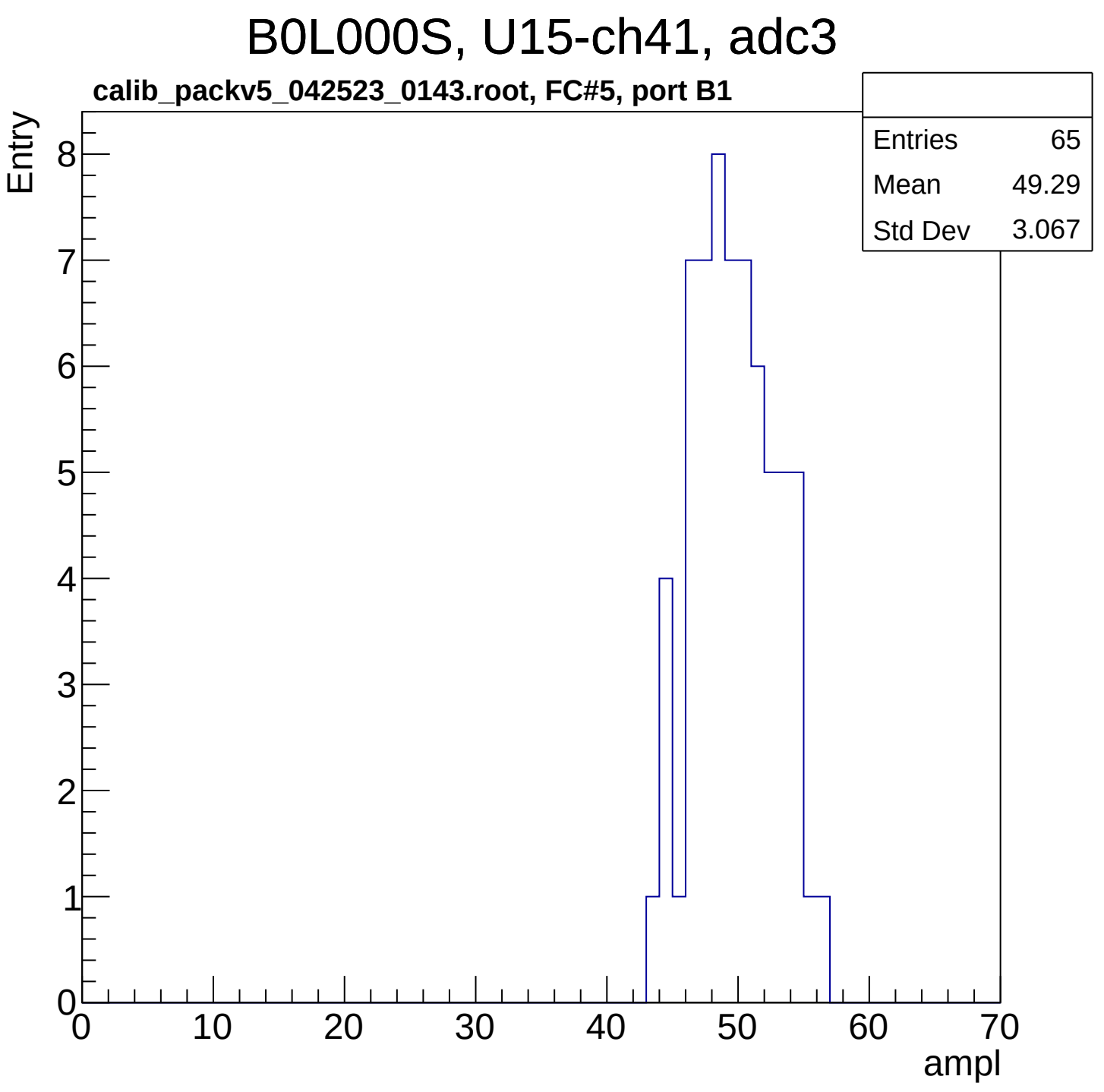
B0L000S, U15-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.29
Std Dev 3.067
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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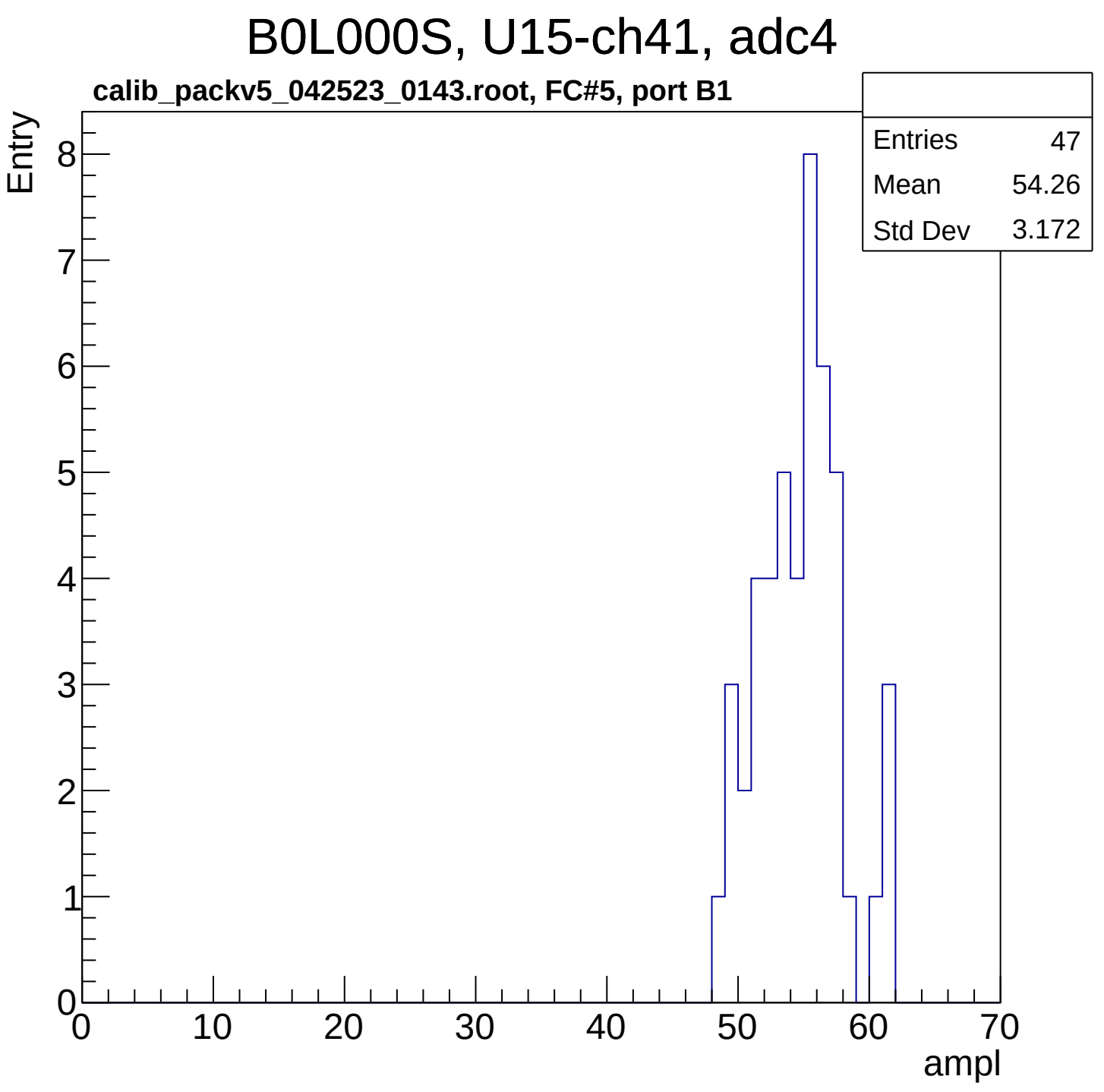
40
50
60
70


B0L000S, U15-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 54.26
Std Dev 3.172
Entry
ampl
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2
3
4
5
6
7
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0
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60
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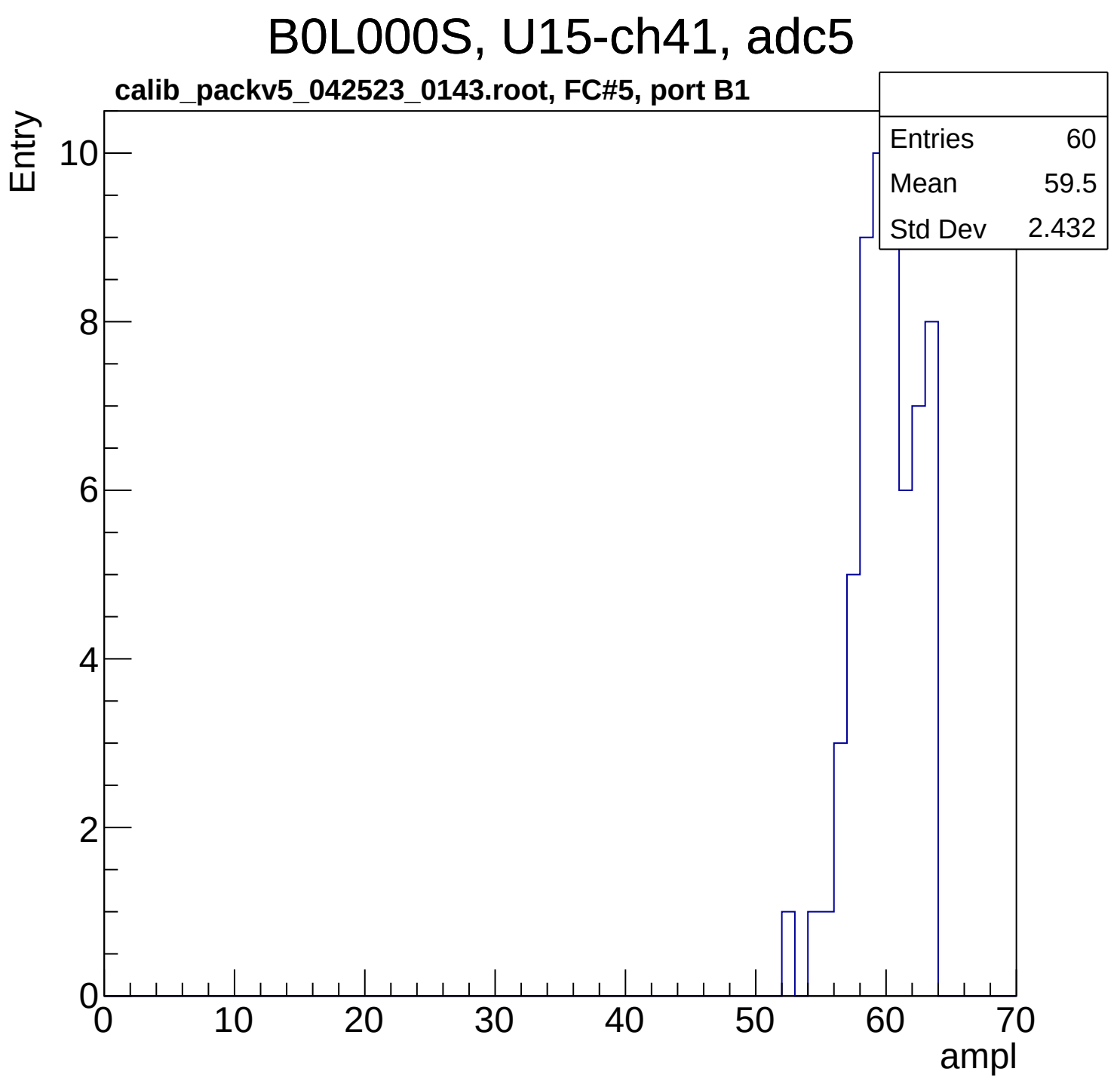
B0L000S, U15-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 59.5
Std Dev 2.432
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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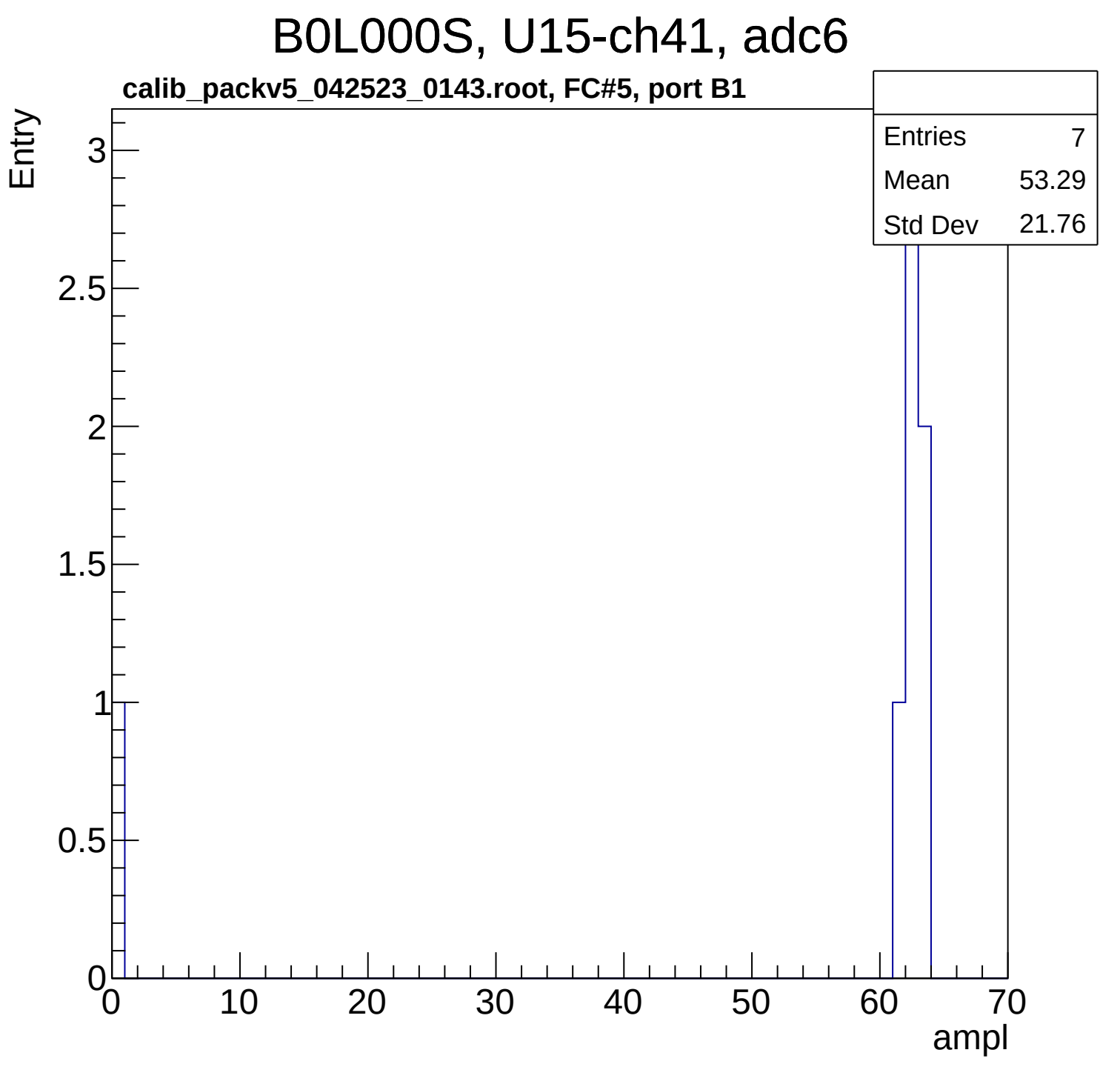
B0L000S, U15-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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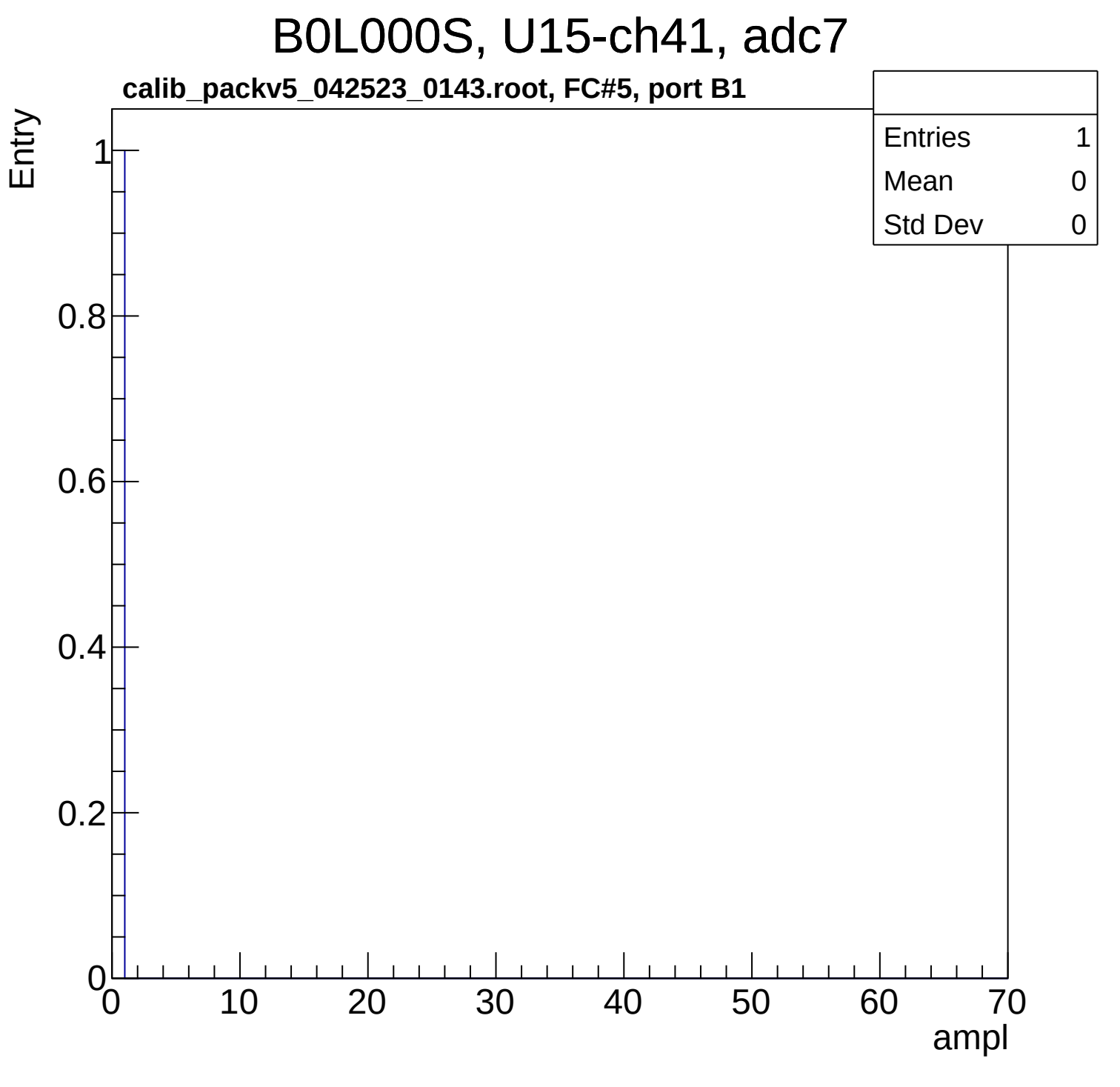
B0L000S, U15-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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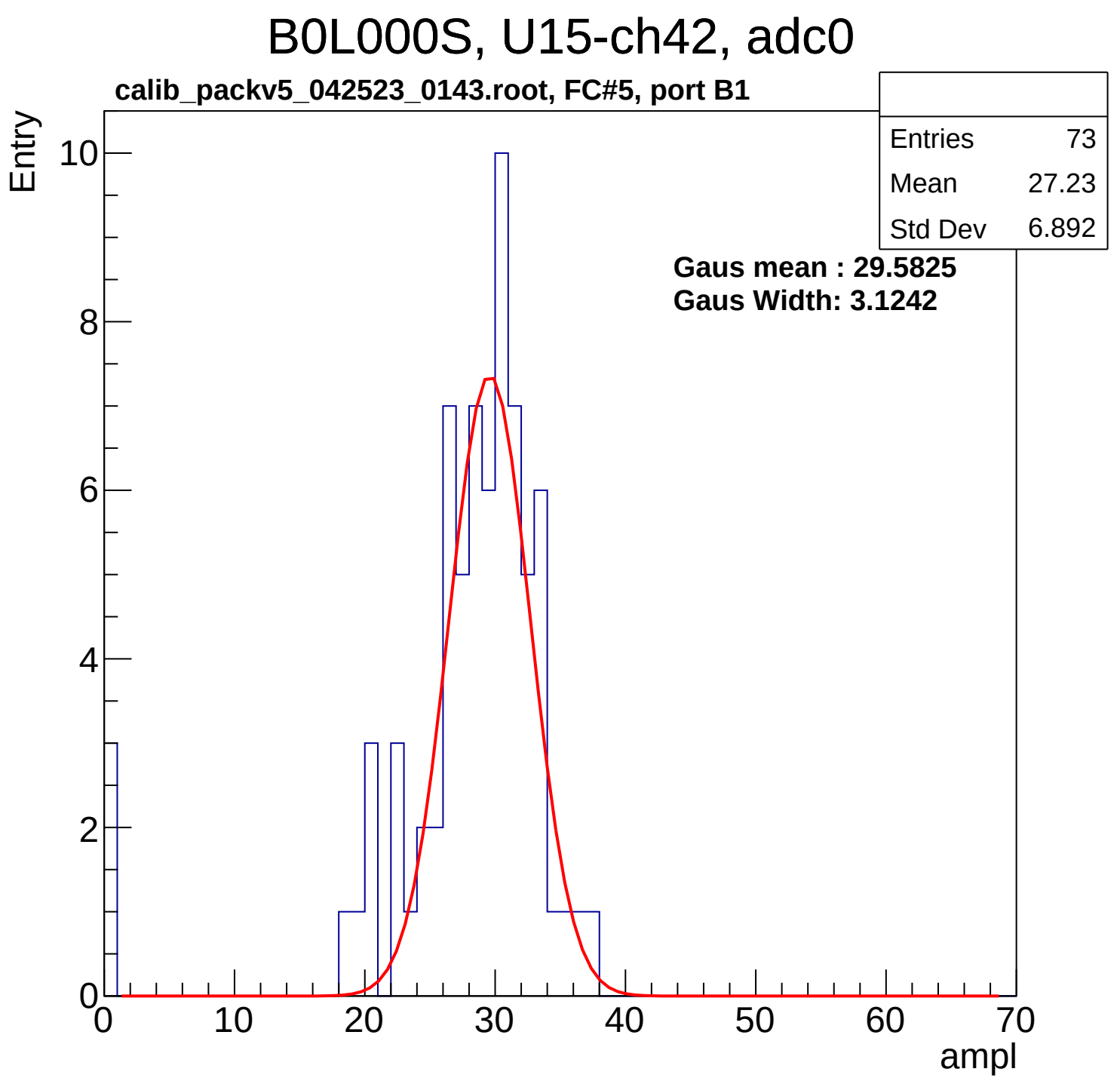
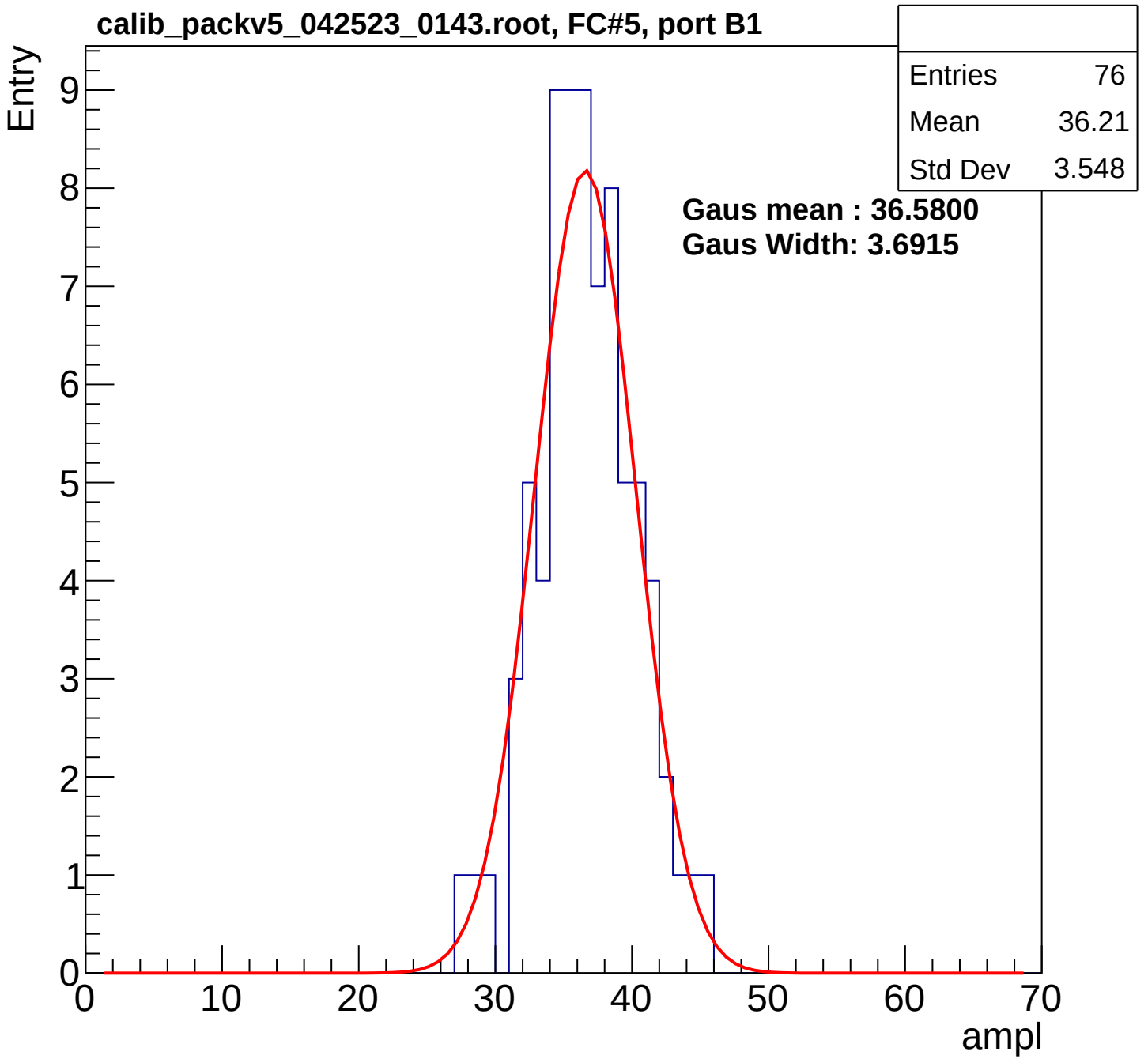
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 27.23
Std Dev 6.892
Gaus mean : 29.5825
Gaus Width: 3.1242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.21
Std Dev 3.548
Gaus mean : 36.5800
Gaus Width: 3.6915
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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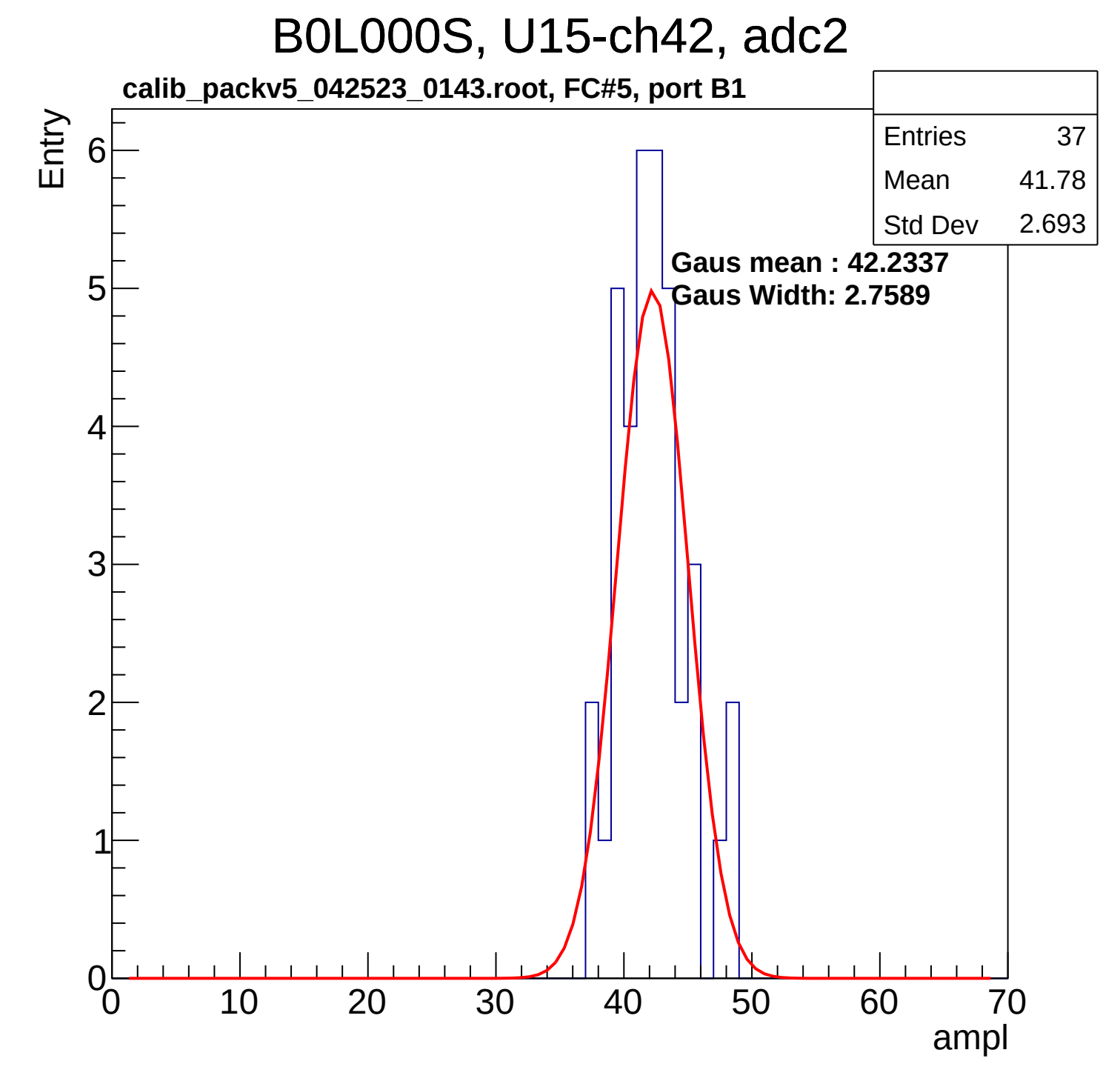
B0L000S, U15-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 41.78
Std Dev 2.693
Gaus mean : 42.2337
Gaus Width: 2.7589
Entry
ampl
0
1
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3
4
5
6
0
10
20
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40
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60
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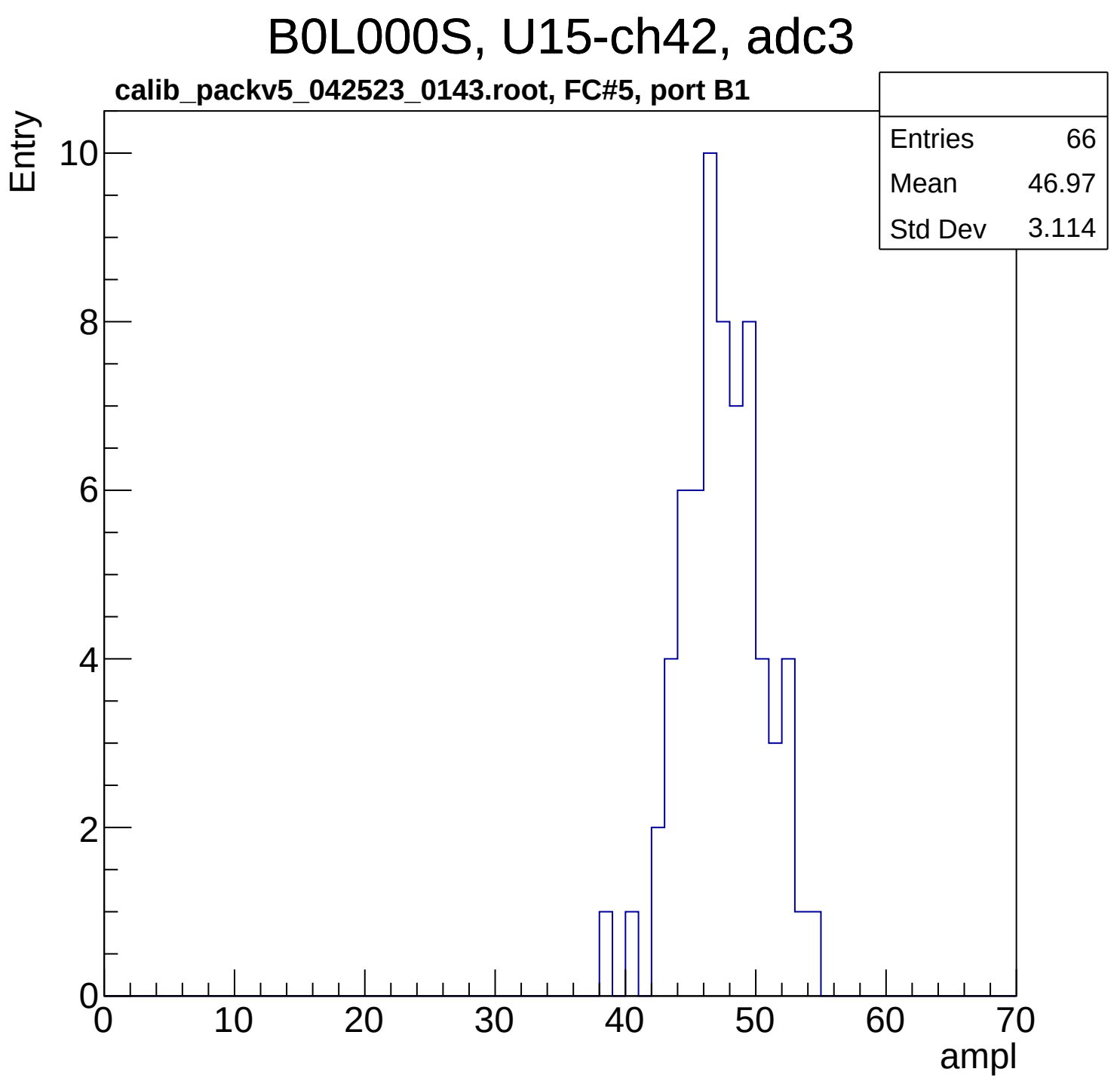
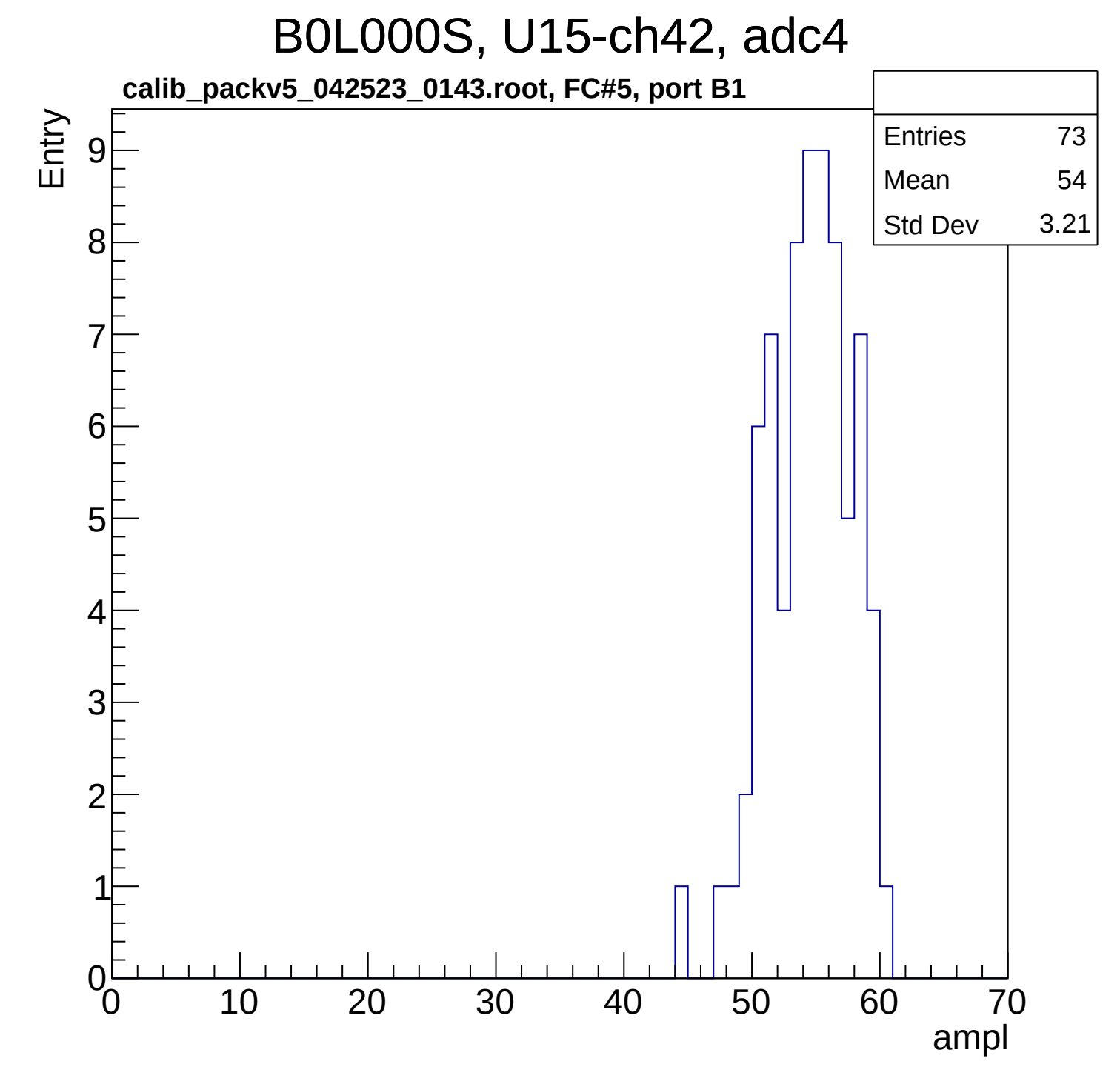

B0L000S, U15-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 46.97
Std Dev 3.114
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 54
Std Dev 3.21
Entry
0
1
2
3
4
5
6
7
8
9
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20
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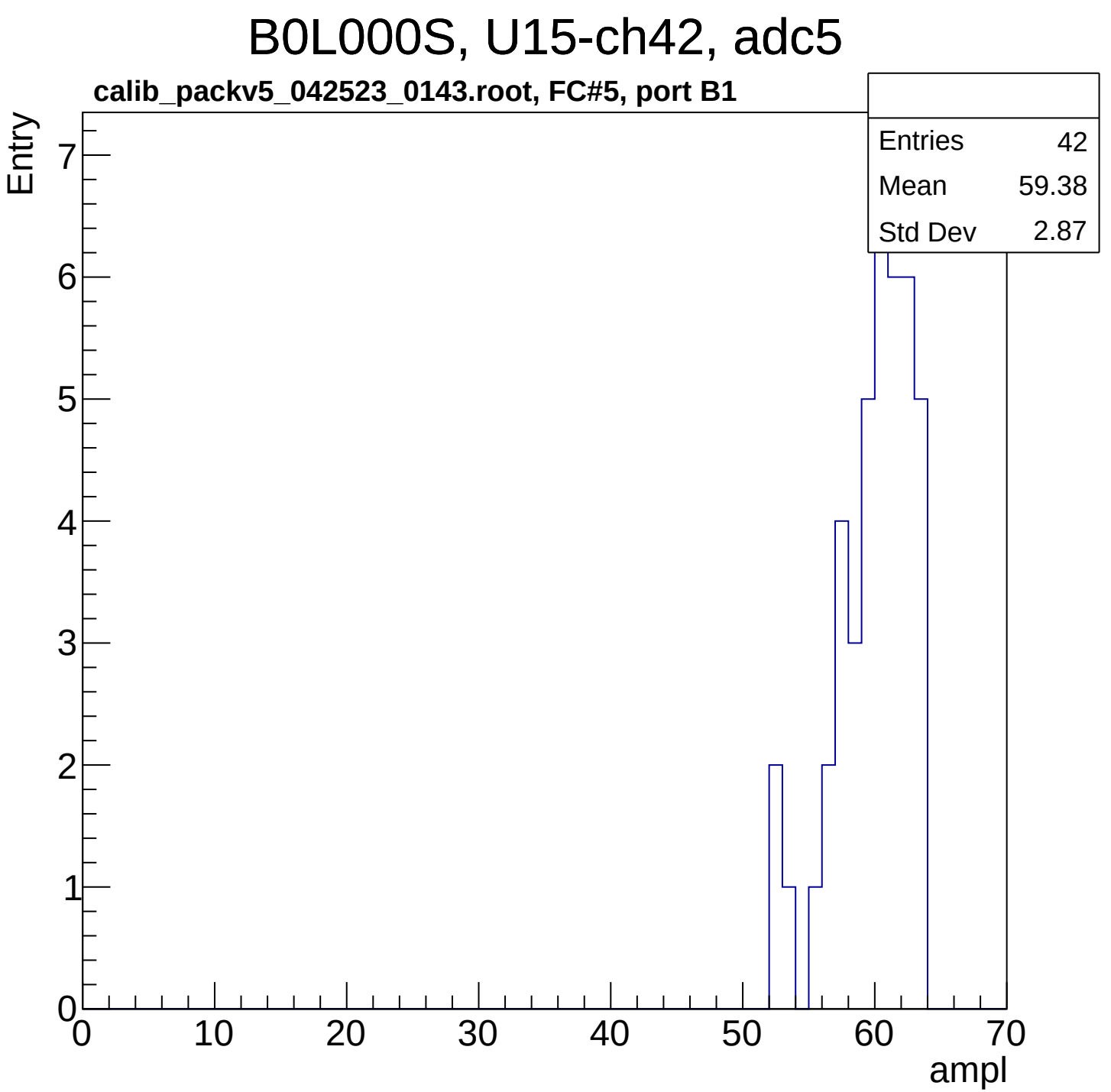
60
70
ampl


B0L000S, U15-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.38
Std Dev 2.87
Entry
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1
2
3
4
5
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7
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10
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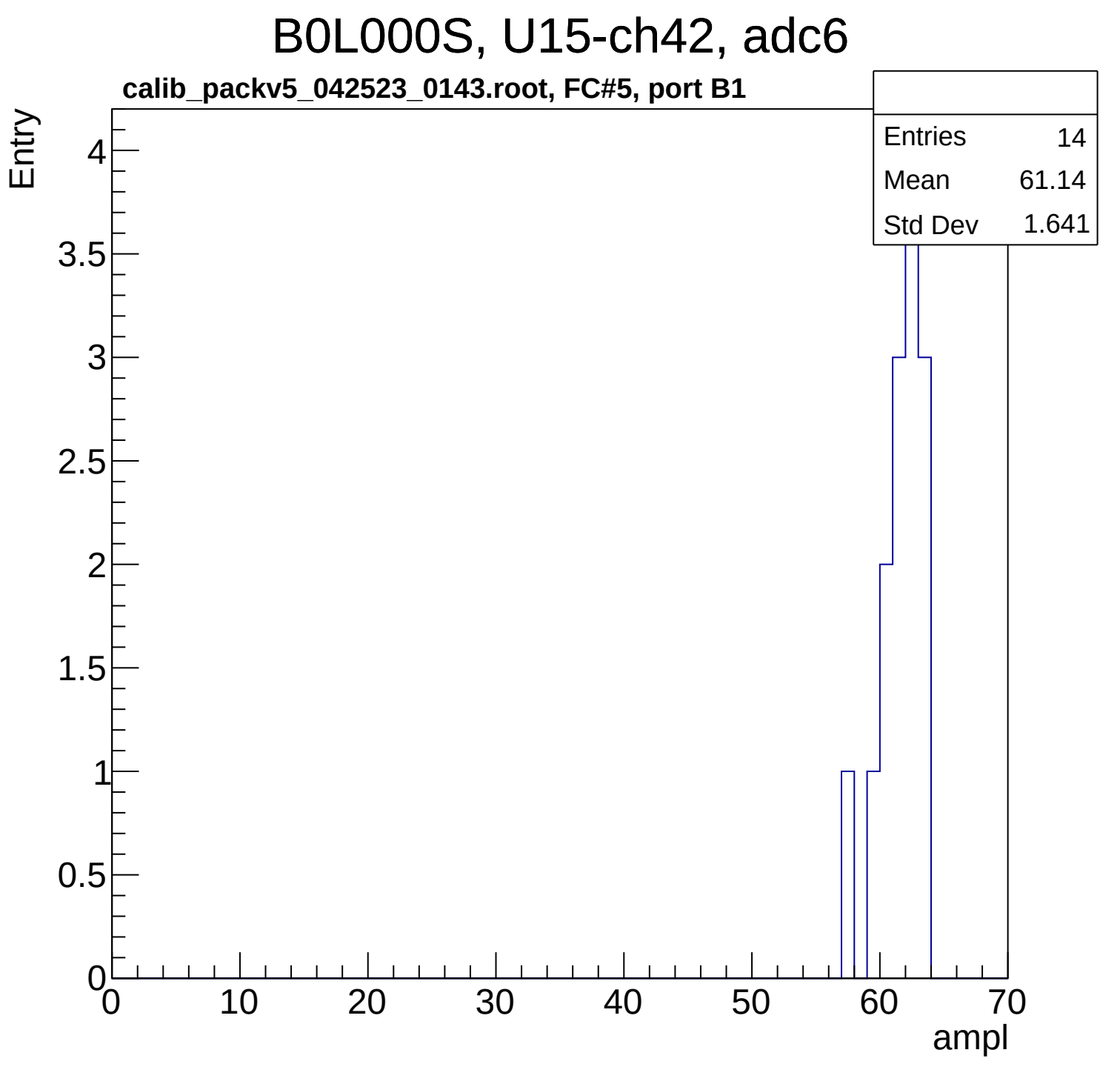
60
70
ampl


B0L000S, U15-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.14
Std Dev 1.641
Entry
ampl
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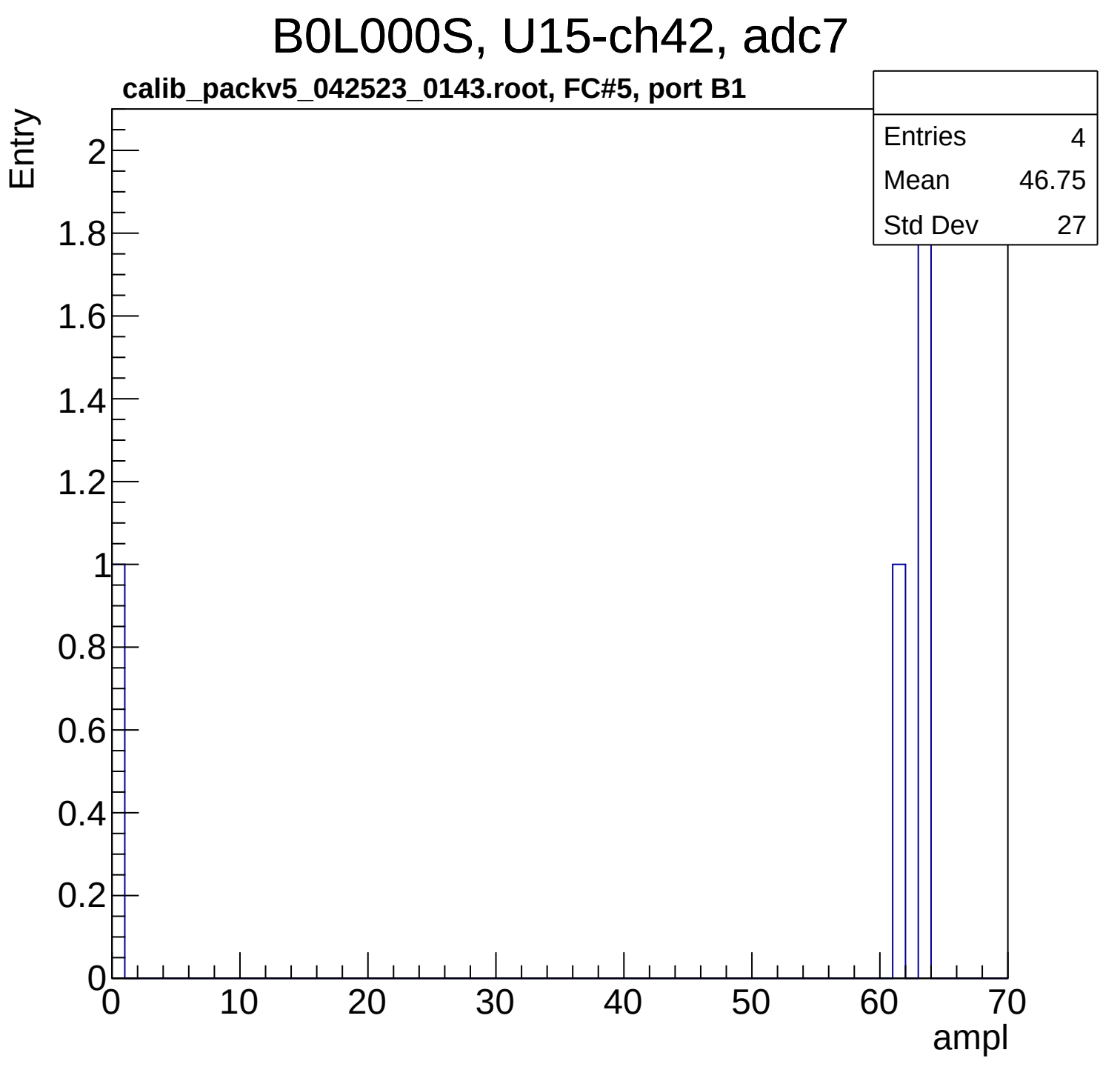
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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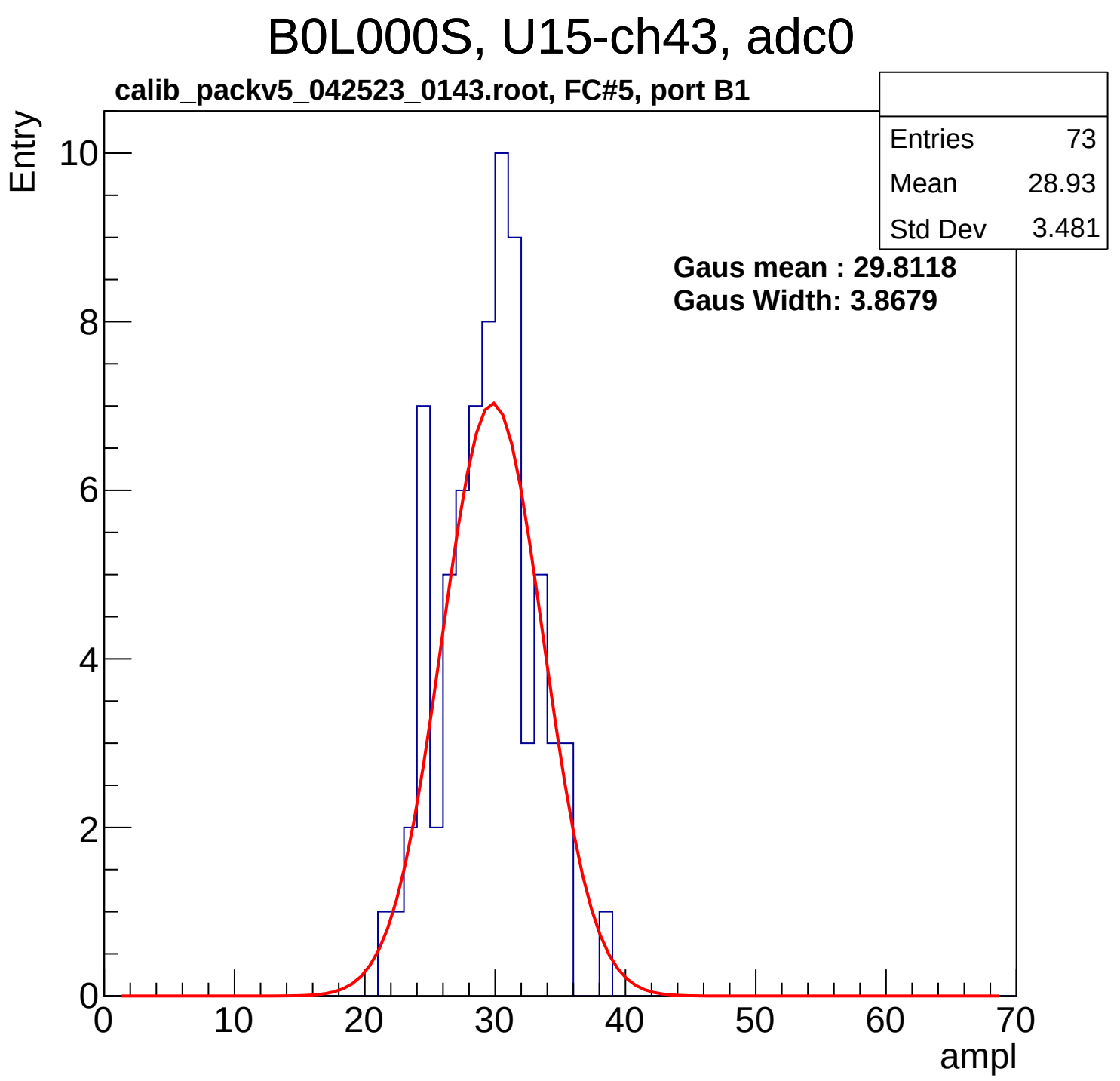
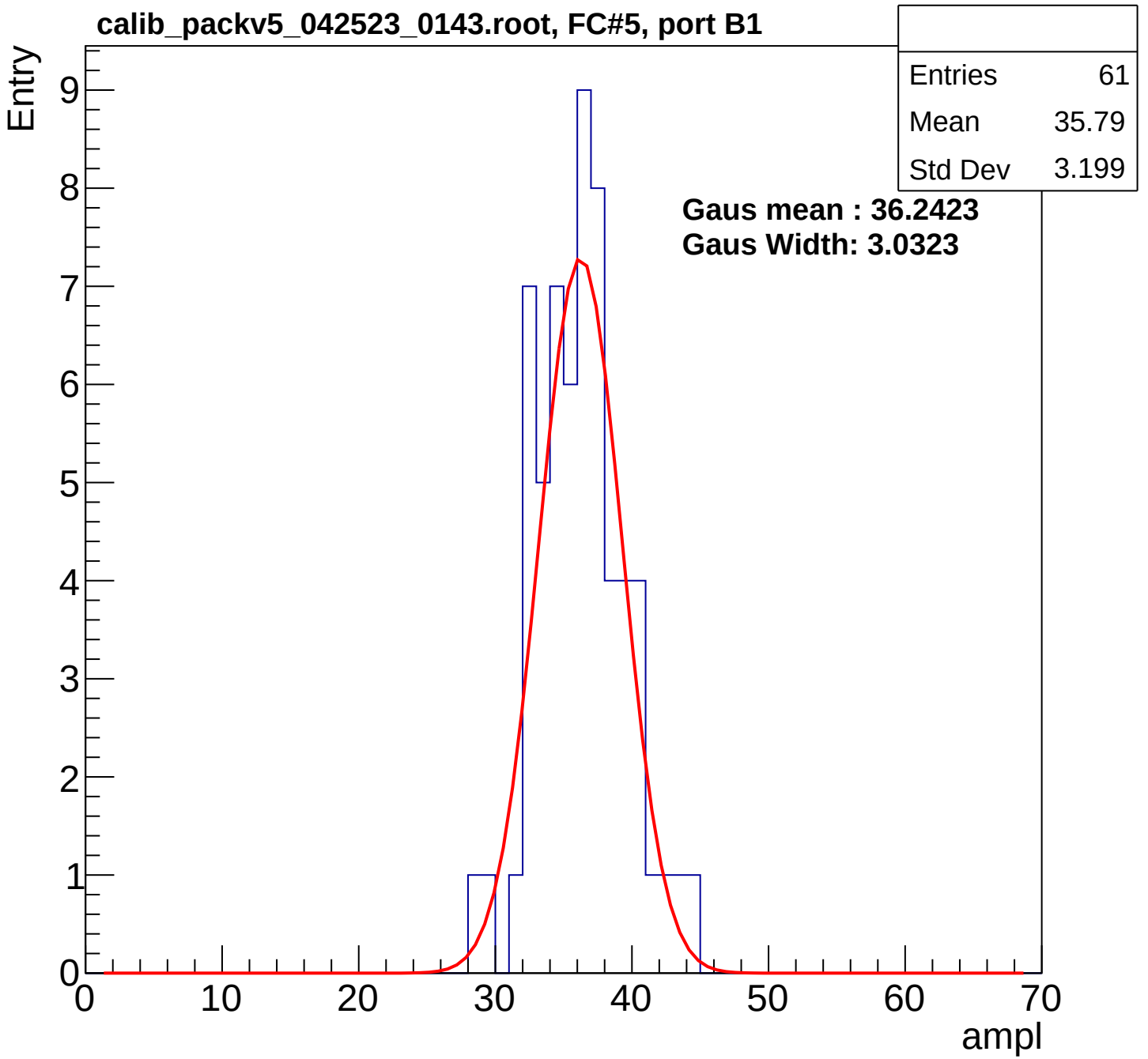

B0L000S, U15-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.93
Std Dev 3.481
Gaus mean : 29.8118
Gaus Width: 3.8679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.79
Std Dev 3.199
Gaus mean : 36.2423
Gaus Width: 3.0323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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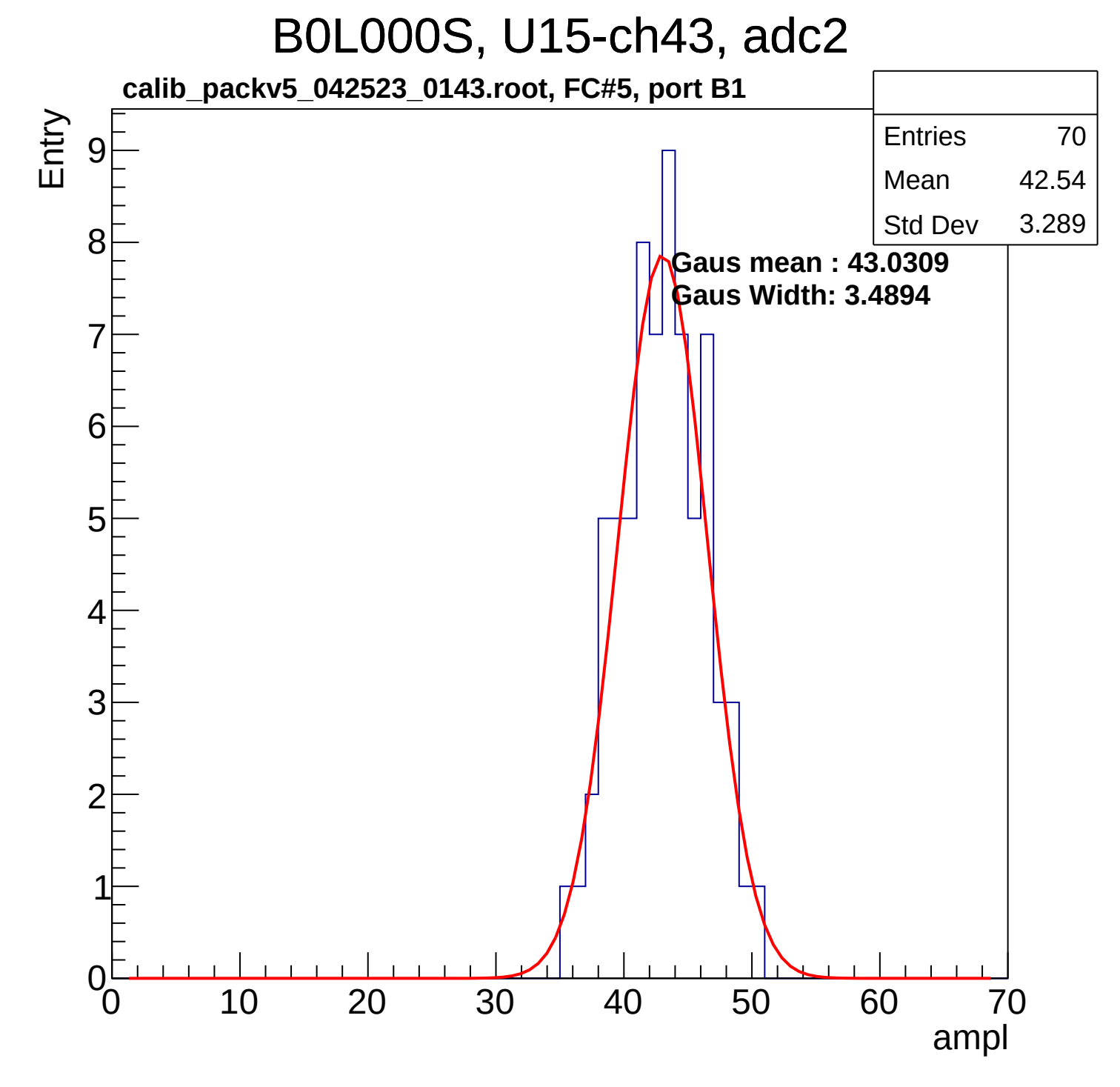
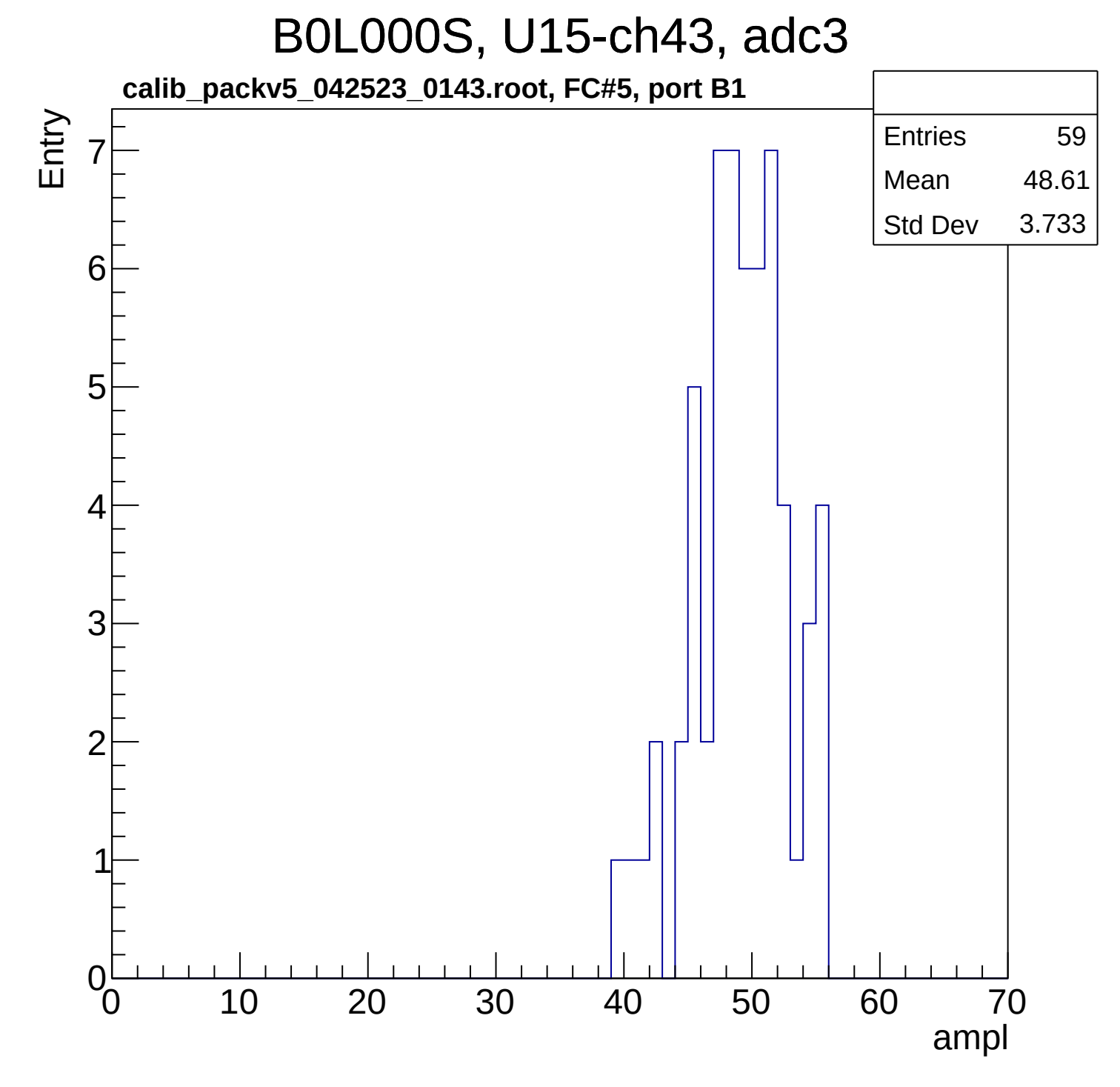

B0L000S, U15-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.54
Std Dev 3.289
Gaus mean : 43.0309
Gaus Width: 3.4894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.61
Std Dev 3.733
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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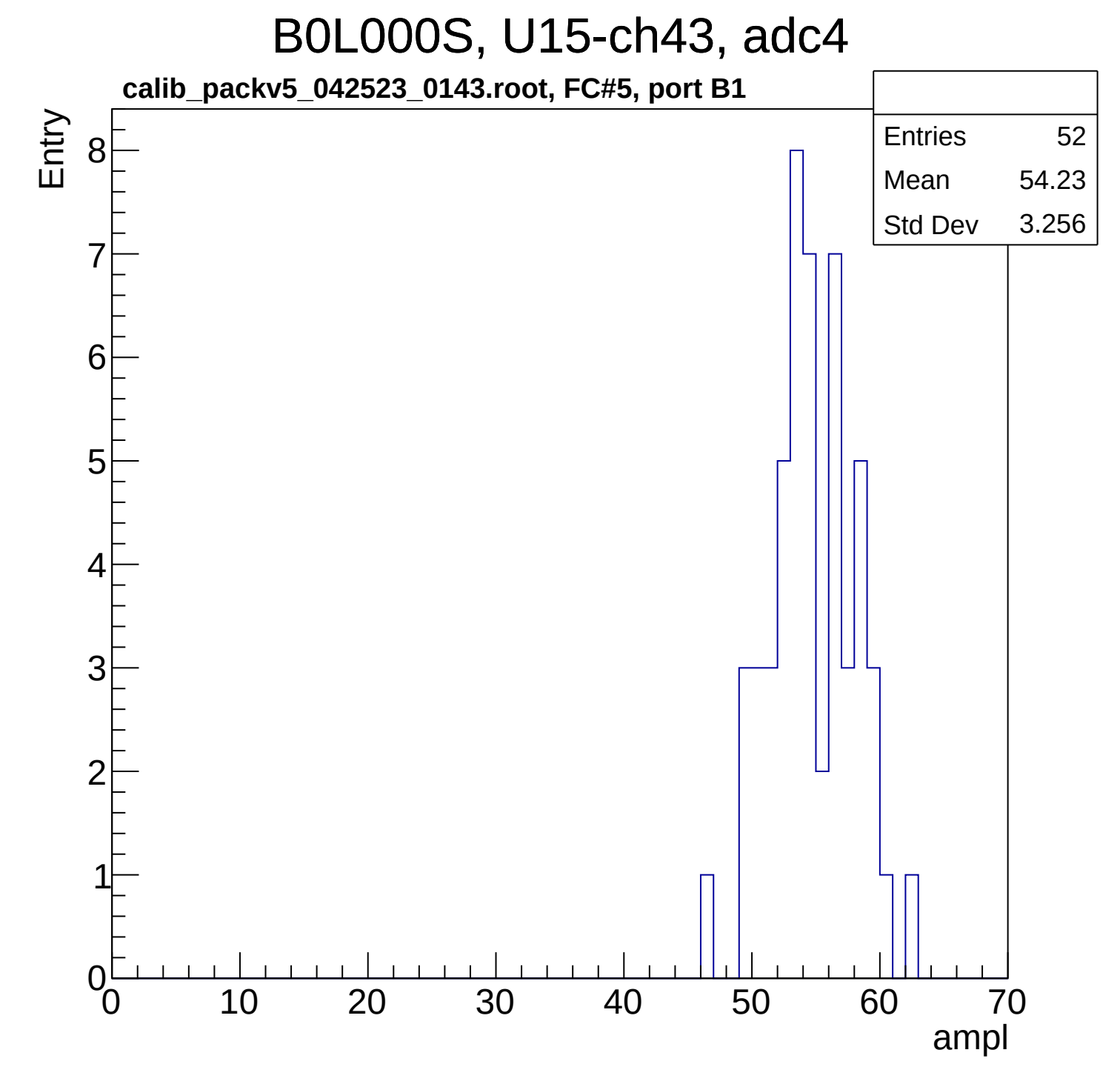
B0L000S, U15-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.23
Std Dev 3.256
Entry
ampl
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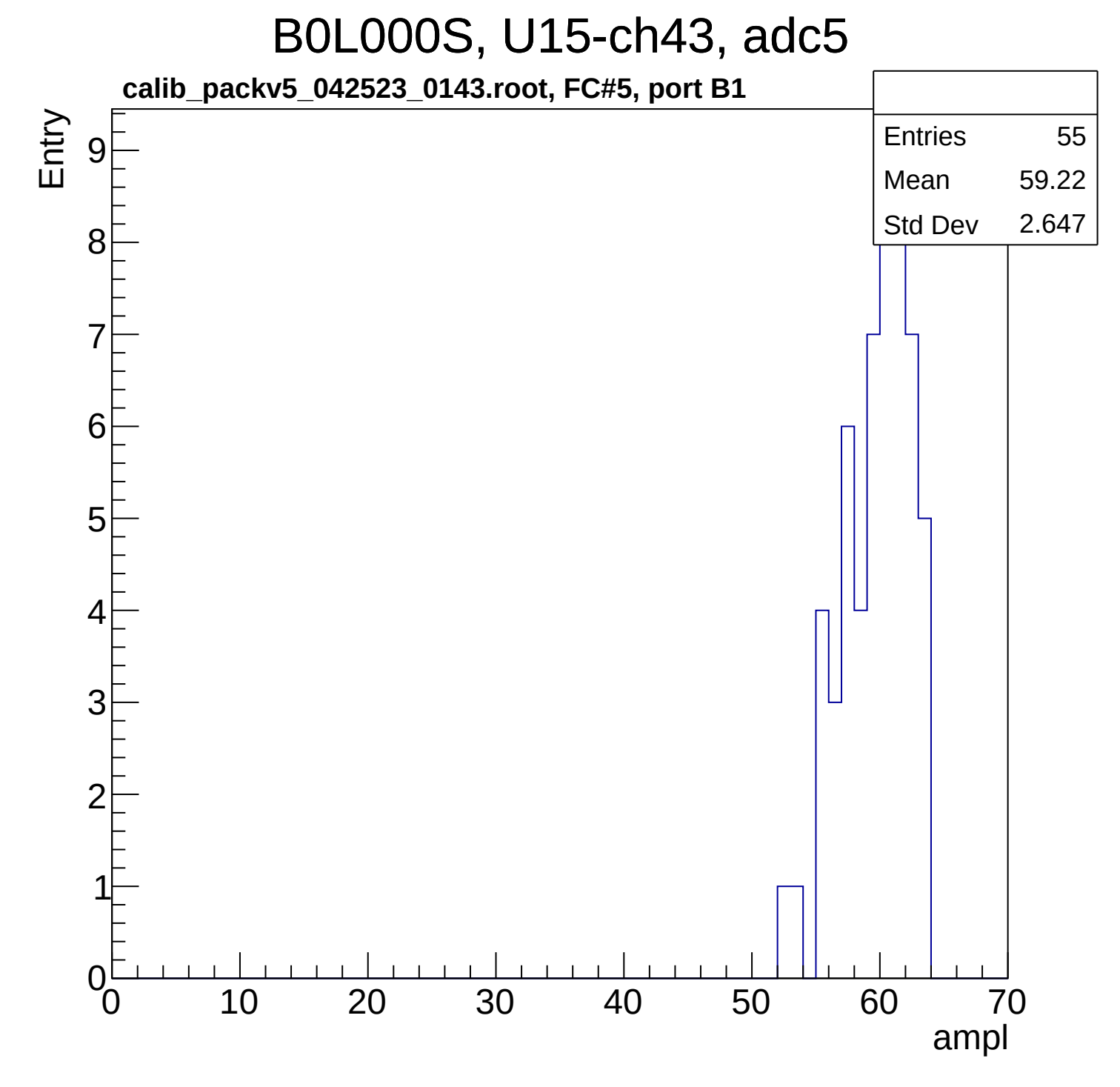
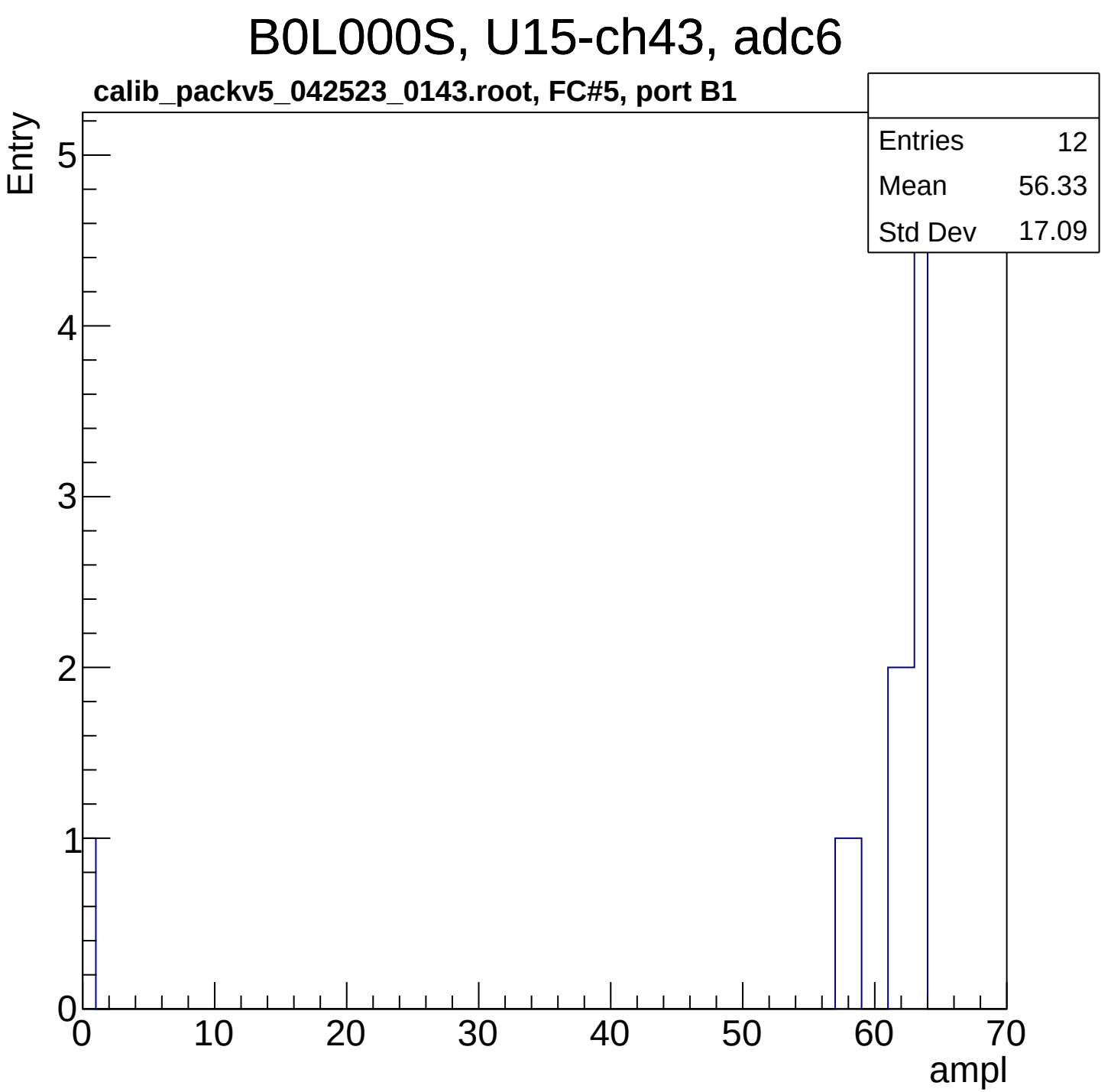
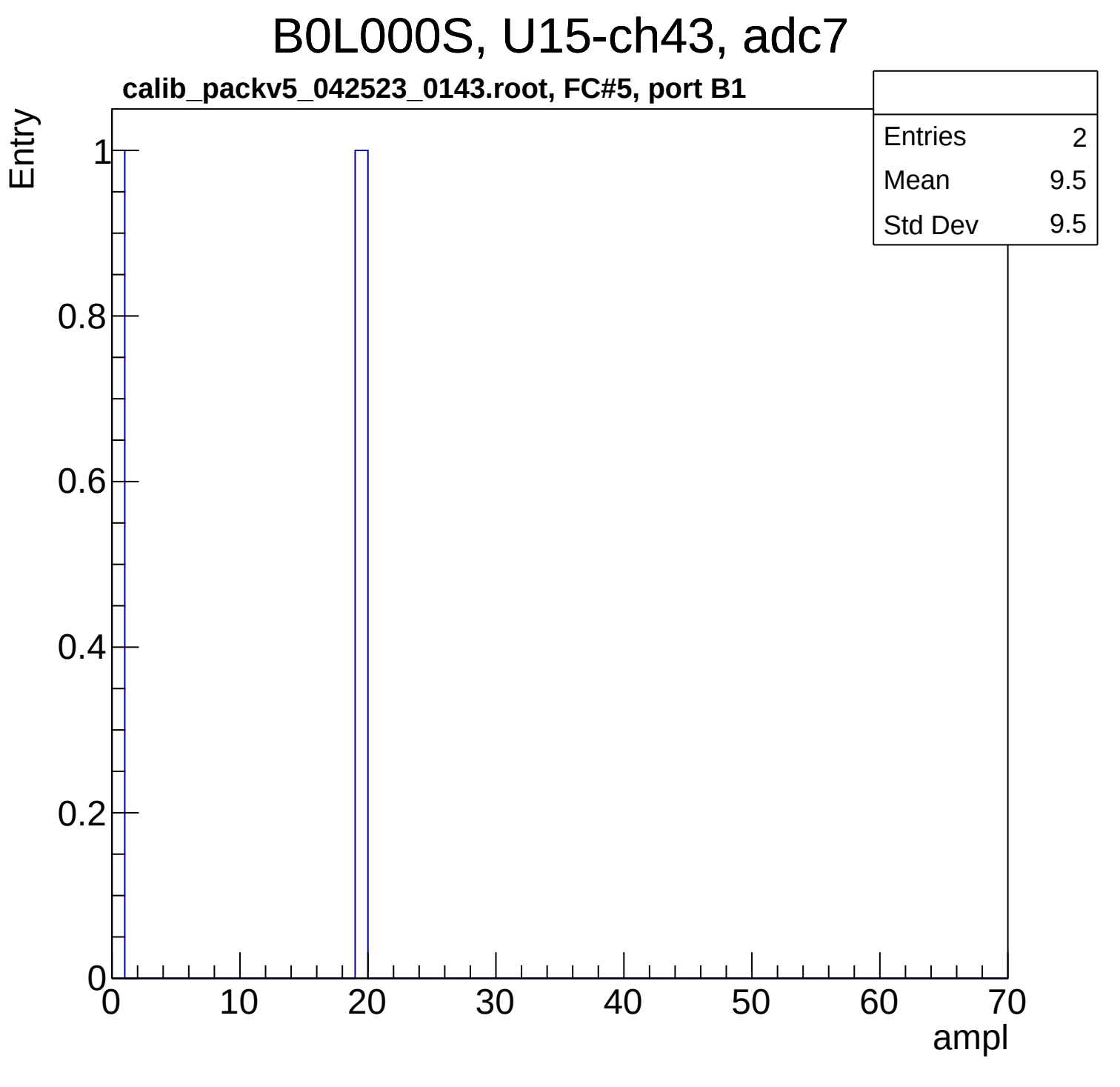
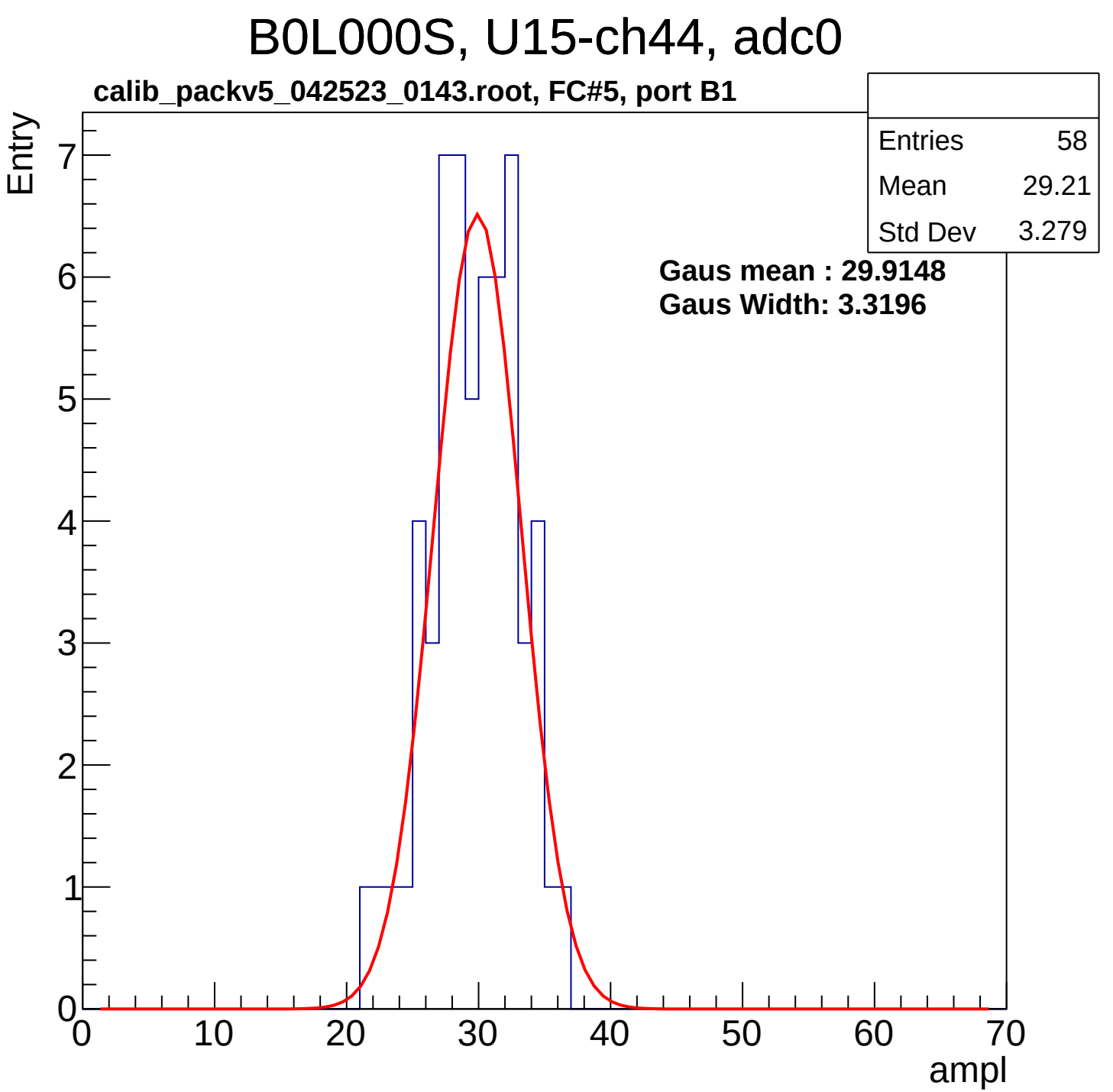
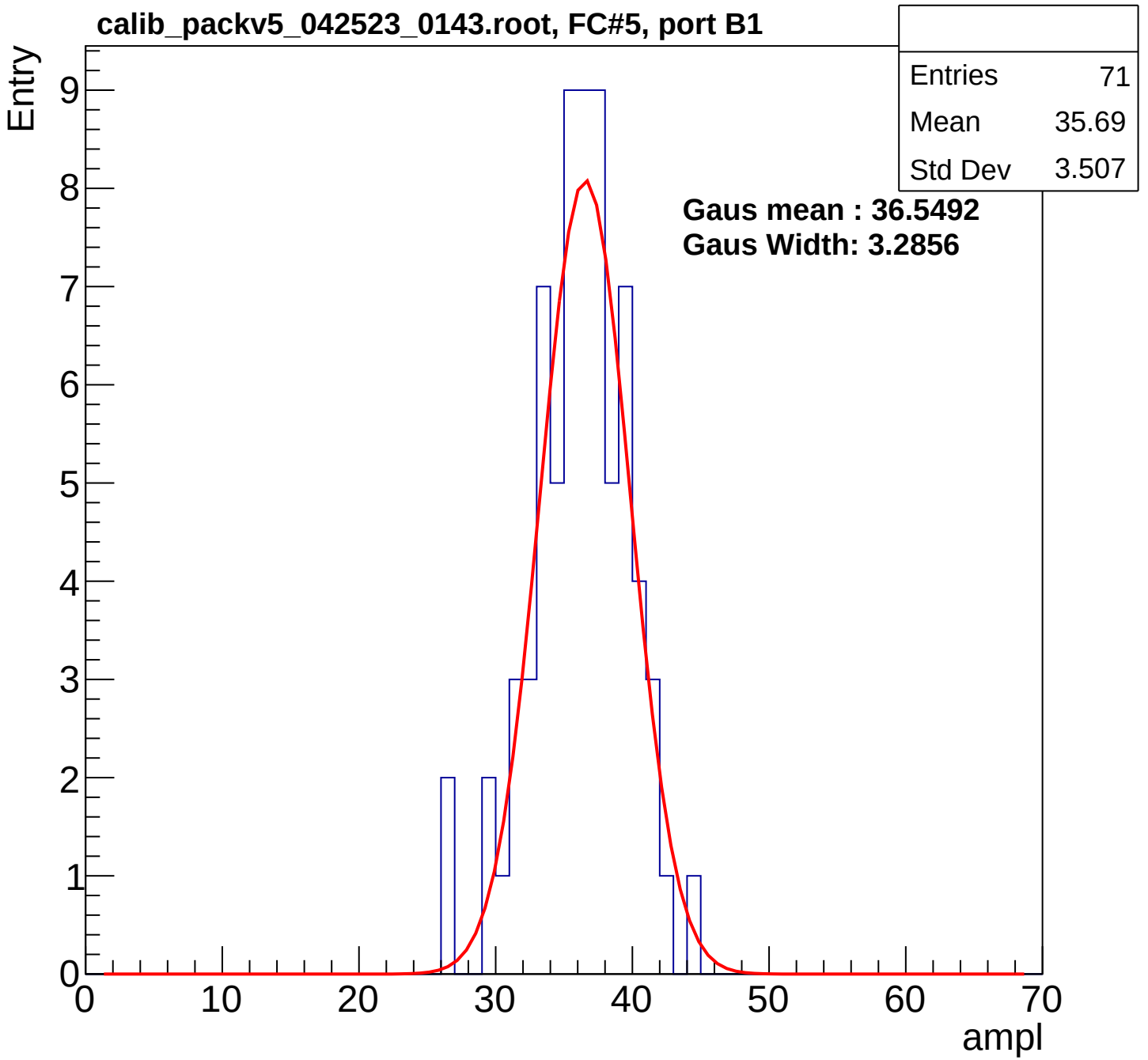
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 59.22
Std Dev 2.647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.33
Std Dev 17.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 29.21
Std Dev 3.279
Gaus mean : 29.9148
Gaus Width: 3.3196
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 35.69
Std Dev 3.507
Gaus mean : 36.5492
Gaus Width: 3.2856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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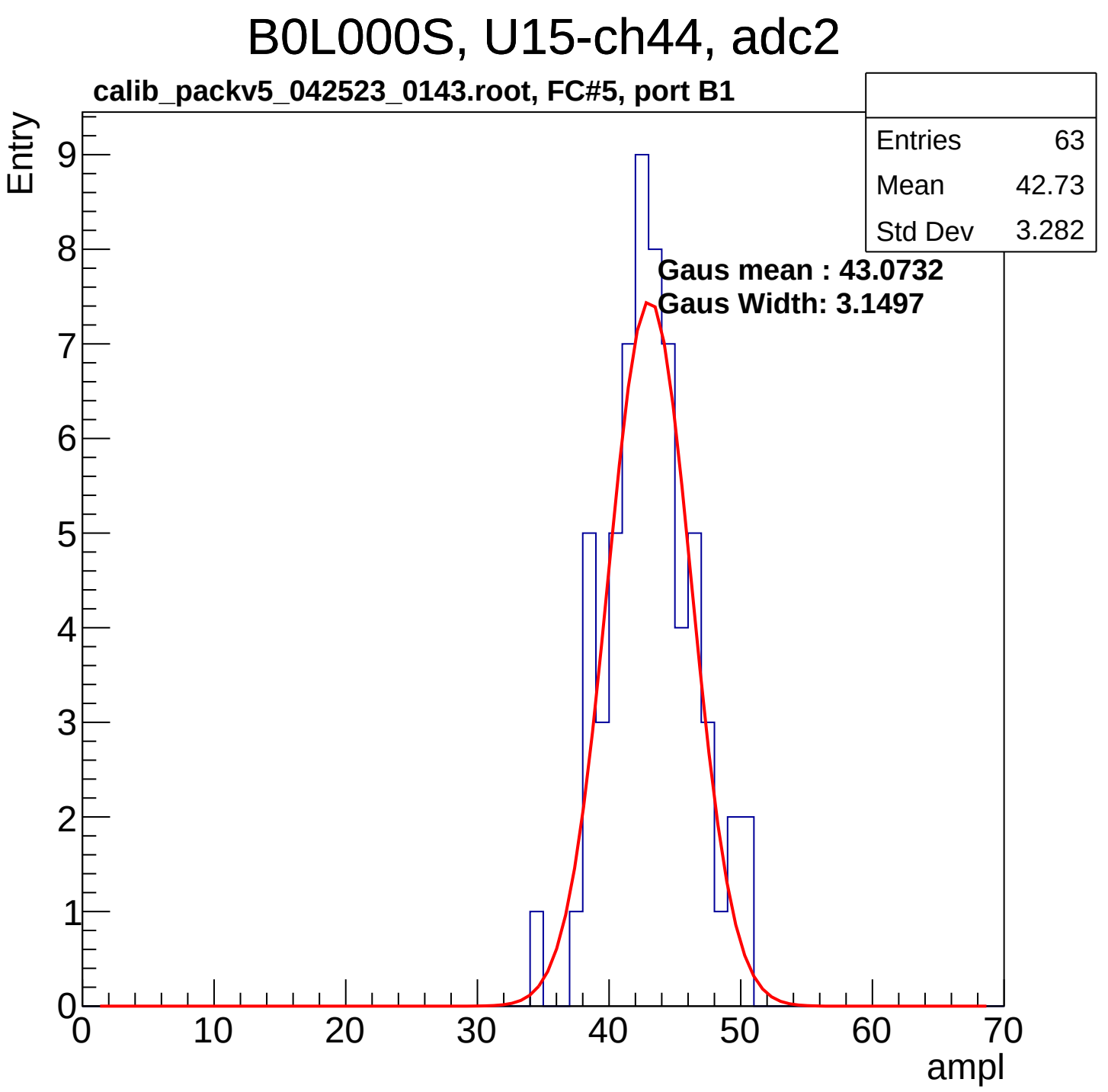
B0L000S, U15-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.73
Std Dev 3.282
Gaus mean : 43.0732
Gaus Width: 3.1497
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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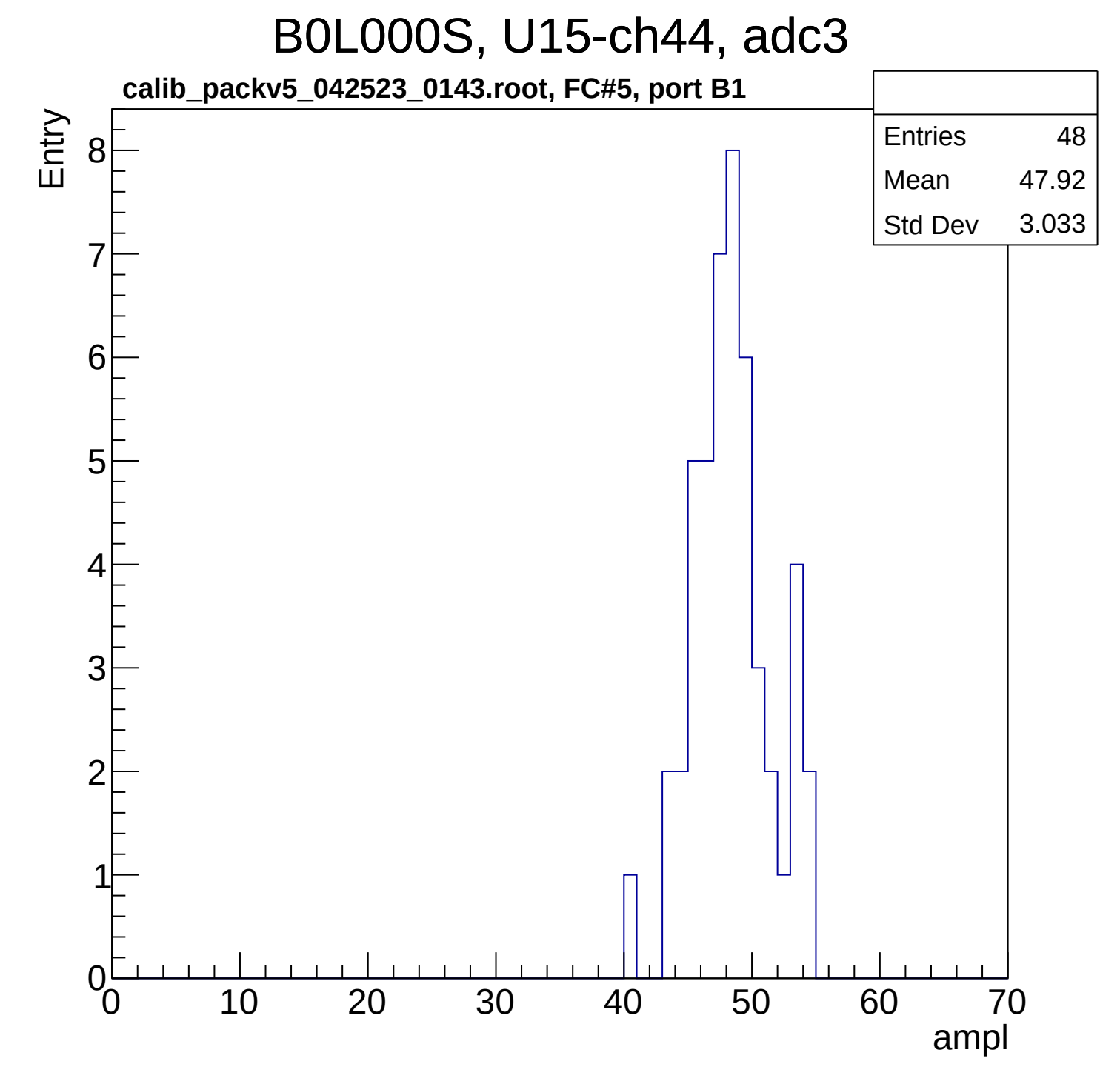
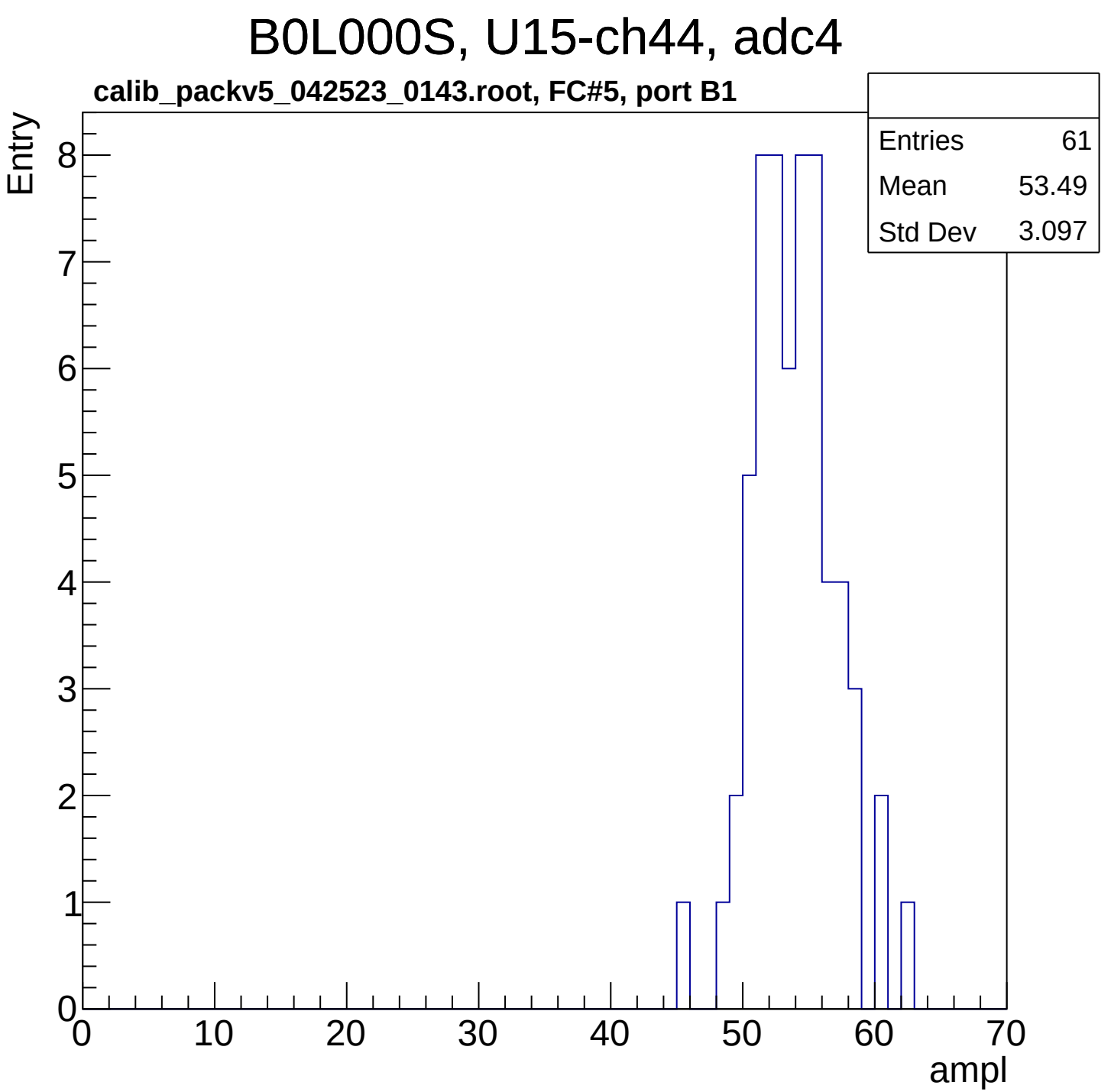
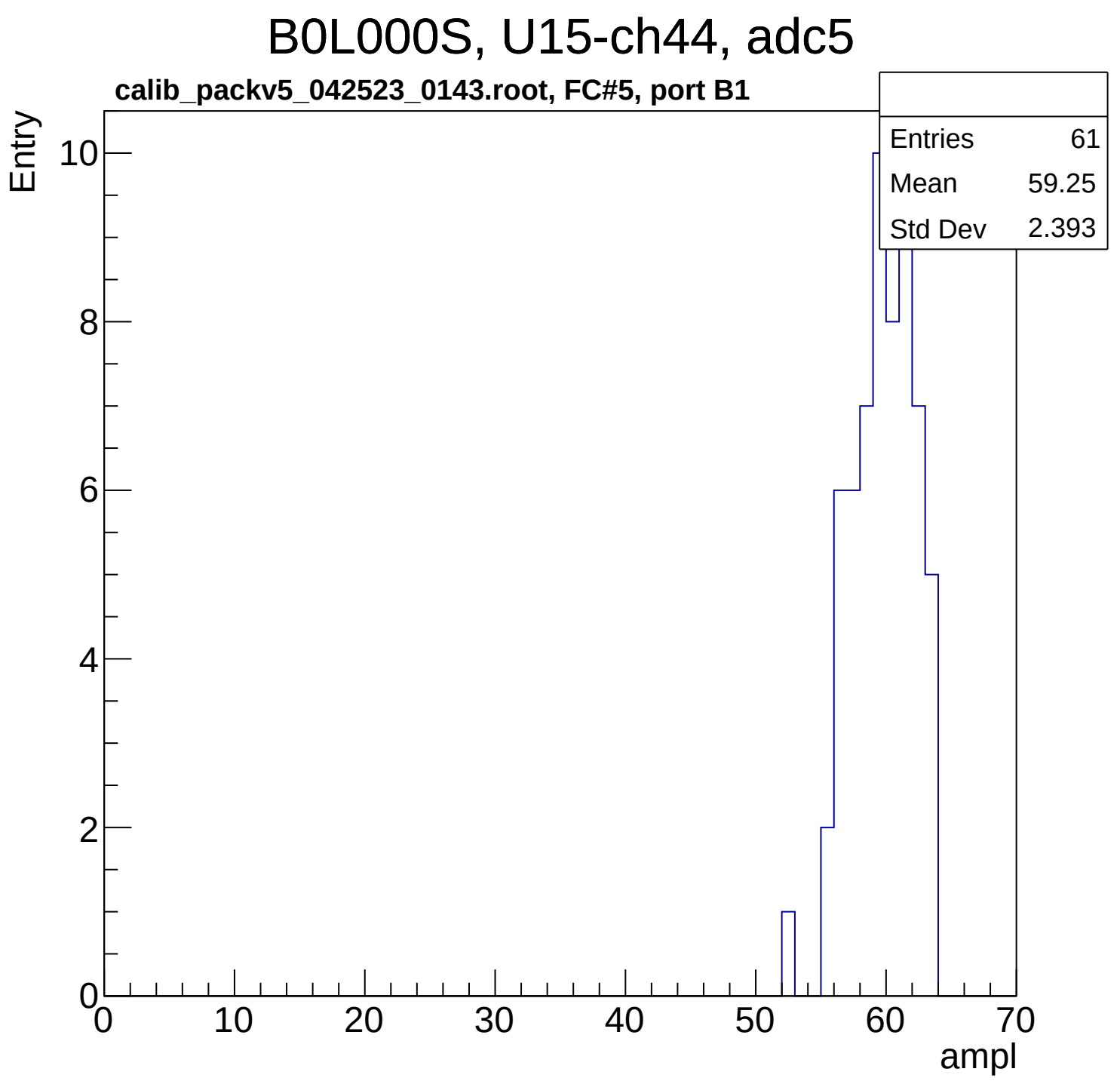
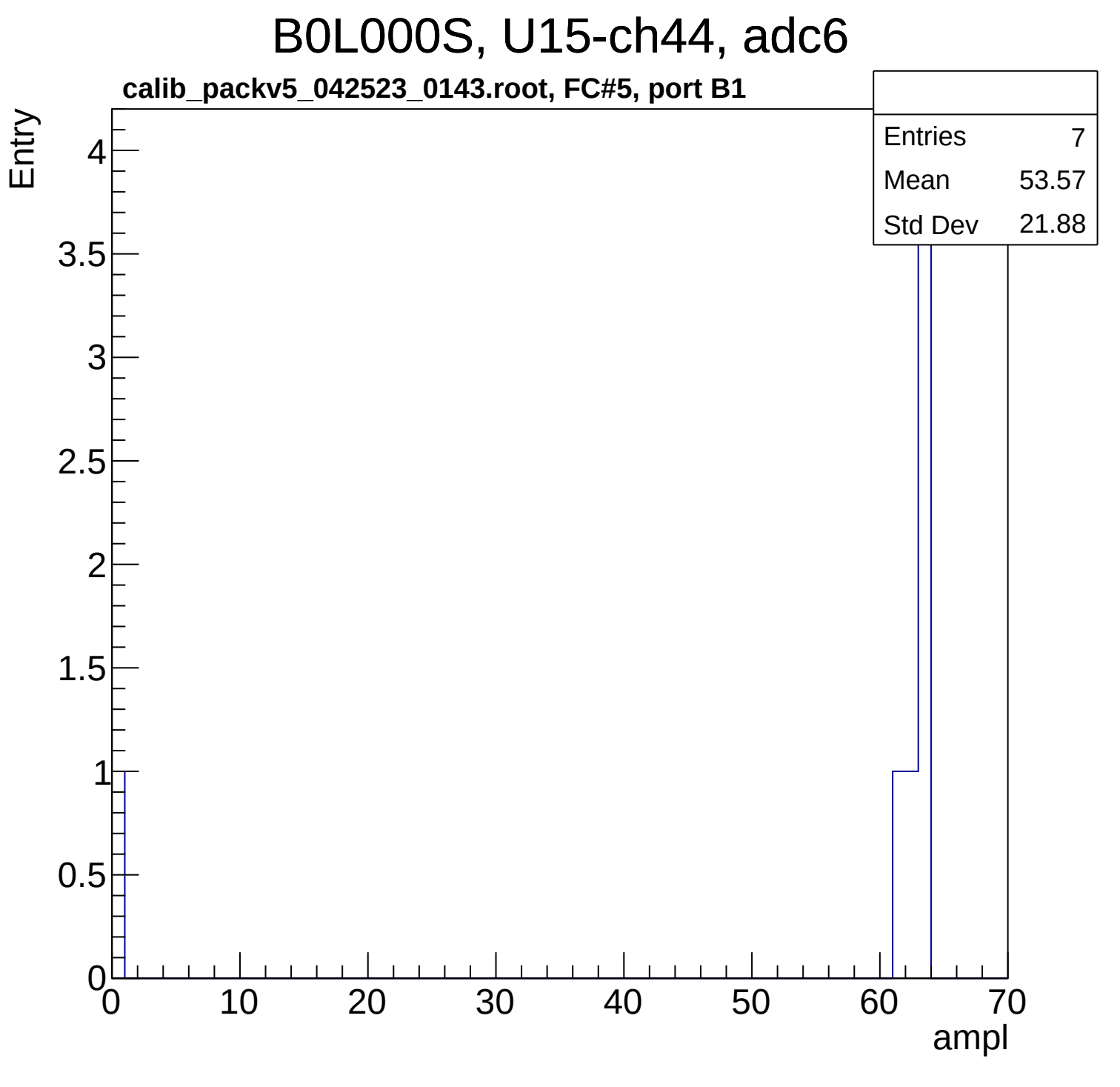

B0L000S, U15-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 47.92
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.49
Std Dev 3.097
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 59.25
Std Dev 2.393
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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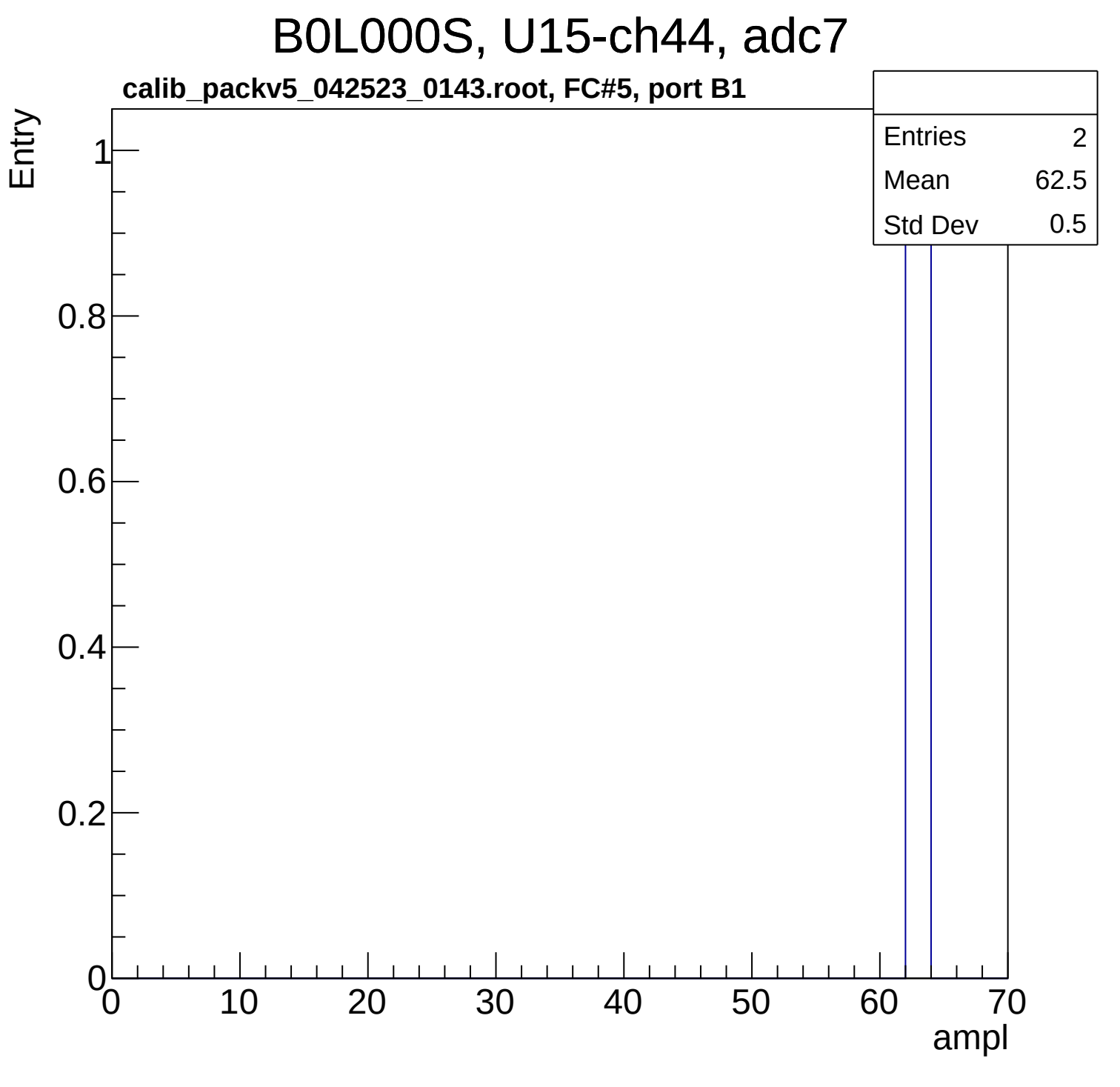
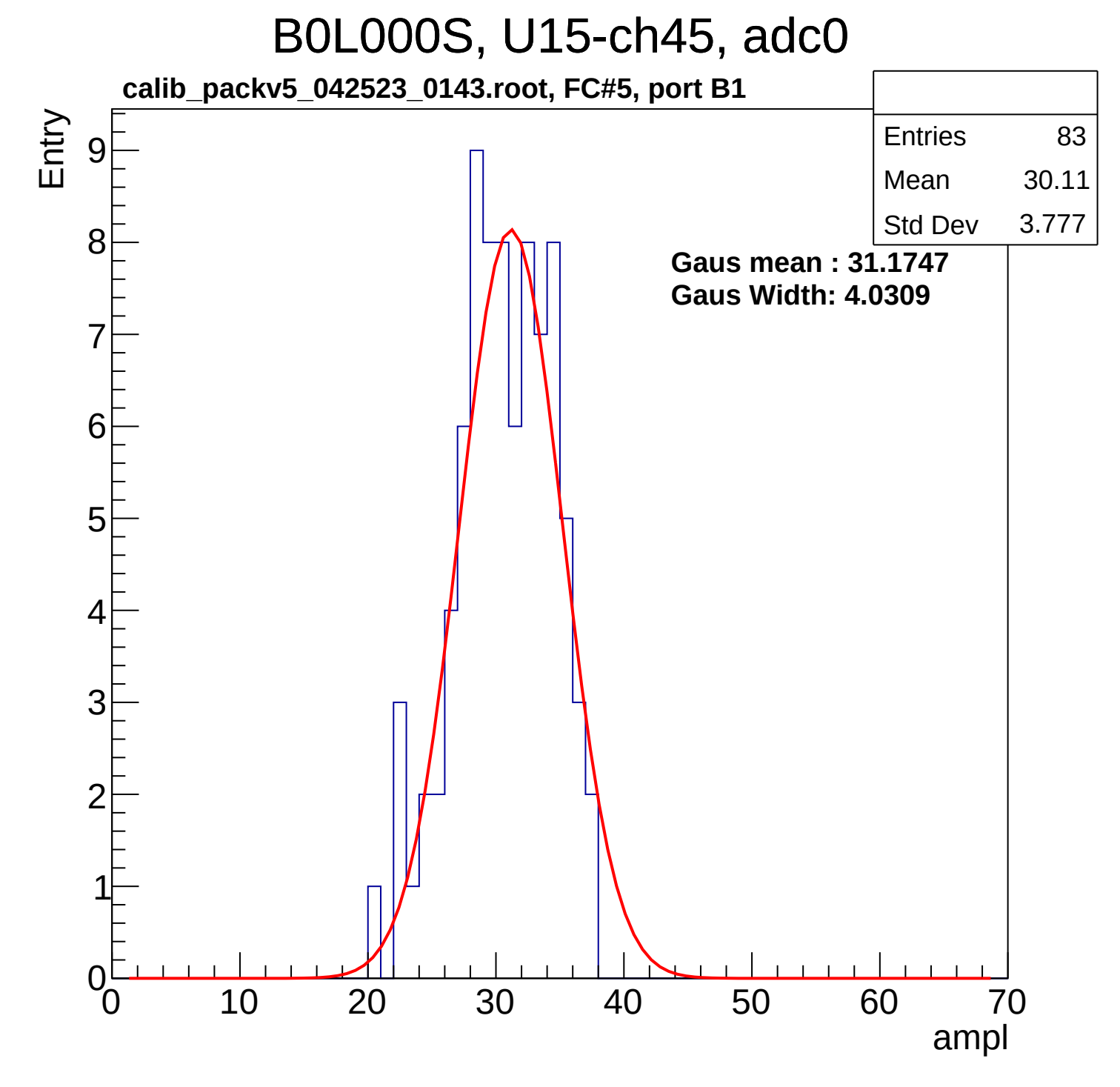
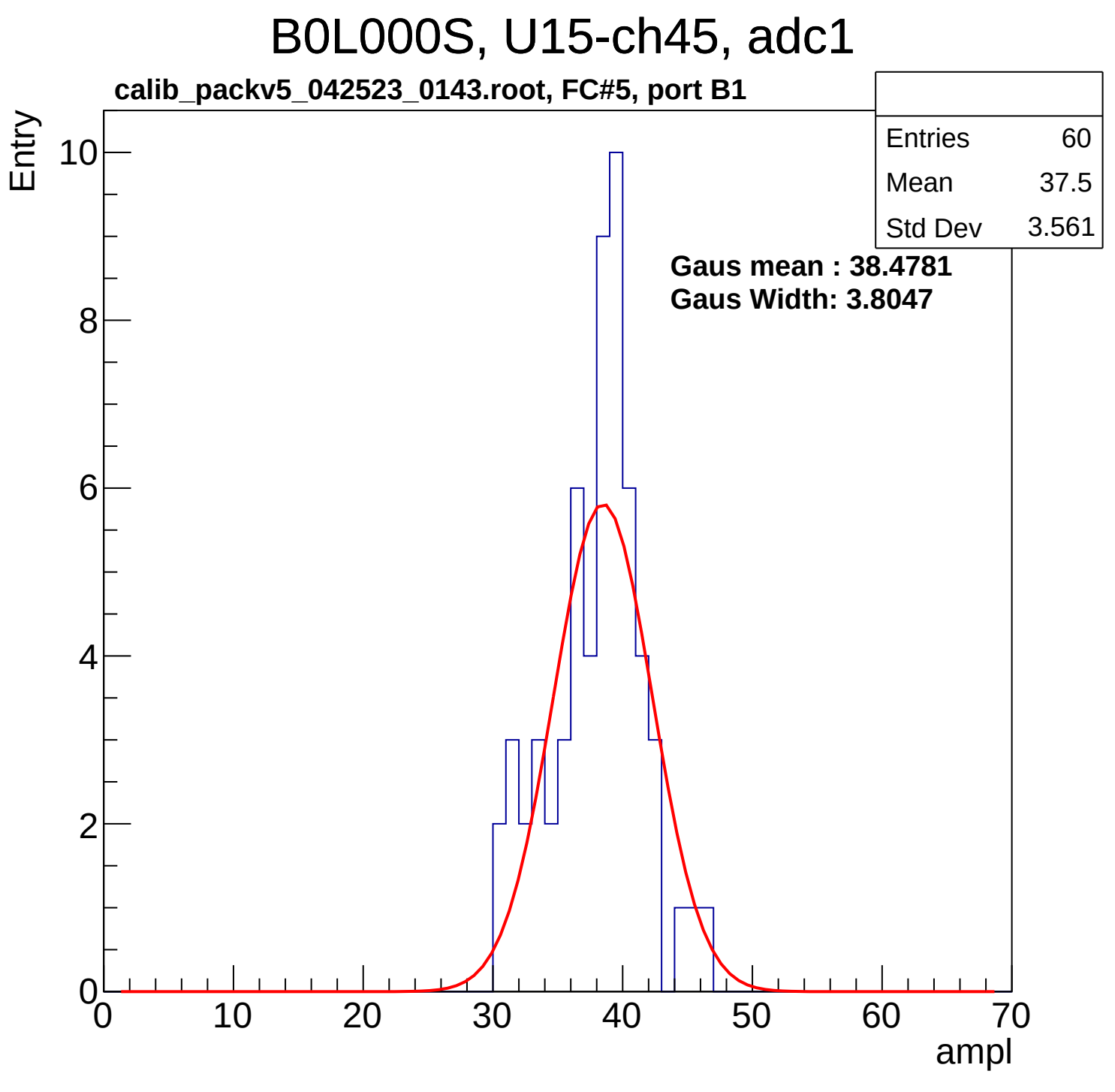
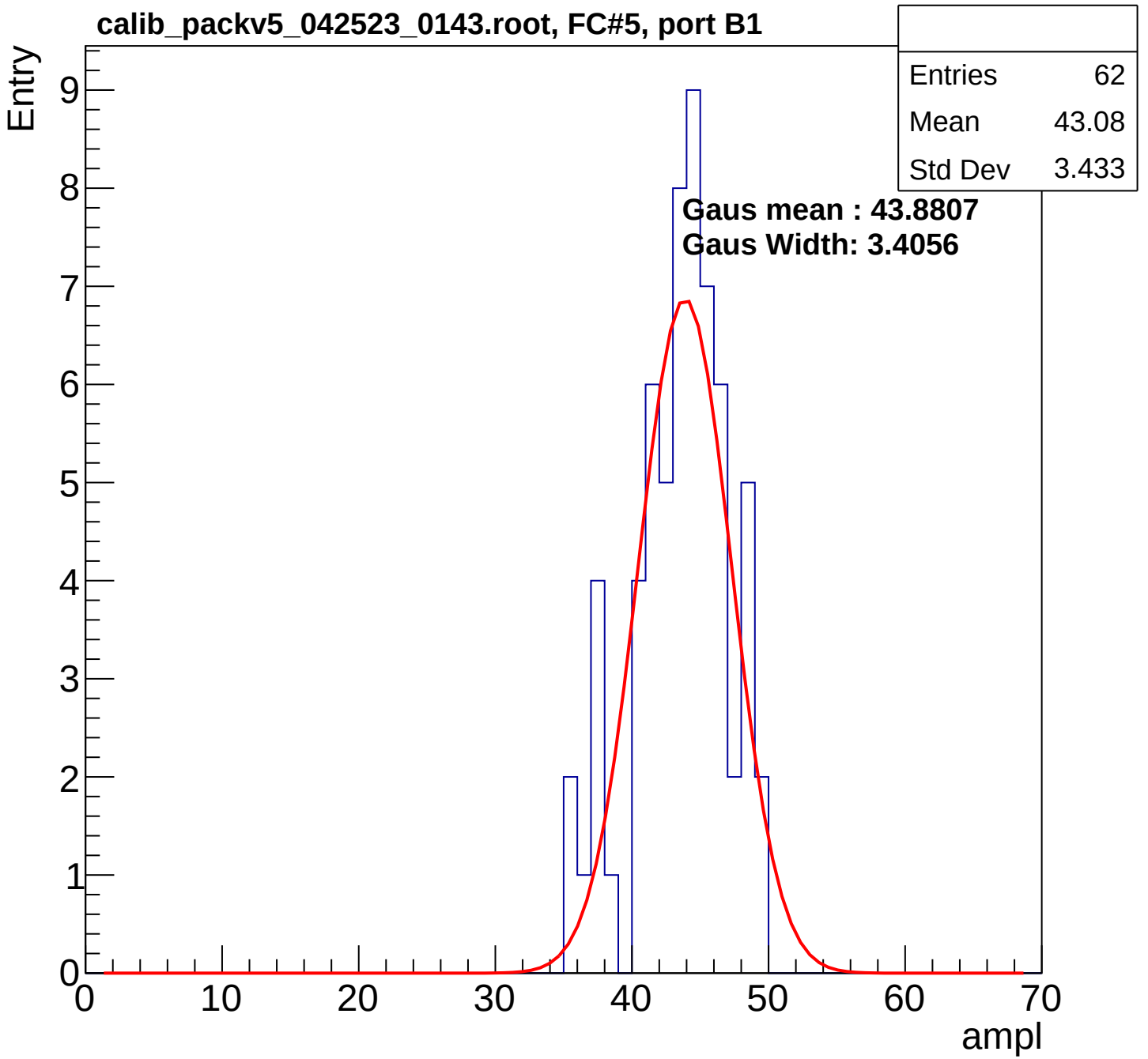
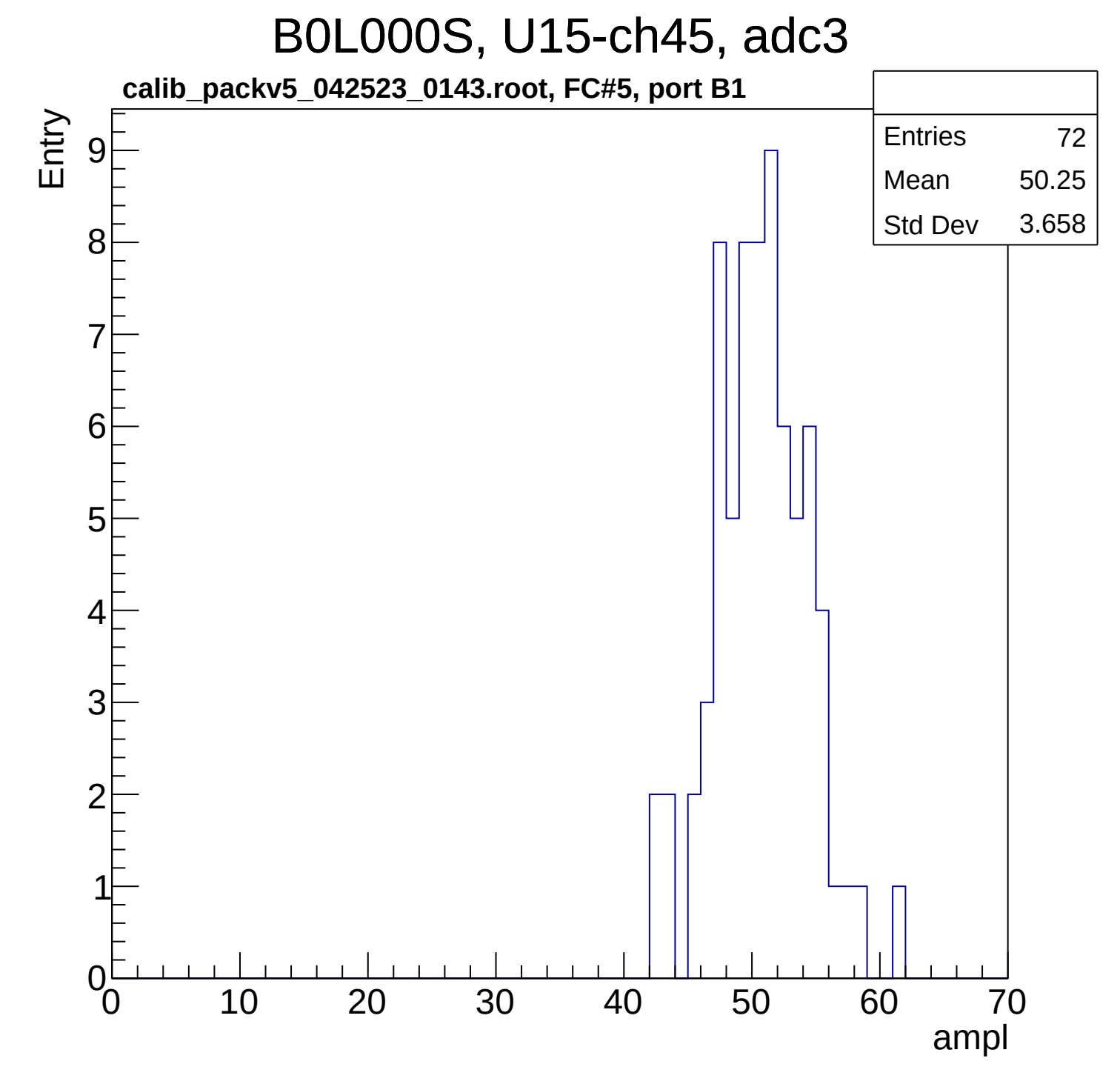
60
70


B0L000S, U15-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.11
Std Dev 3.777
Gaus mean : 31.1747
Gaus Width: 4.0309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.5
Std Dev 3.561
Gaus mean : 38.4781
Gaus Width: 3.8047
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.08
Std Dev 3.433
Gaus mean : 43.8807
Gaus Width: 3.4056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 50.25
Std Dev 3.658
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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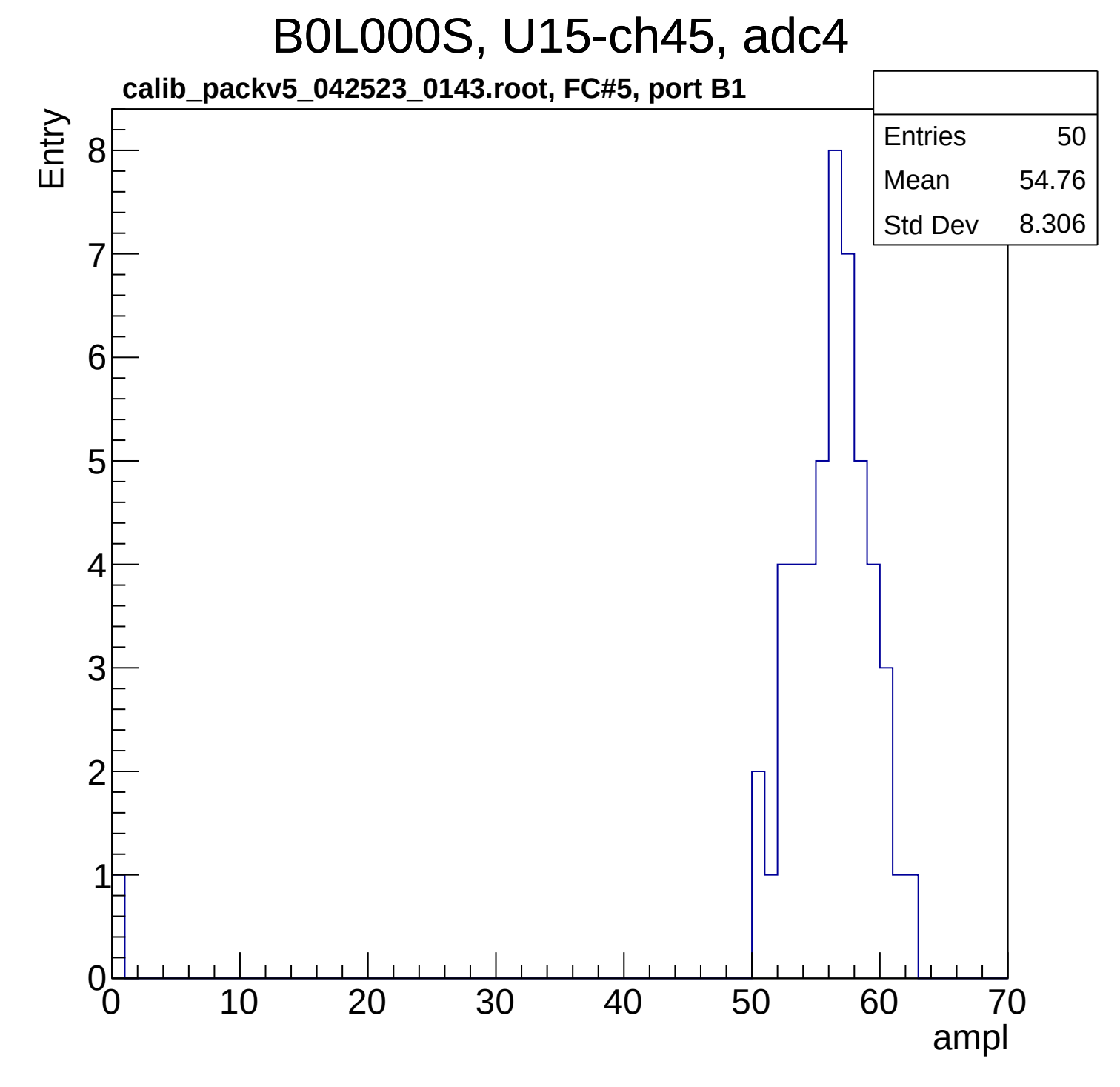
B0L000S, U15-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.76
Std Dev 8.306
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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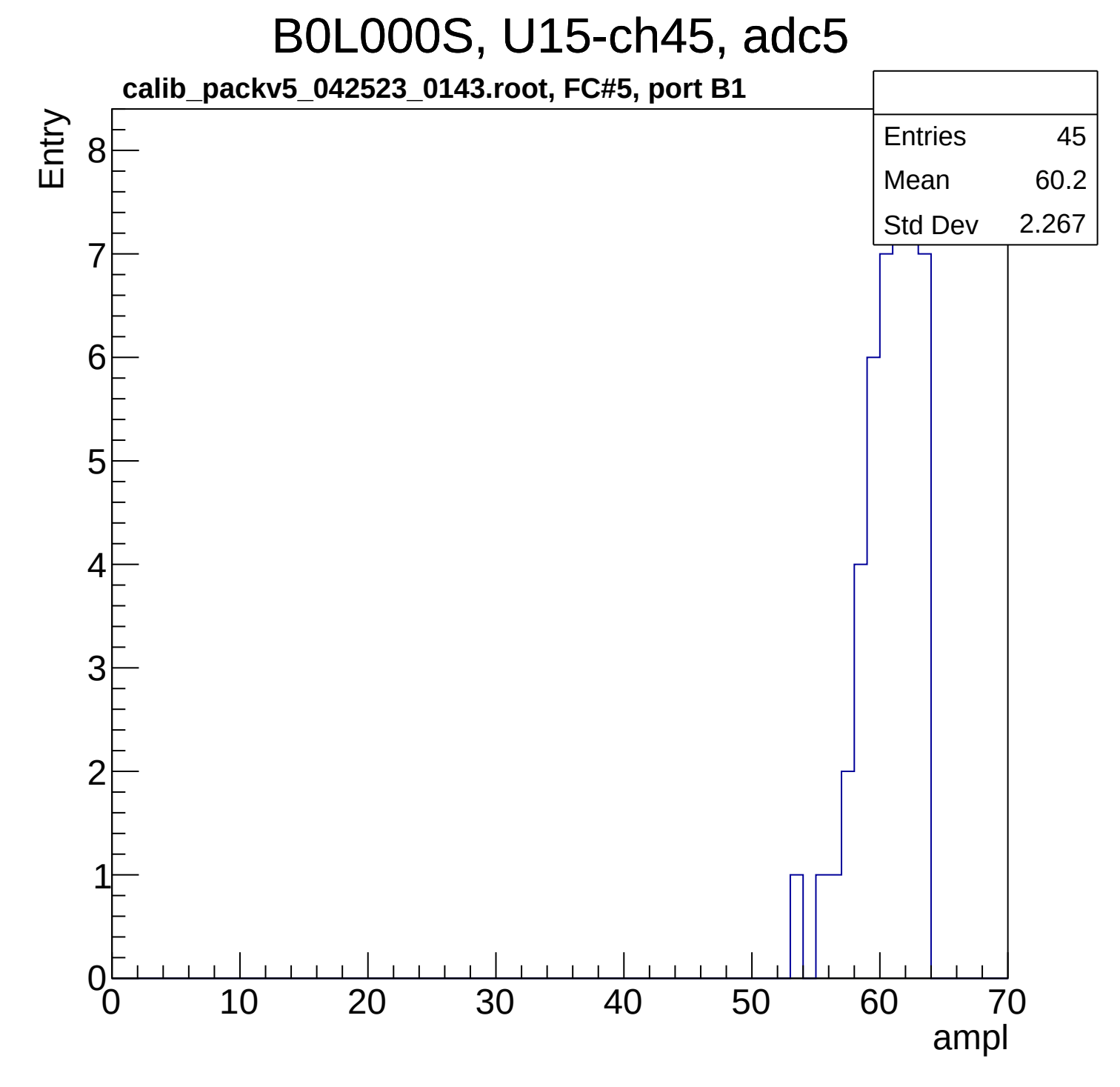
B0L000S, U15-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.2
Std Dev 2.267
Entry
ampl
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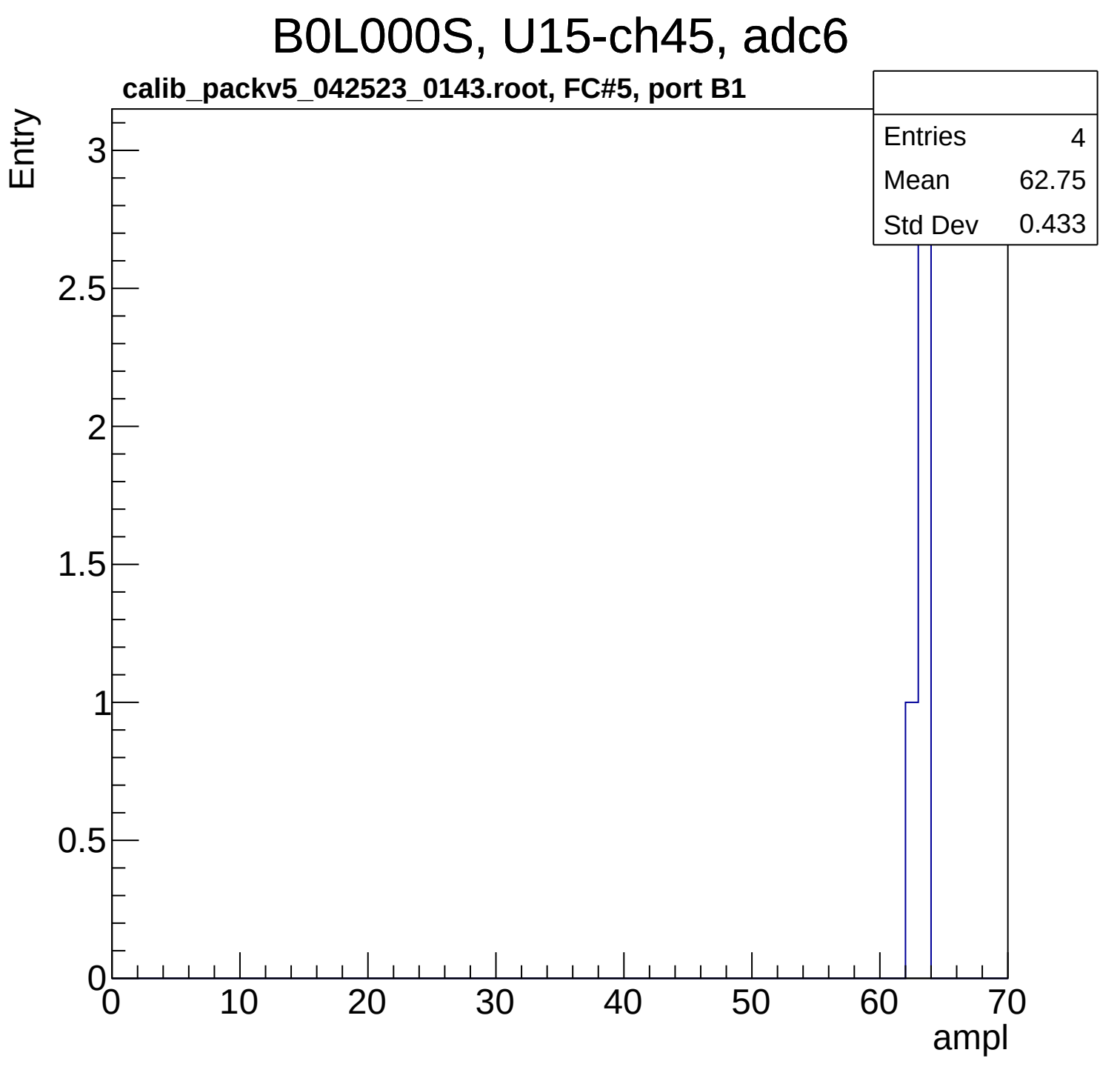
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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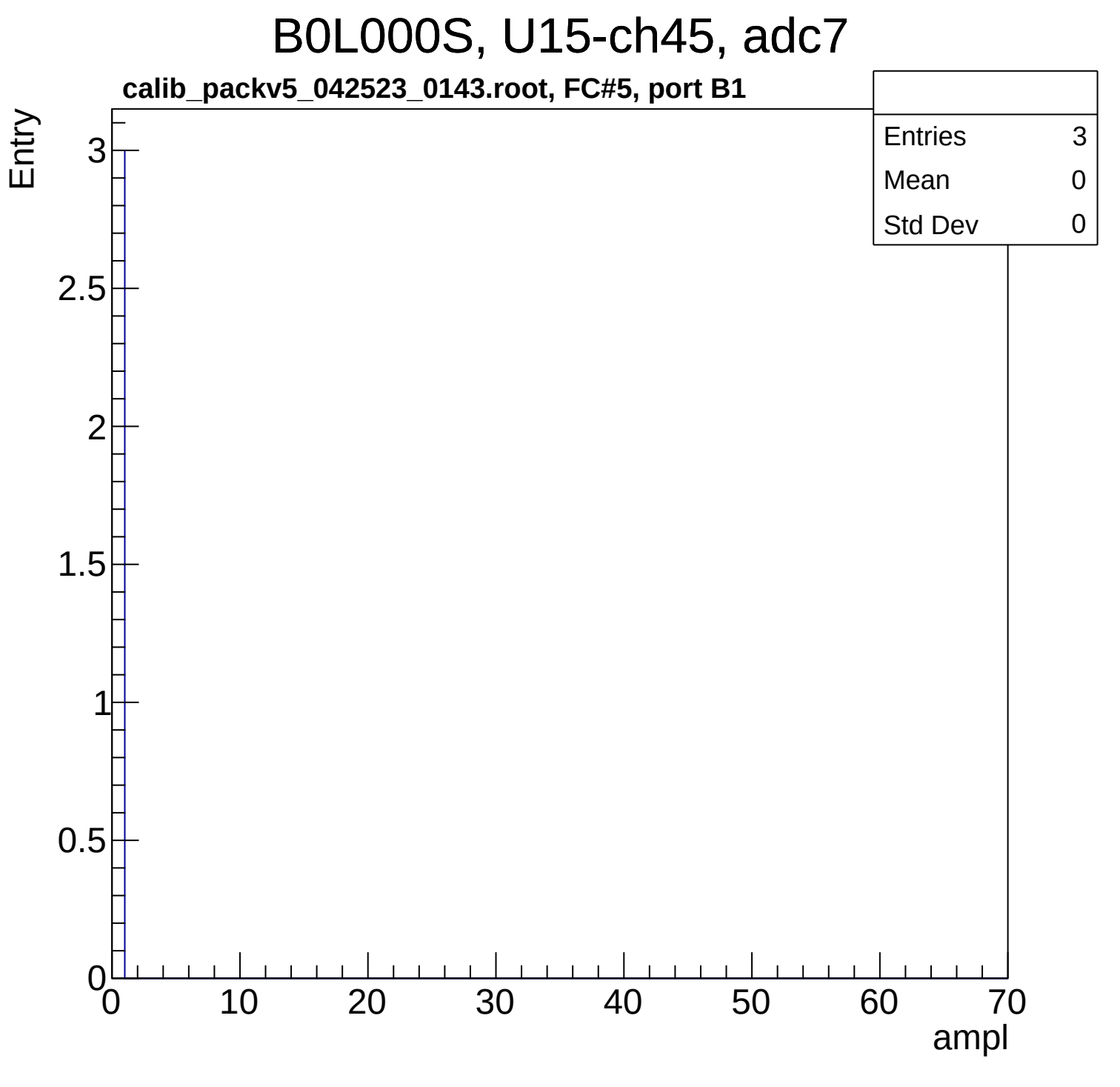
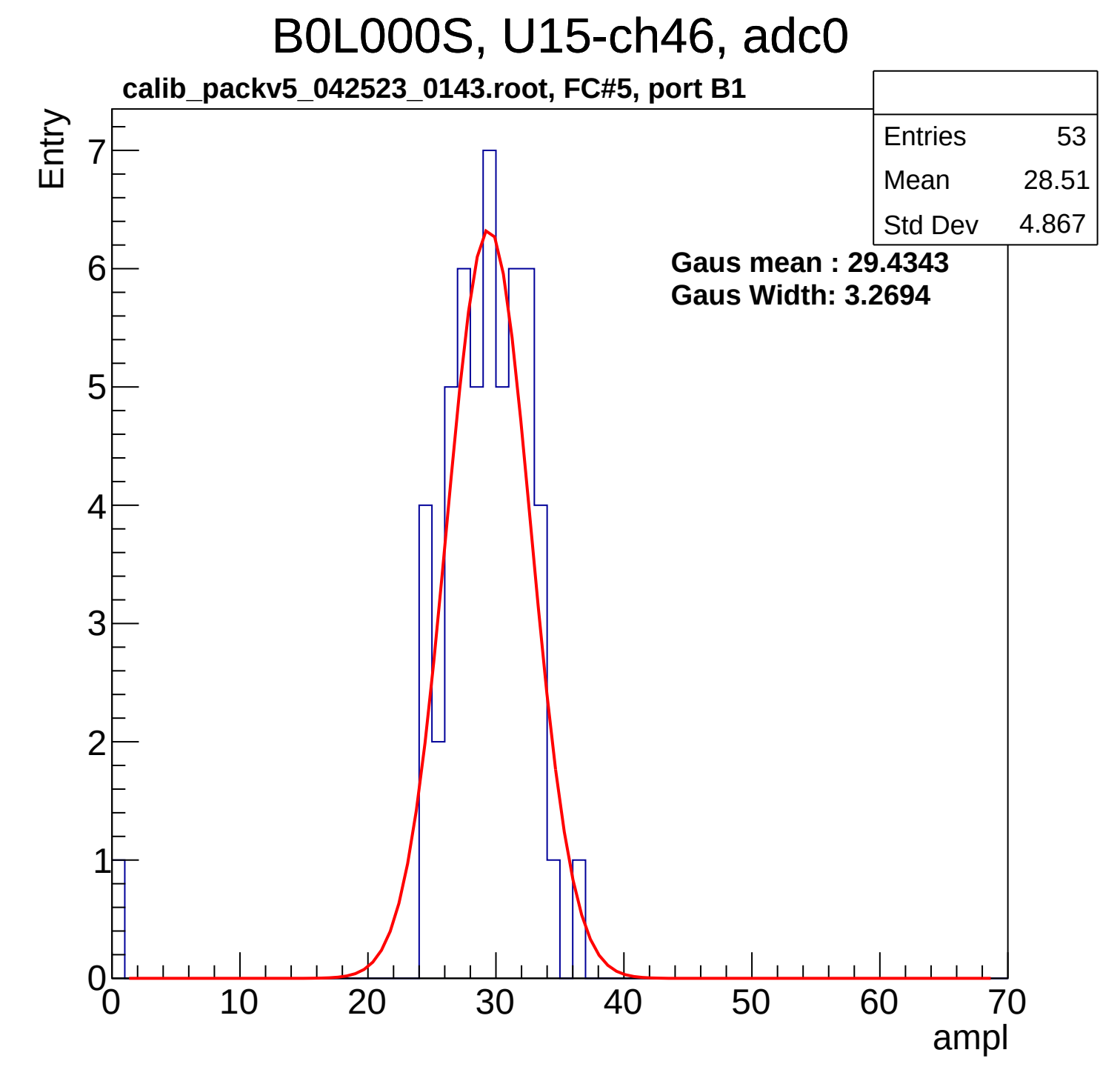
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 28.51
Std Dev 4.867
Gaus mean : 29.4343
Gaus Width: 3.2694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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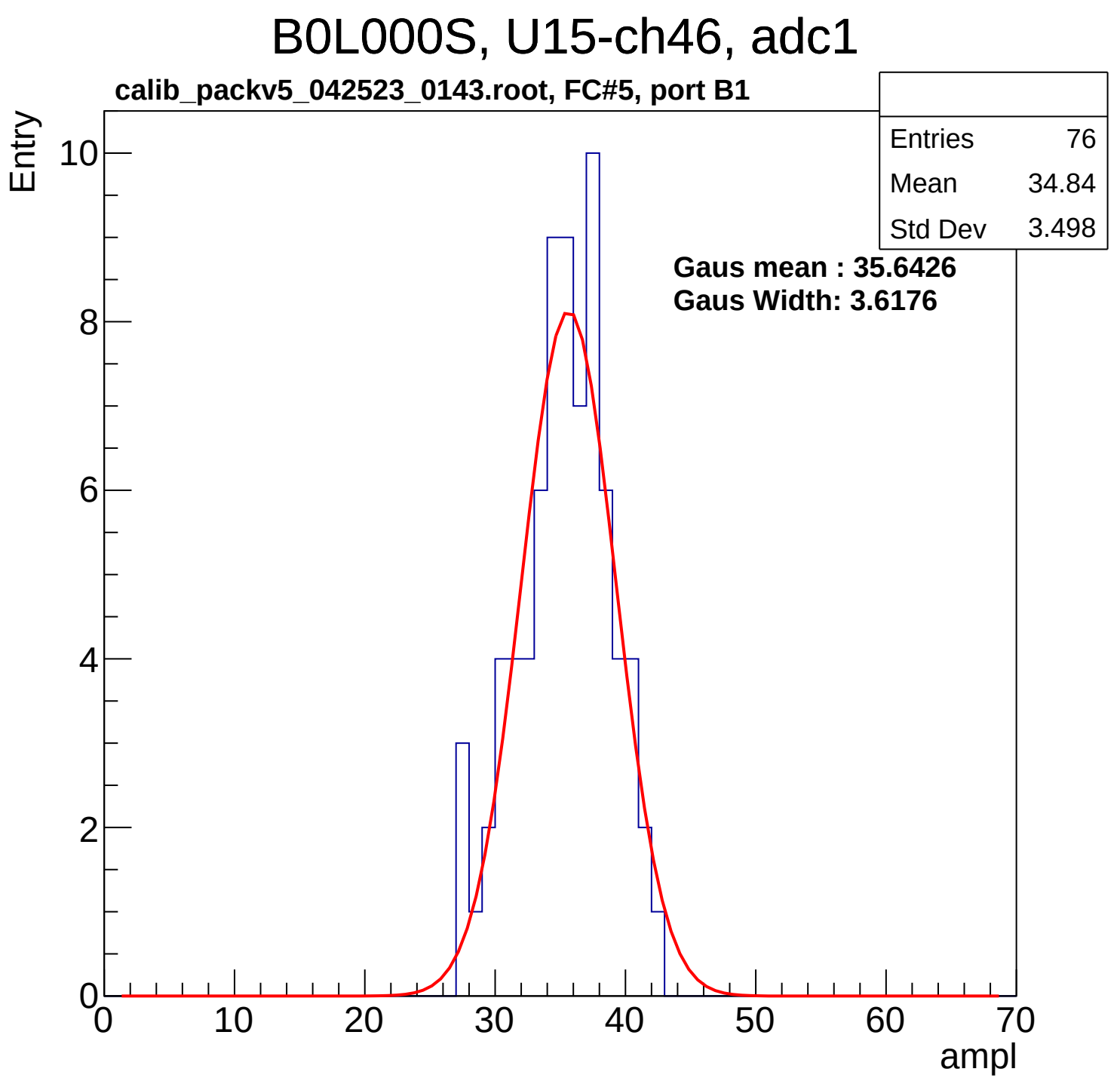
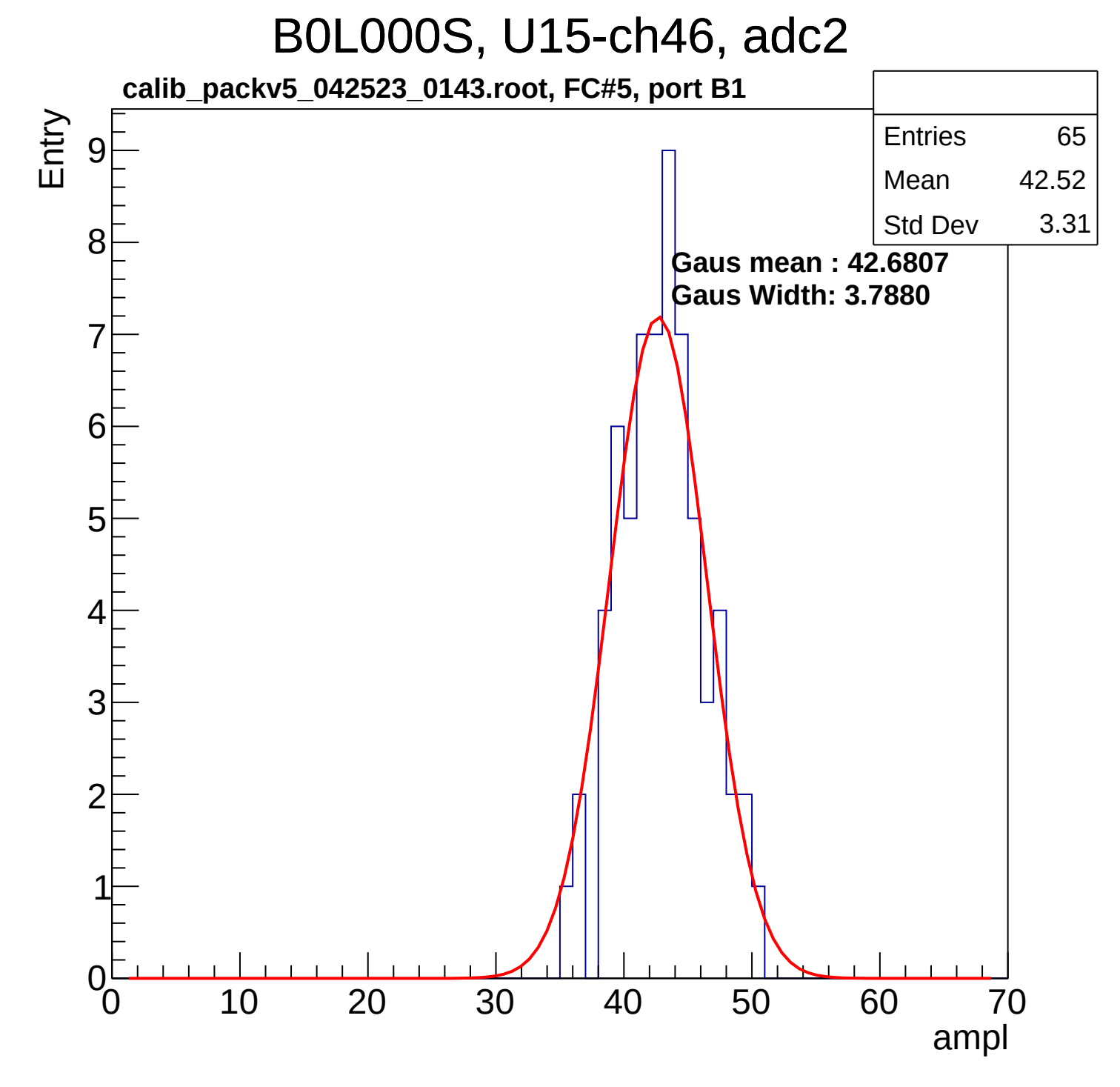

B0L000S, U15-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 34.84
Std Dev 3.498
Gaus mean : 35.6426
Gaus Width: 3.6176
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.52
Std Dev 3.31
Gaus mean : 42.6807
Gaus Width: 3.7880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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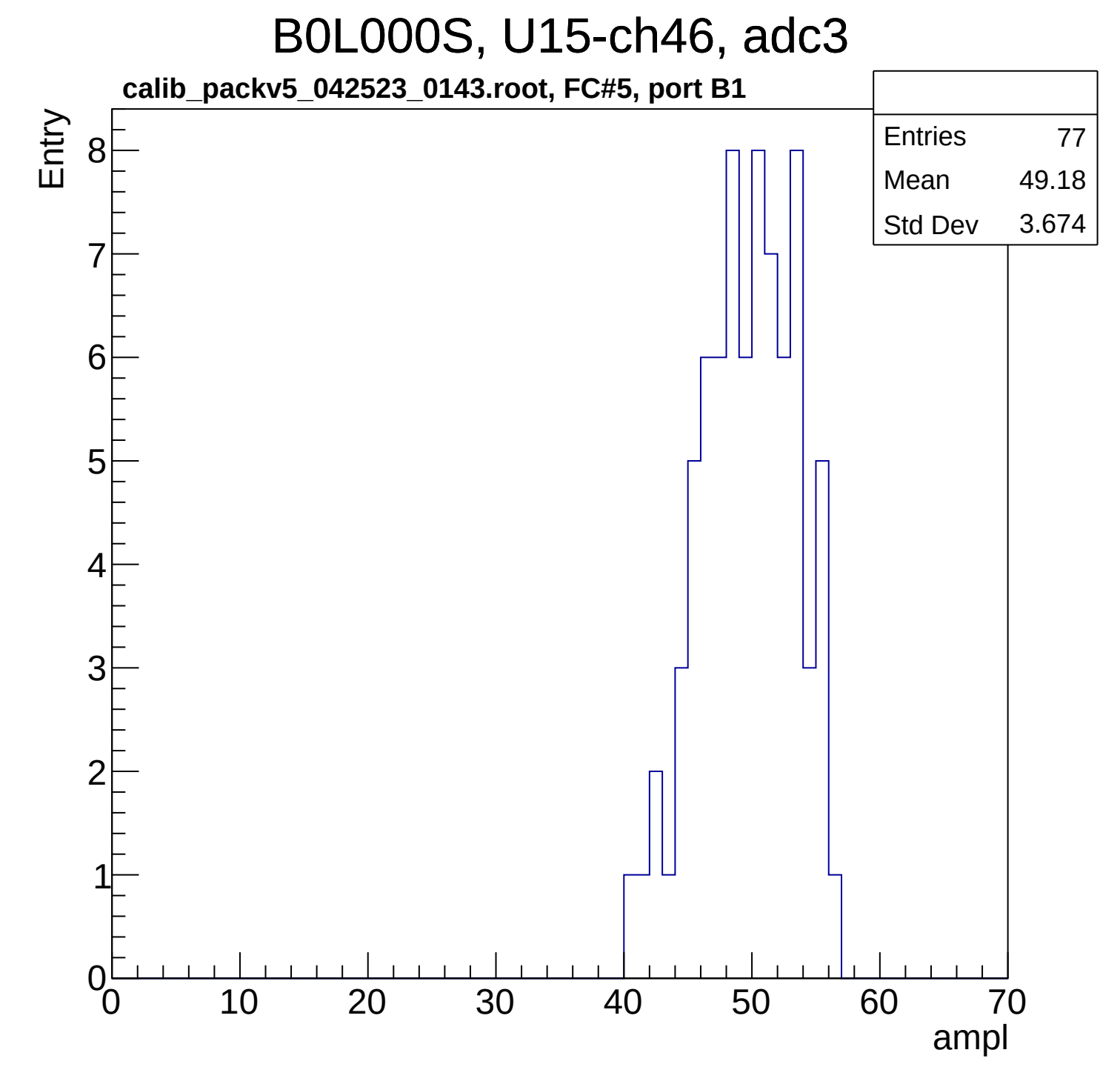
B0L000S, U15-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.18
Std Dev 3.674
Entry
ampl
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1
2
3
4
5
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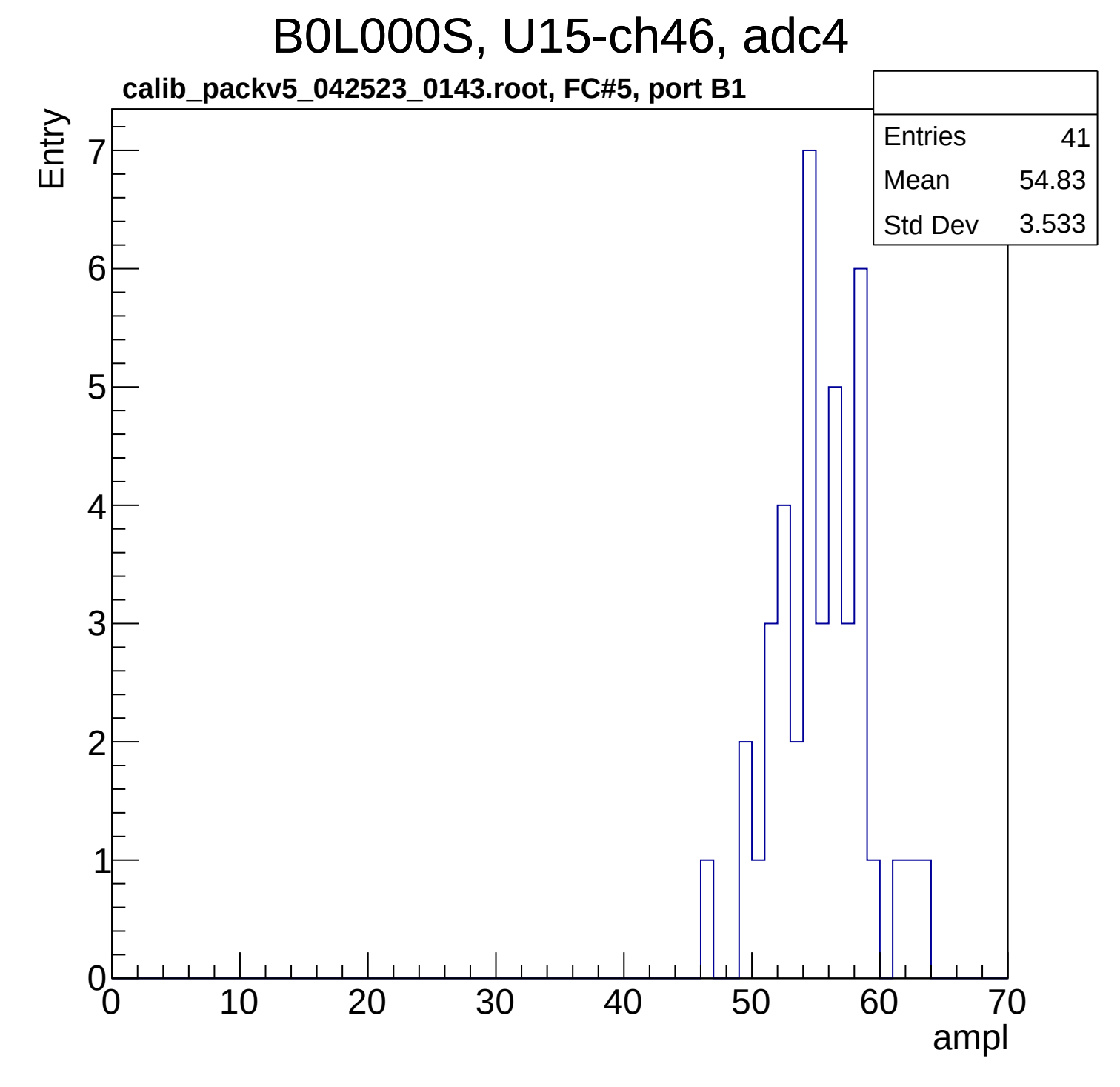
B0L000S, U15-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 54.83
Std Dev 3.533
Entry
ampl
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1
2
3
4
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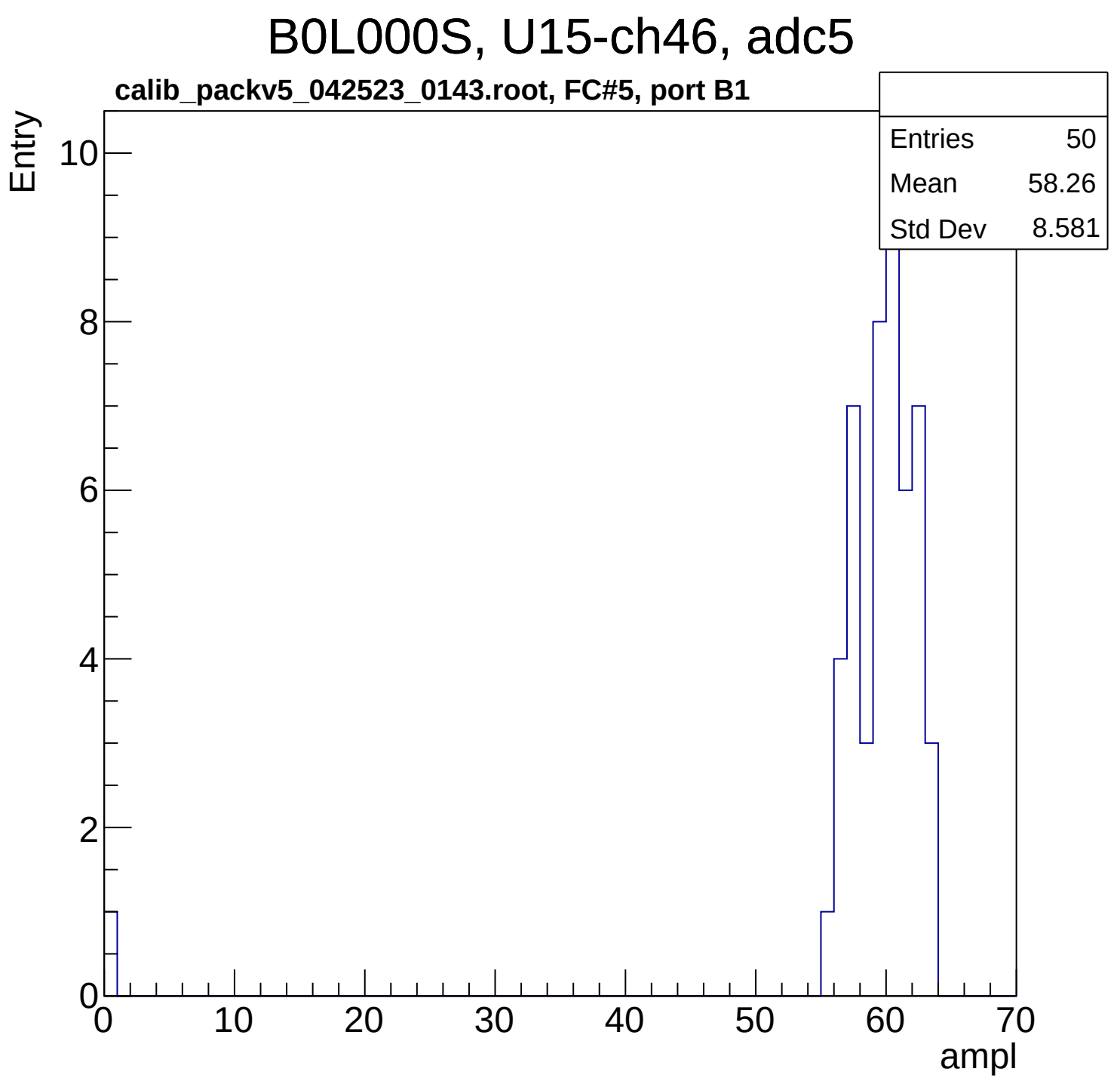
B0L000S, U15-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.26
Std Dev 8.581
Entry
0
2
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10
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10
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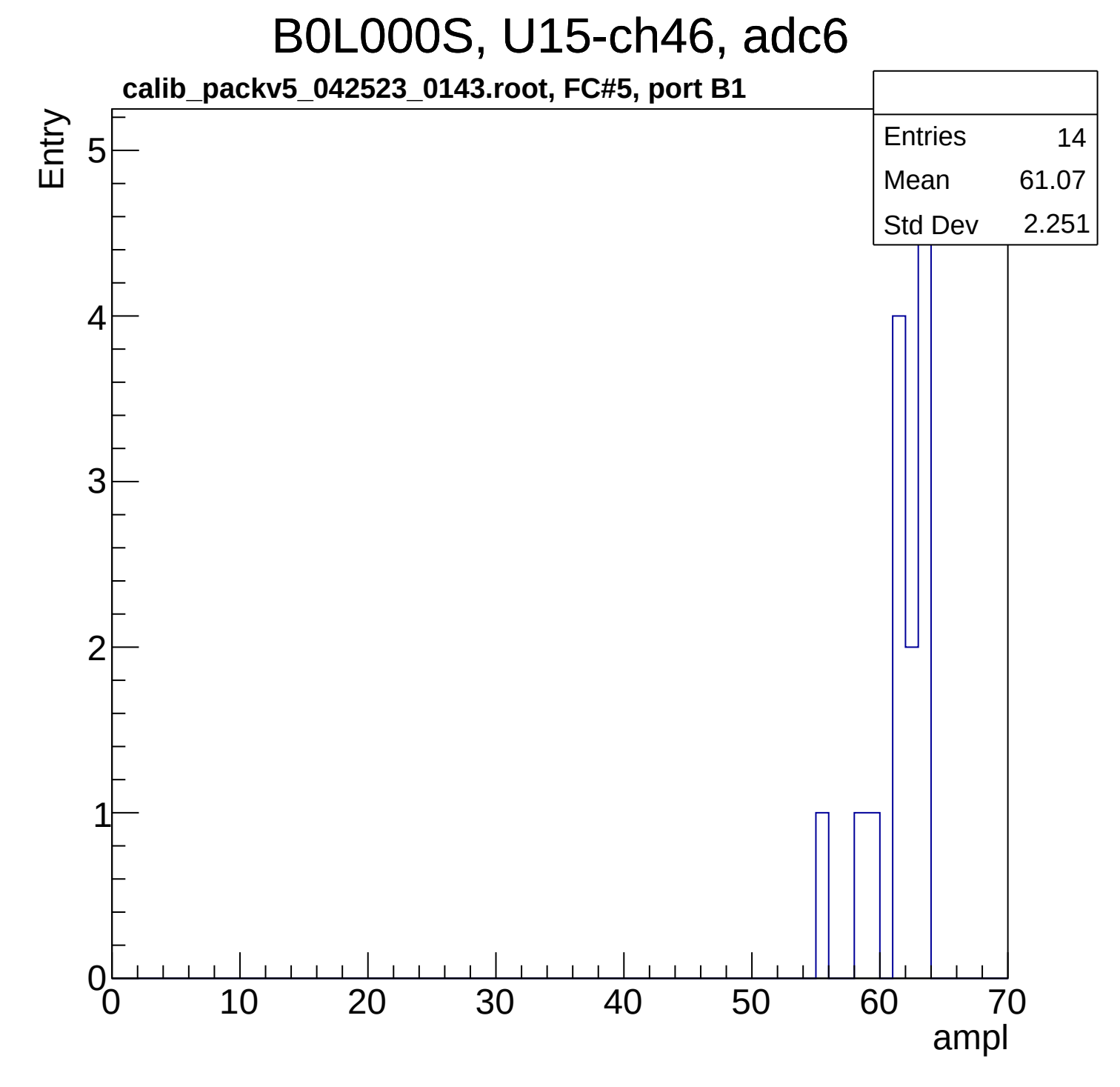
60
70
ampl


B0L000S, U15-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.07
Std Dev 2.251
Entry
ampl
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2
3
4
5
0
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40
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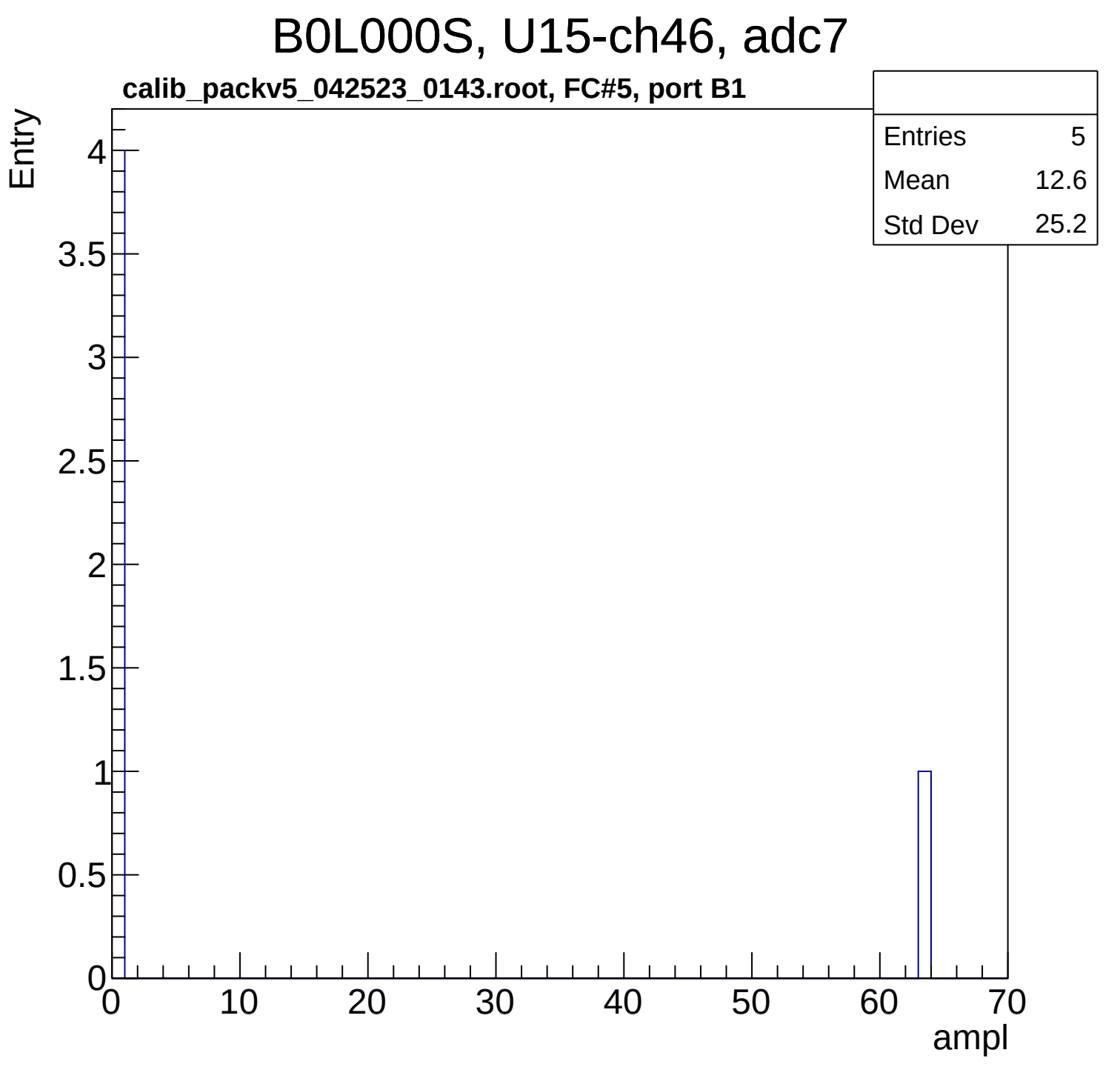
B0L000S, U15-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 12.6
Std Dev 25.2
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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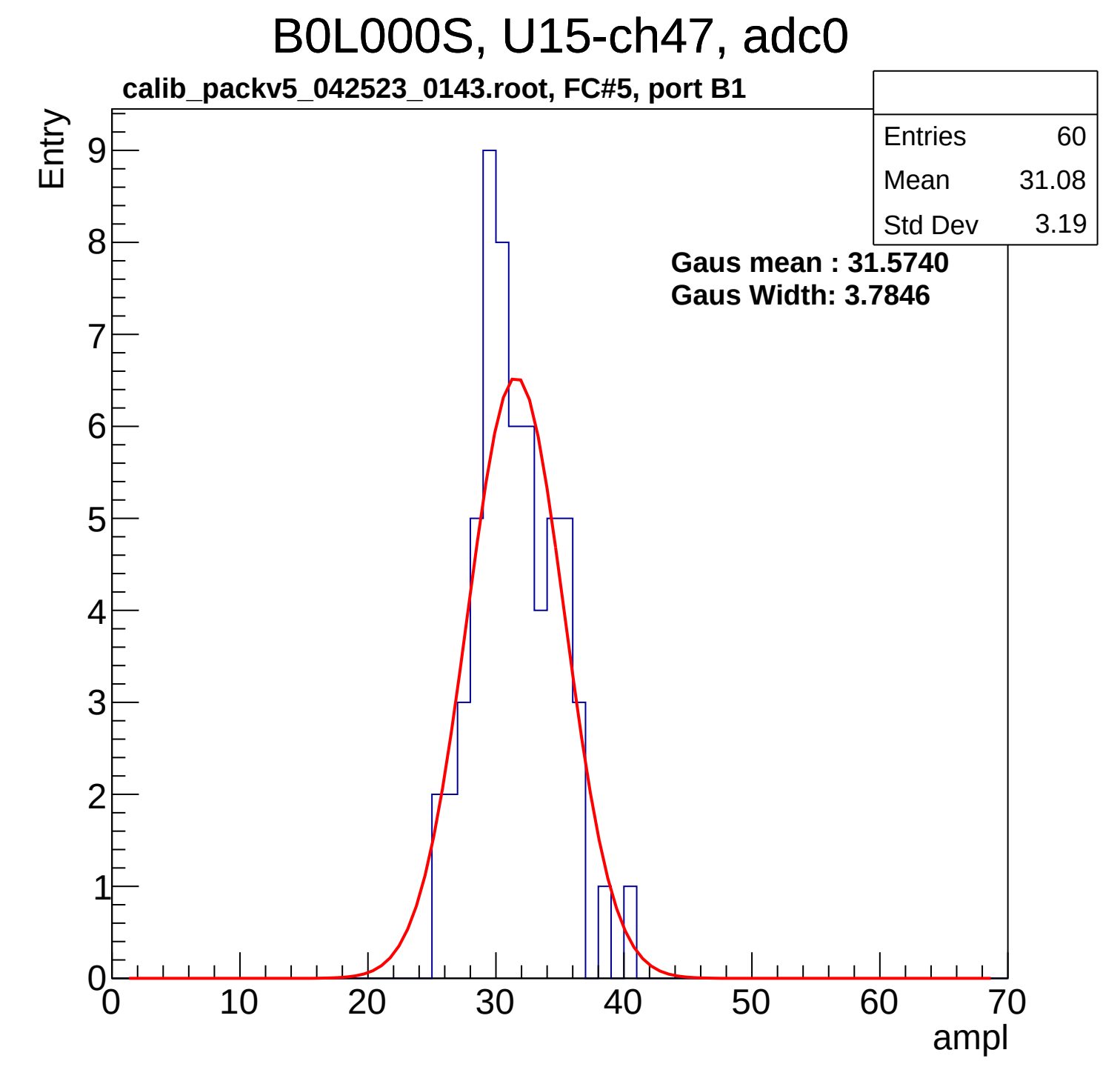
B0L000S, U15-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 31.08
Std Dev 3.19
Gaus mean : 31.5740
Gaus Width: 3.7846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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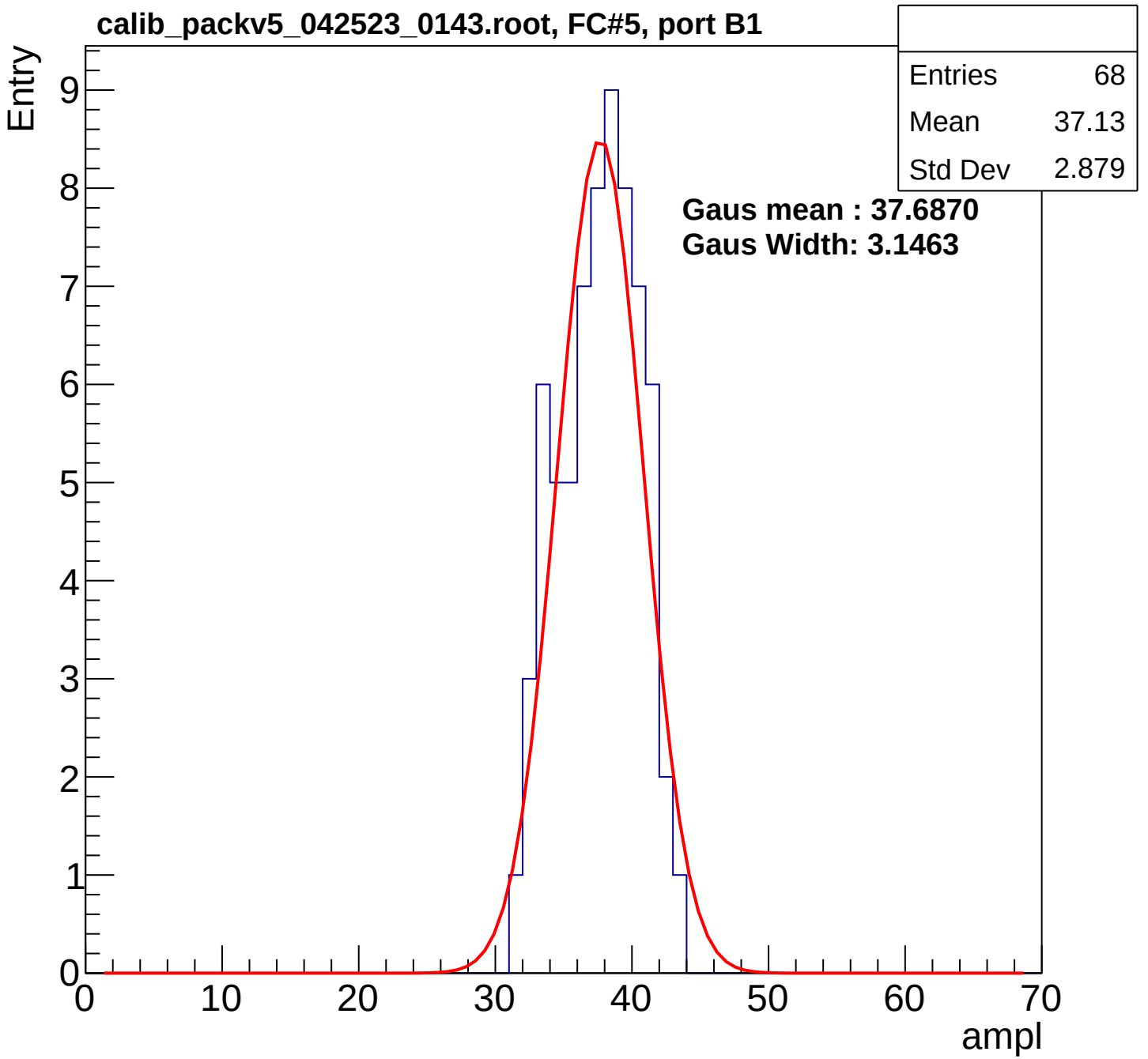
B0L000S, U15-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 37.13
Std Dev 2.879
Gaus mean : 37.6870
Gaus Width: 3.1463
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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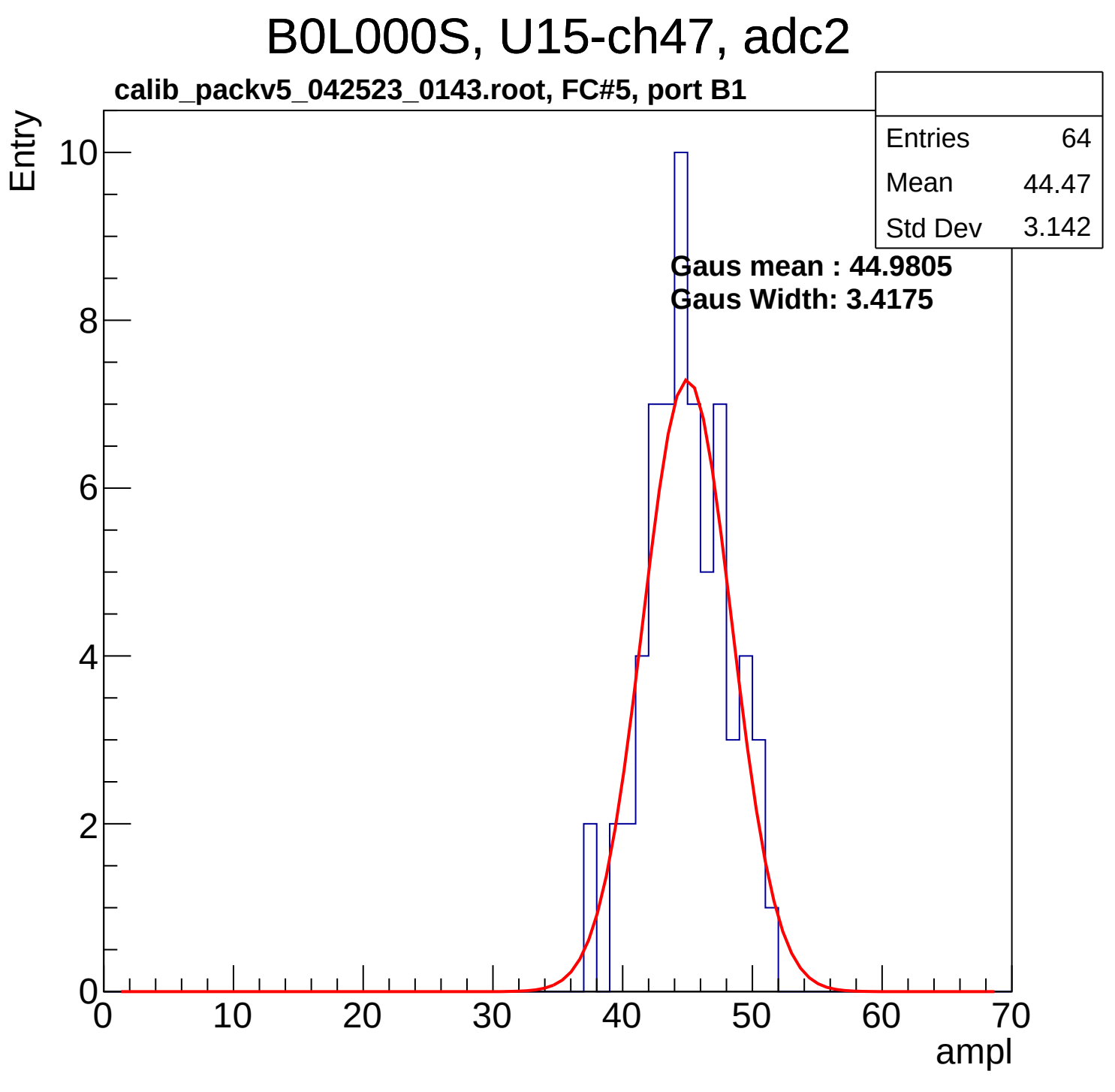
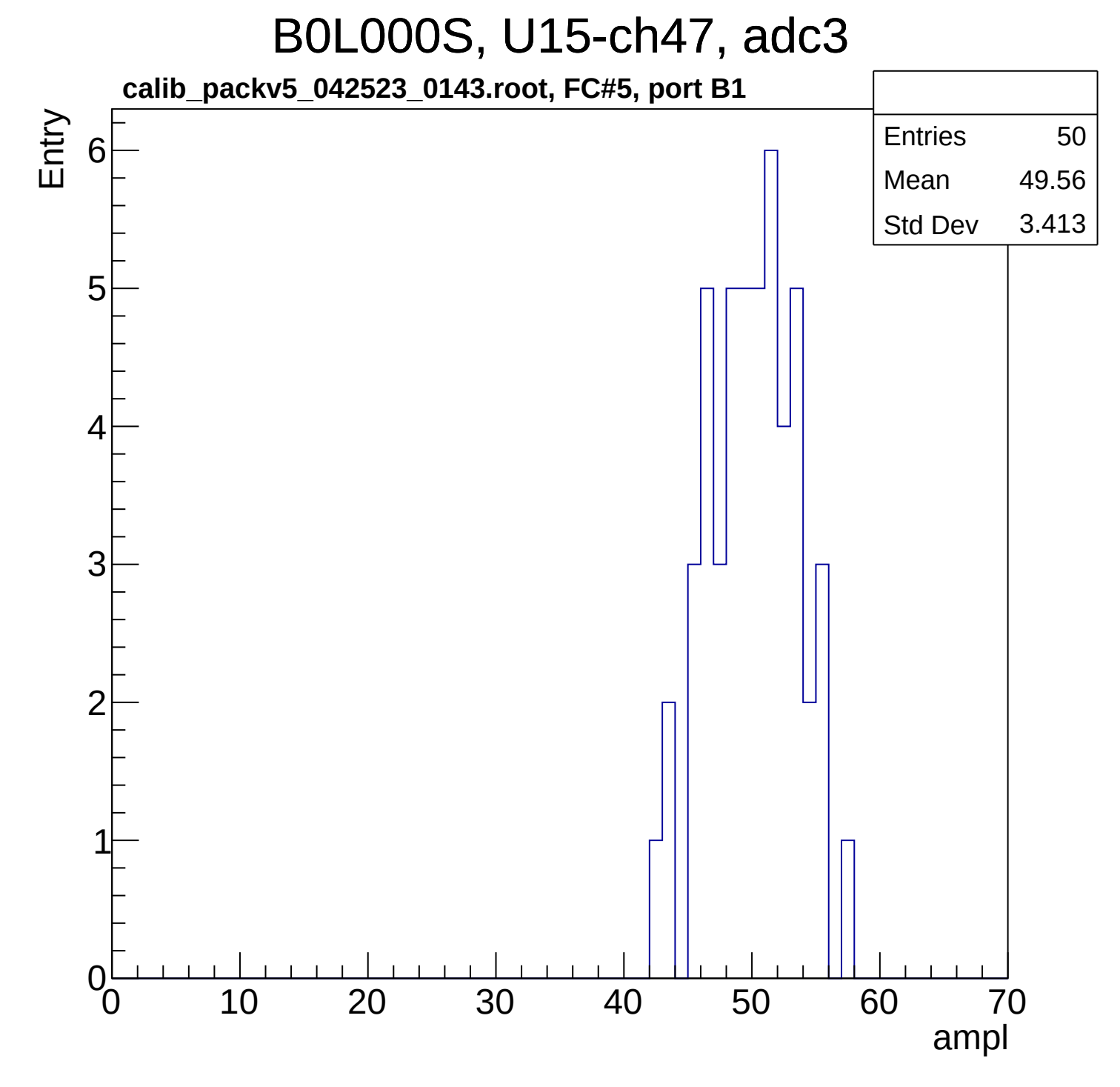
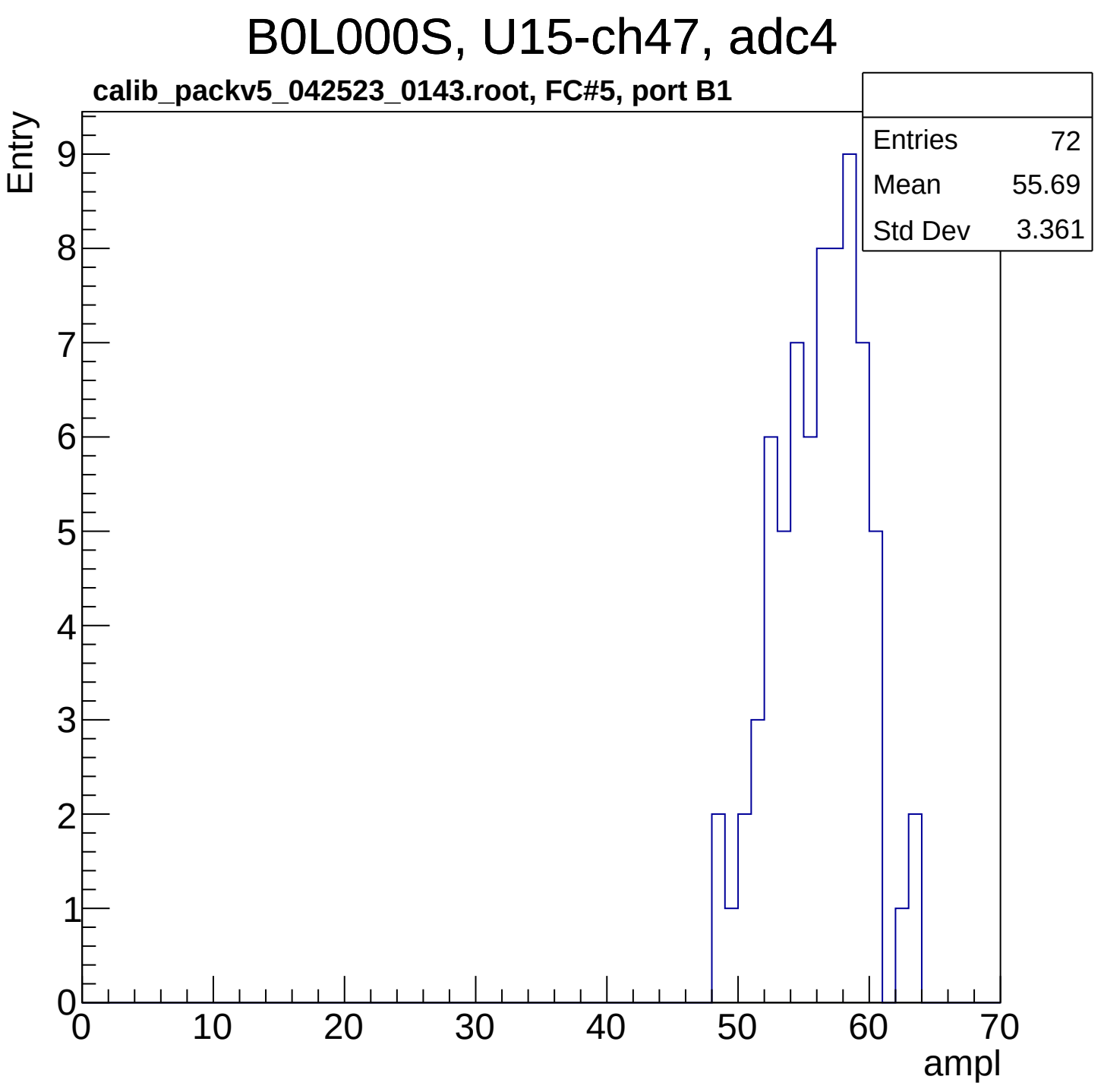
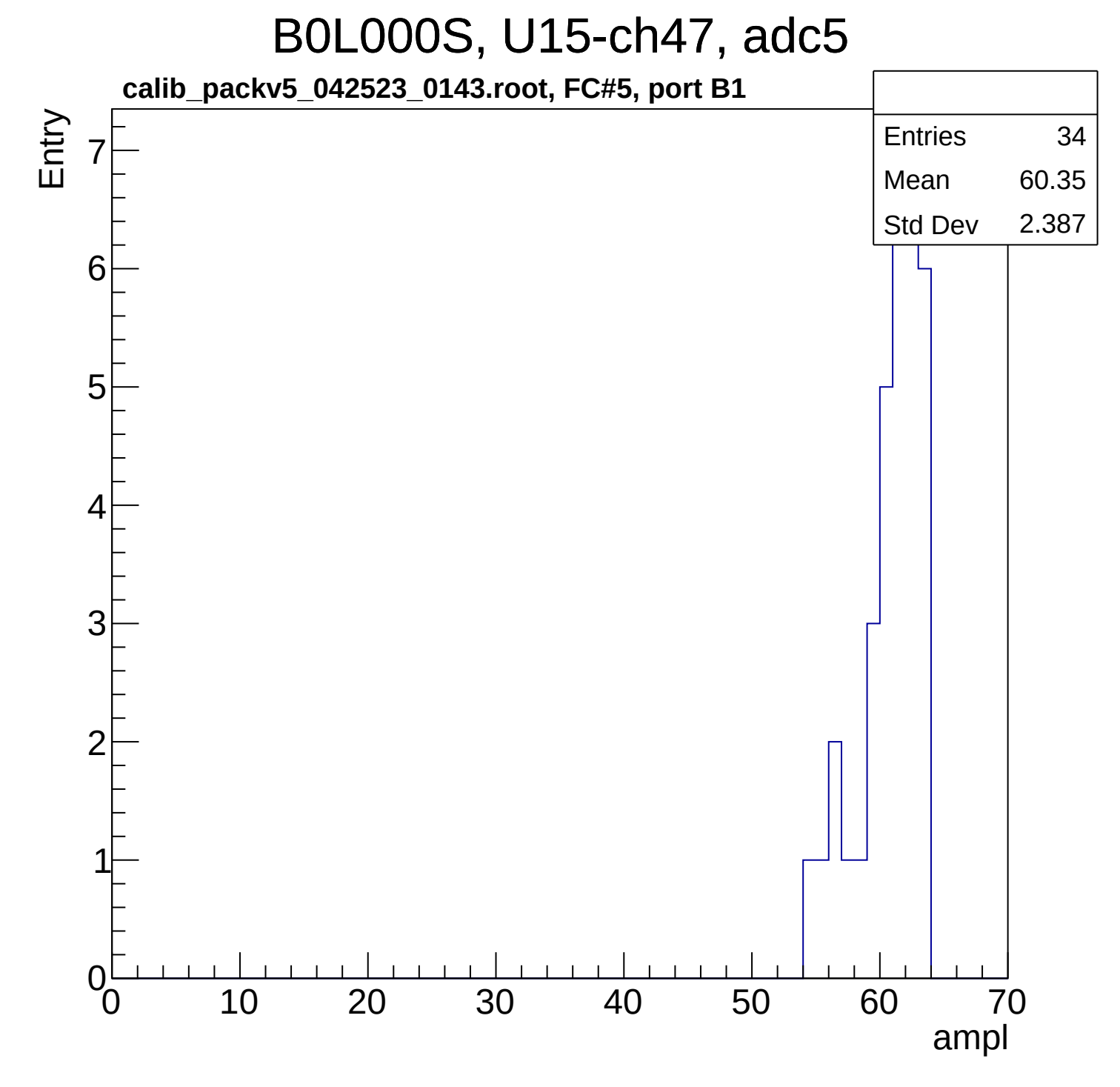

B0L000S, U15-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.47
Std Dev 3.142
Gaus mean : 44.9805
Gaus Width: 3.4175
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 49.56
Std Dev 3.413
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 55.69
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 60.35
Std Dev 2.387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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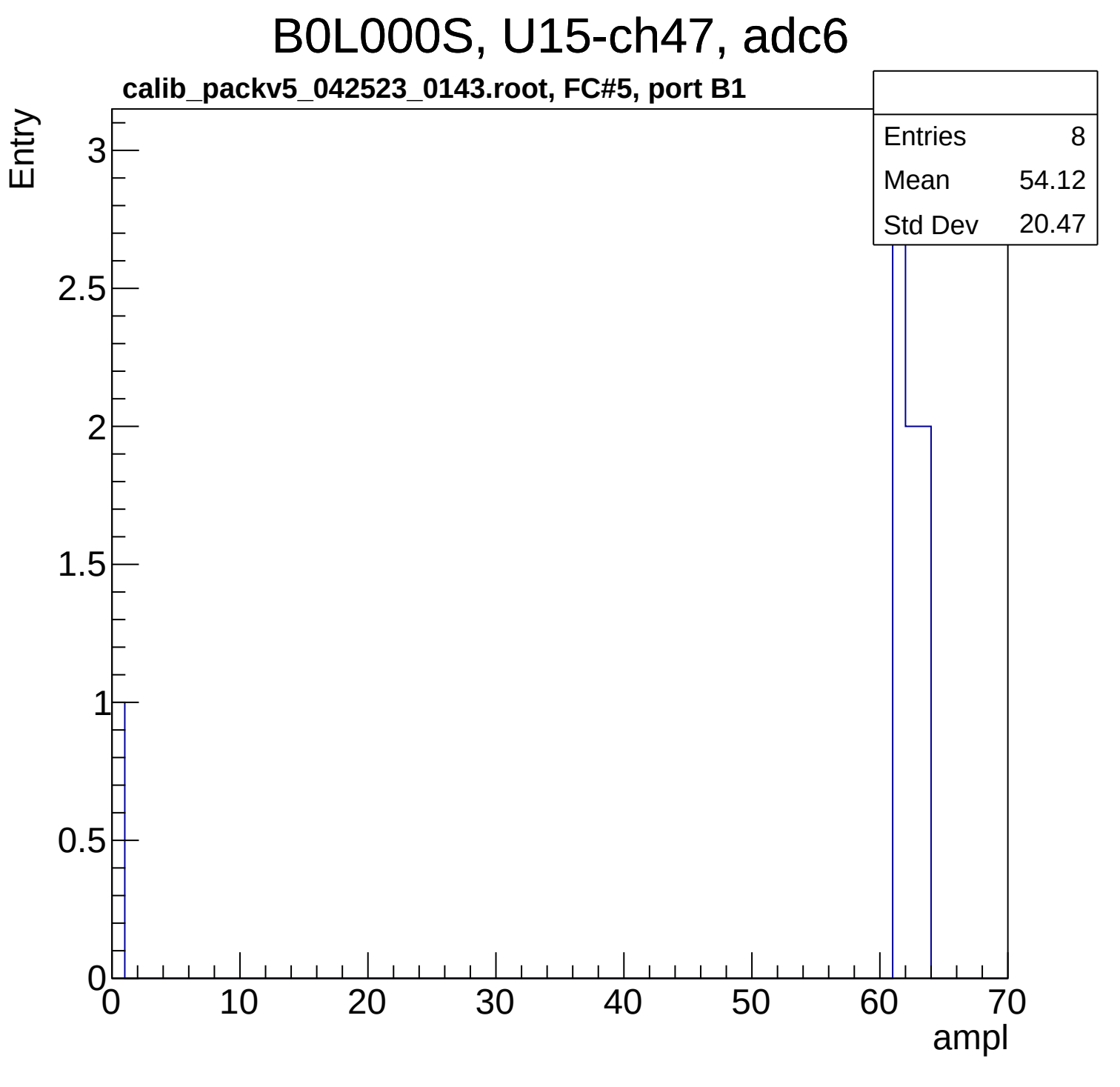
B0L000S, U15-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.12
Std Dev 20.47
Entry
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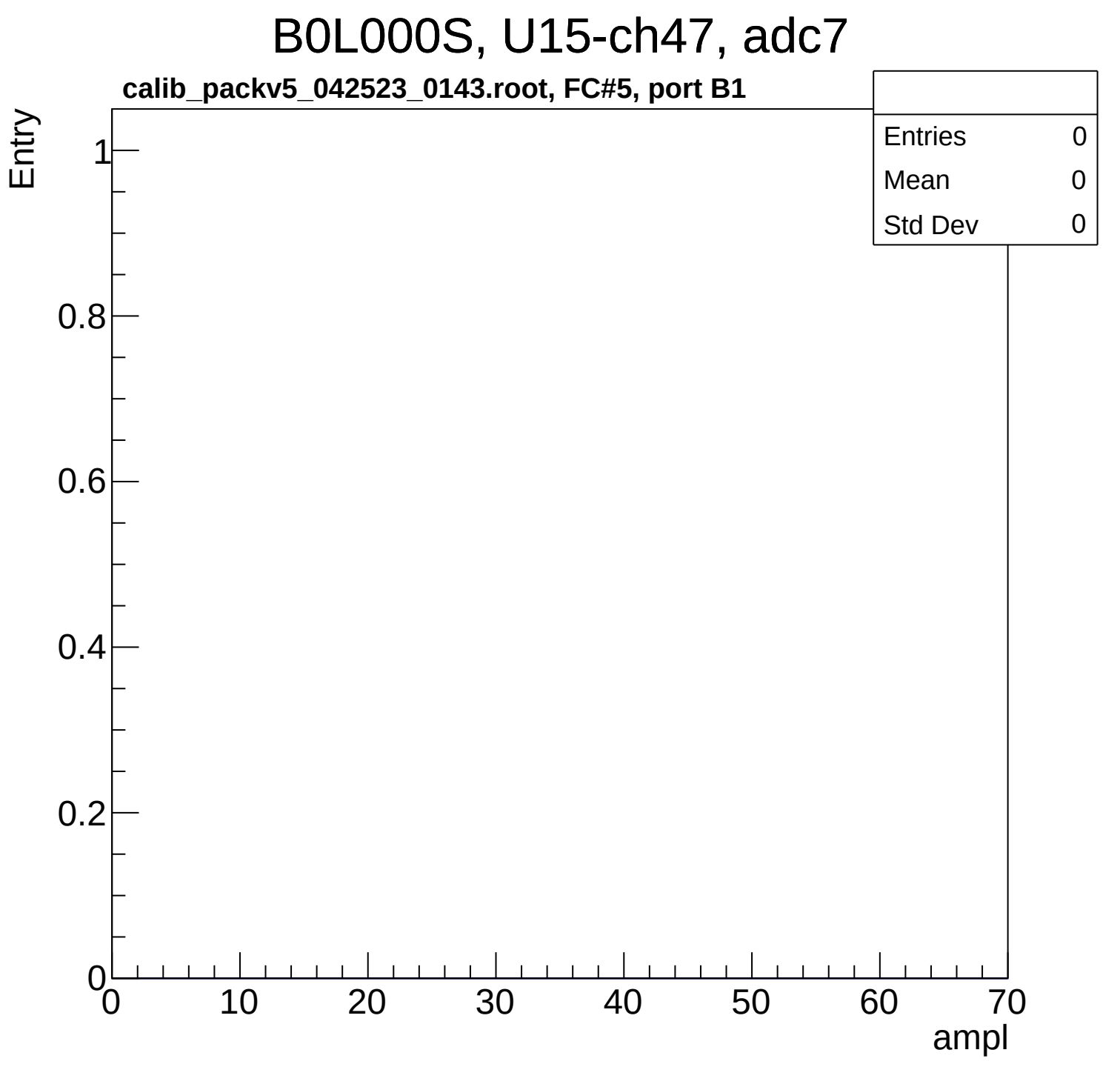
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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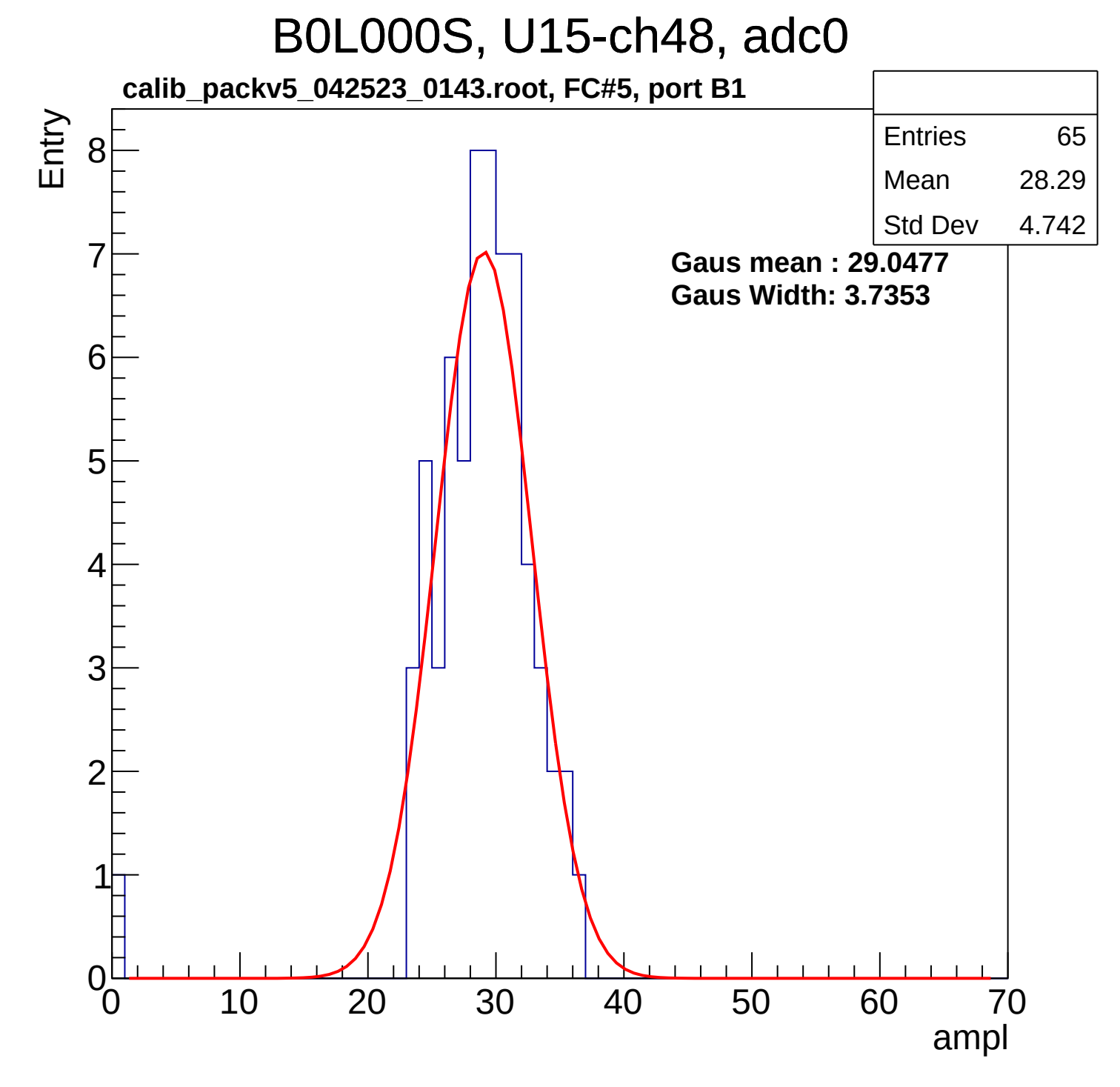
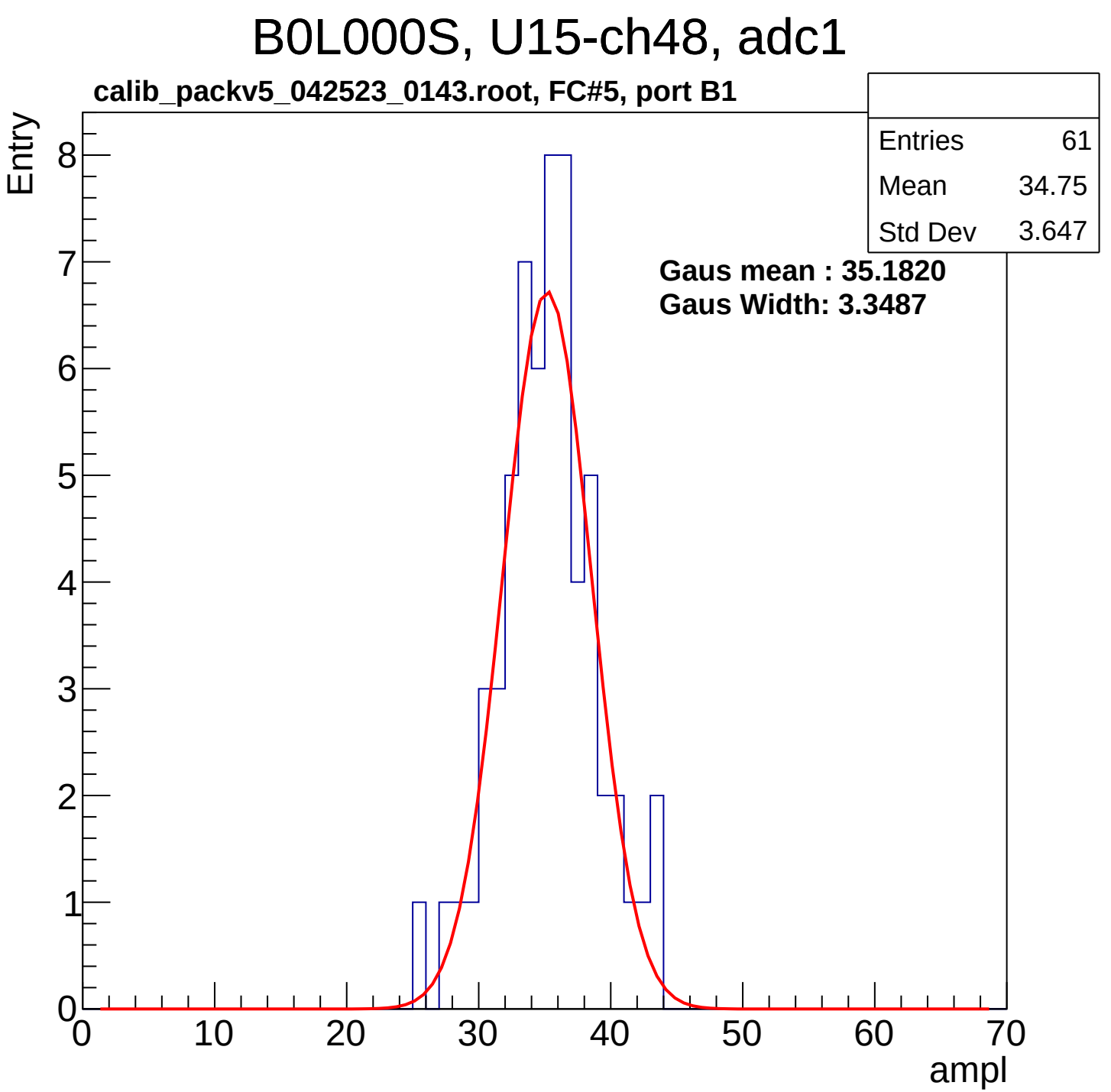
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.29
Std Dev 4.742
Gaus mean : 29.0477
Gaus Width: 3.7353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.75
Std Dev 3.647
Gaus mean : 35.1820
Gaus Width: 3.3487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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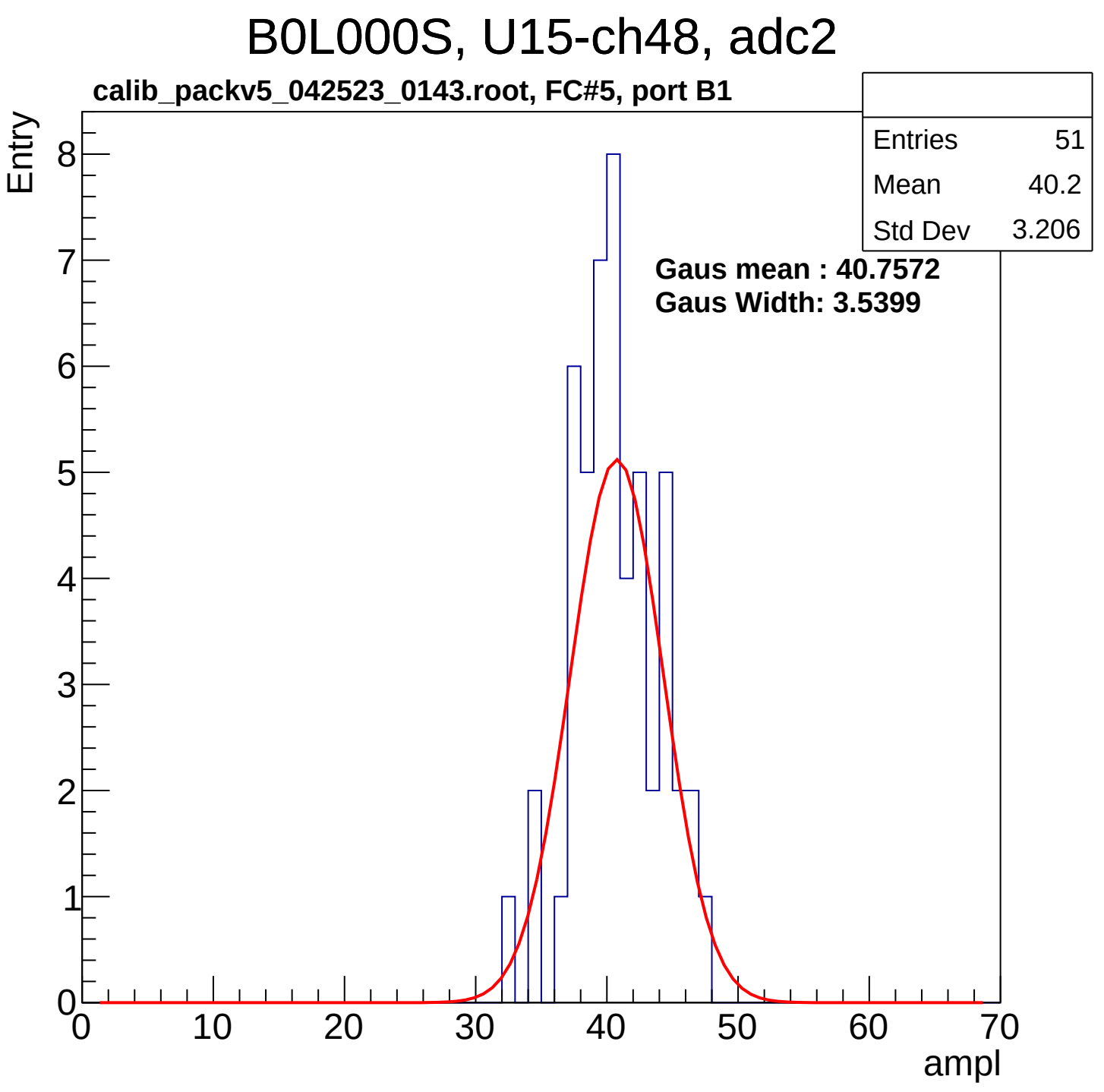
B0L000S, U15-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 40.2
Std Dev 3.206
Gaus mean : 40.7572
Gaus Width: 3.5399
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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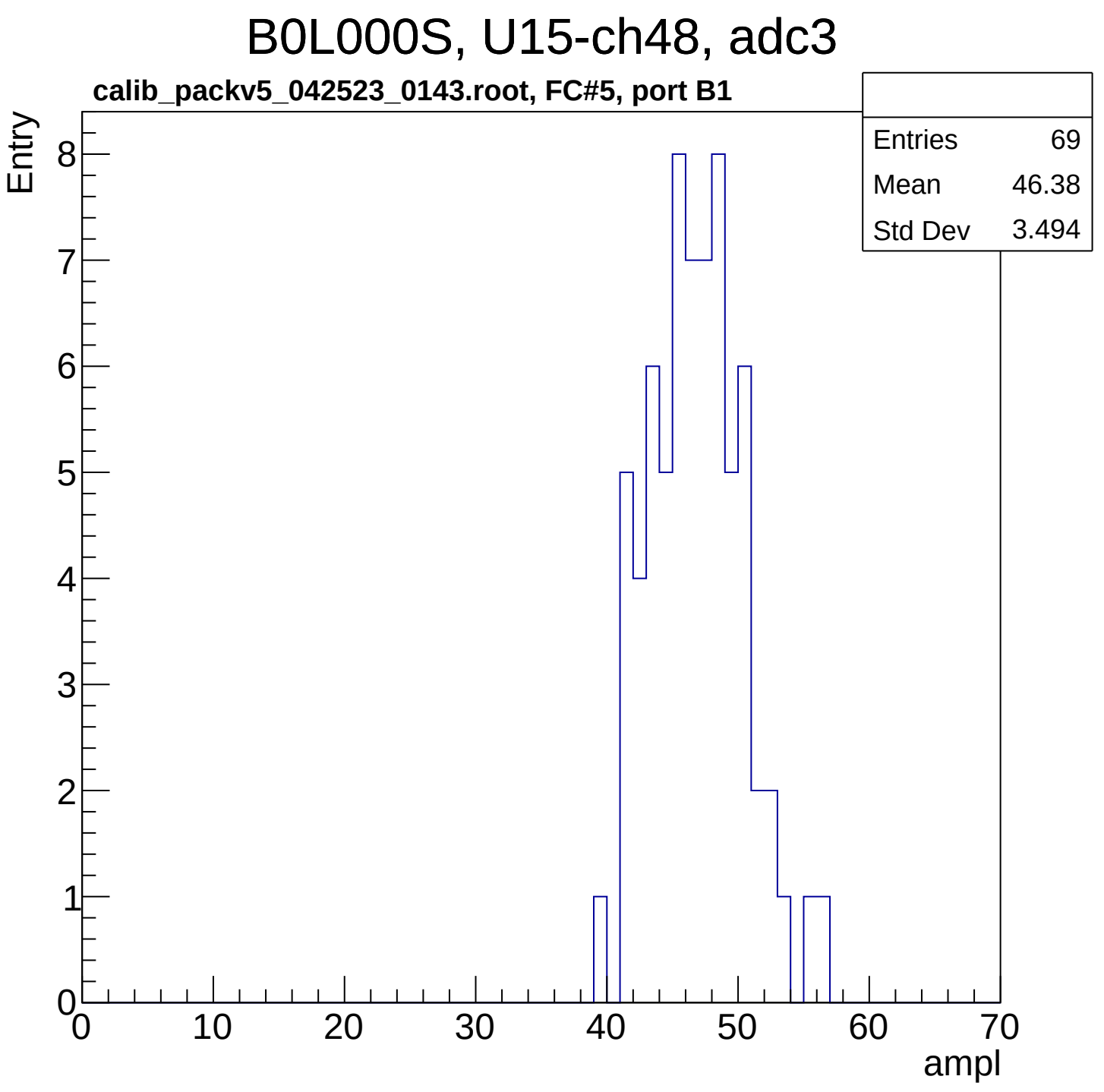
B0L000S, U15-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 46.38
Std Dev 3.494
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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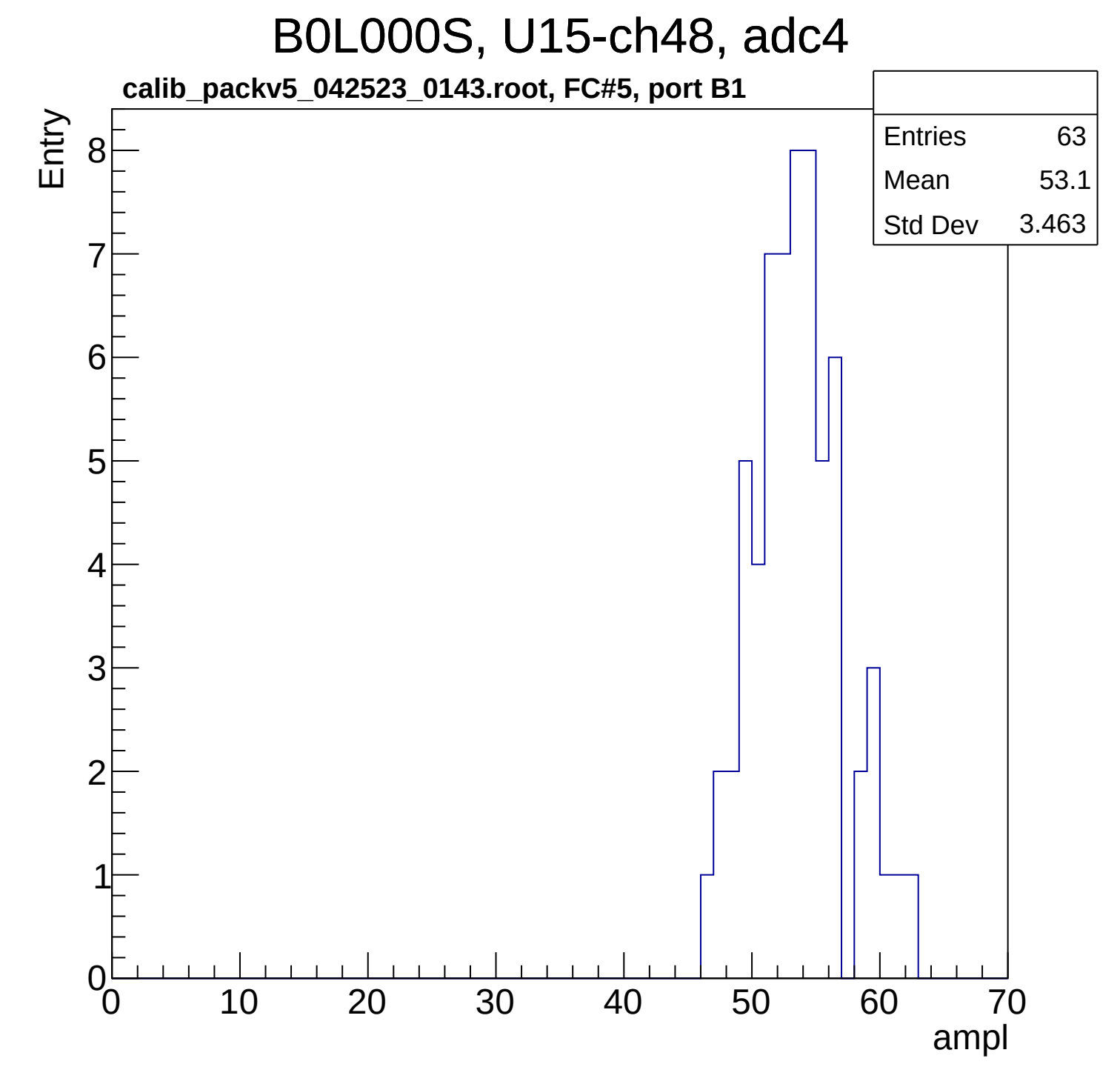
B0L000S, U15-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 53.1
Std Dev 3.463
Entry
ampl
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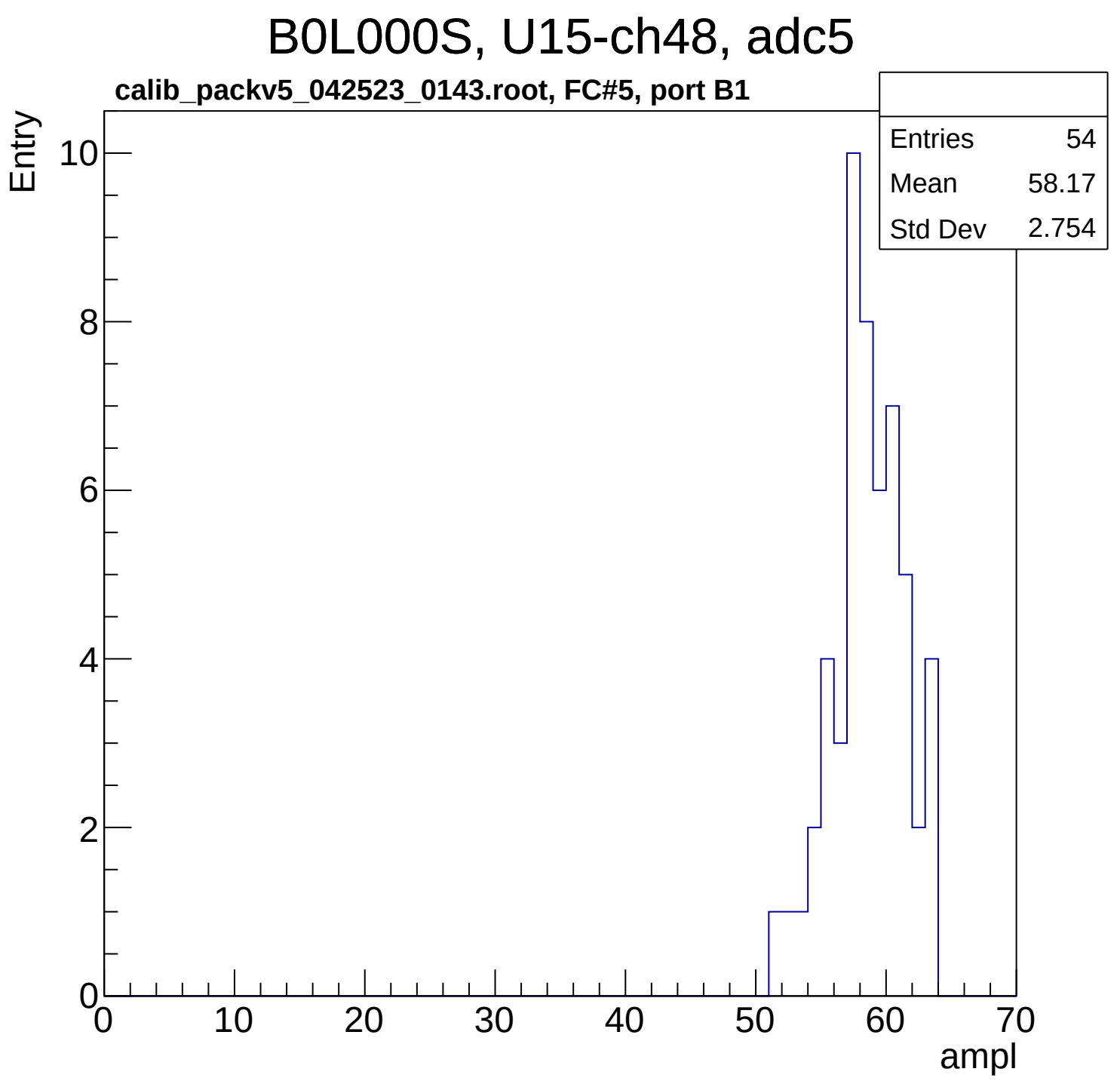
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.17
Std Dev 2.754
Entry
0
2
4
6
8
10
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10
20
30
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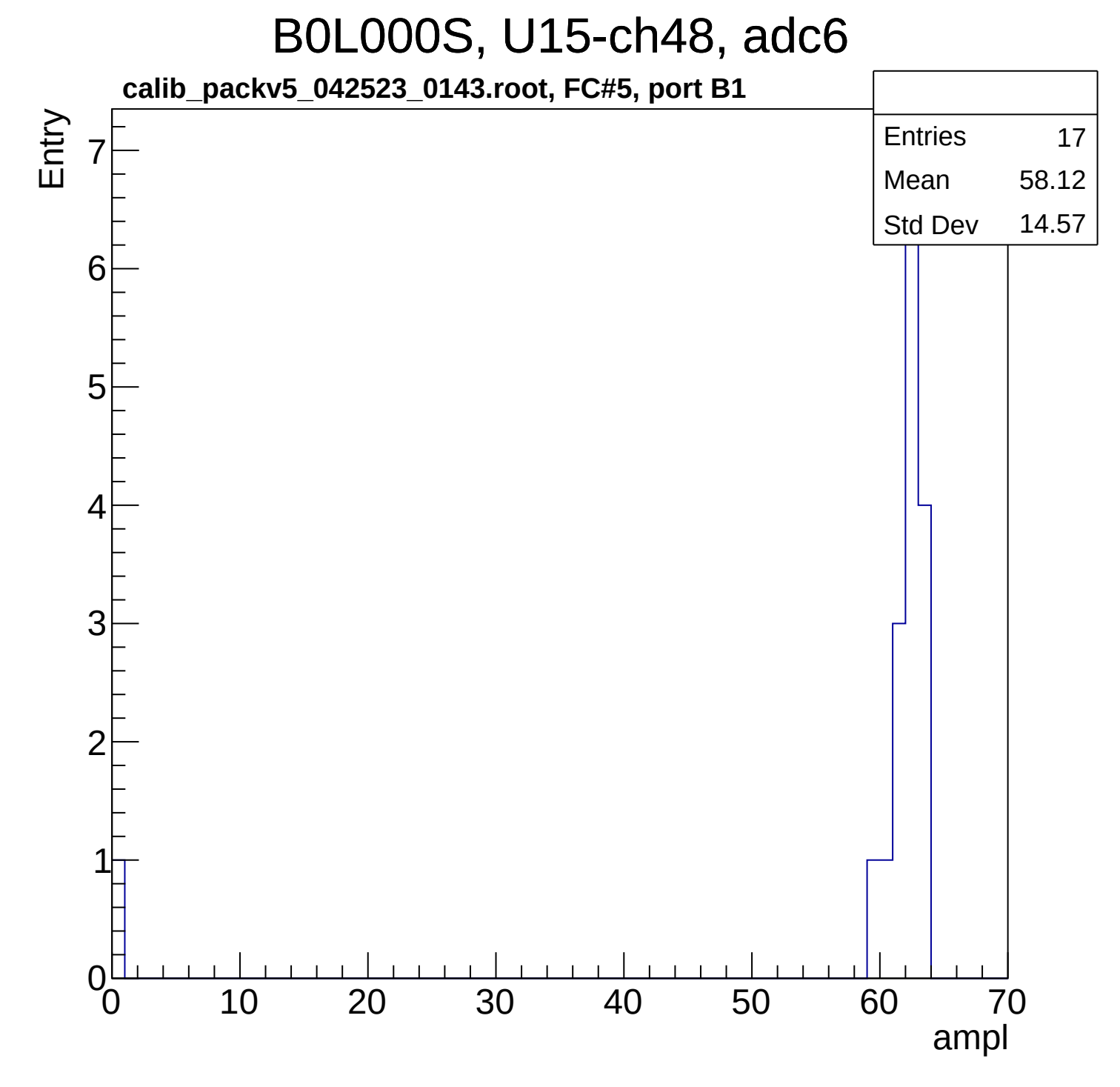
50
60
70
ampl


B0L000S, U15-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 58.12
Std Dev 14.57
Entry
ampl
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1
2
3
4
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0
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50
60
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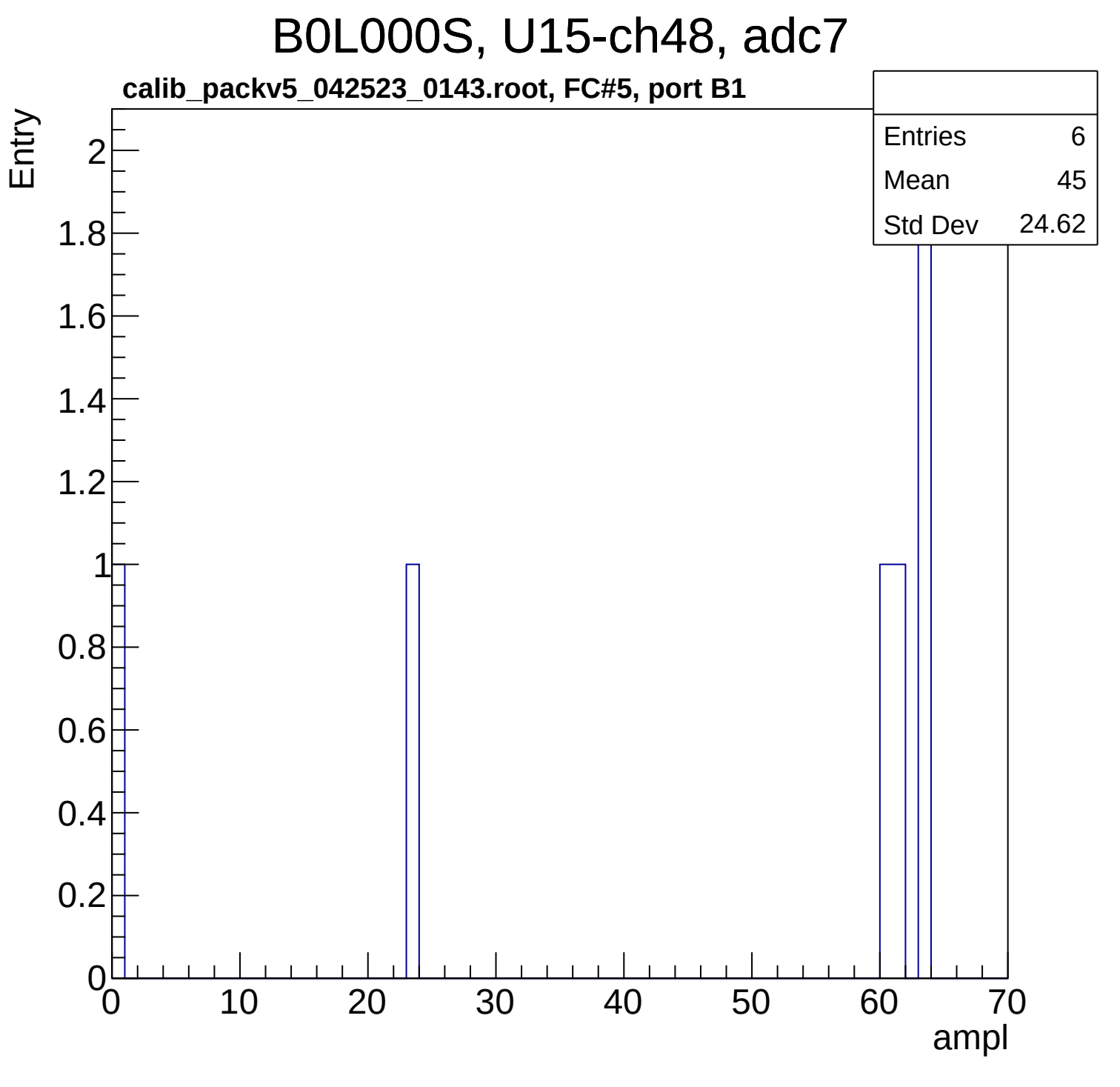
B0L000S, U15-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 45
Std Dev 24.62
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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60
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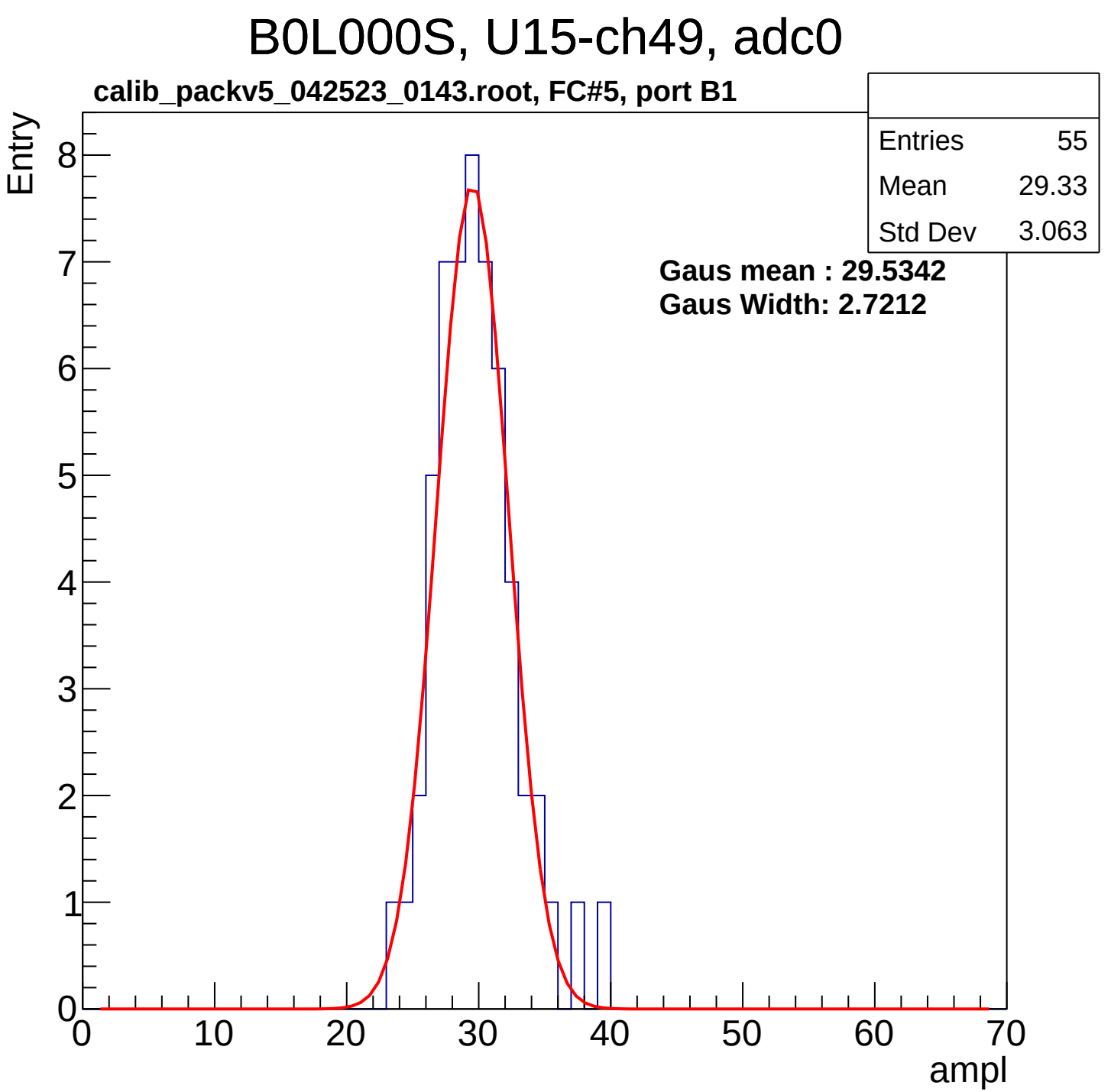
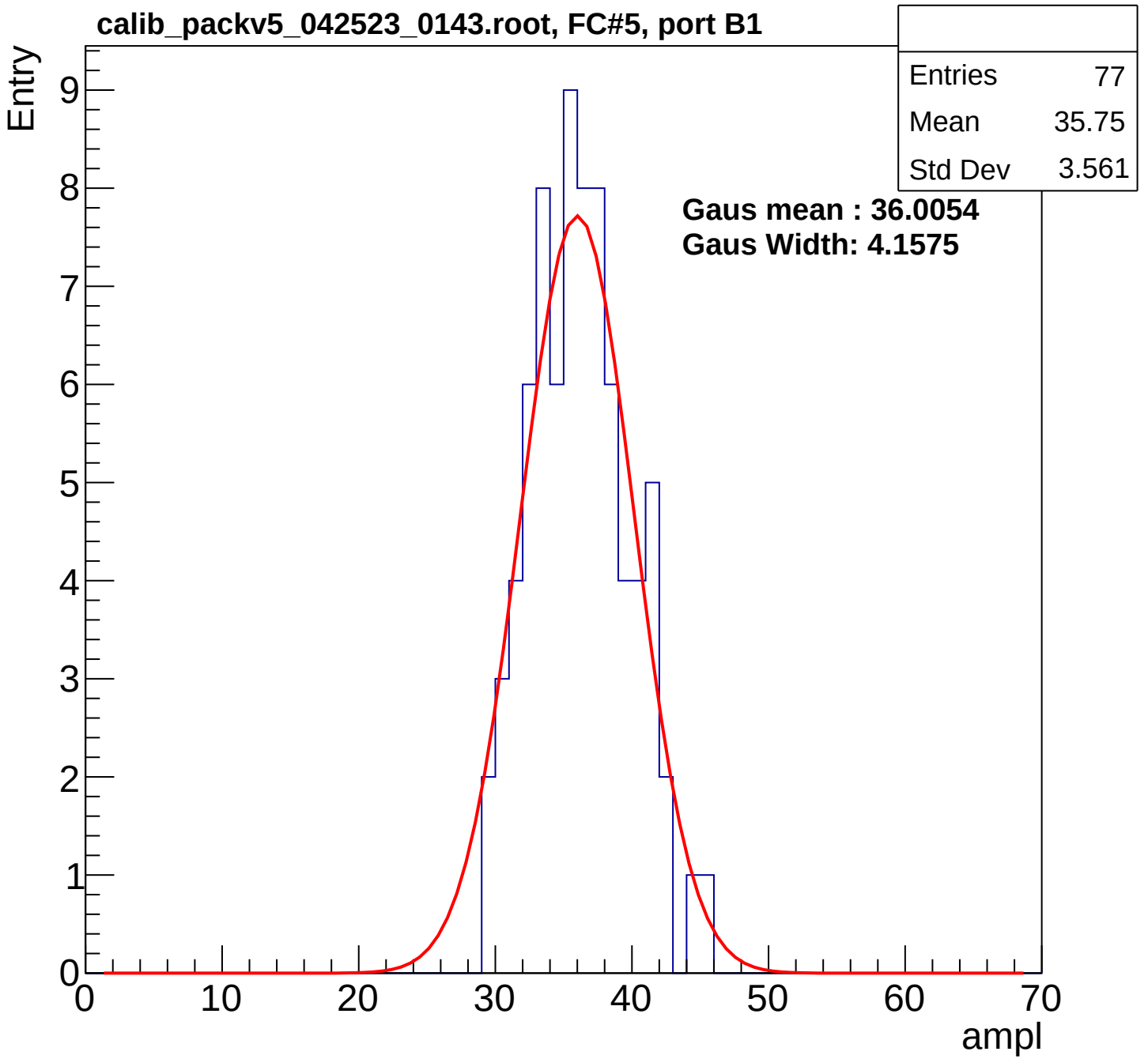

B0L000S, U15-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.33
Std Dev 3.063
Gaus mean : 29.5342
Gaus Width: 2.7212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 35.75
Std Dev 3.561
Gaus mean : 36.0054
Gaus Width: 4.1575
Entry
ampl
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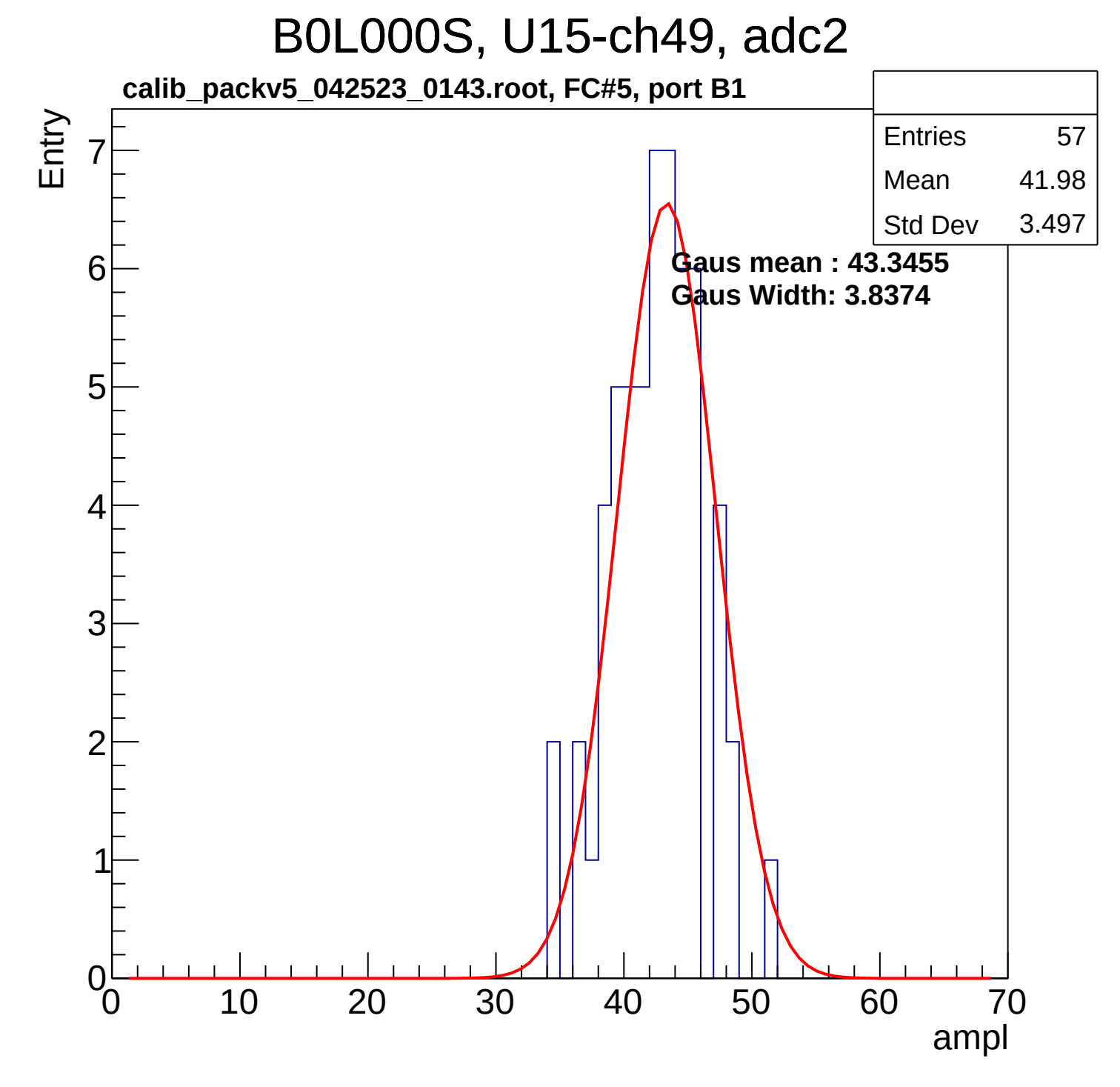
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 41.98
Std Dev 3.497
Gaus mean : 43.3455
Gaus Width: 3.8374
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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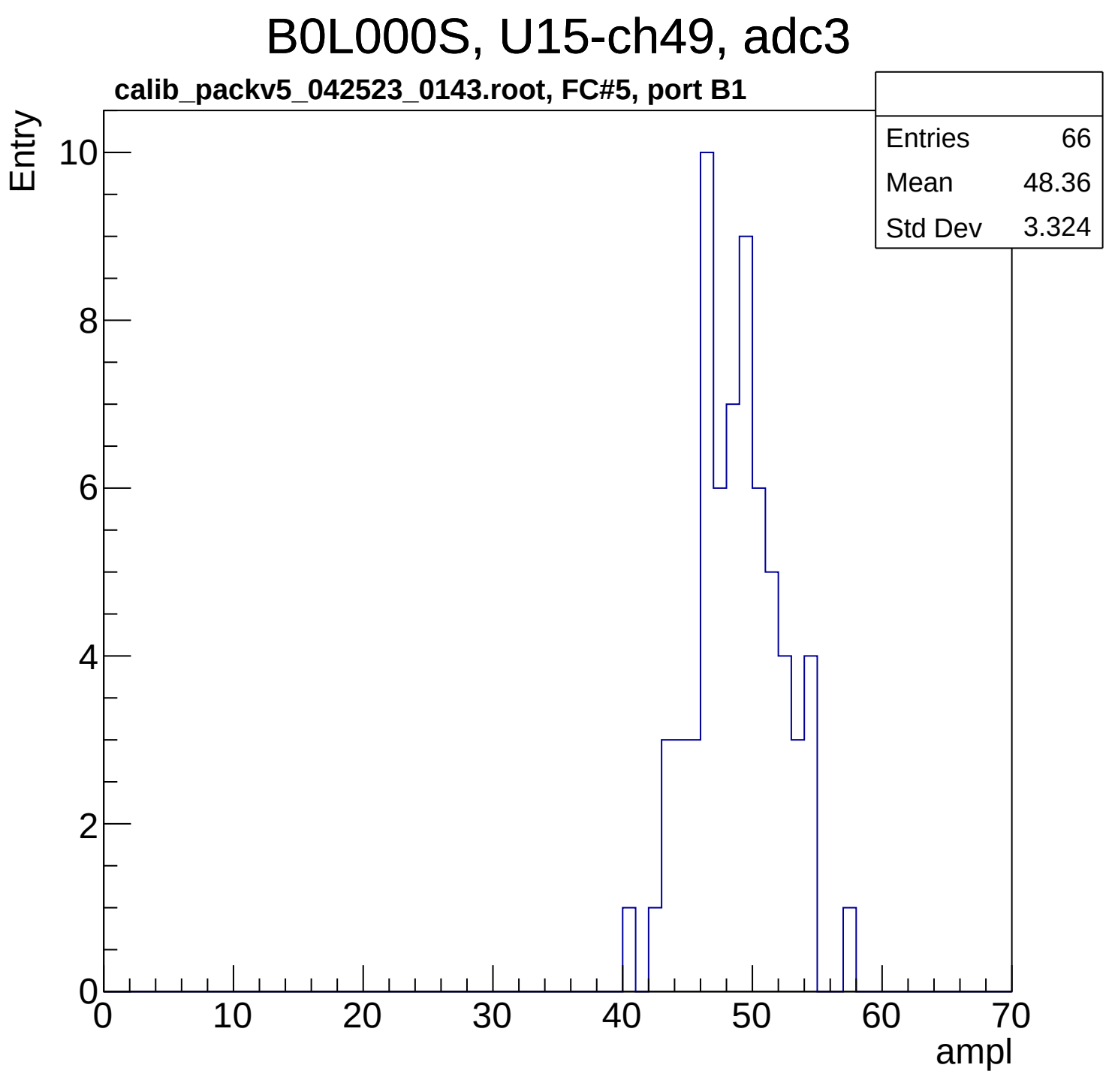
B0L000S, U15-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 48.36
Std Dev 3.324
Entry
ampl
0
2
4
6
8
10
0
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20
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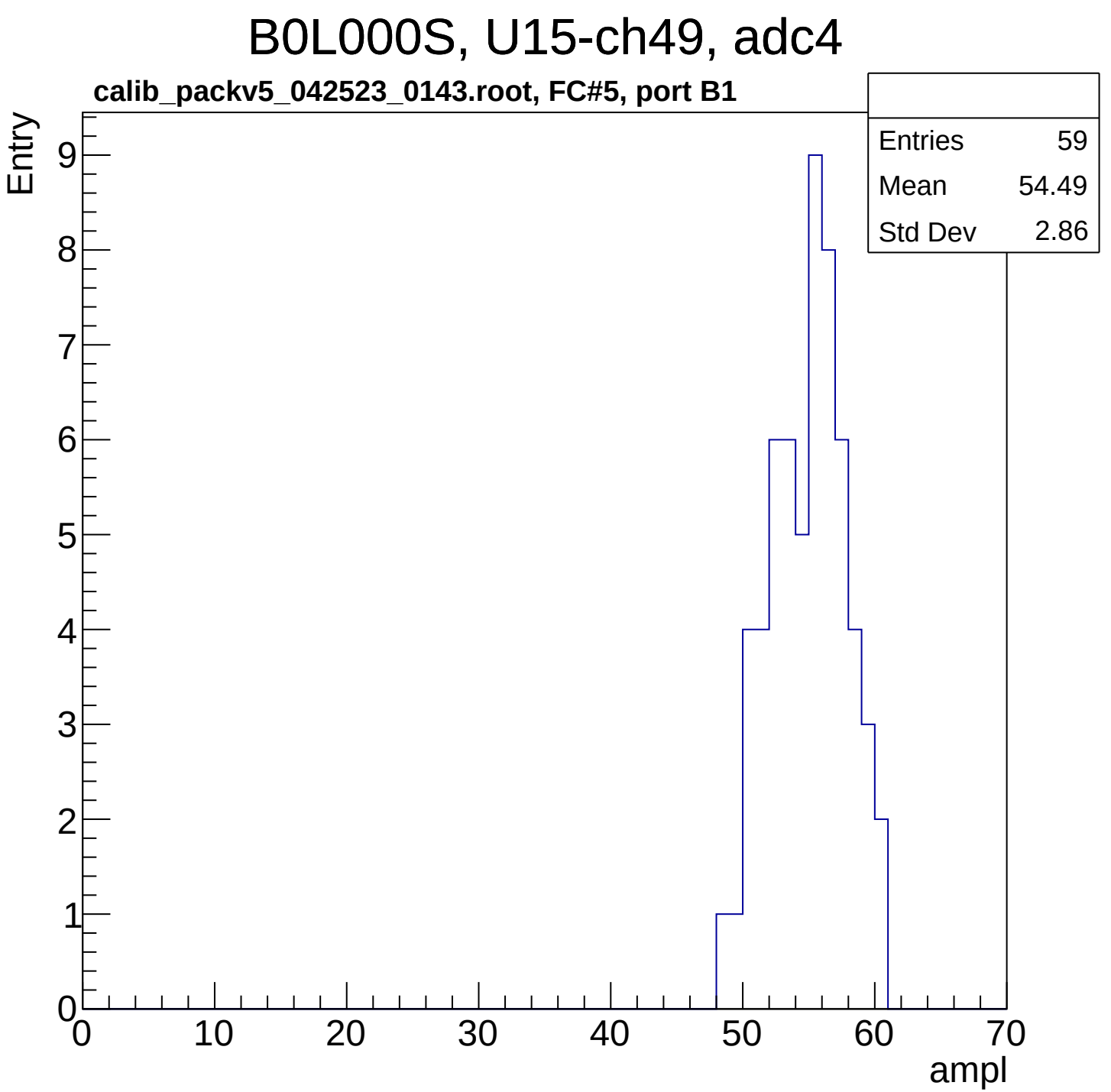
40
50
60
70


B0L000S, U15-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.49
Std Dev 2.86
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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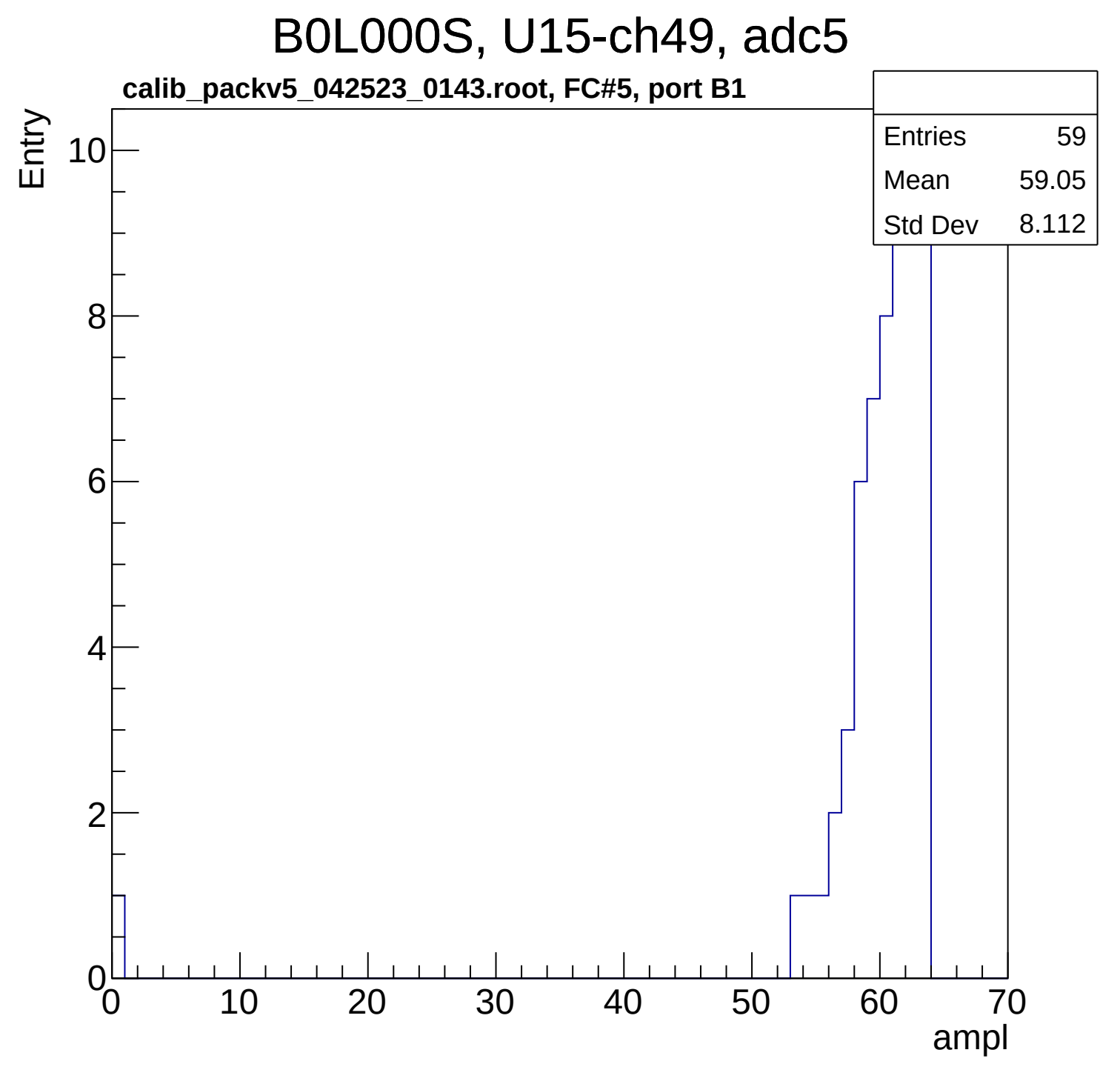
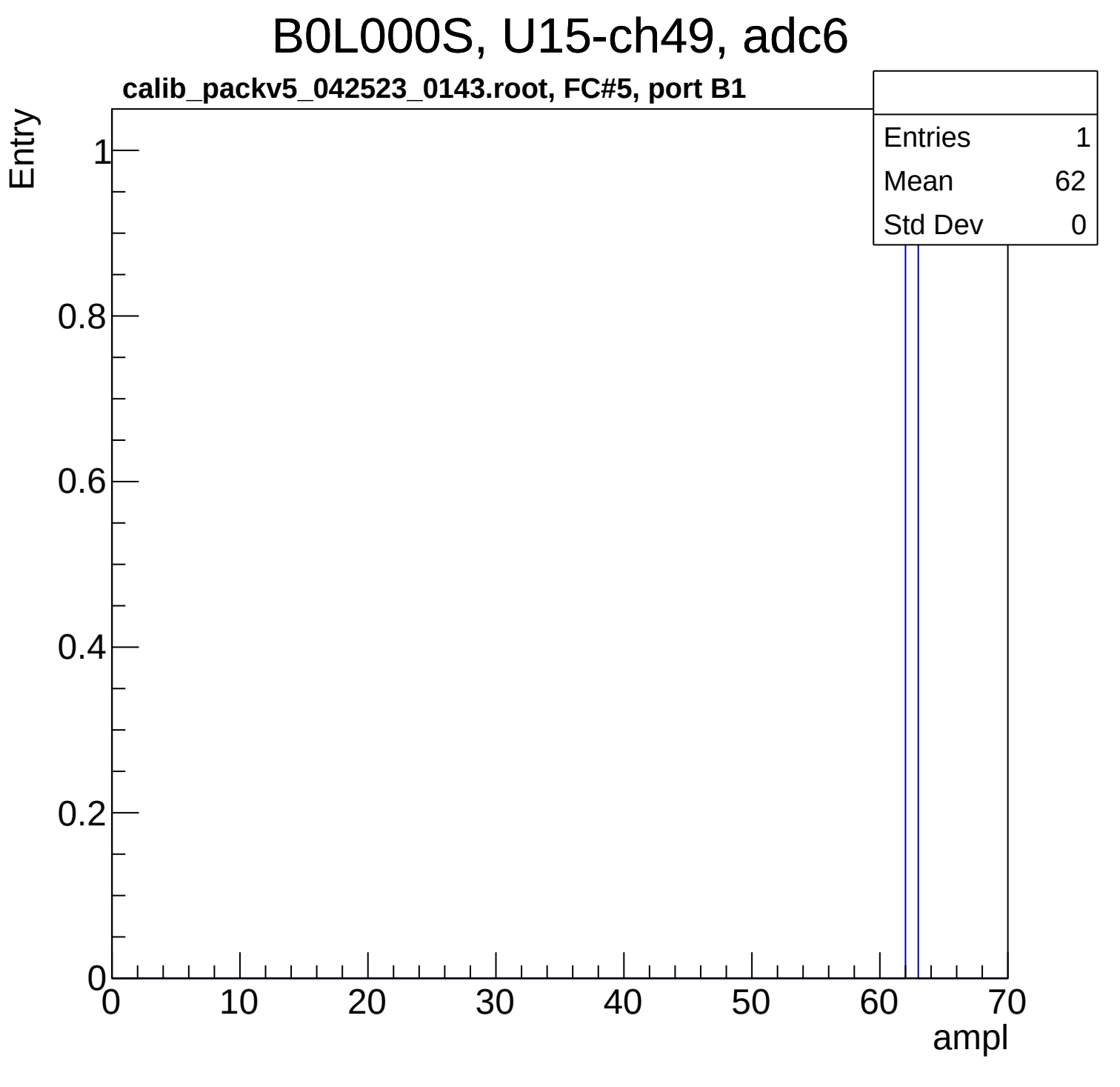
60
70


B0L000S, U15-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 59.05
Std Dev 8.112
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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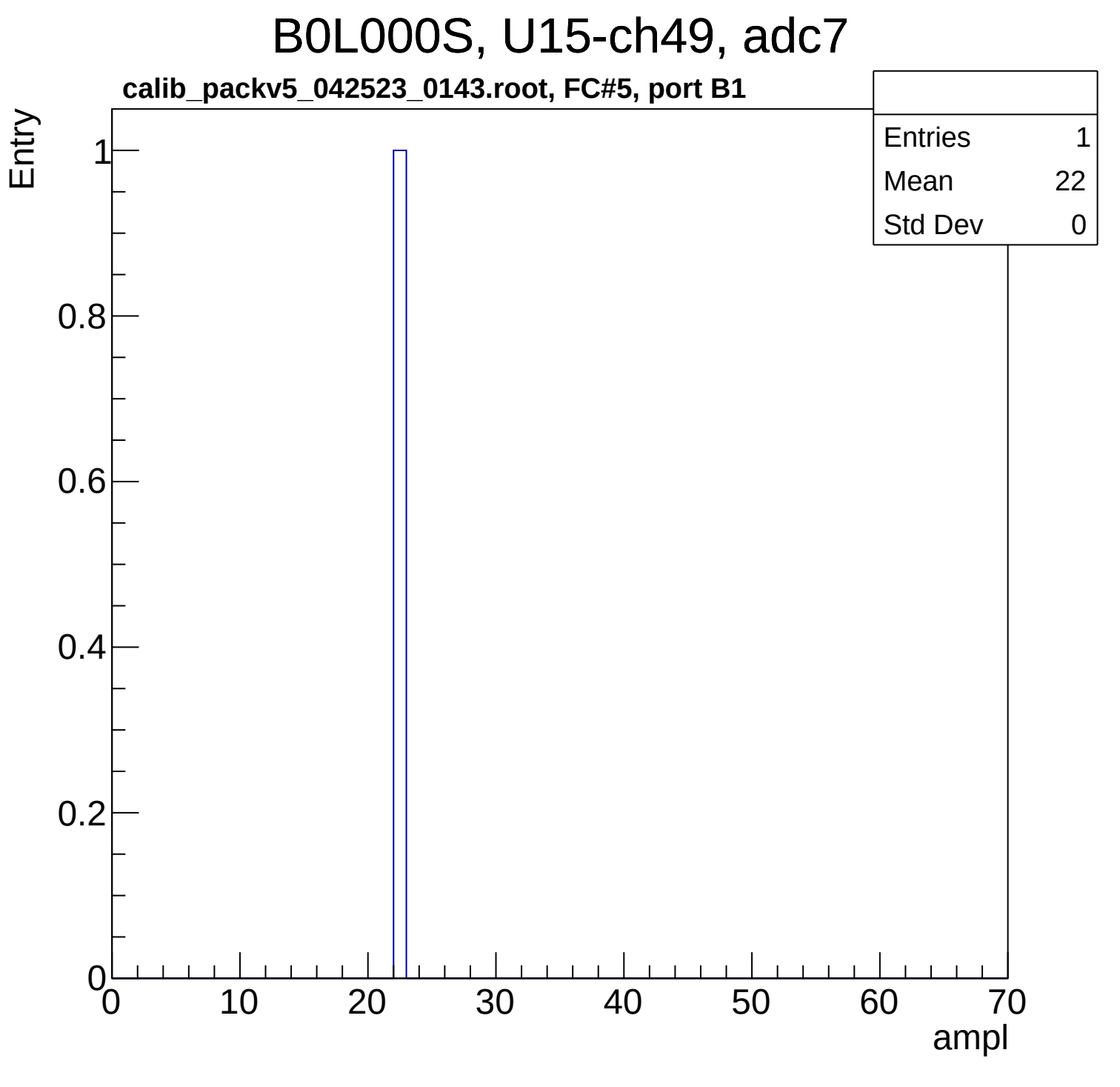
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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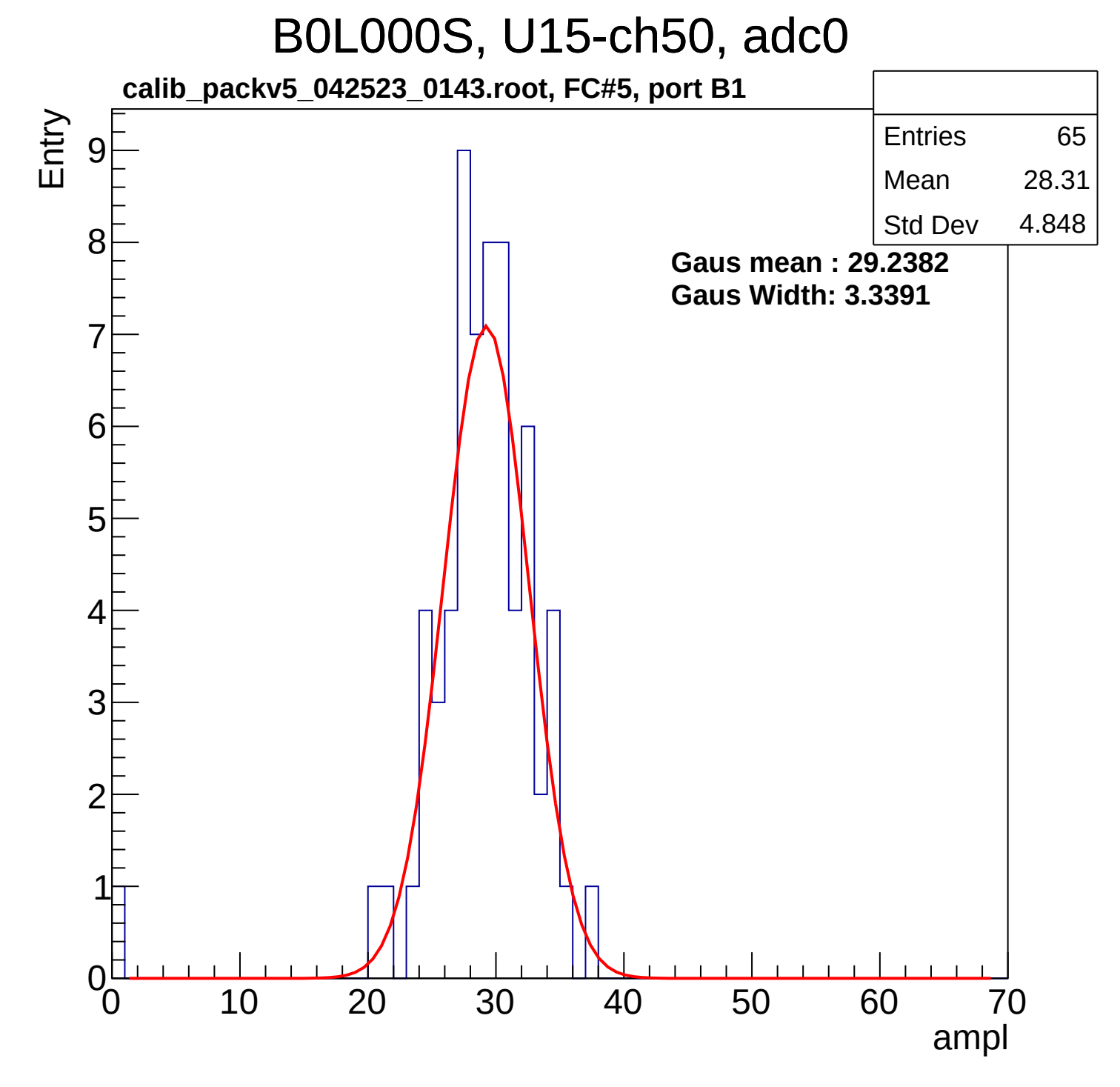
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.31
Std Dev 4.848
Gaus mean : 29.2382
Gaus Width: 3.3391
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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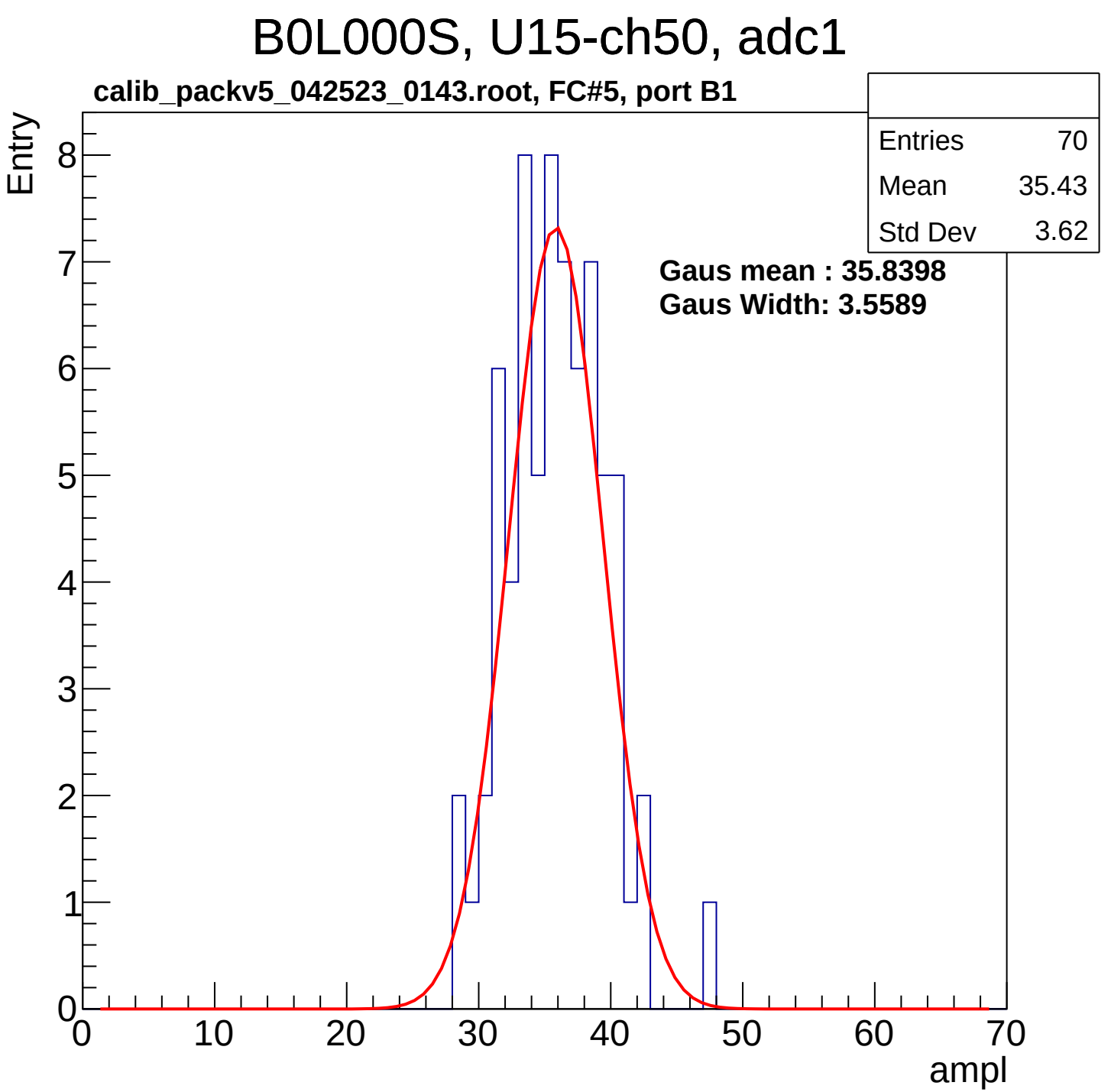
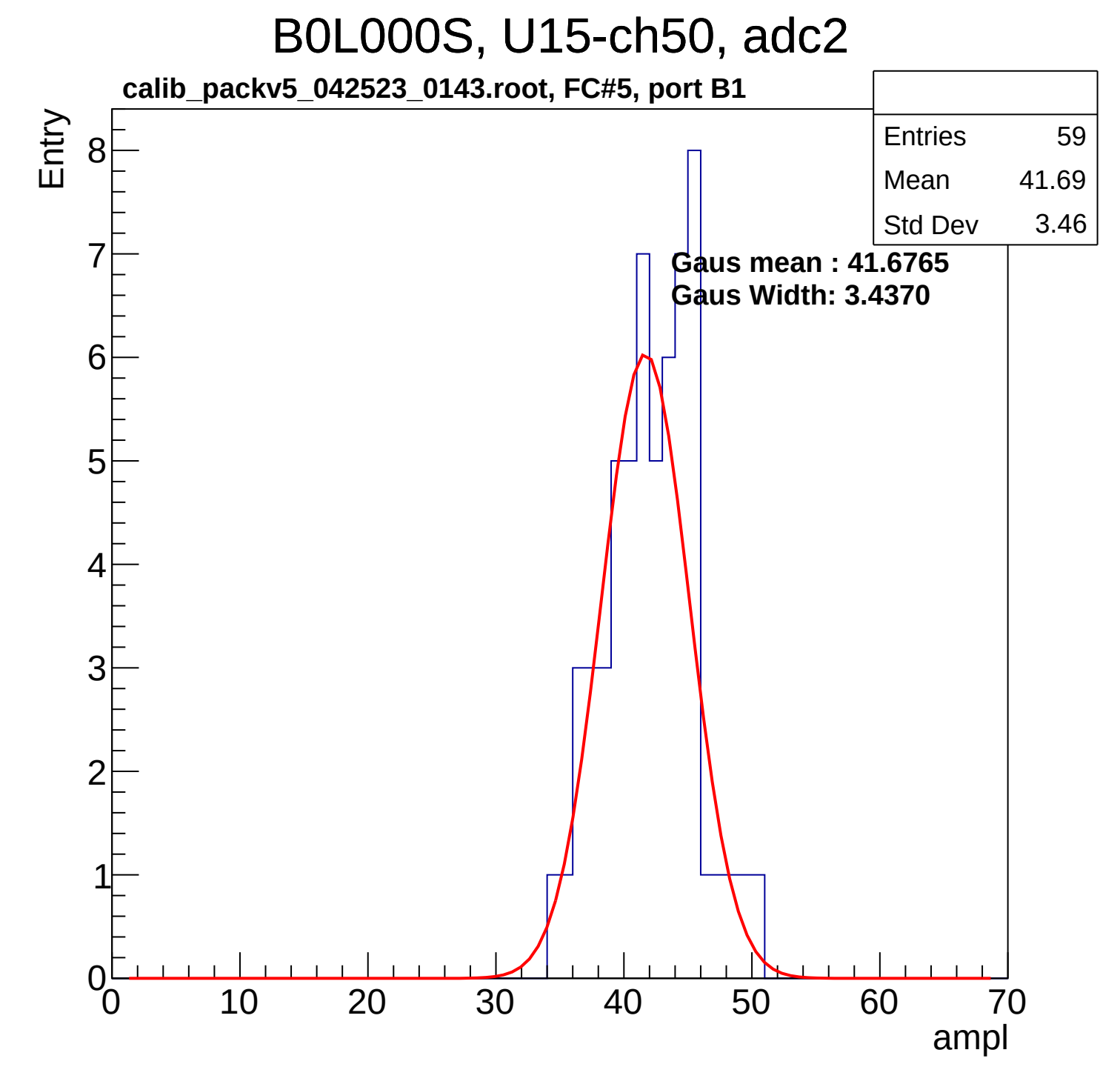

B0L000S, U15-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 35.43
Std Dev 3.62
Gaus mean : 35.8398
Gaus Width: 3.5589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 41.69
Std Dev 3.46
Gaus mean : 41.6765
Gaus Width: 3.4370
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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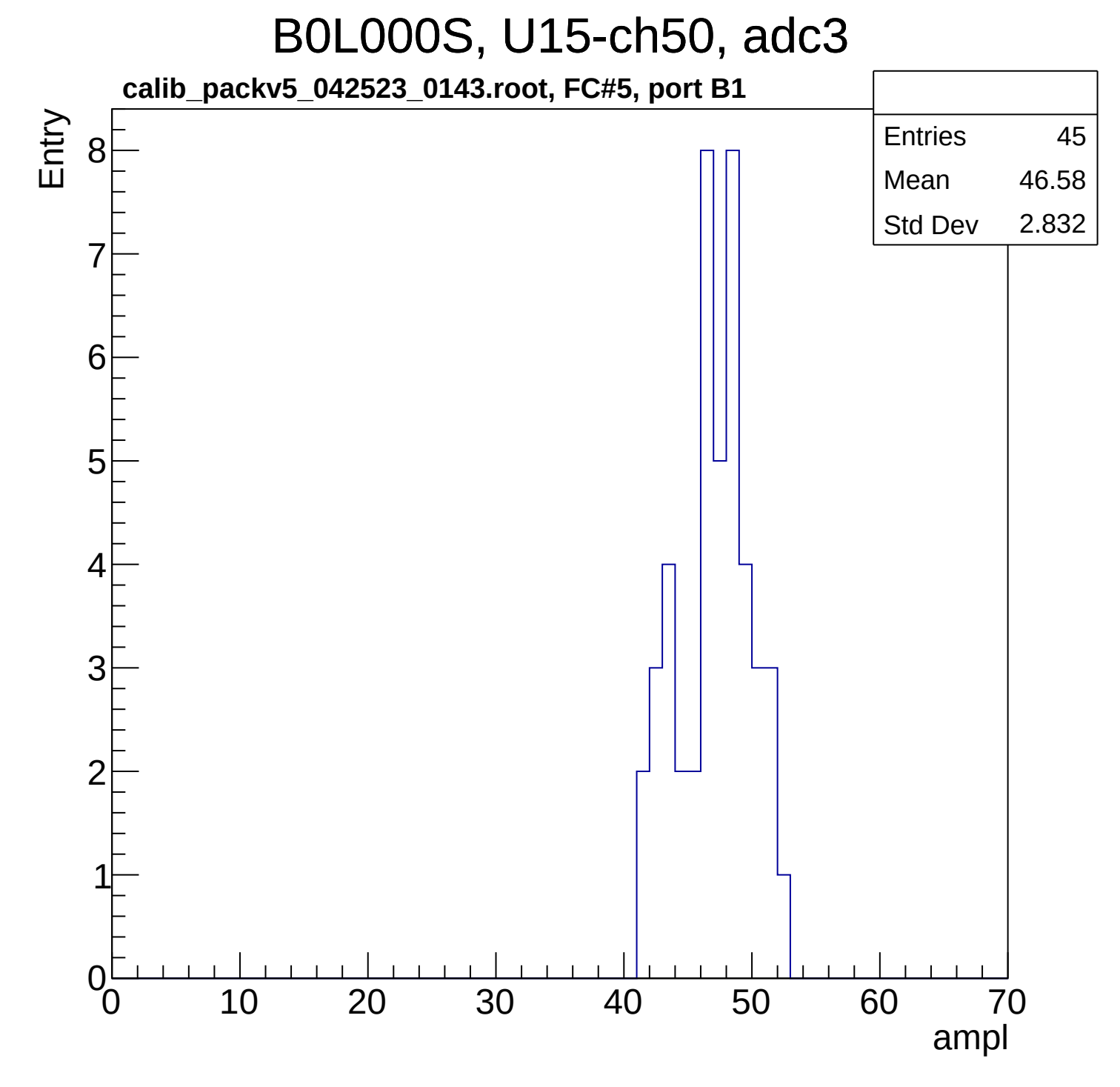
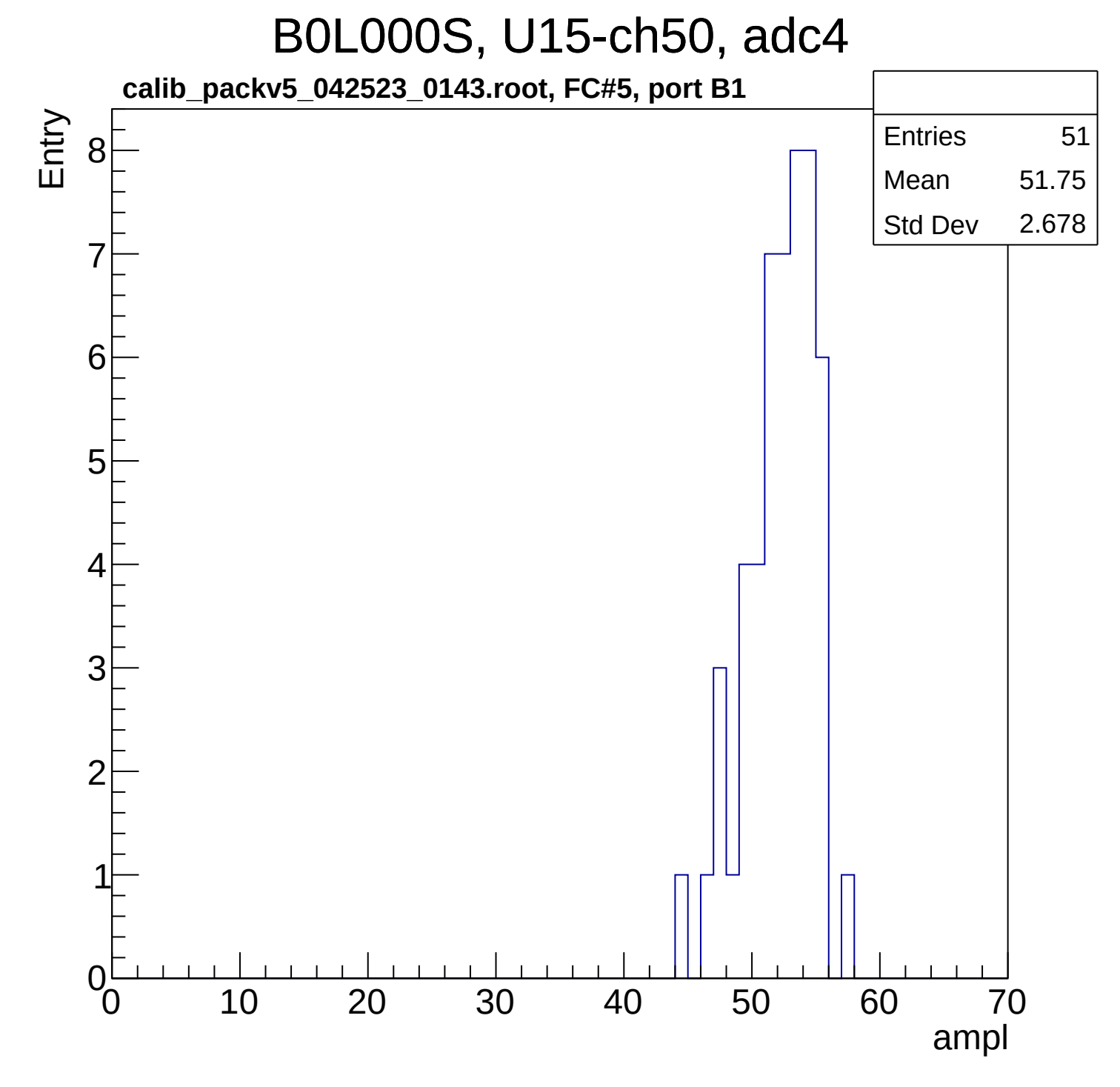

B0L000S, U15-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 46.58
Std Dev 2.832
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 51.75
Std Dev 2.678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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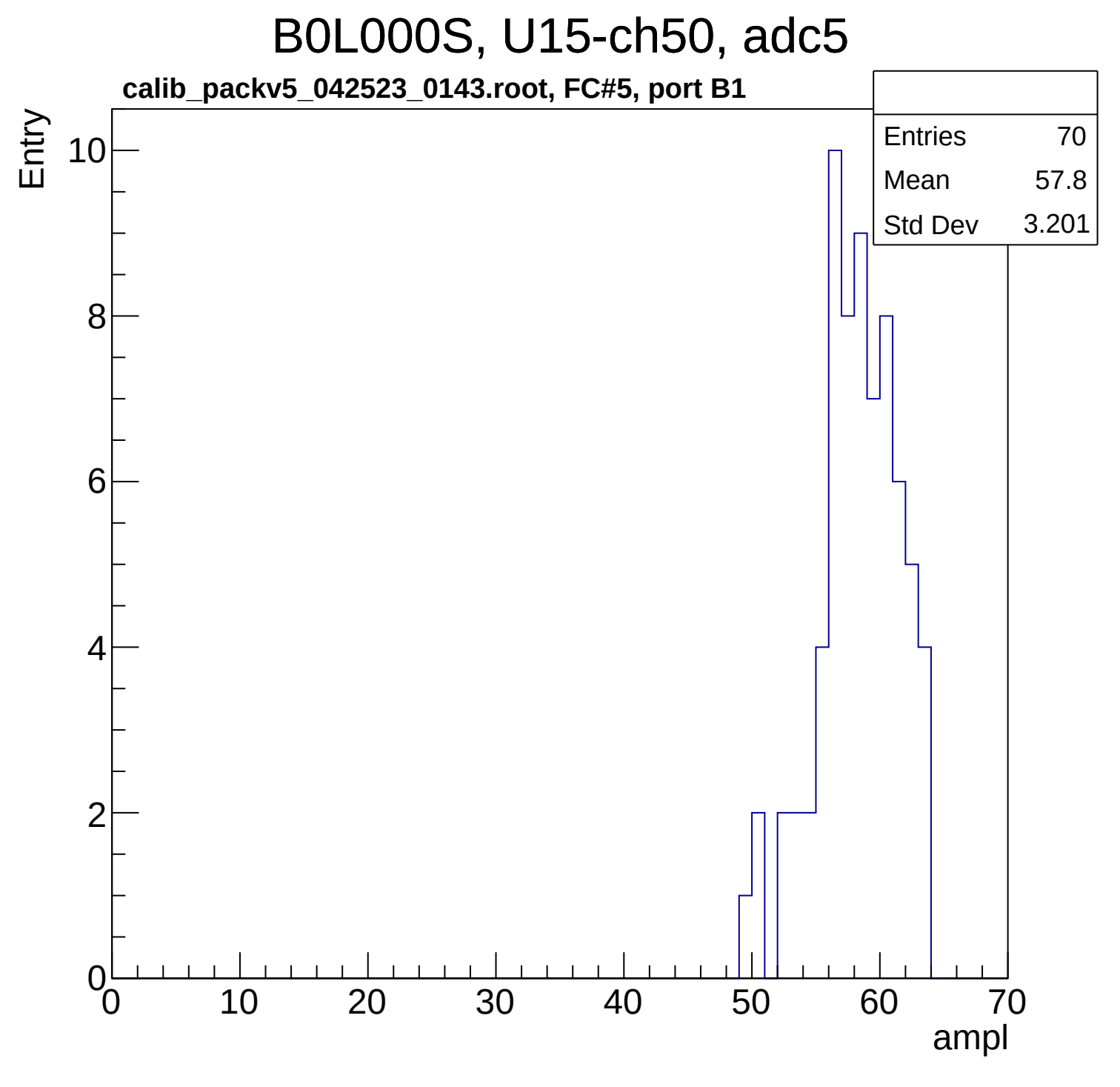
B0L000S, U15-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 57.8
Std Dev 3.201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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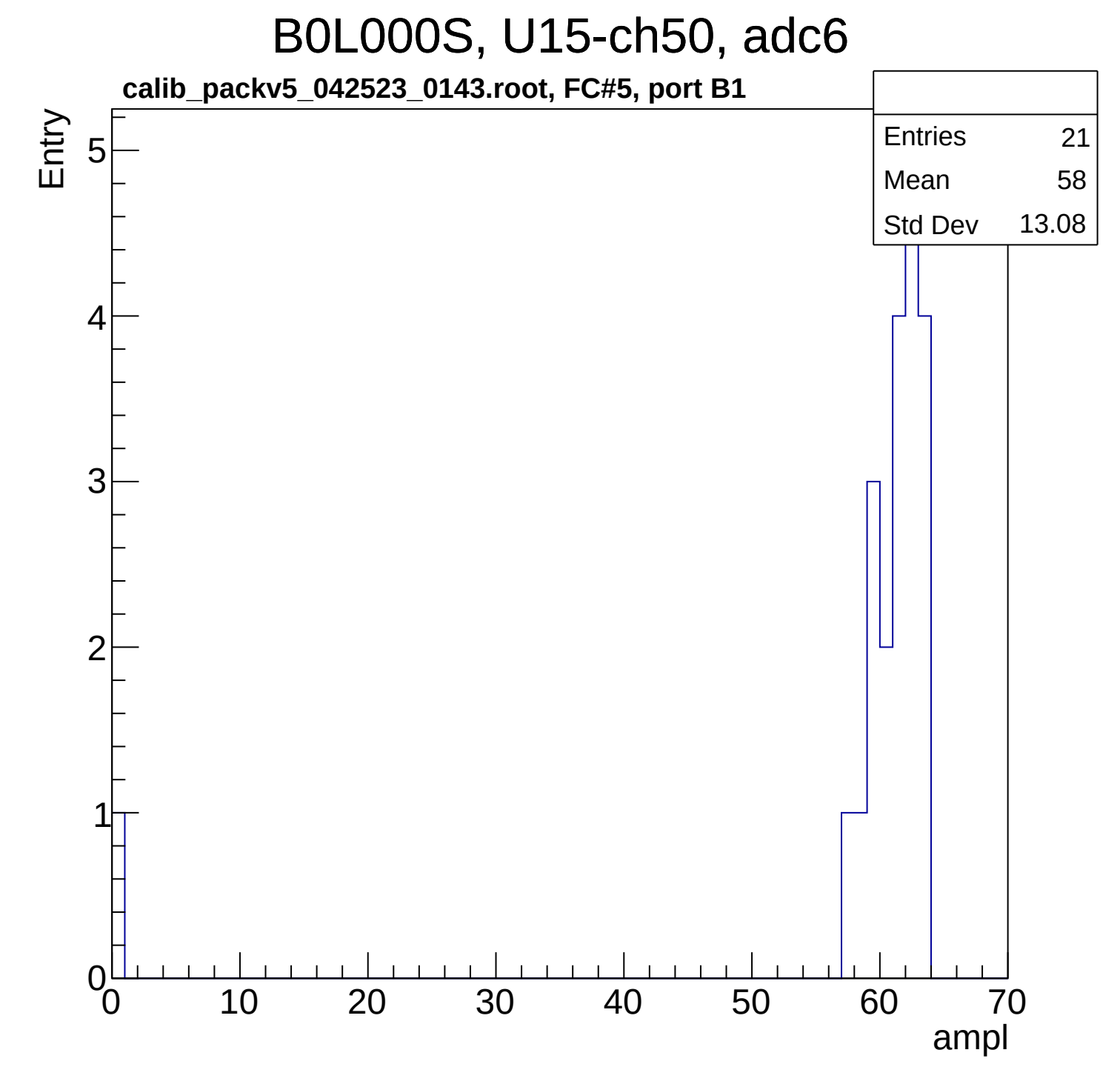
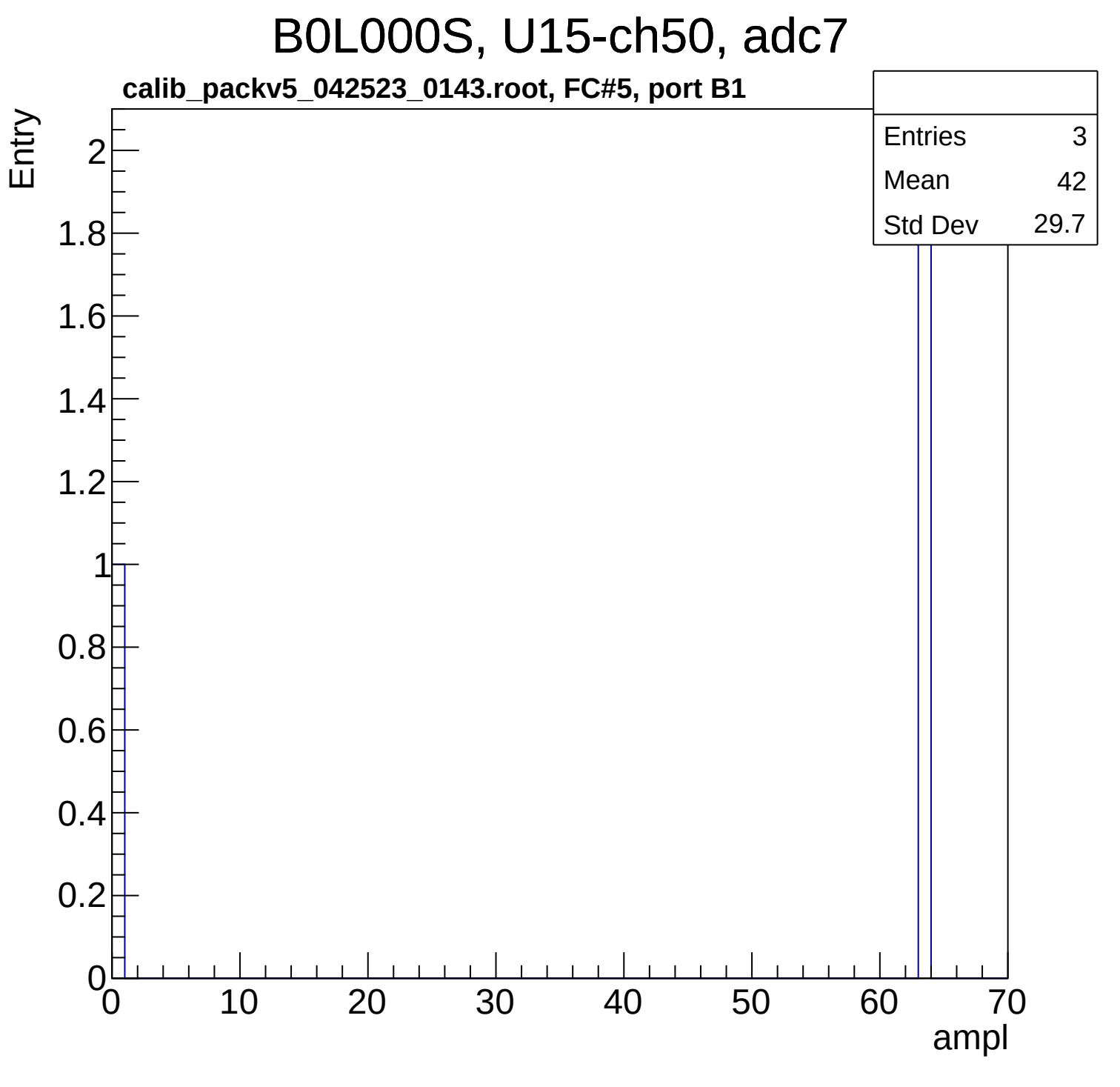
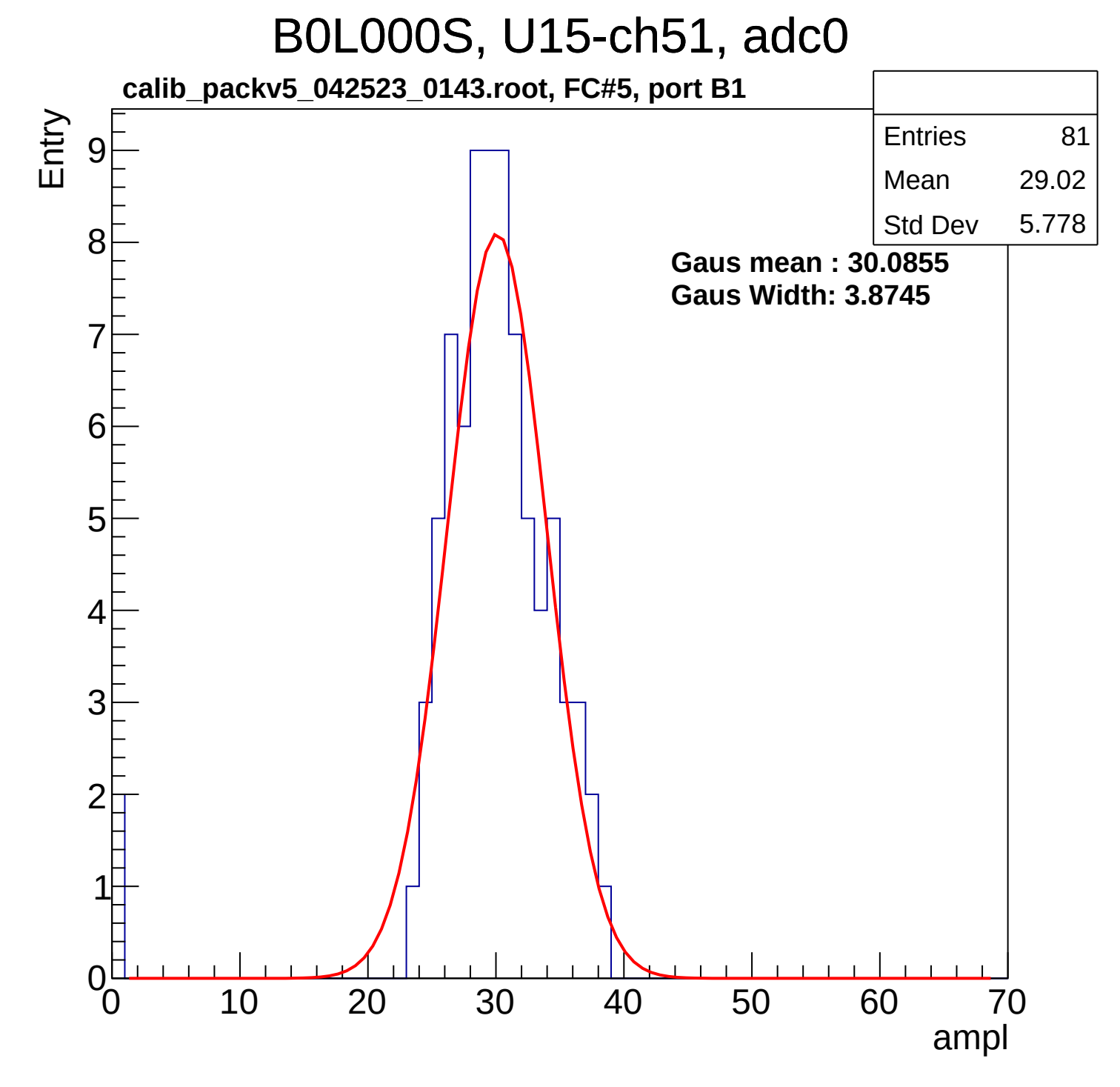
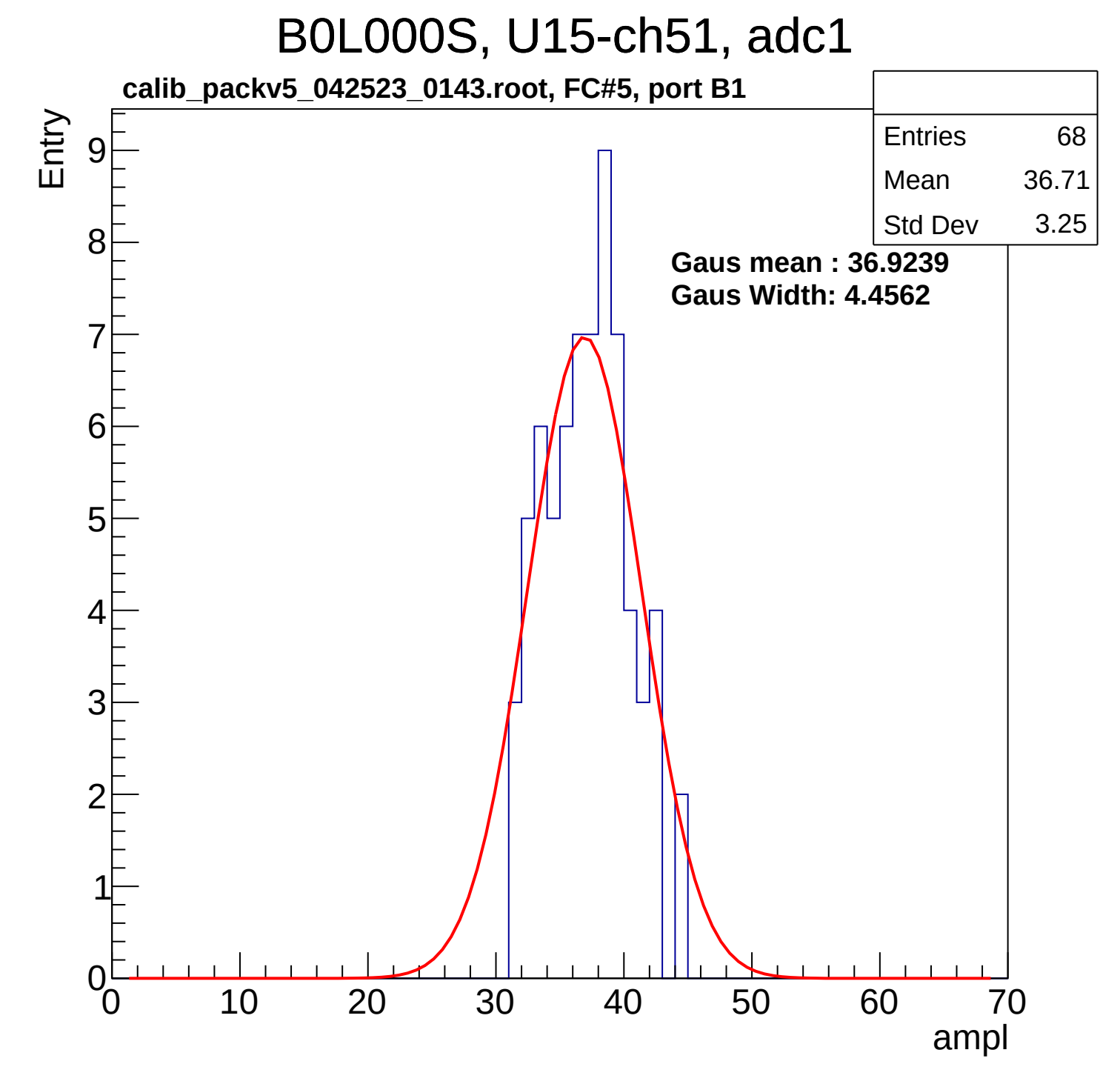

B0L000S, U15-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58
Std Dev 13.08
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 29.02
Std Dev 5.778
Gaus mean : 30.0855
Gaus Width: 3.8745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.71
Std Dev 3.25
Gaus mean : 36.9239
Gaus Width: 4.4562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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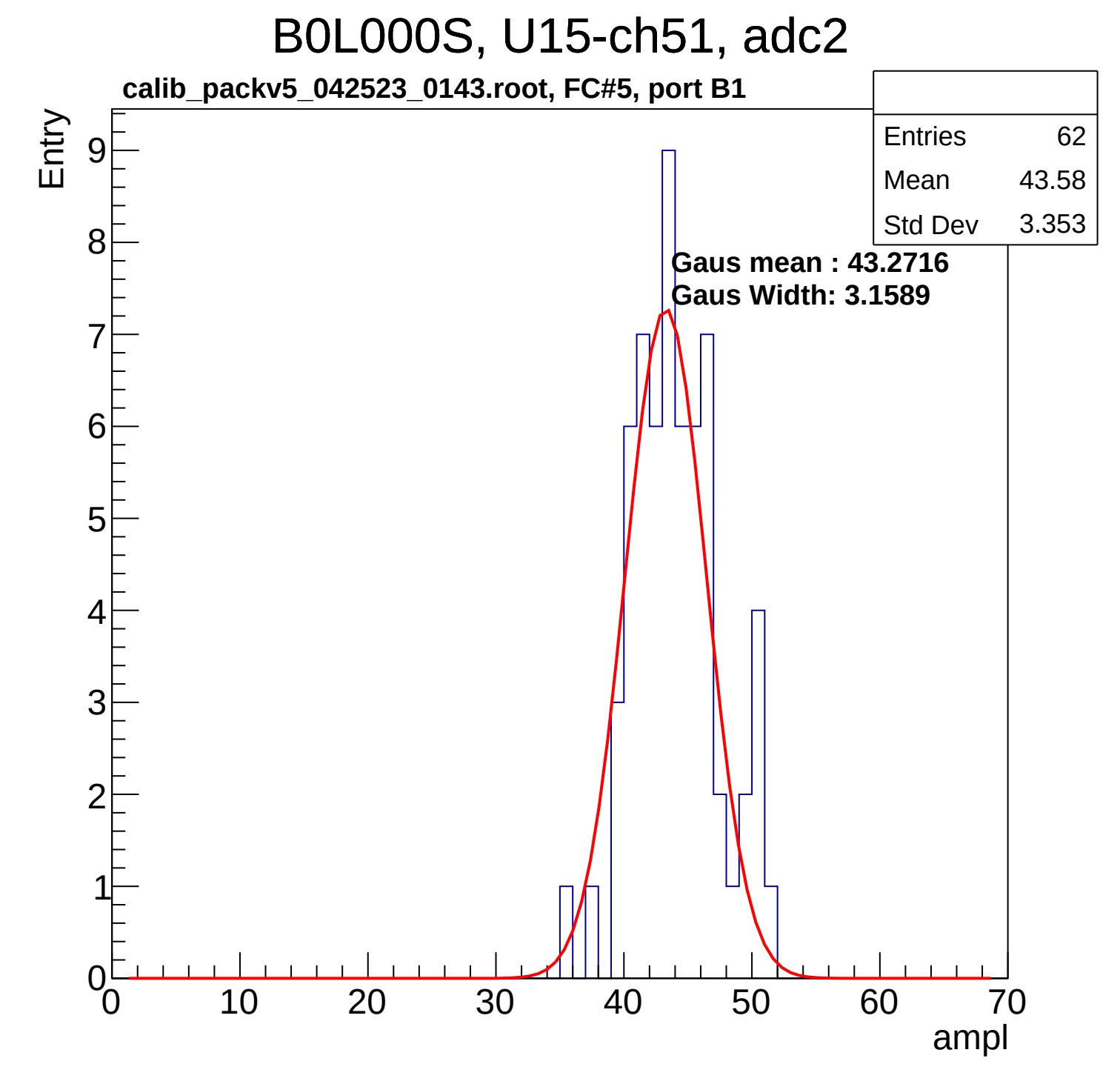
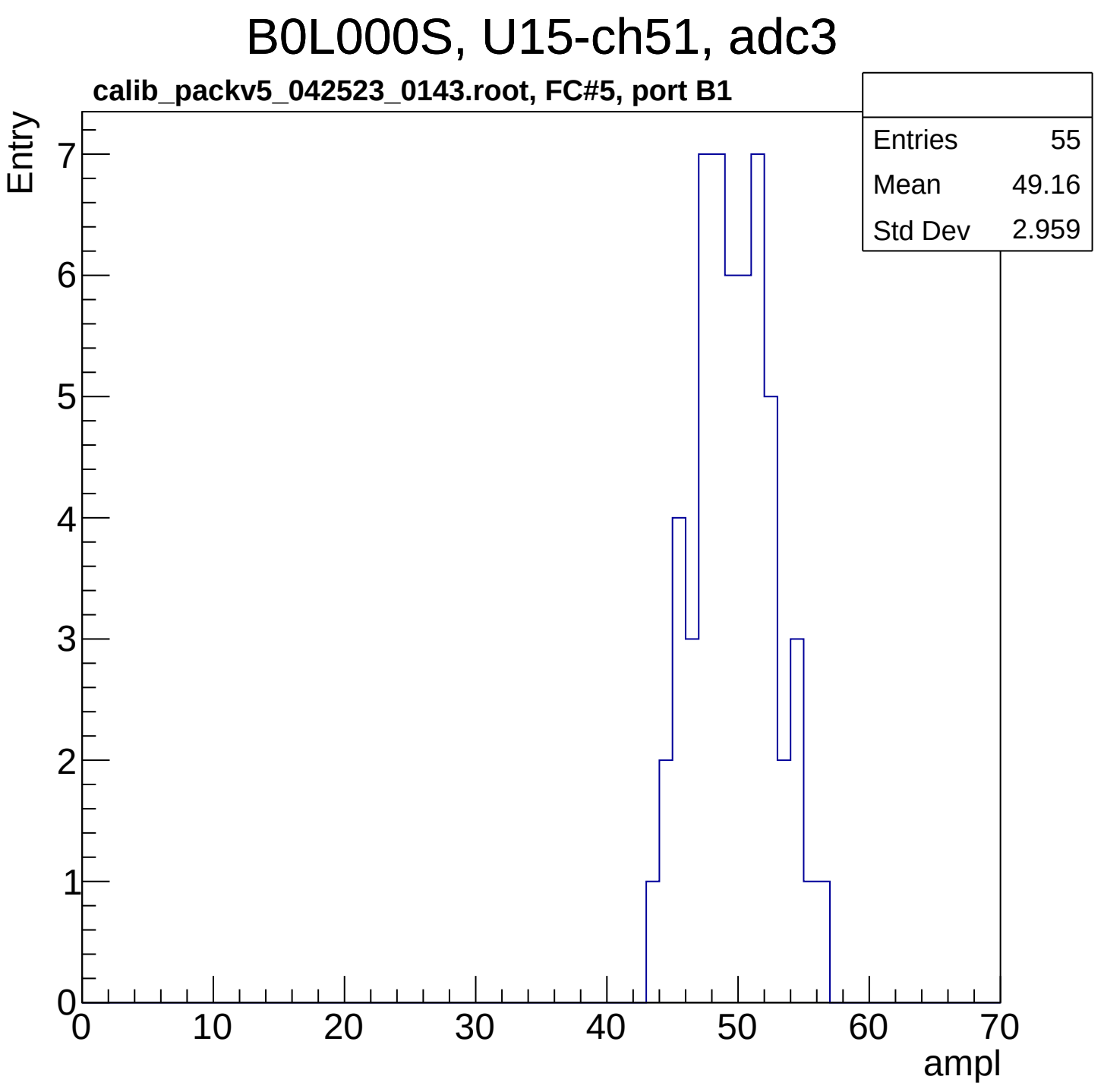
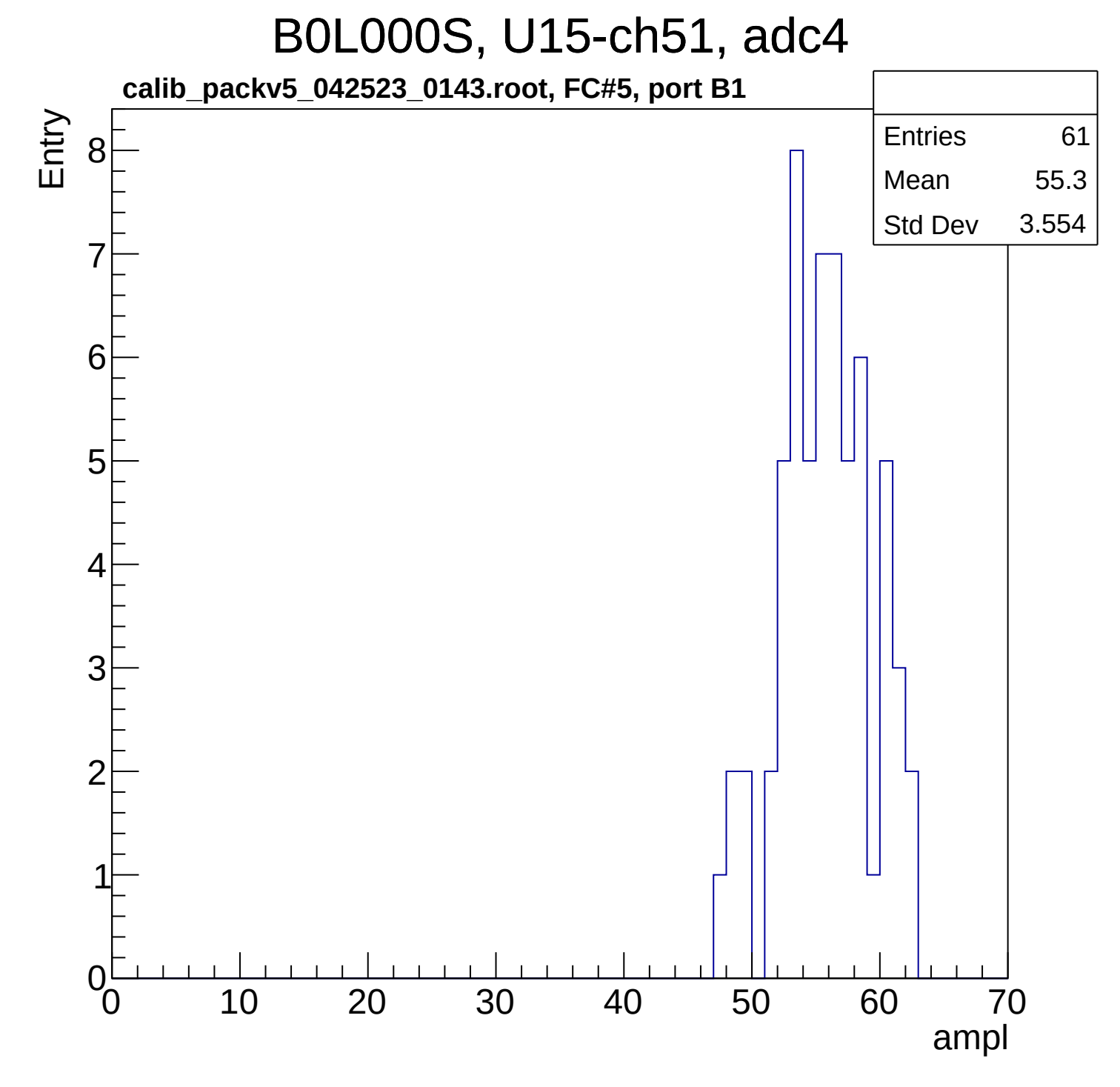
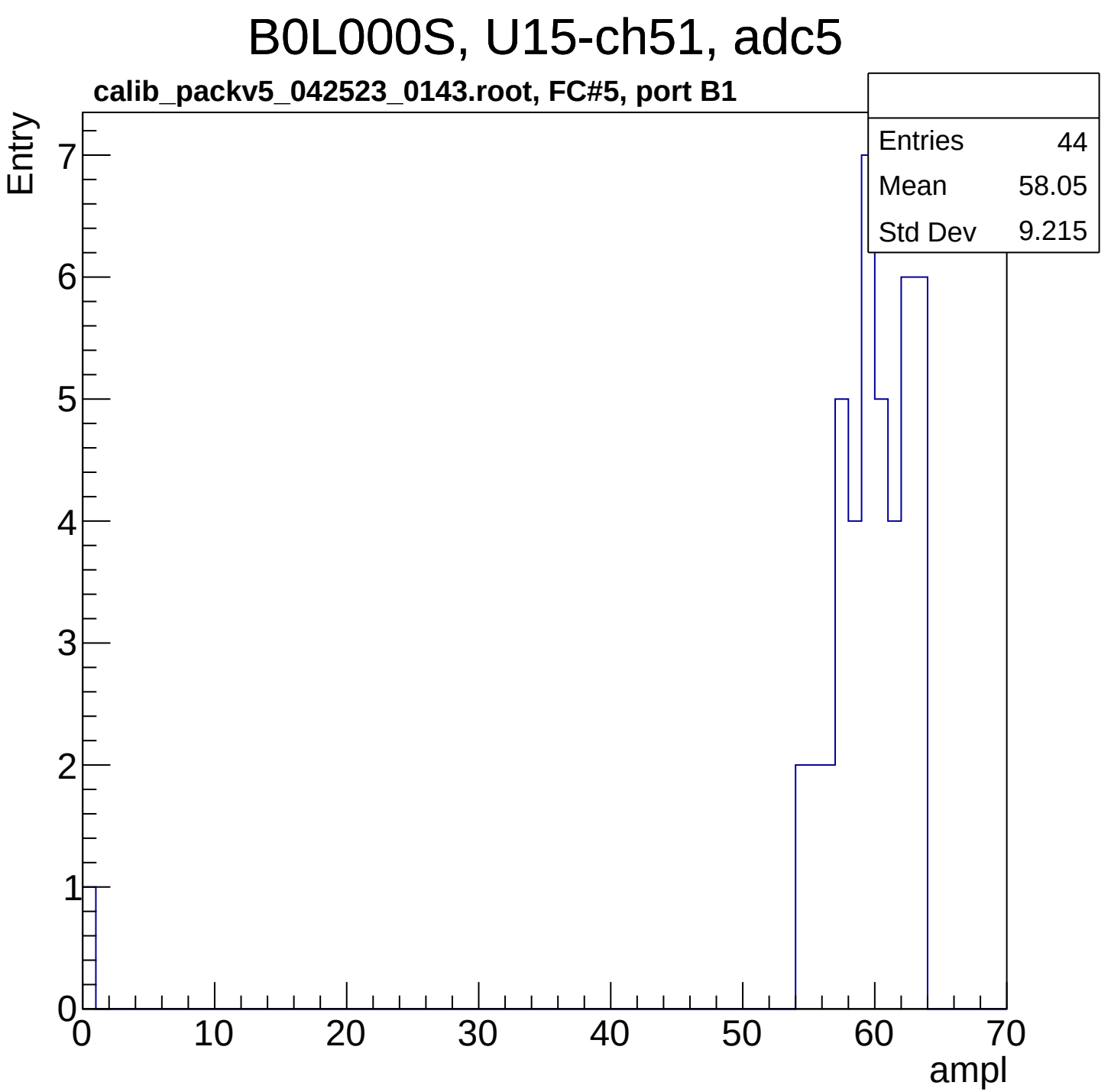
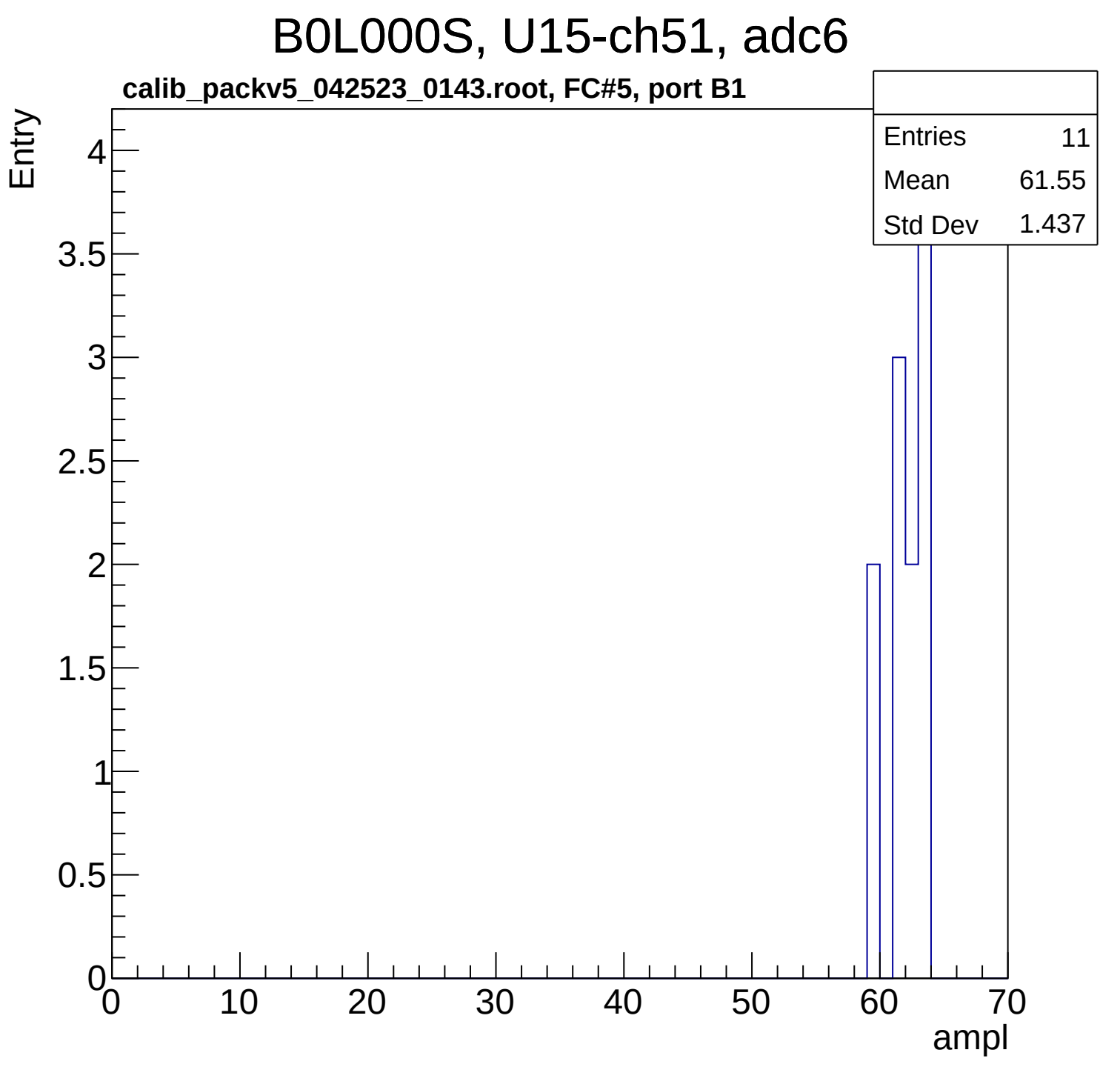
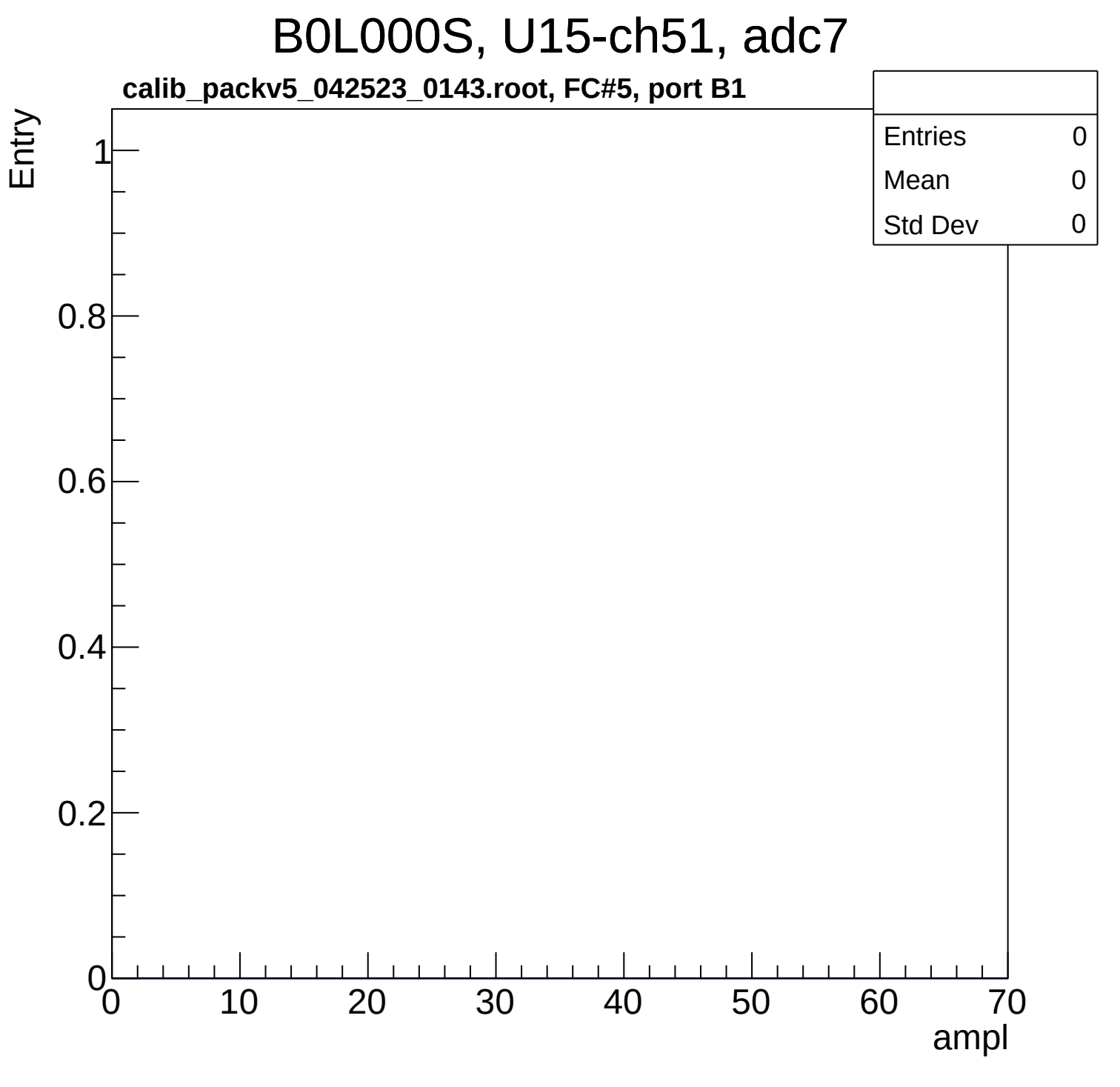

B0L000S, U15-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.58
Std Dev 3.353
Gaus mean : 43.2716
Gaus Width: 3.1589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

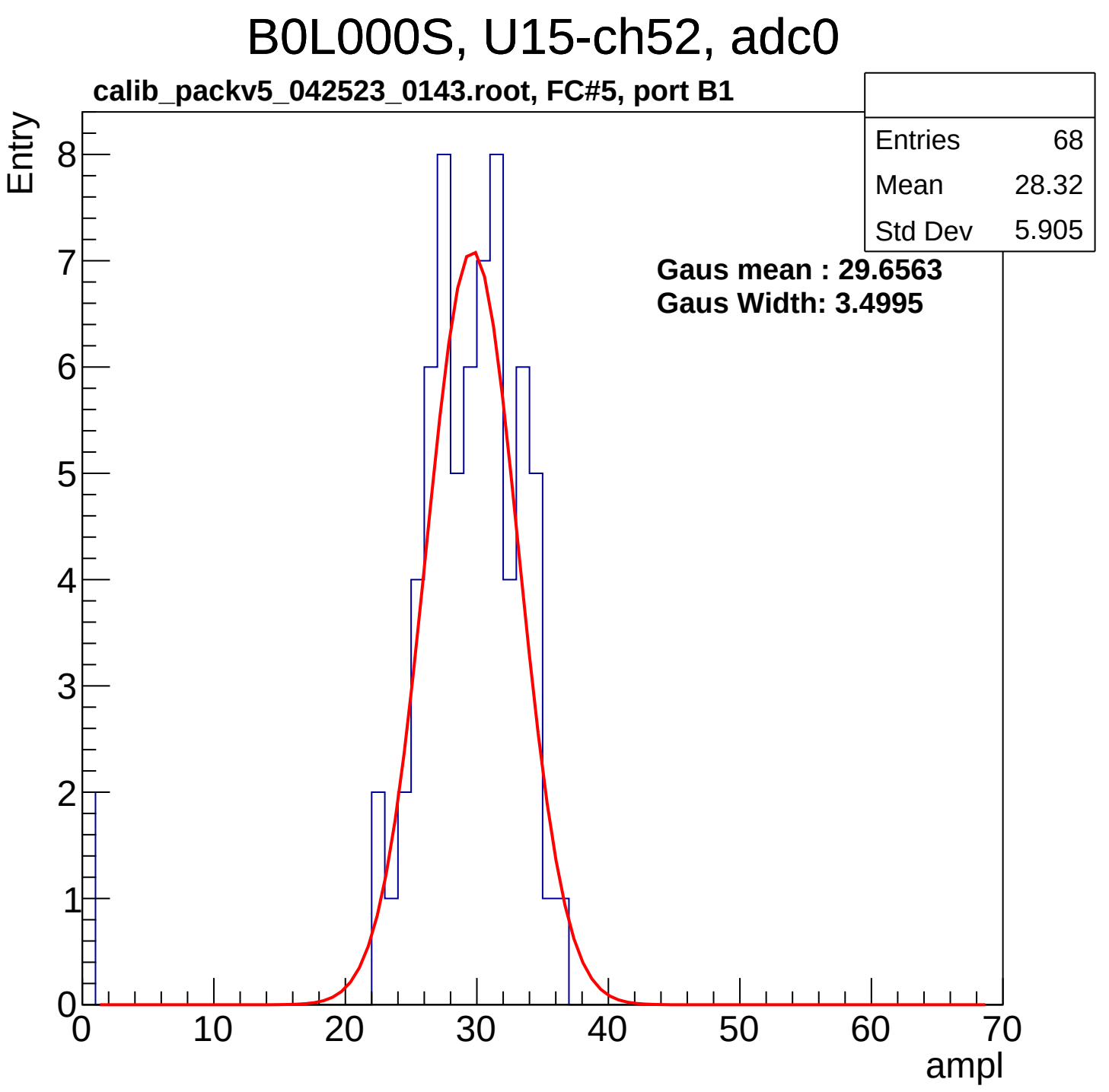

B0L000S, U15-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 49.16
Std Dev 2.959
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

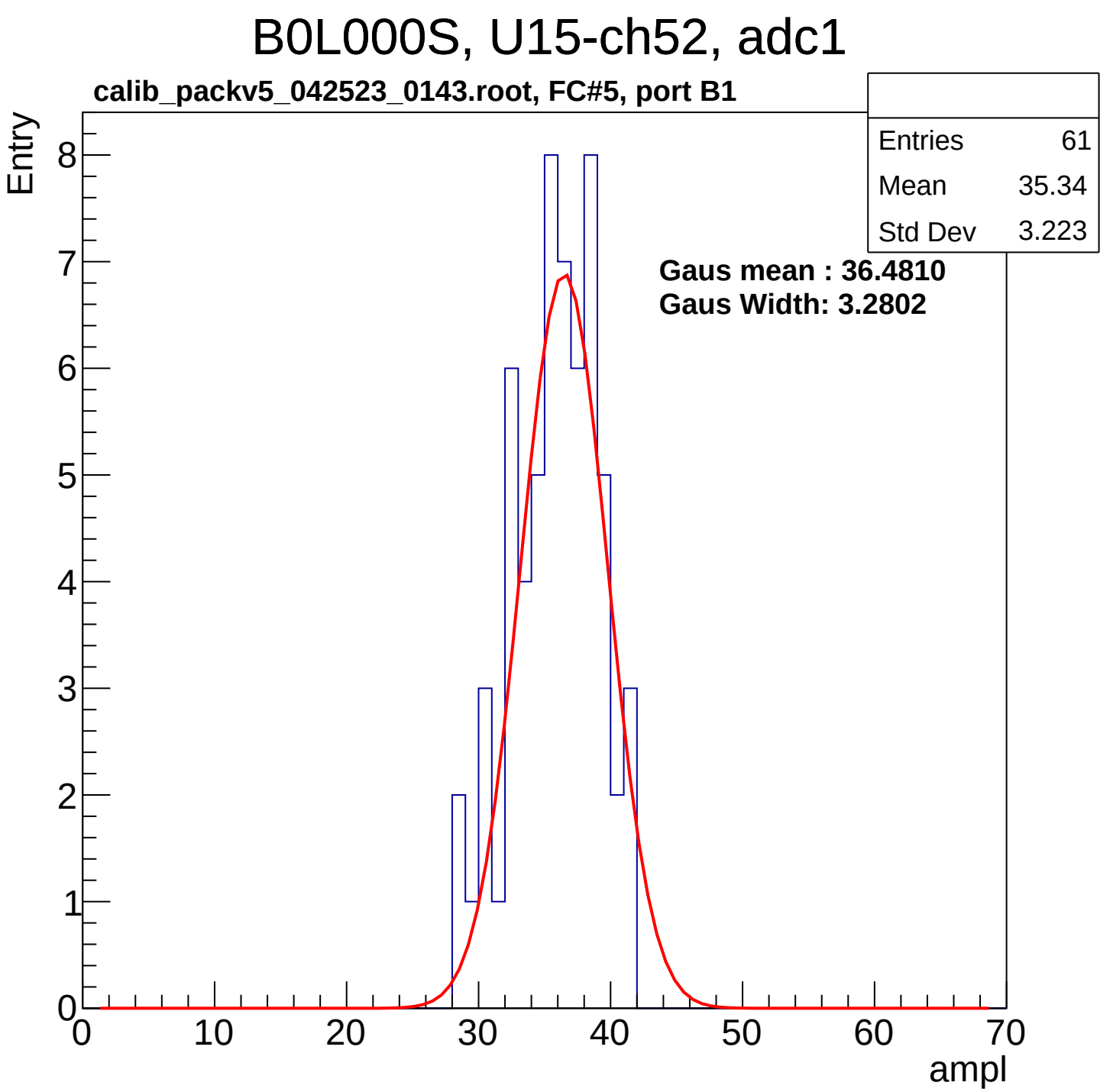

B0L000S, U15-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.3
Std Dev 3.554
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

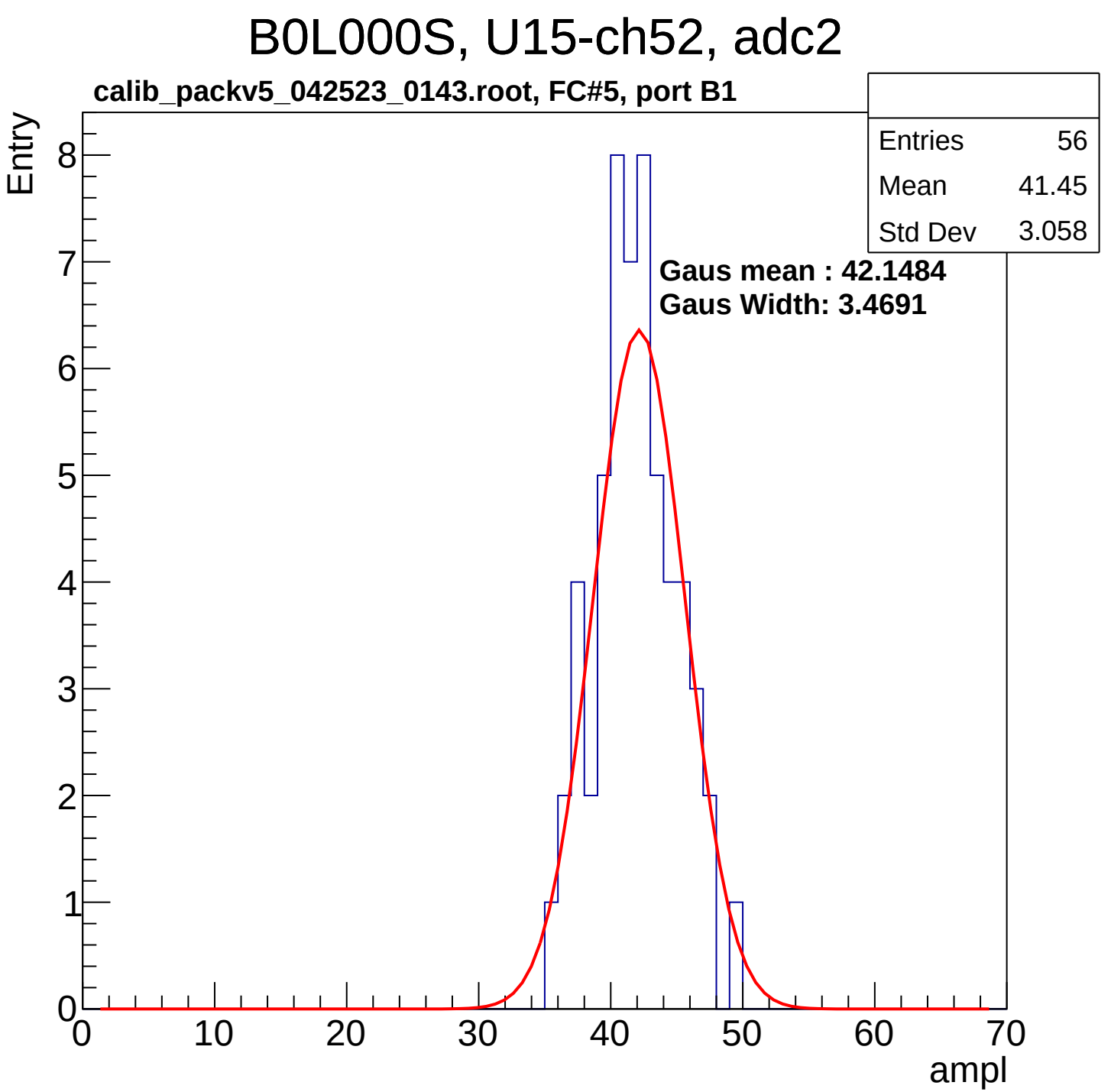

B0L000S, U15-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.05
Std Dev 9.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

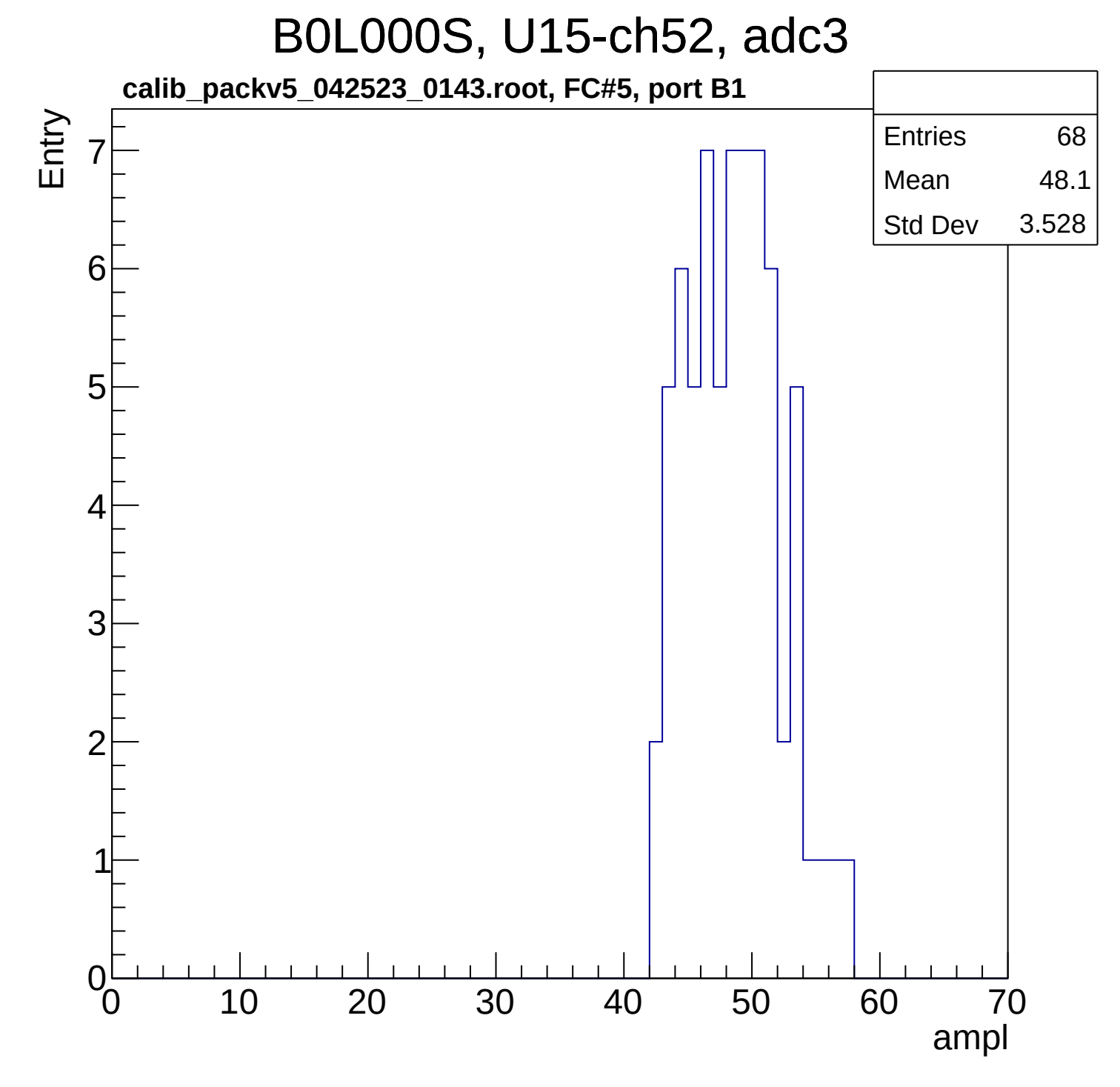

B0L000S, U15-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.55
Std Dev 1.437
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

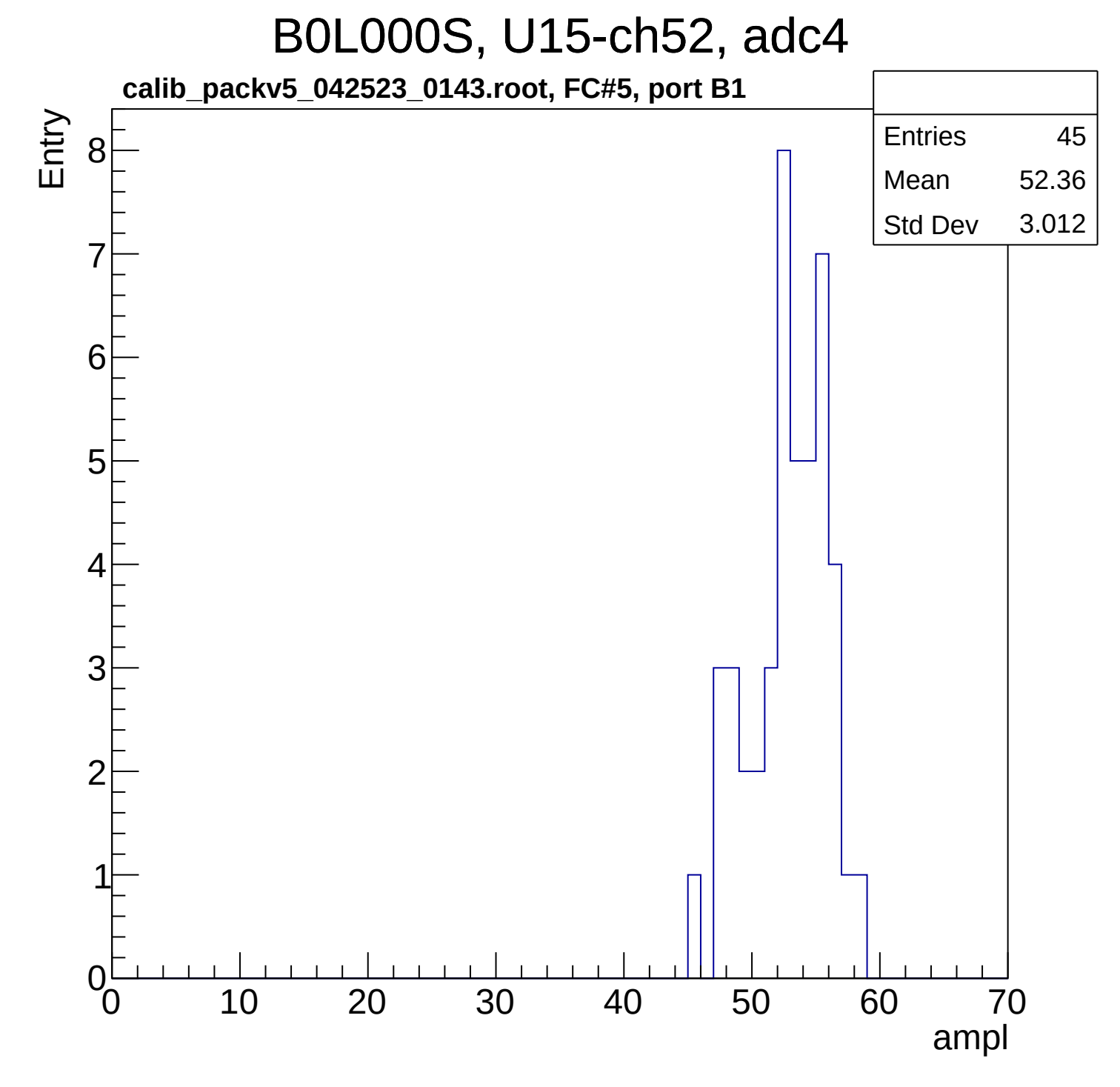

B0L000S, U15-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.32
Std Dev 5.905
Gaus mean : 29.6563
Gaus Width: 3.4995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.34
Std Dev 3.223
Gaus mean : 36.4810
Gaus Width: 3.2802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 41.45
Std Dev 3.058
Gaus mean : 42.1484
Gaus Width: 3.4691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.1
Std Dev 3.528
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 52.36
Std Dev 3.012
Entry
ampl
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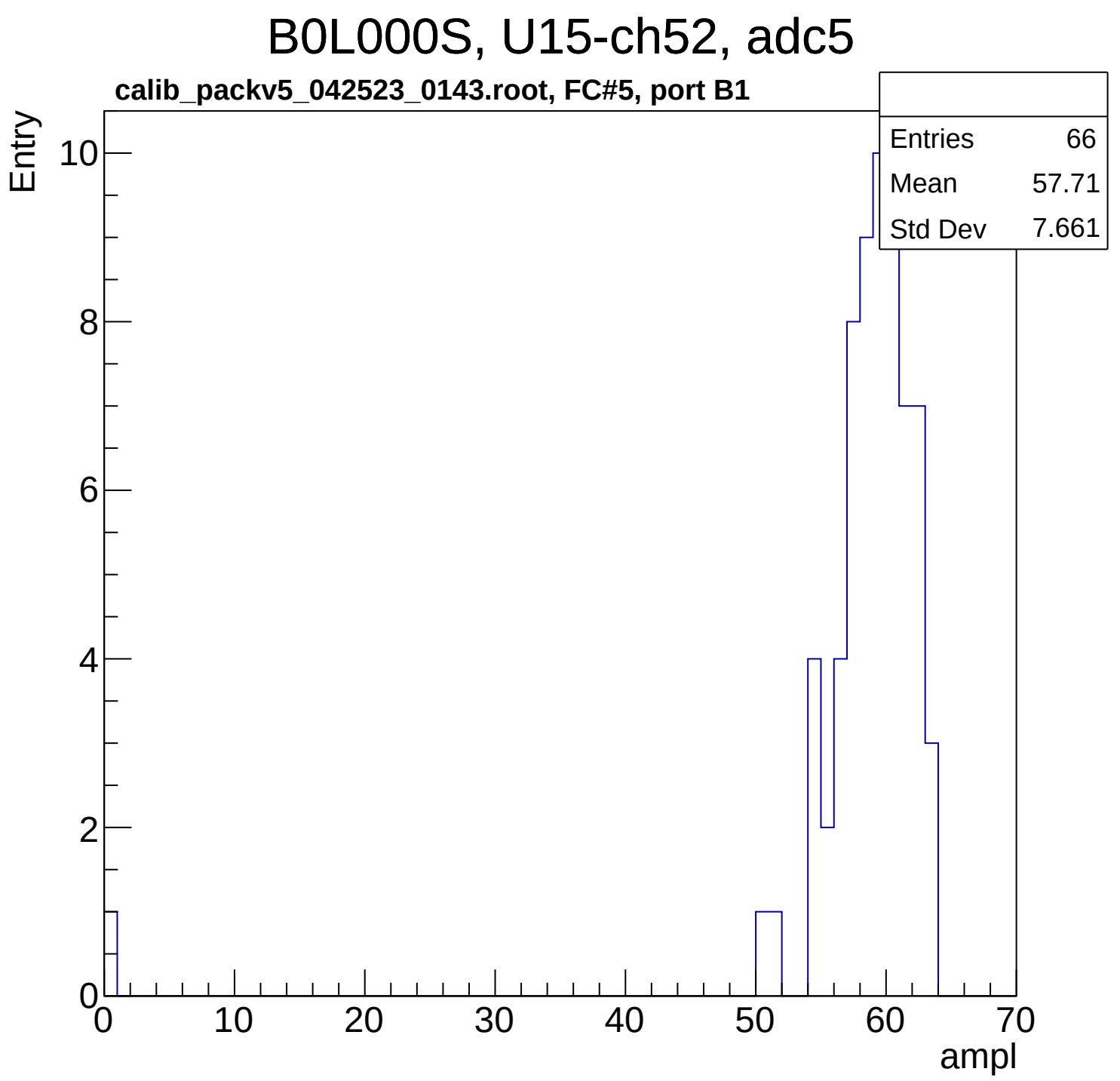
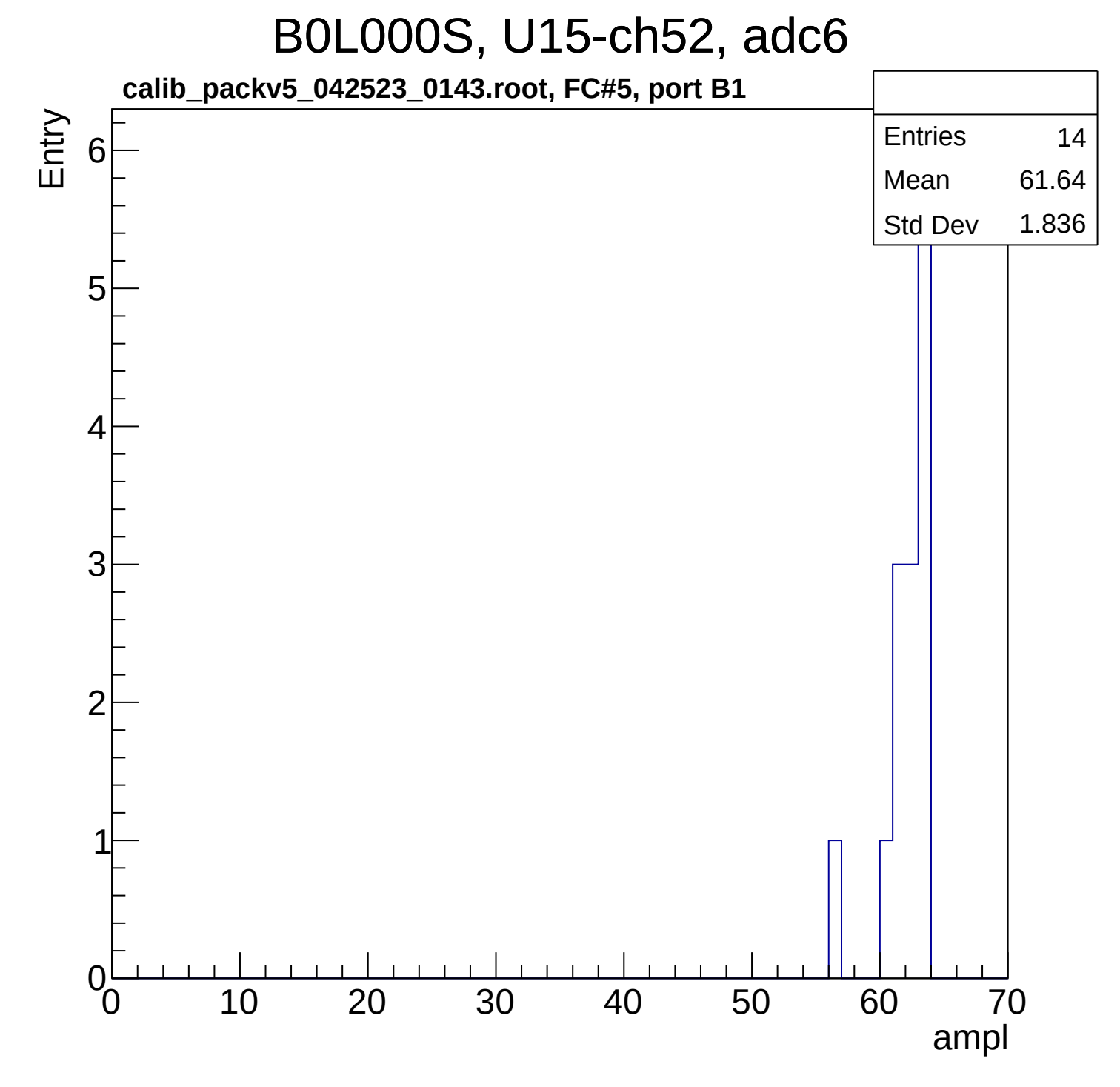
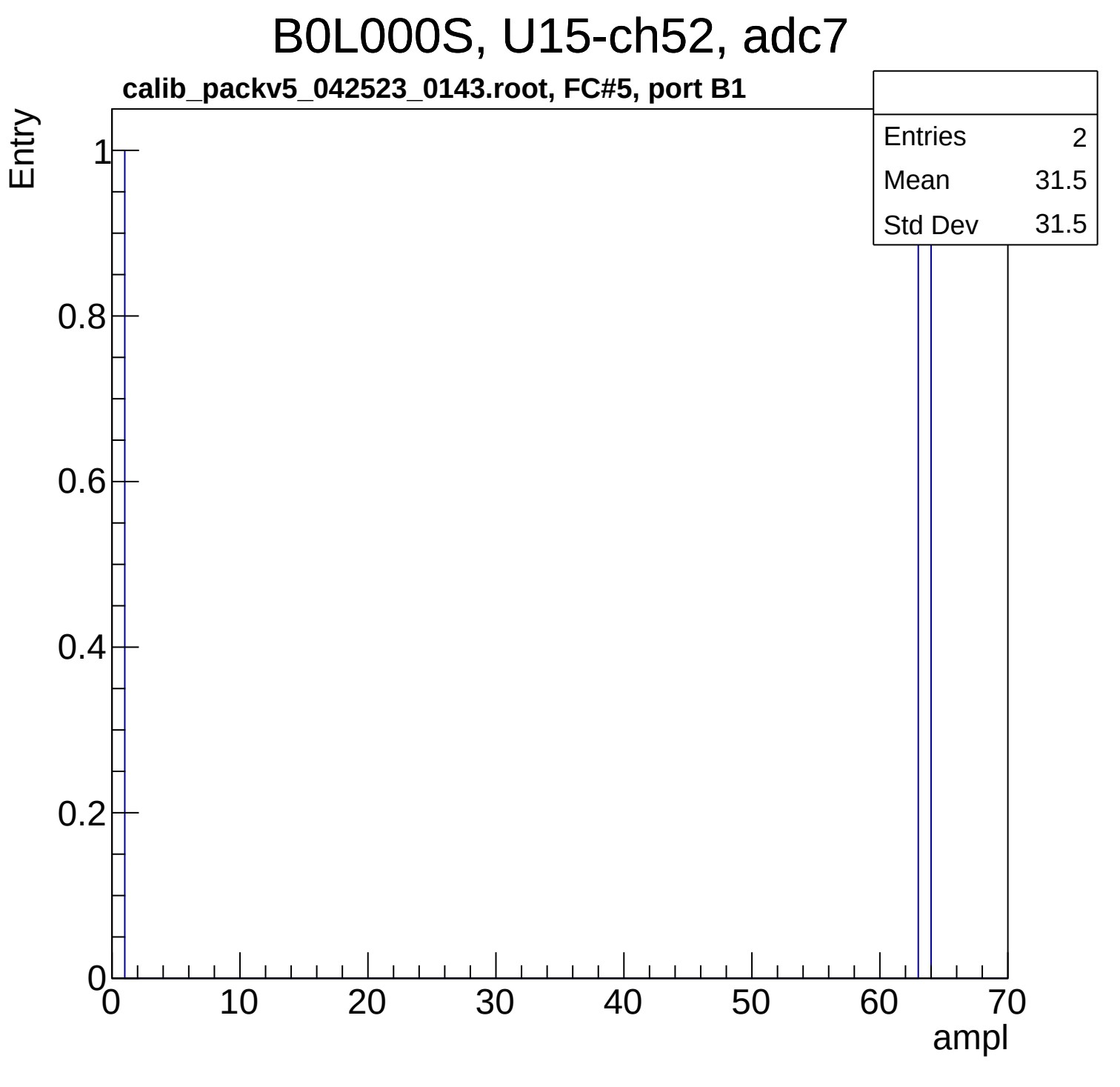
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.71
Std Dev 7.661
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.64
Std Dev 1.836
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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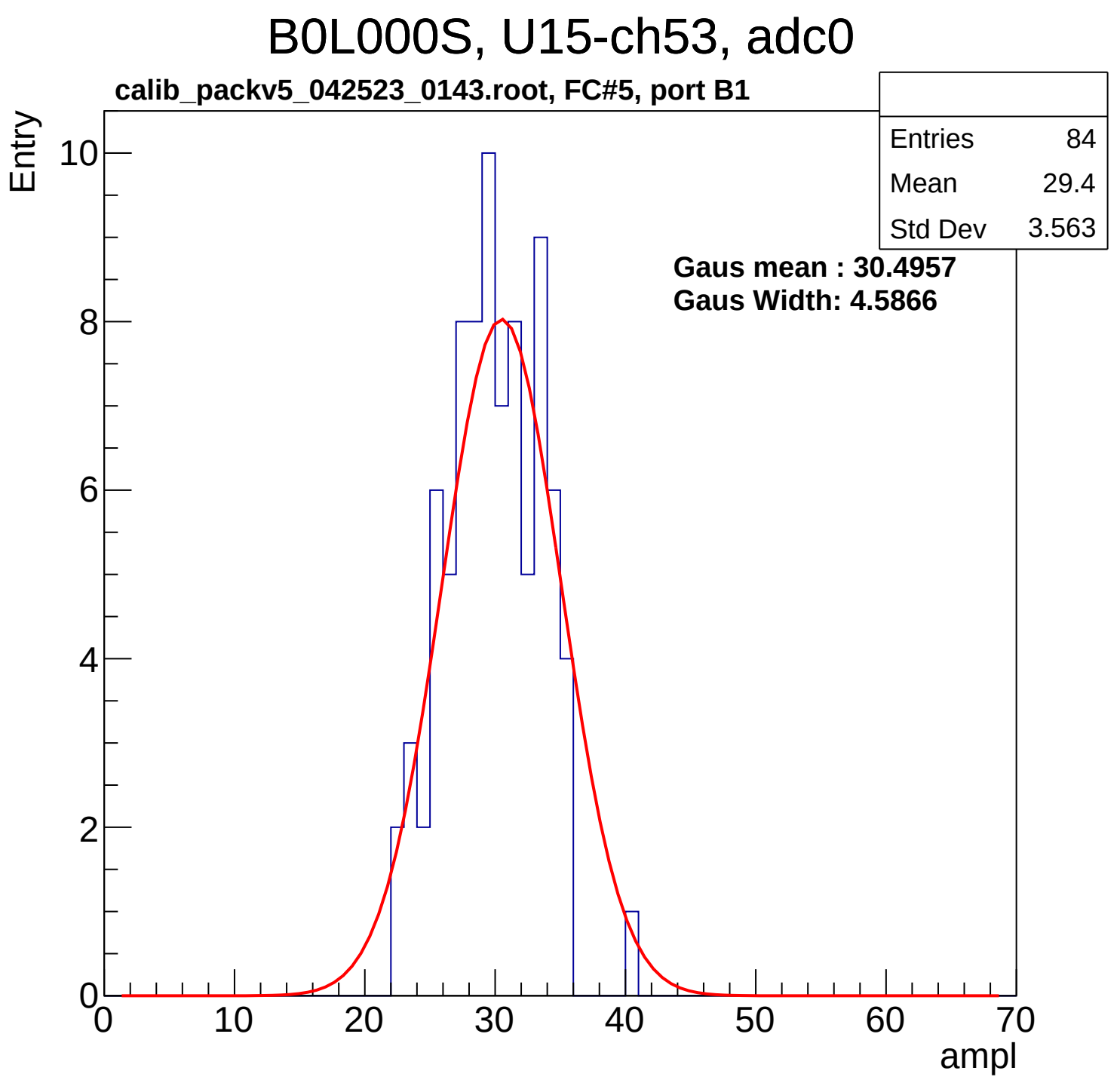
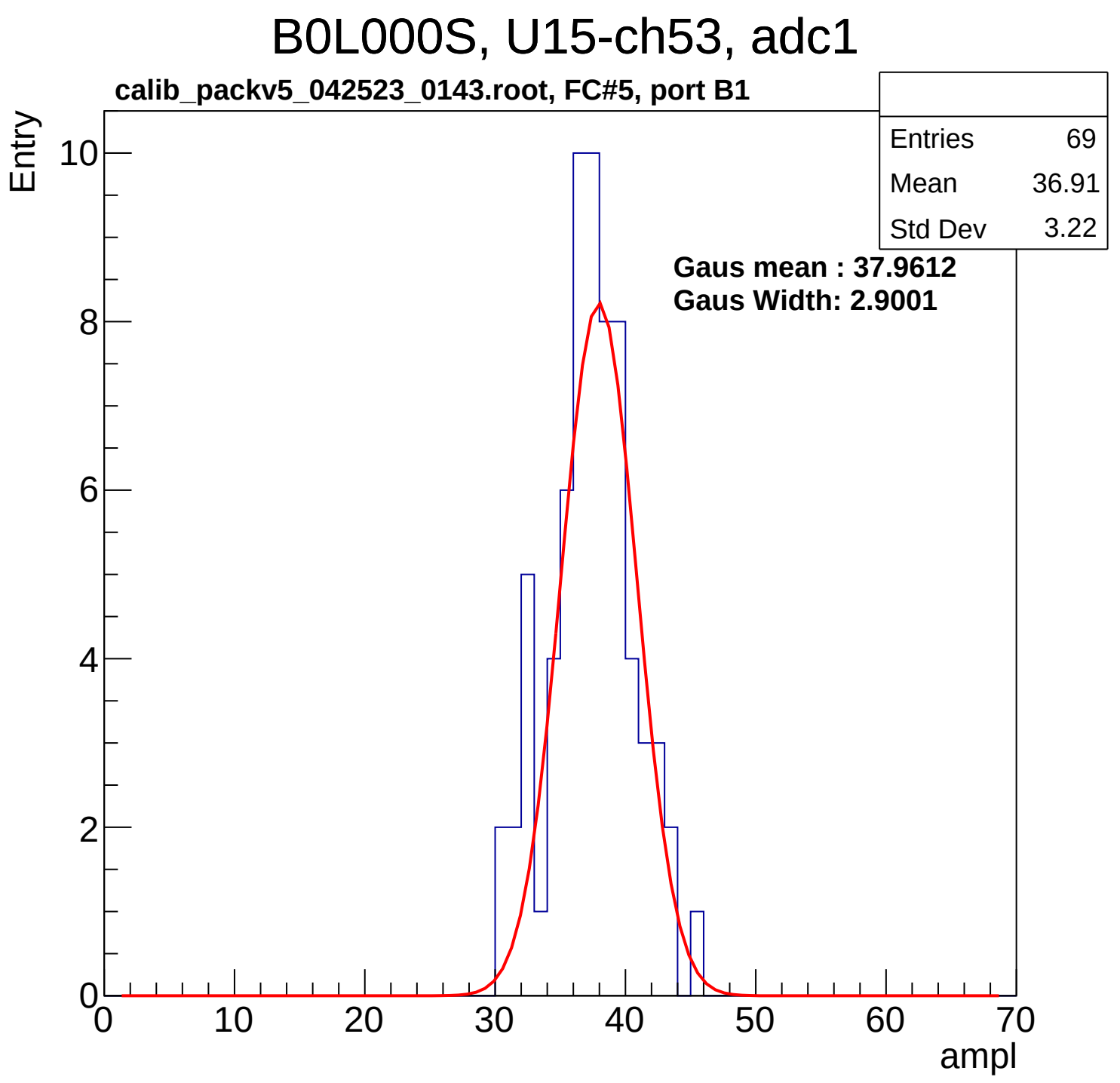
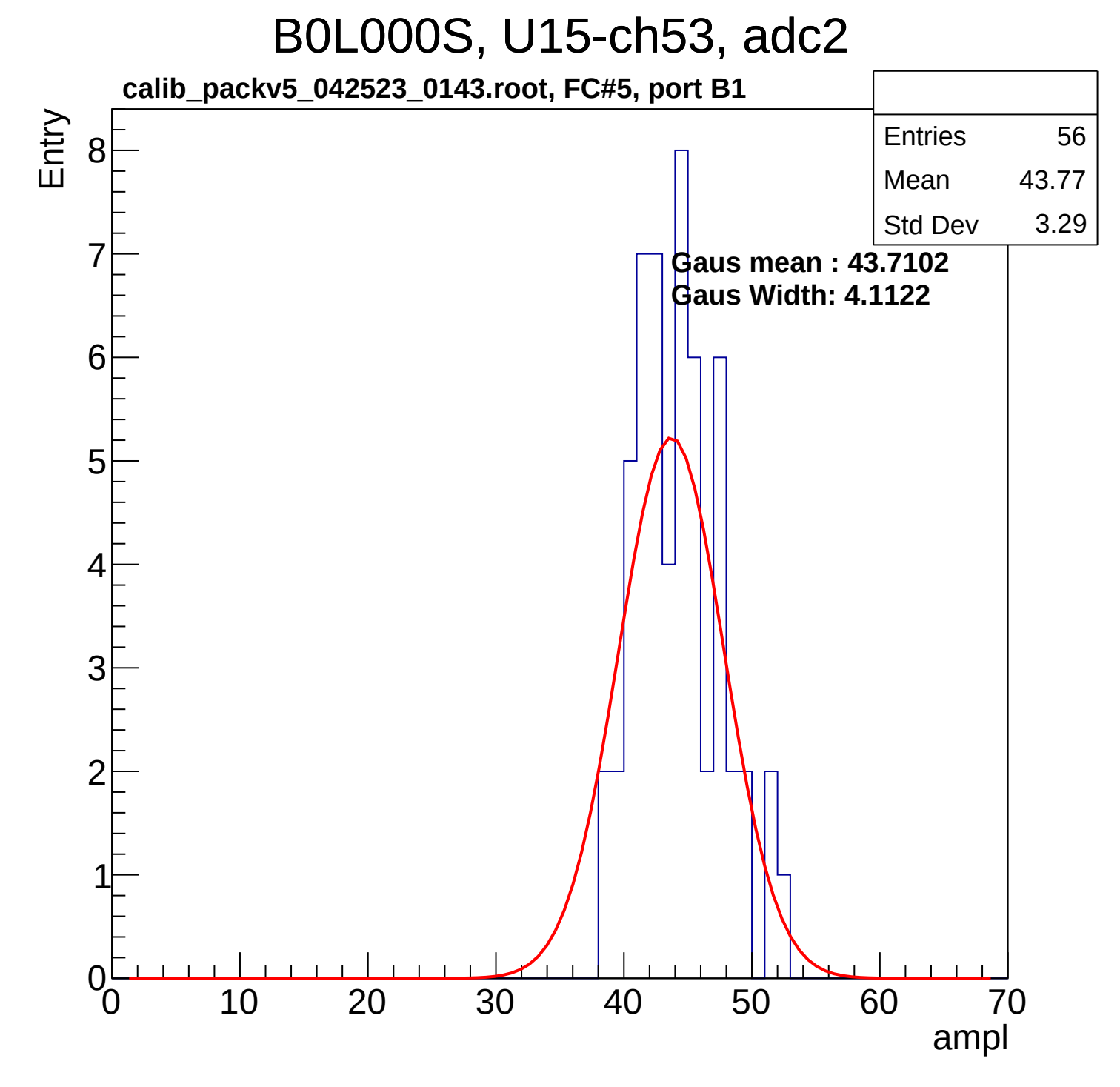
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 29.4
Std Dev 3.563
Gaus mean : 30.4957
Gaus Width: 4.5866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.91
Std Dev 3.22
Gaus mean : 37.9612
Gaus Width: 2.9001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.77
Std Dev 3.29
Gaus mean : 43.7102
Gaus Width: 4.1122
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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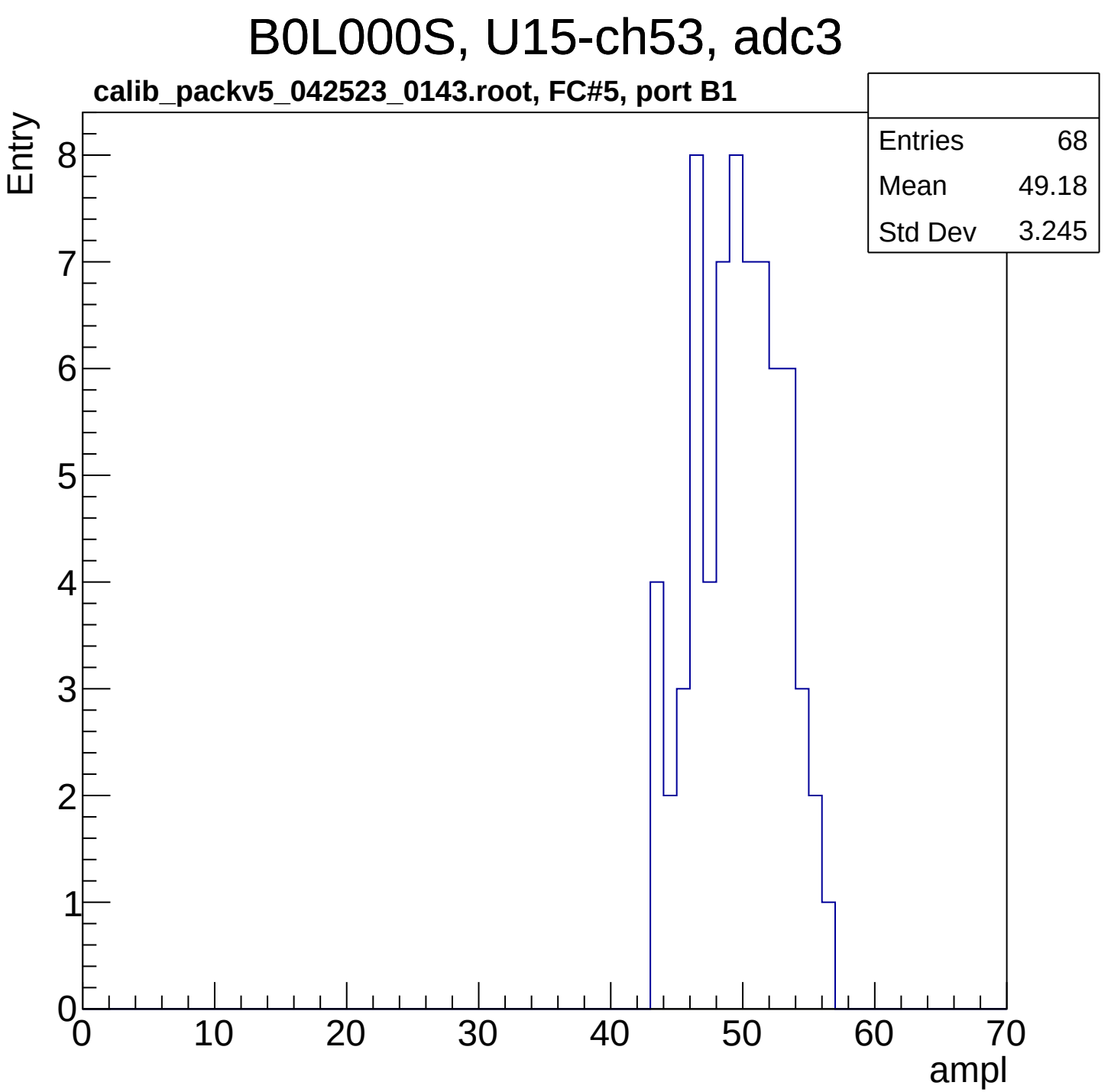
B0L000S, U15-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.18
Std Dev 3.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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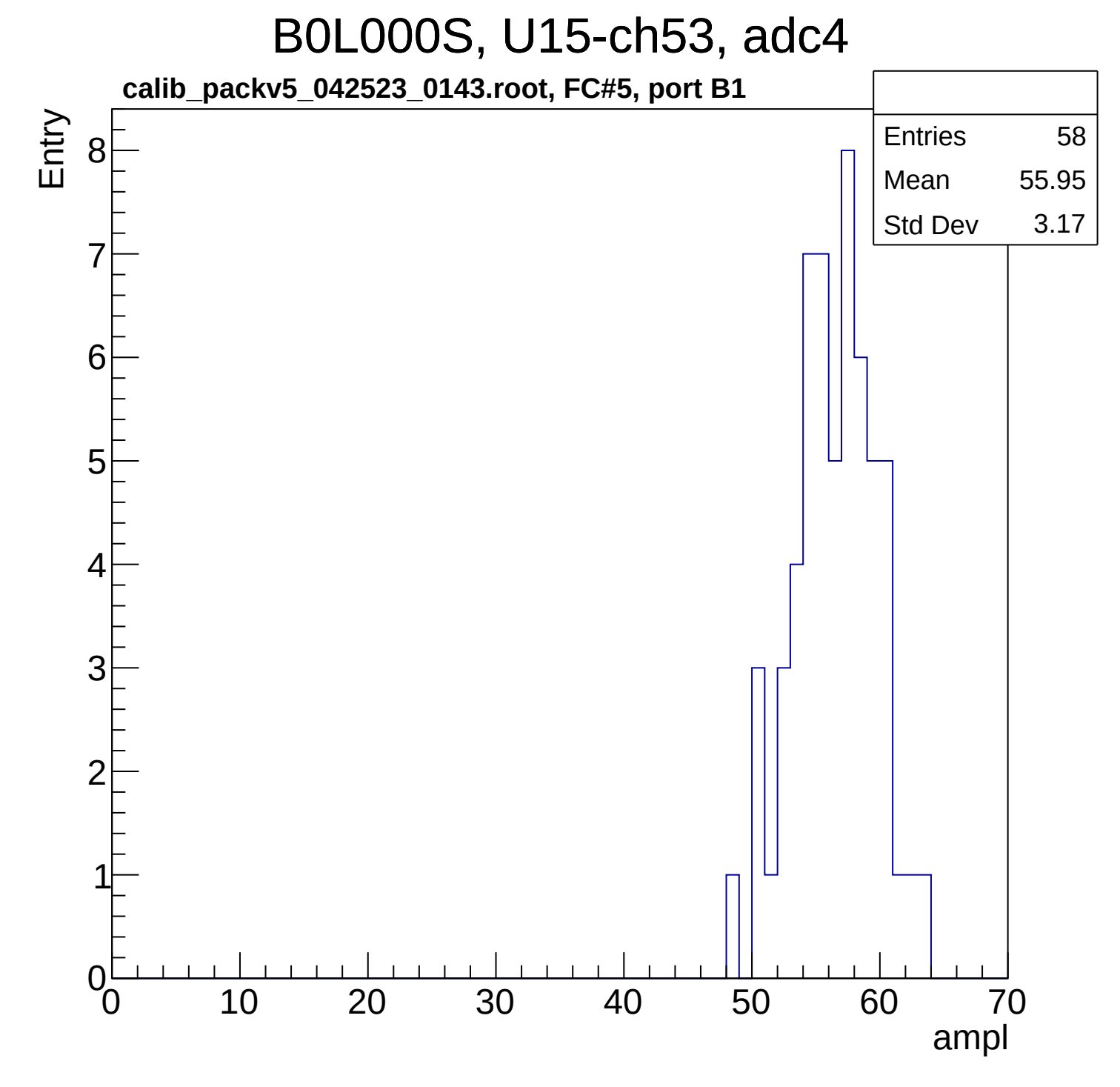
B0L000S, U15-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.95
Std Dev 3.17
Entry
ampl
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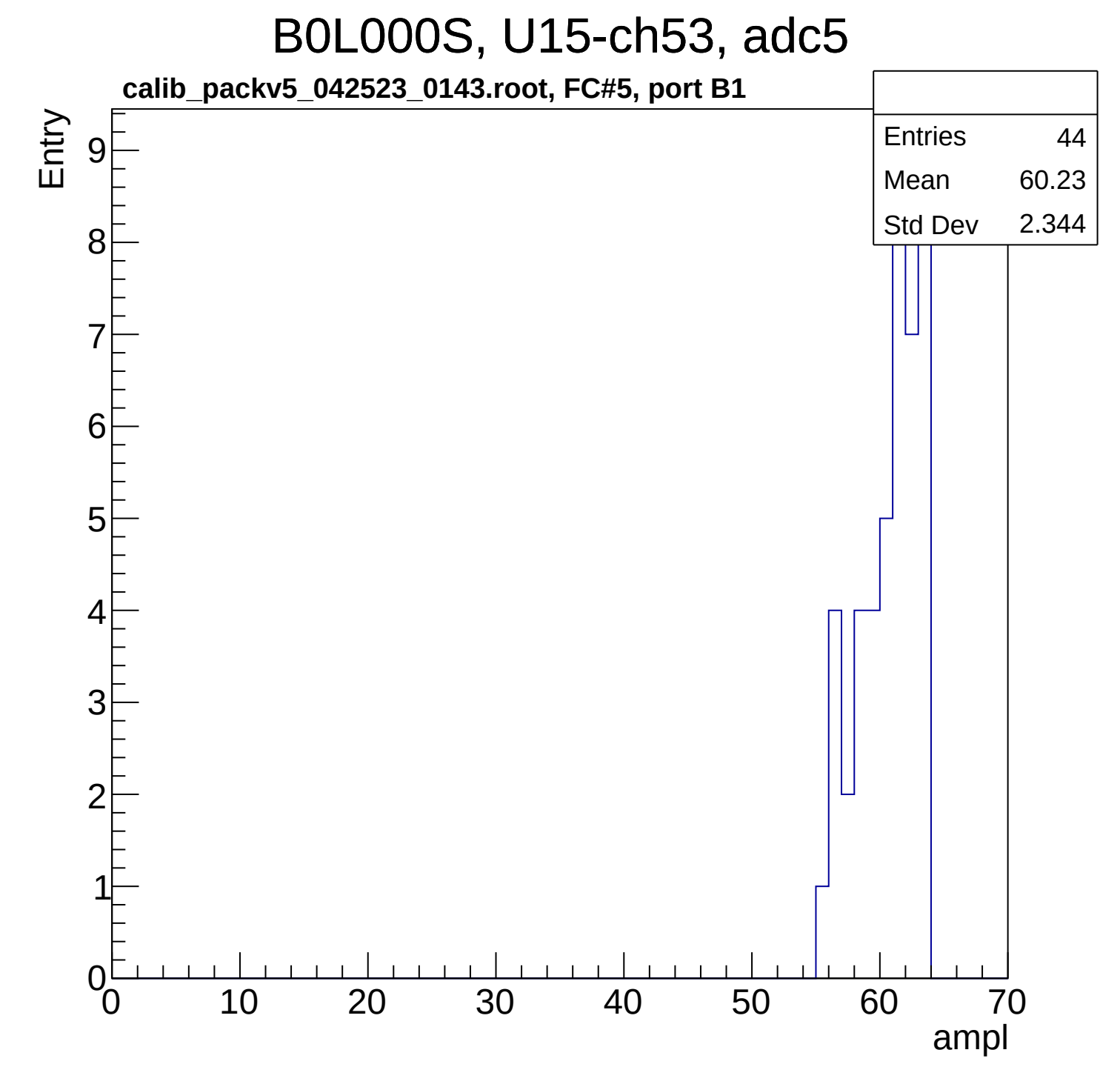
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 60.23
Std Dev 2.344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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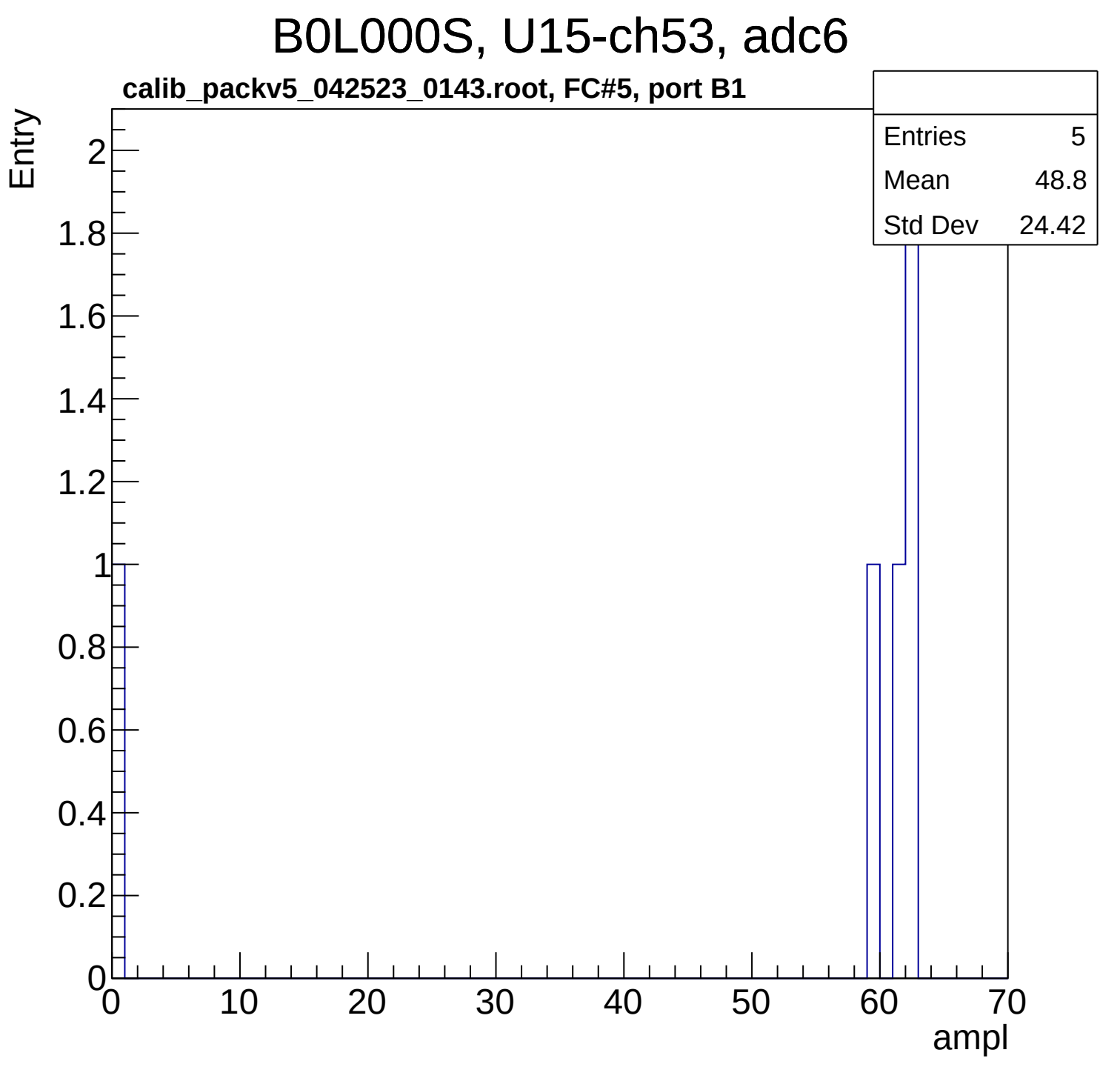
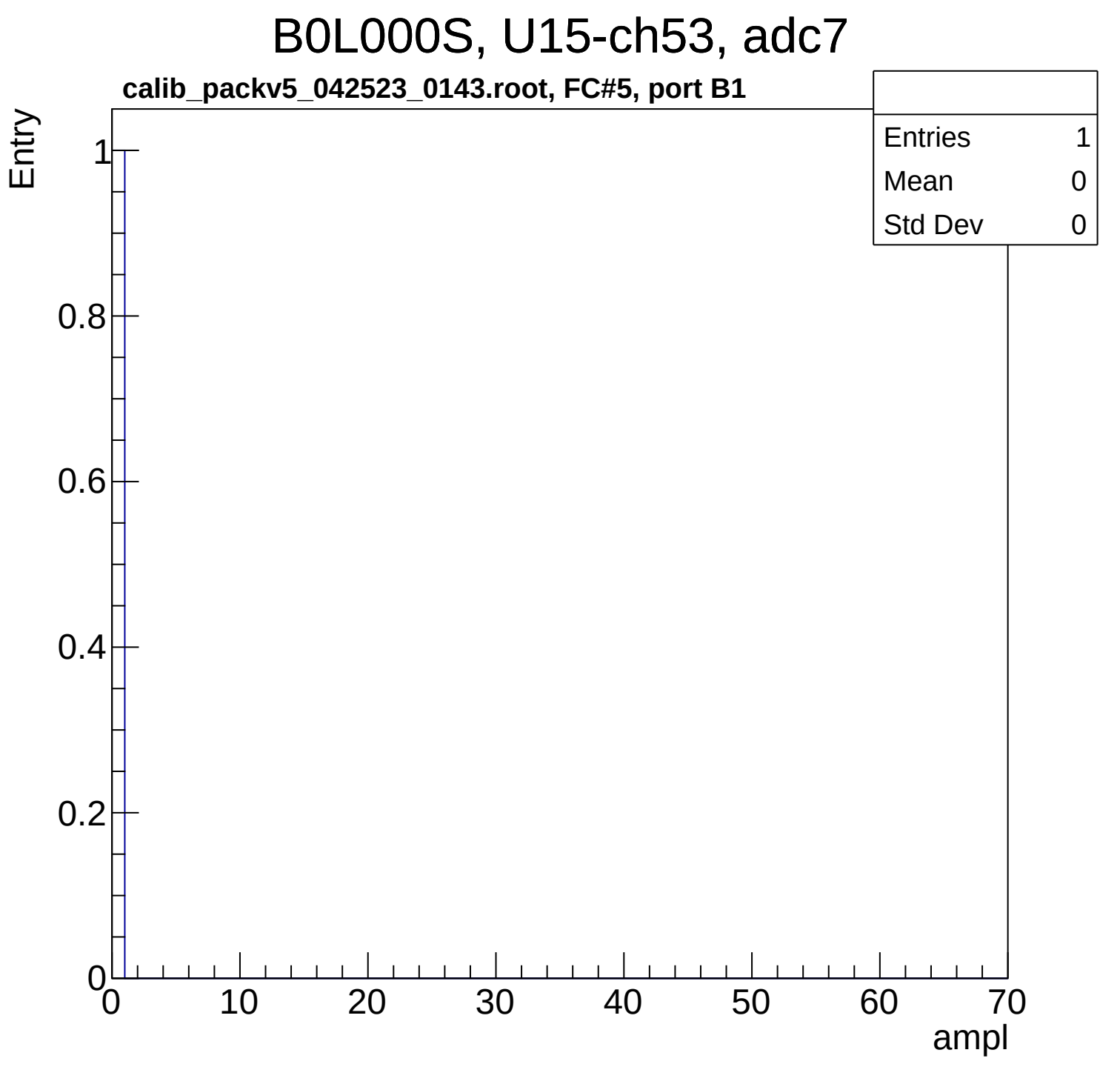
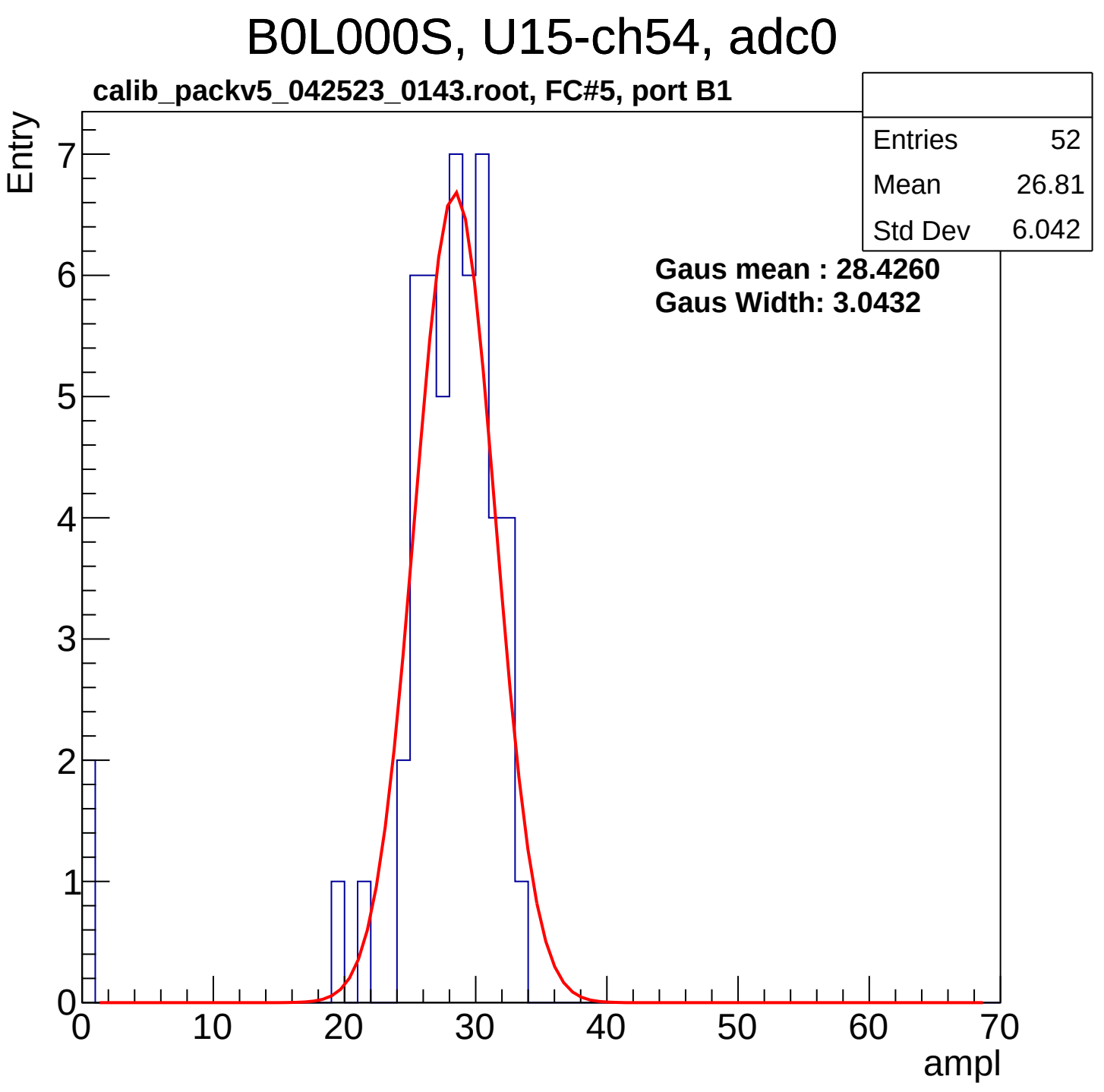

B0L000S, U15-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 48.8
Std Dev 24.42
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 26.81
Std Dev 6.042
Gaus mean : 28.4260
Gaus Width: 3.0432
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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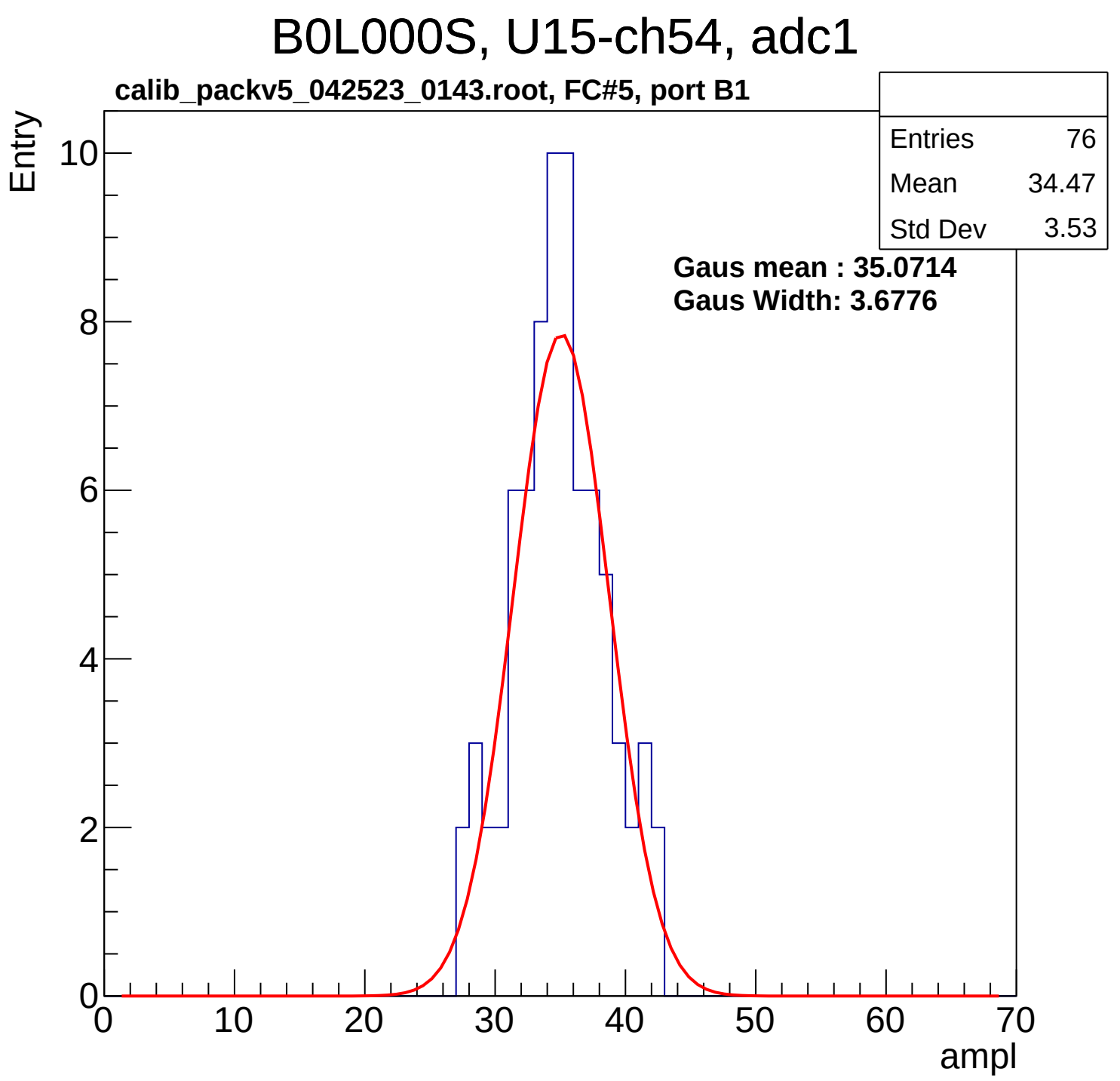
B0L000S, U15-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 34.47
Std Dev 3.53
Gaus mean : 35.0714
Gaus Width: 3.6776
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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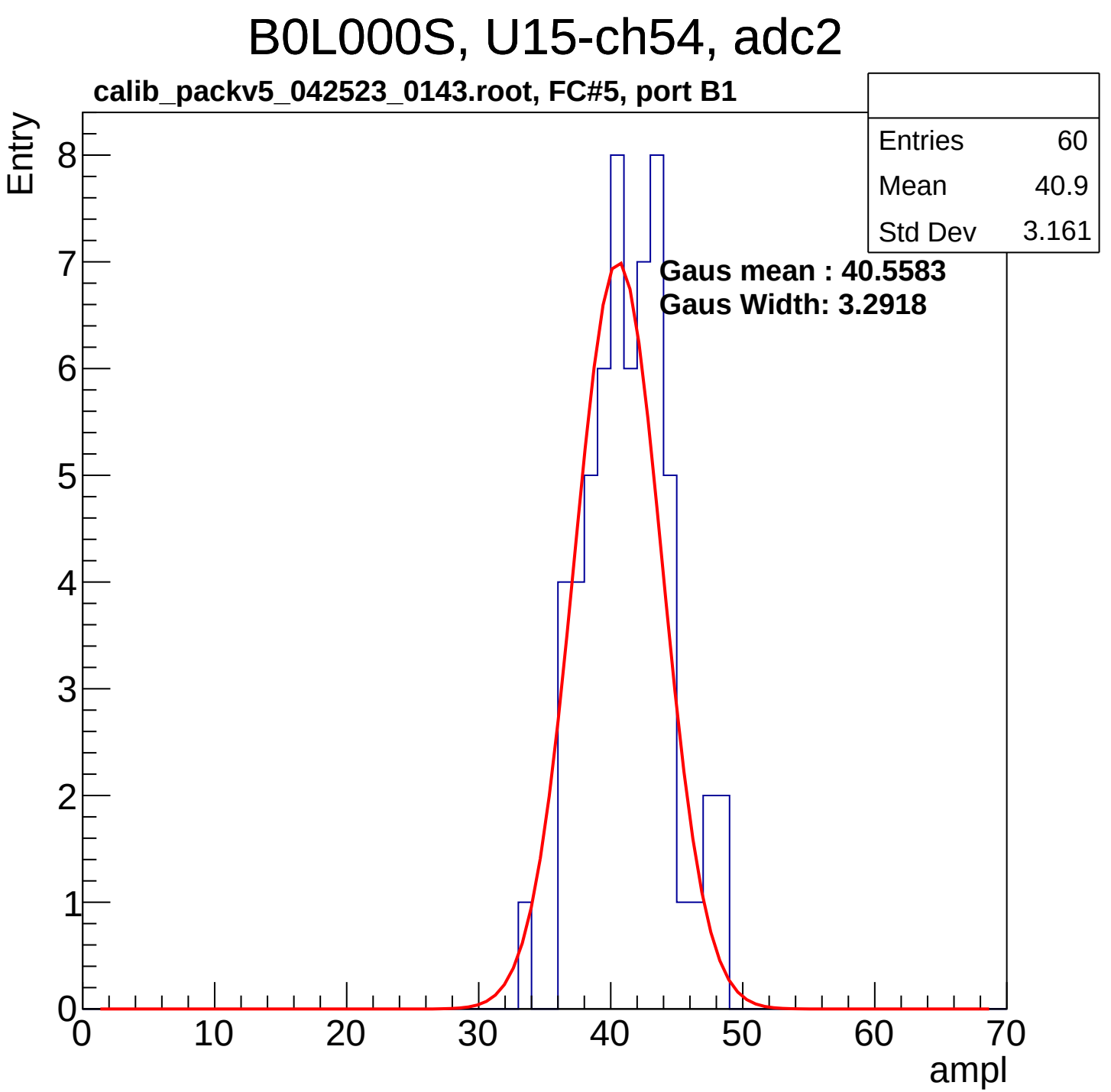
B0L000S, U15-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 40.9
Std Dev 3.161
Gaus mean : 40.5583
Gaus Width: 3.2918
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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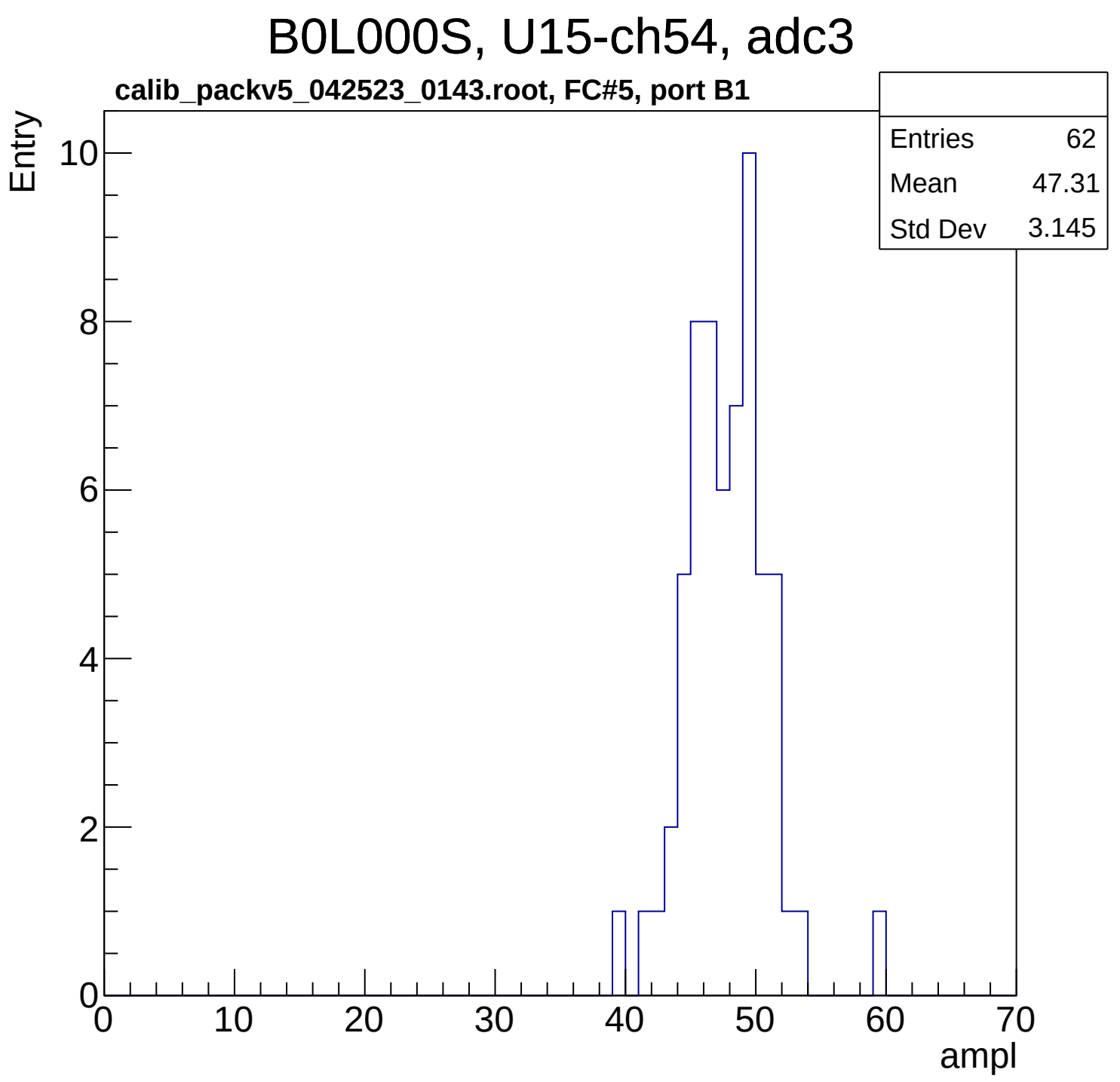
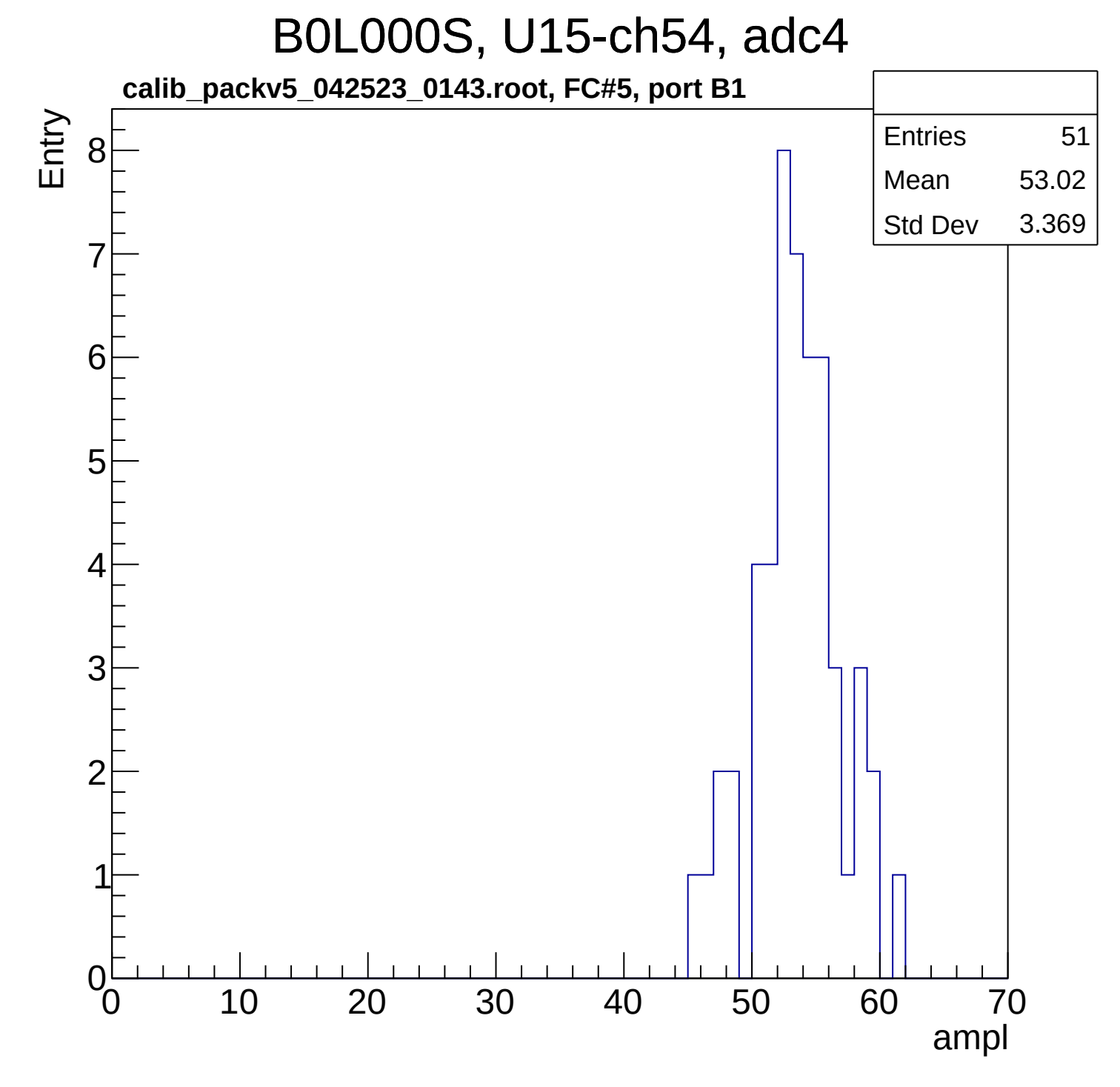

B0L000S, U15-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 47.31
Std Dev 3.145
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.02
Std Dev 3.369
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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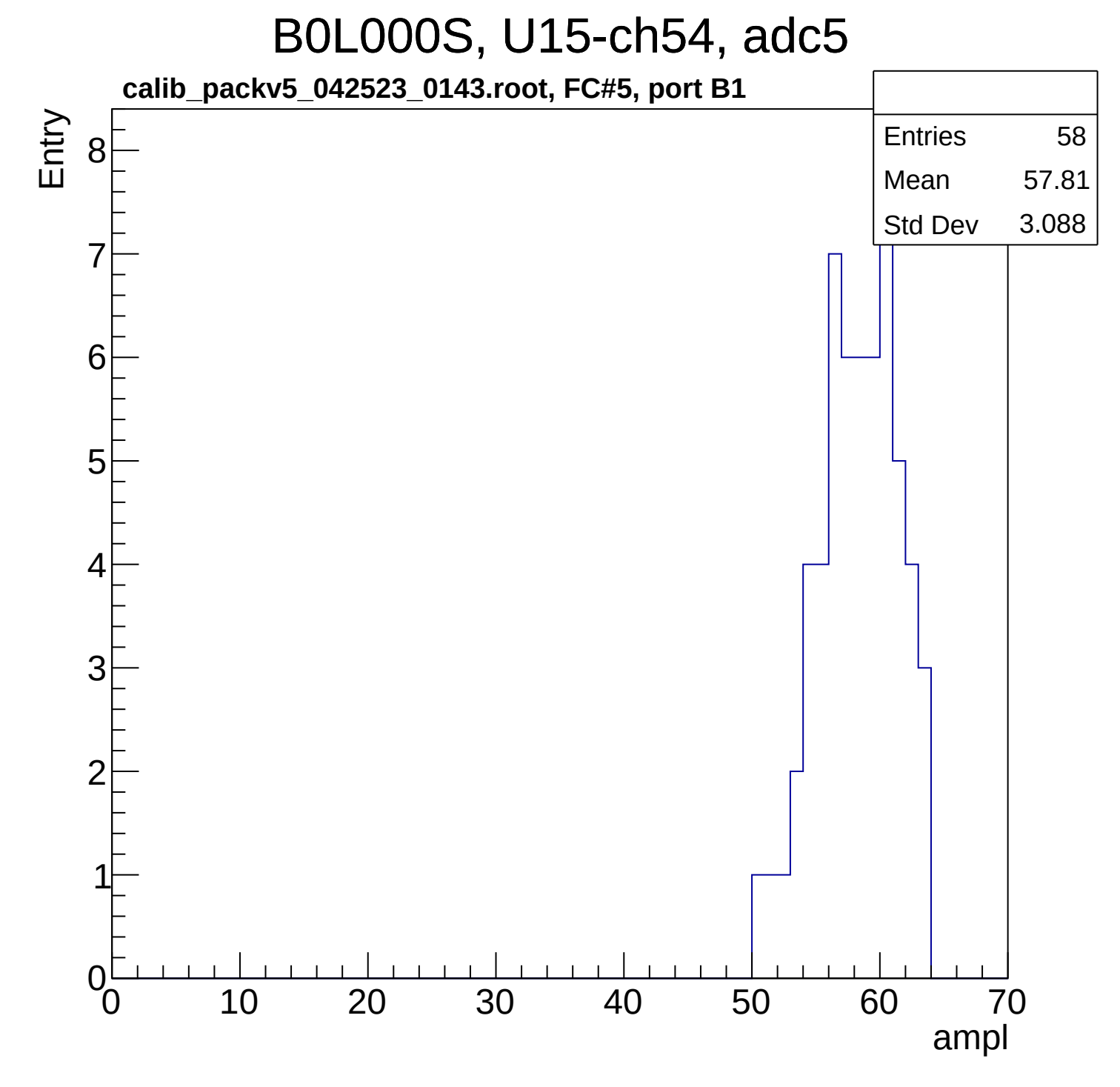
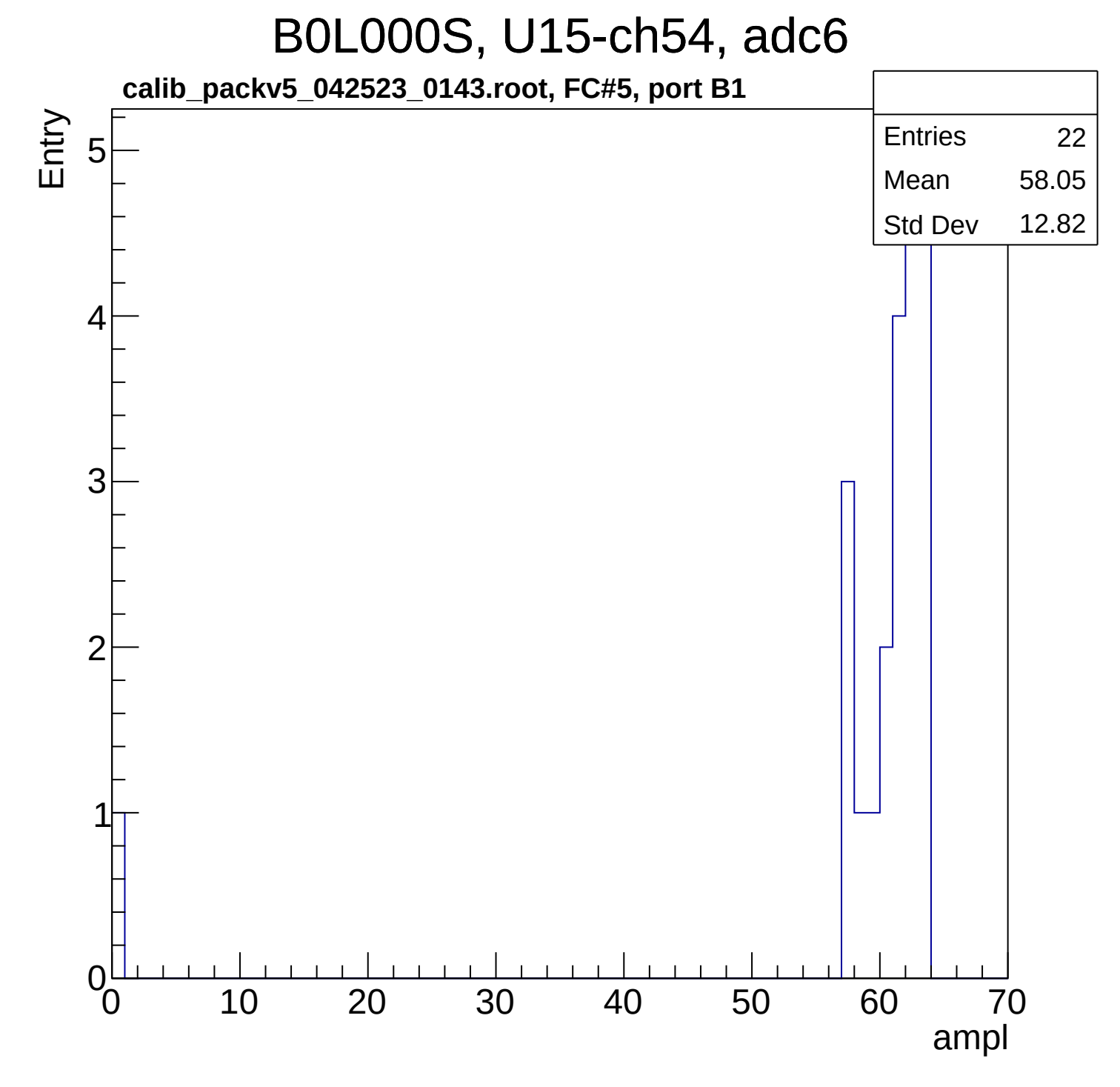
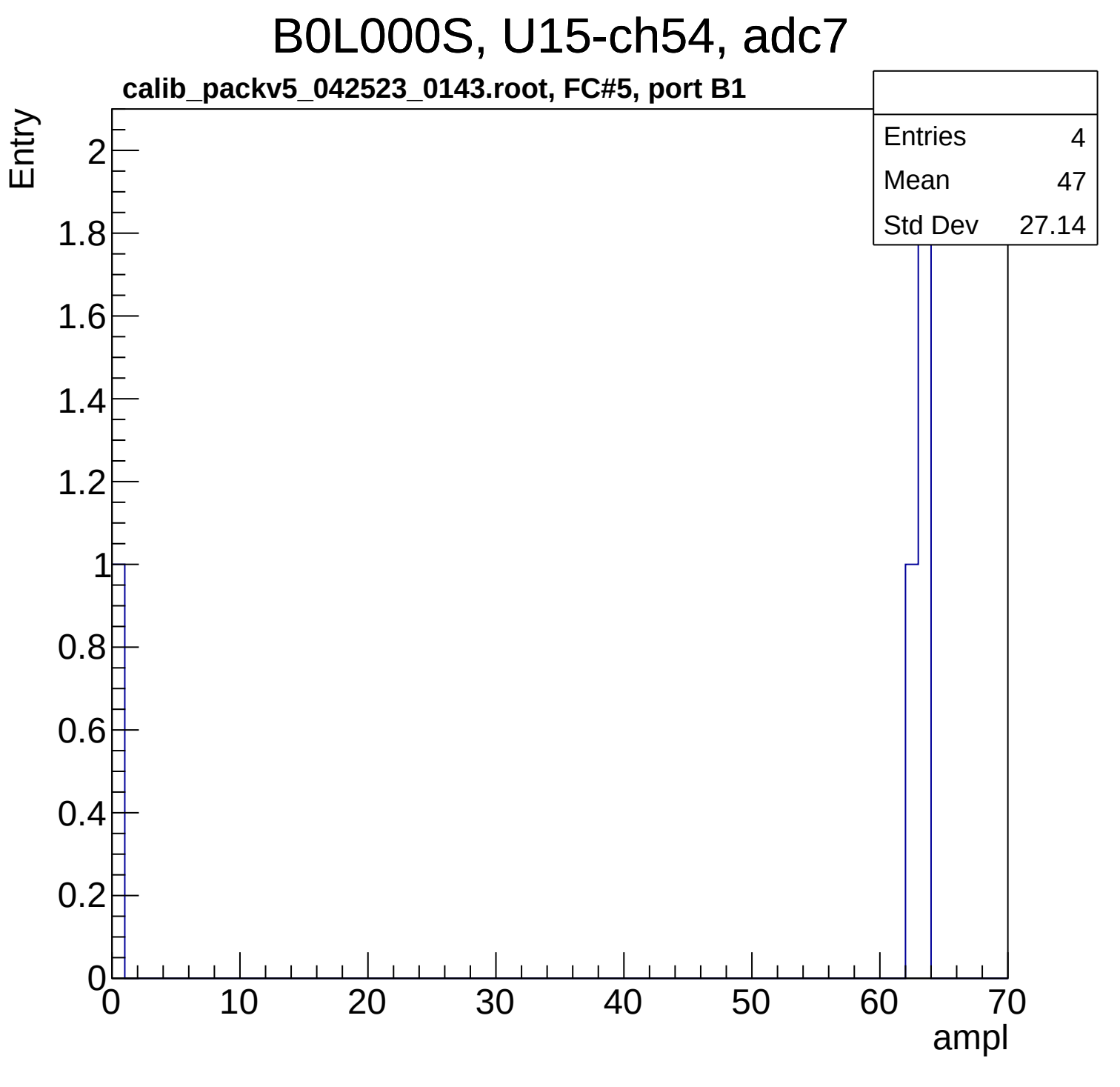

B0L000S, U15-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.81
Std Dev 3.088
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.05
Std Dev 12.82
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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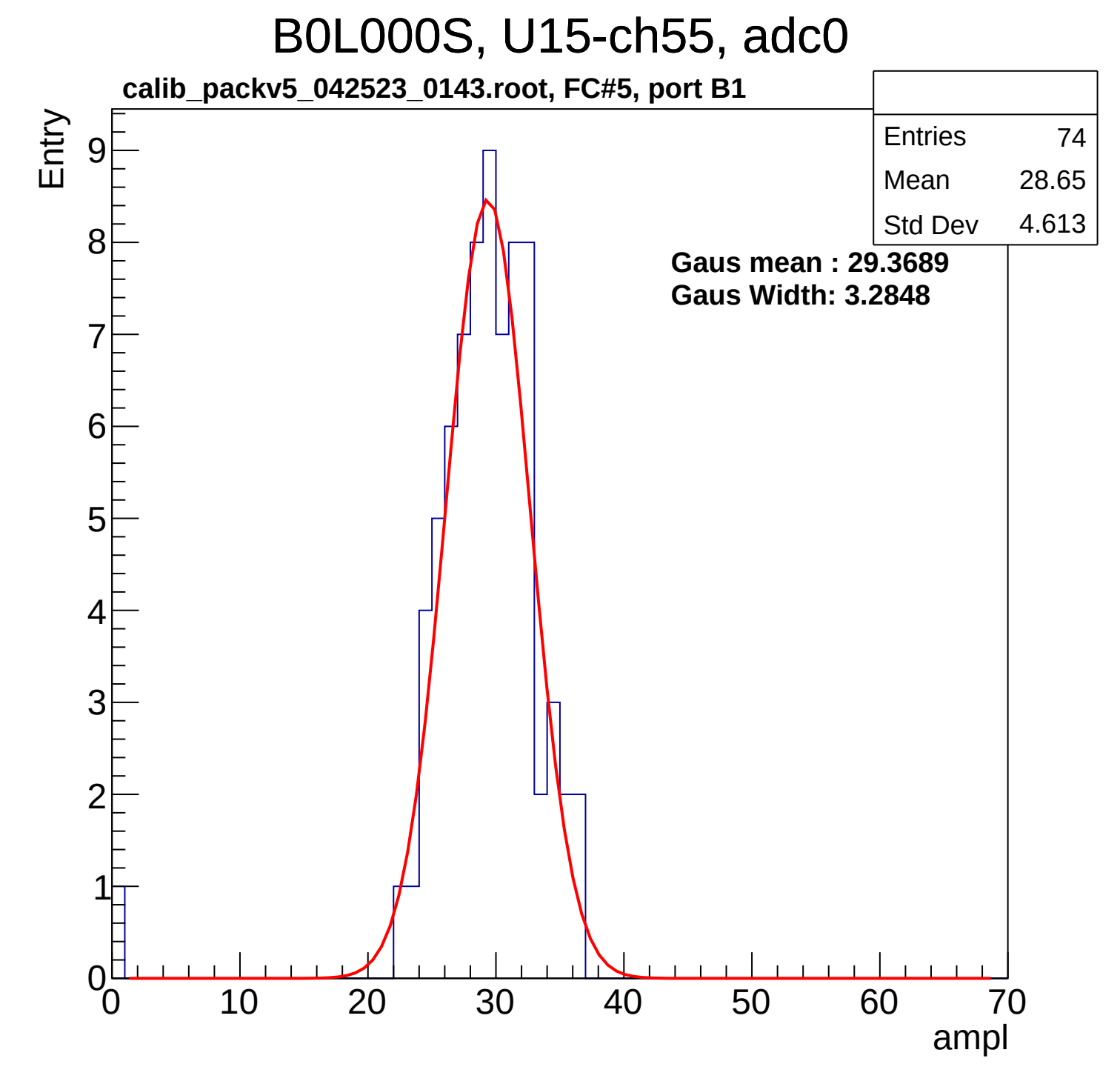
B0L000S, U15-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.65
Std Dev 4.613
Gaus mean : 29.3689
Gaus Width: 3.2848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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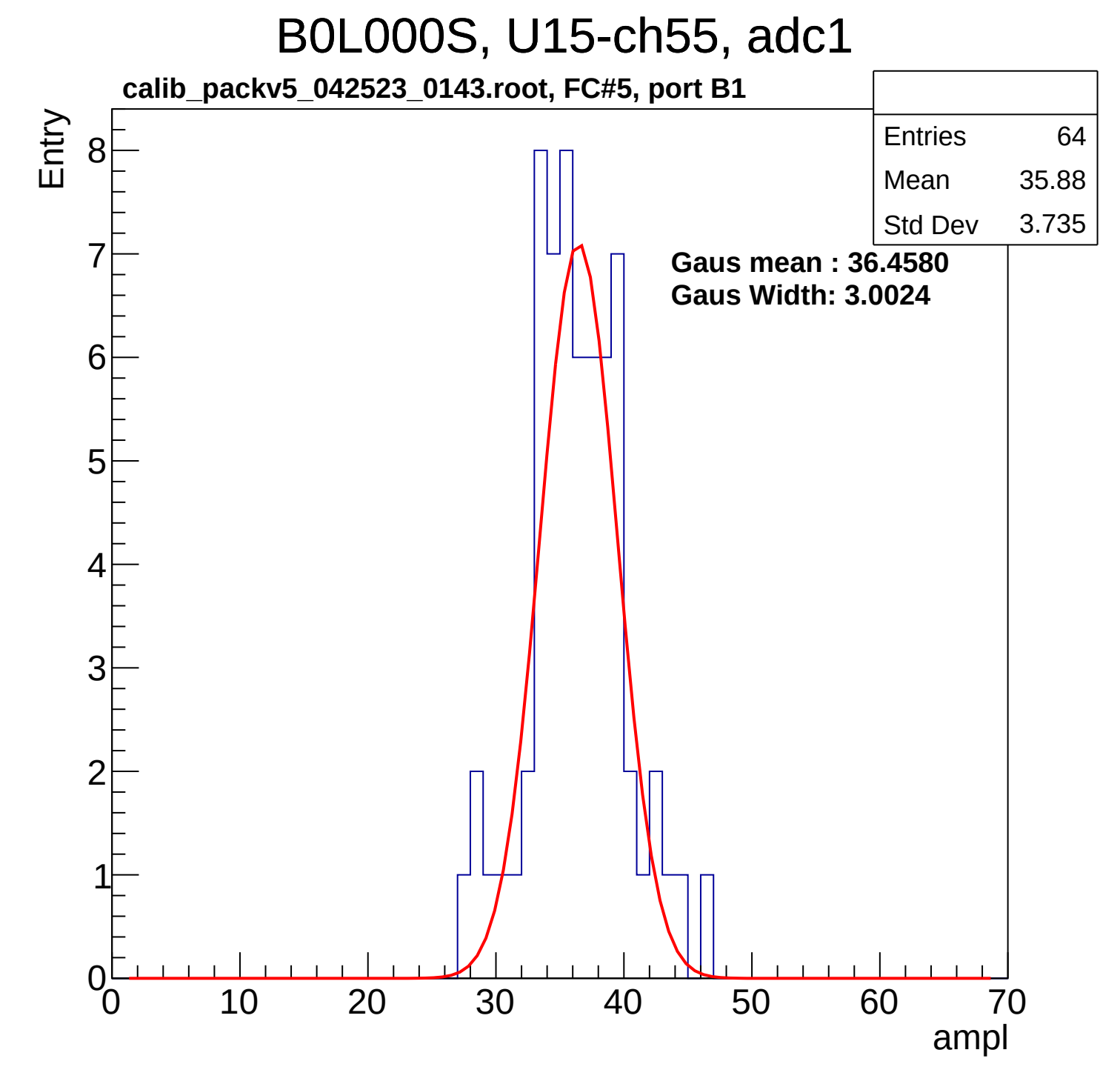
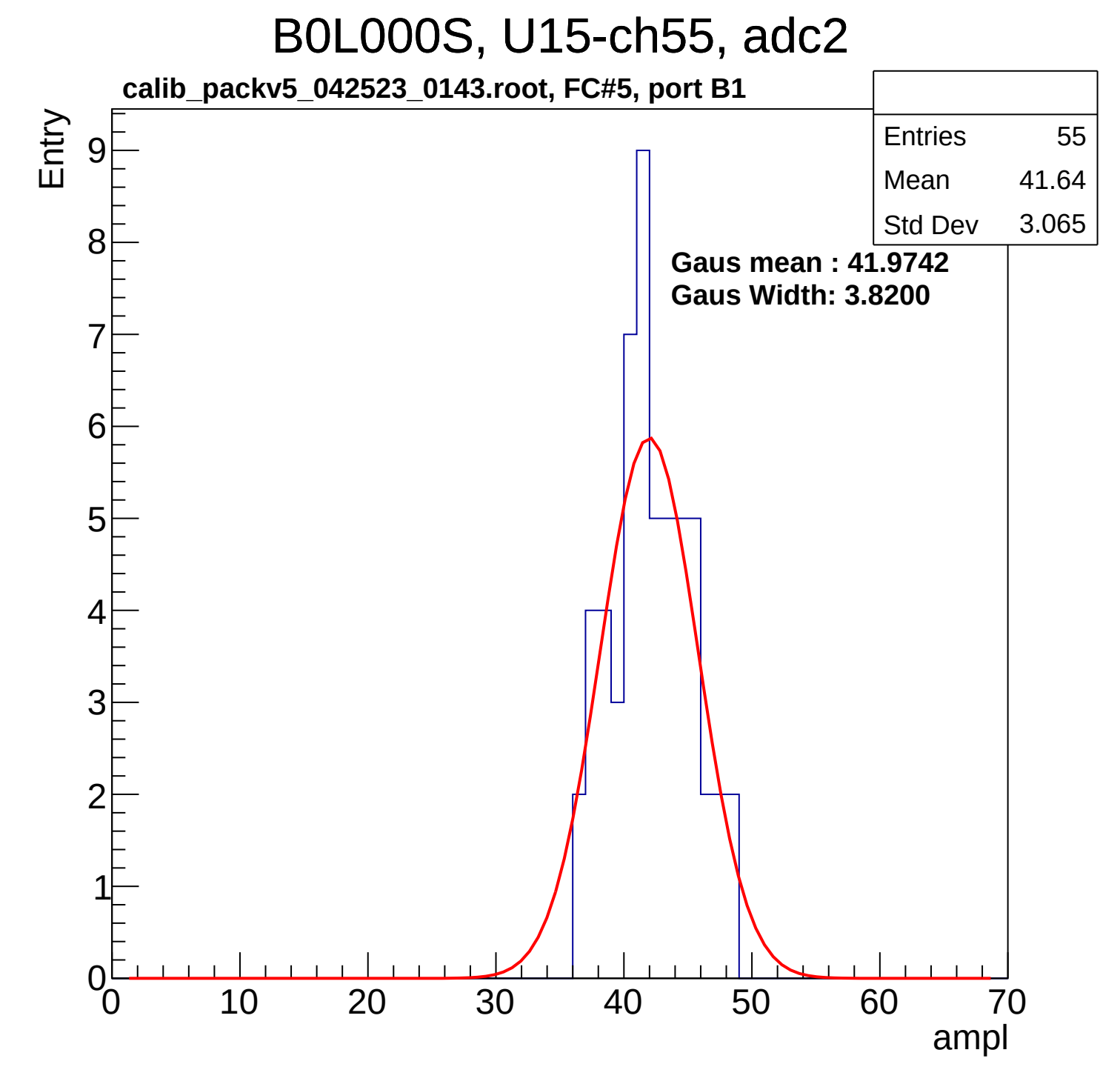
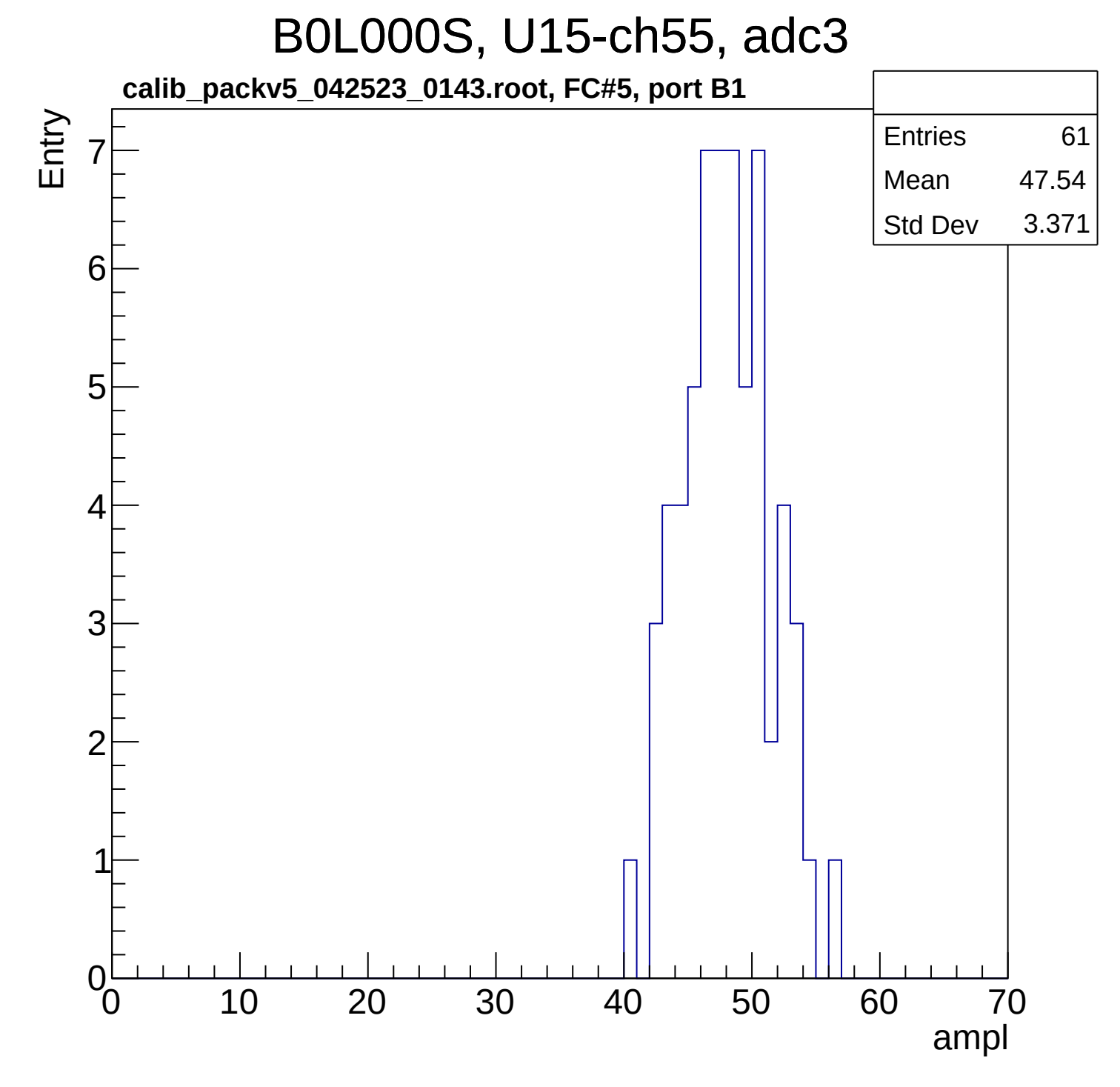
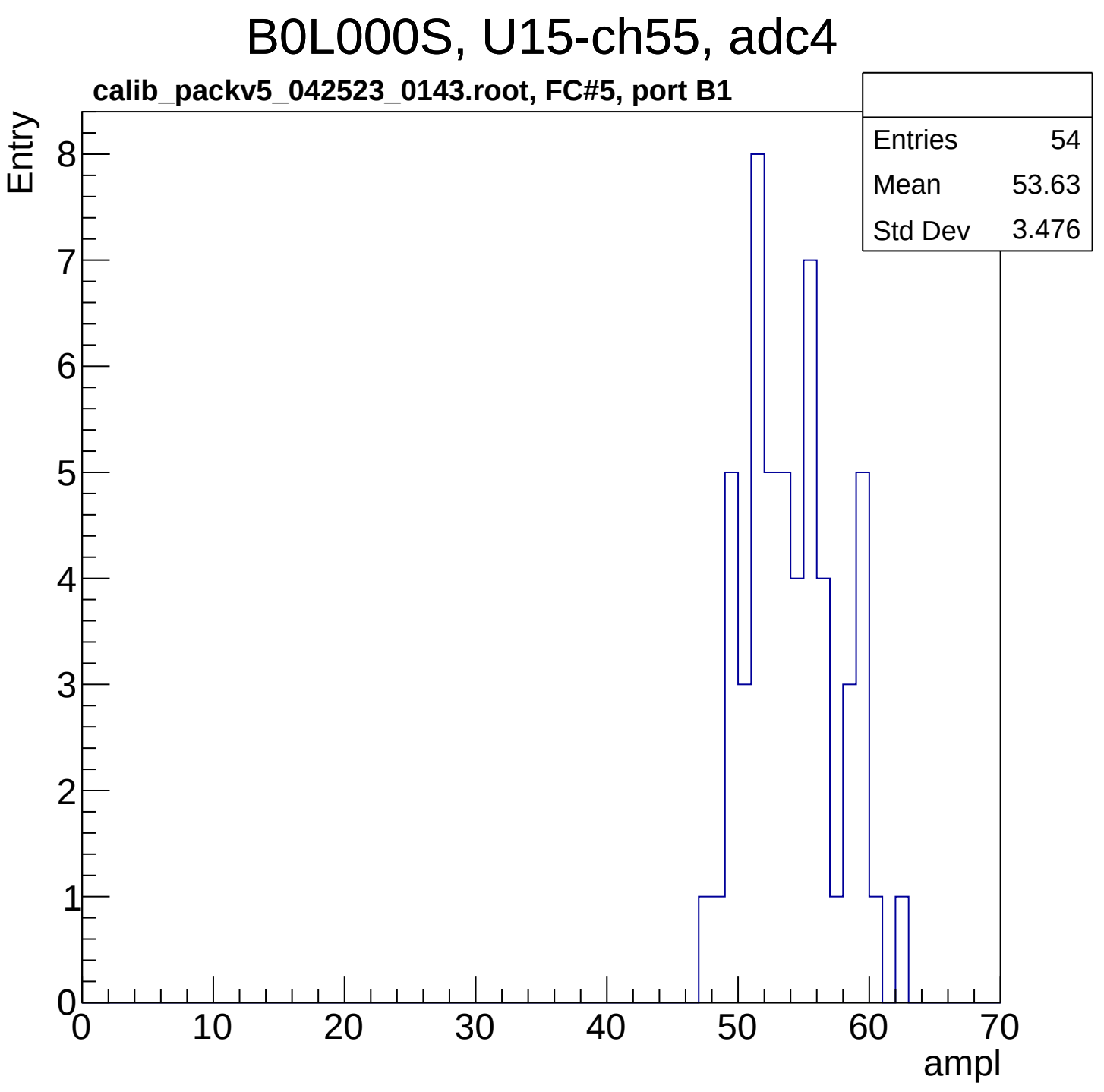

B0L000S, U15-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.88
Std Dev 3.735
Gaus mean : 36.4580
Gaus Width: 3.0024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.64
Std Dev 3.065
Gaus mean : 41.9742
Gaus Width: 3.8200
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.54
Std Dev 3.371
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.63
Std Dev 3.476
Entry
ampl
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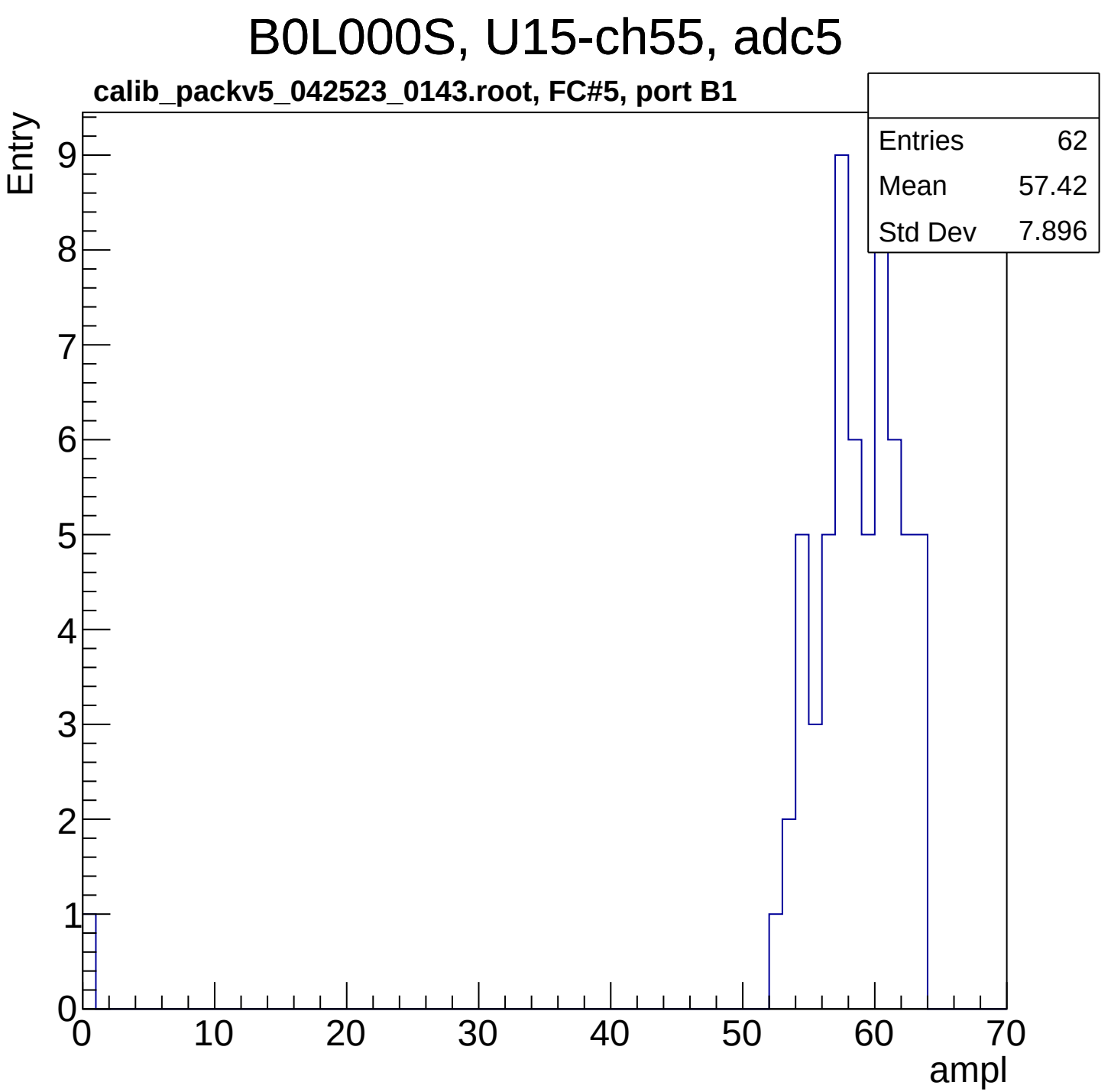
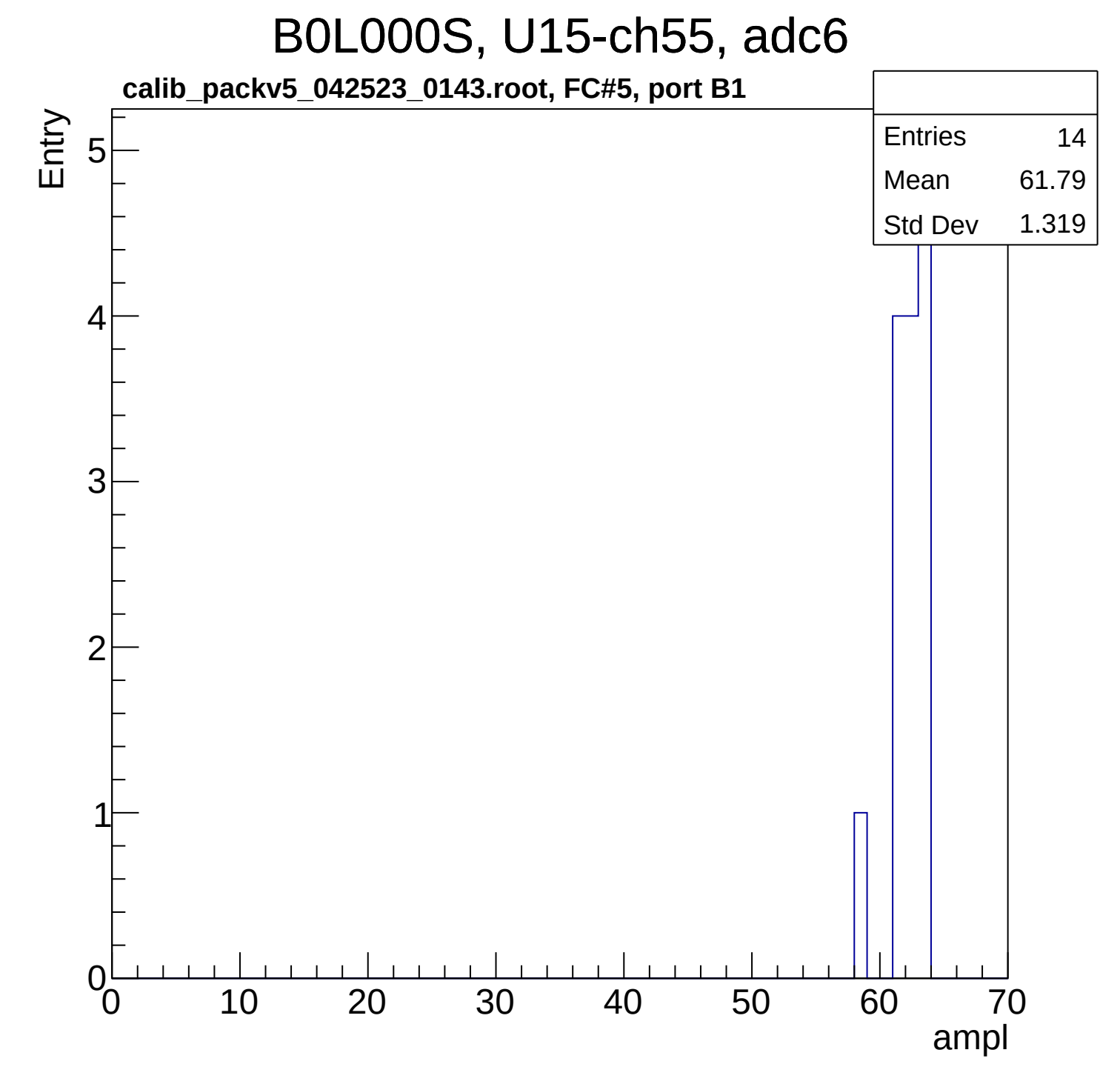
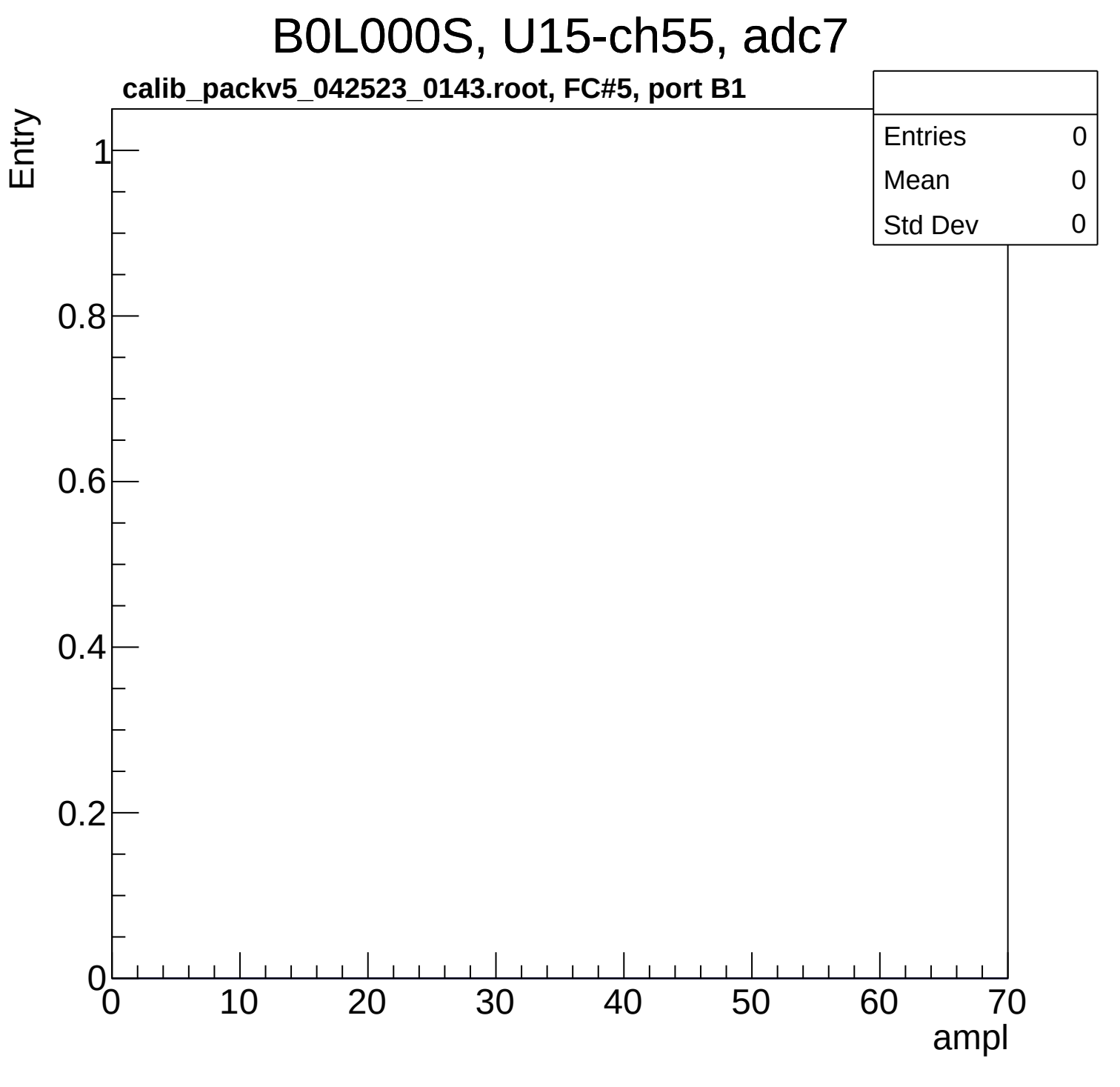
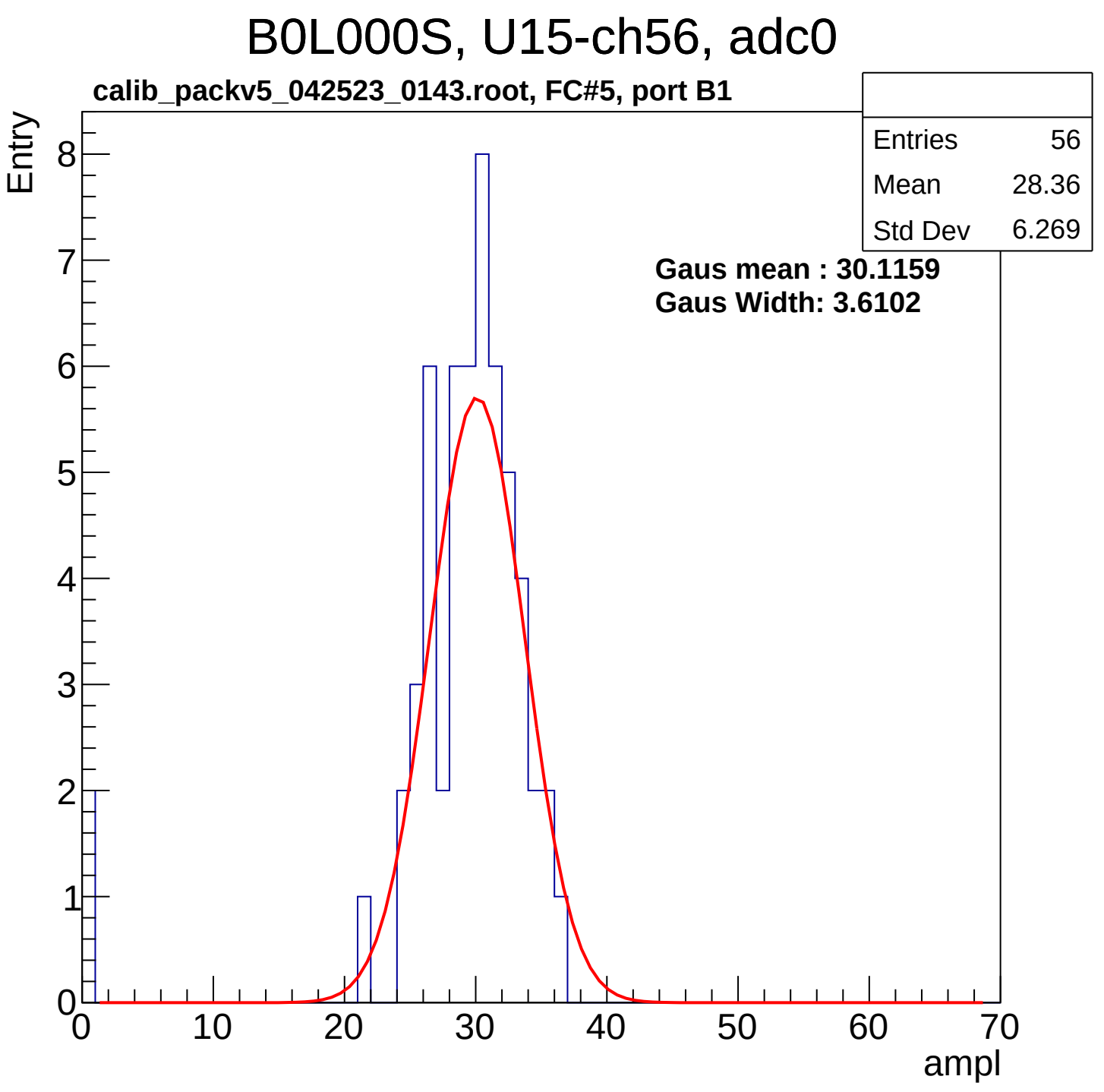
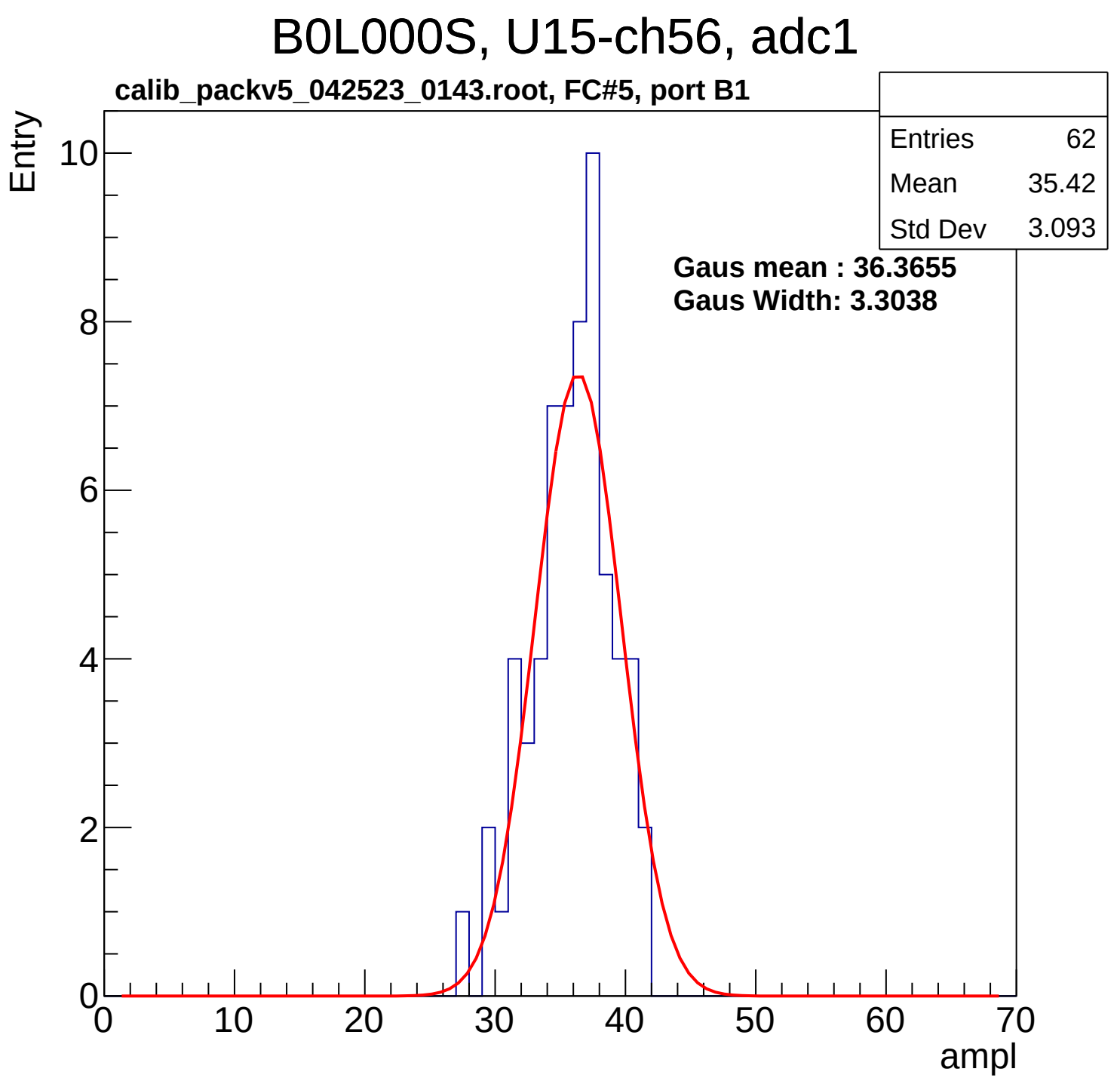
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

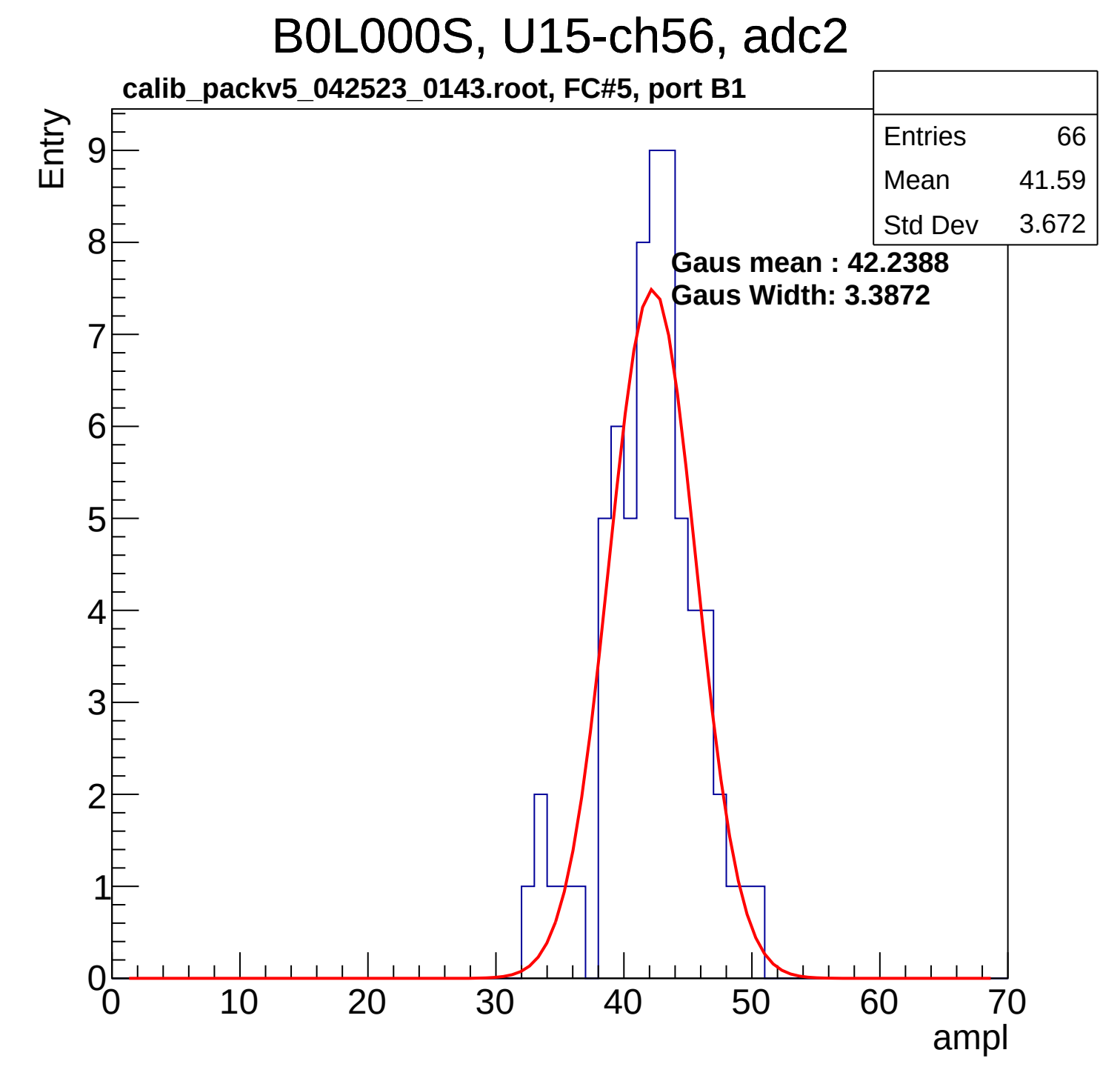

B0L000S, U15-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.42
Std Dev 7.896
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

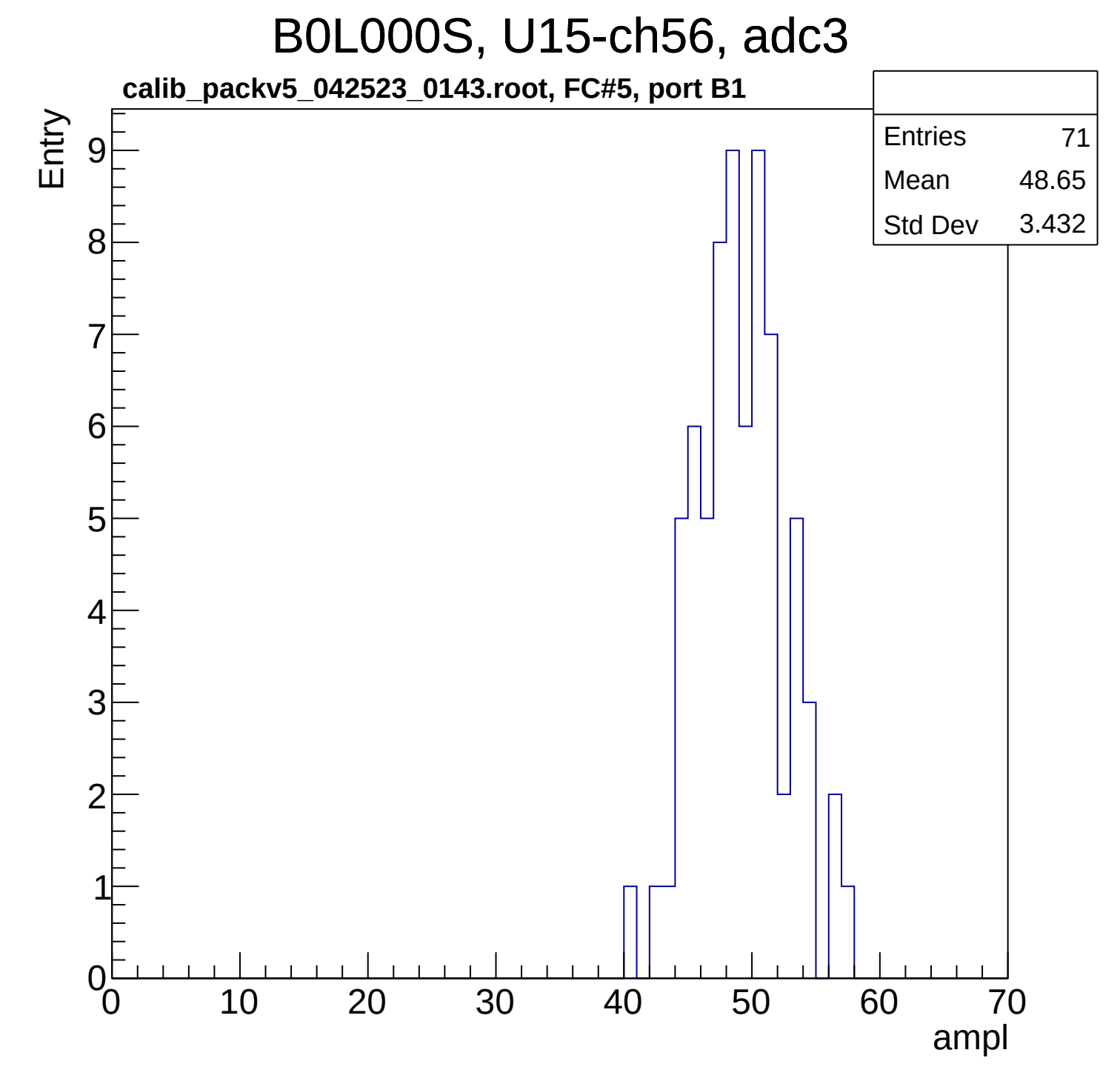

B0L000S, U15-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.79
Std Dev 1.319
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U15-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 28.36
Std Dev 6.269
Gaus mean : 30.1159
Gaus Width: 3.6102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.42
Std Dev 3.093
Gaus mean : 36.3655
Gaus Width: 3.3038
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.59
Std Dev 3.672
Gaus mean : 42.2388
Gaus Width: 3.3872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.65
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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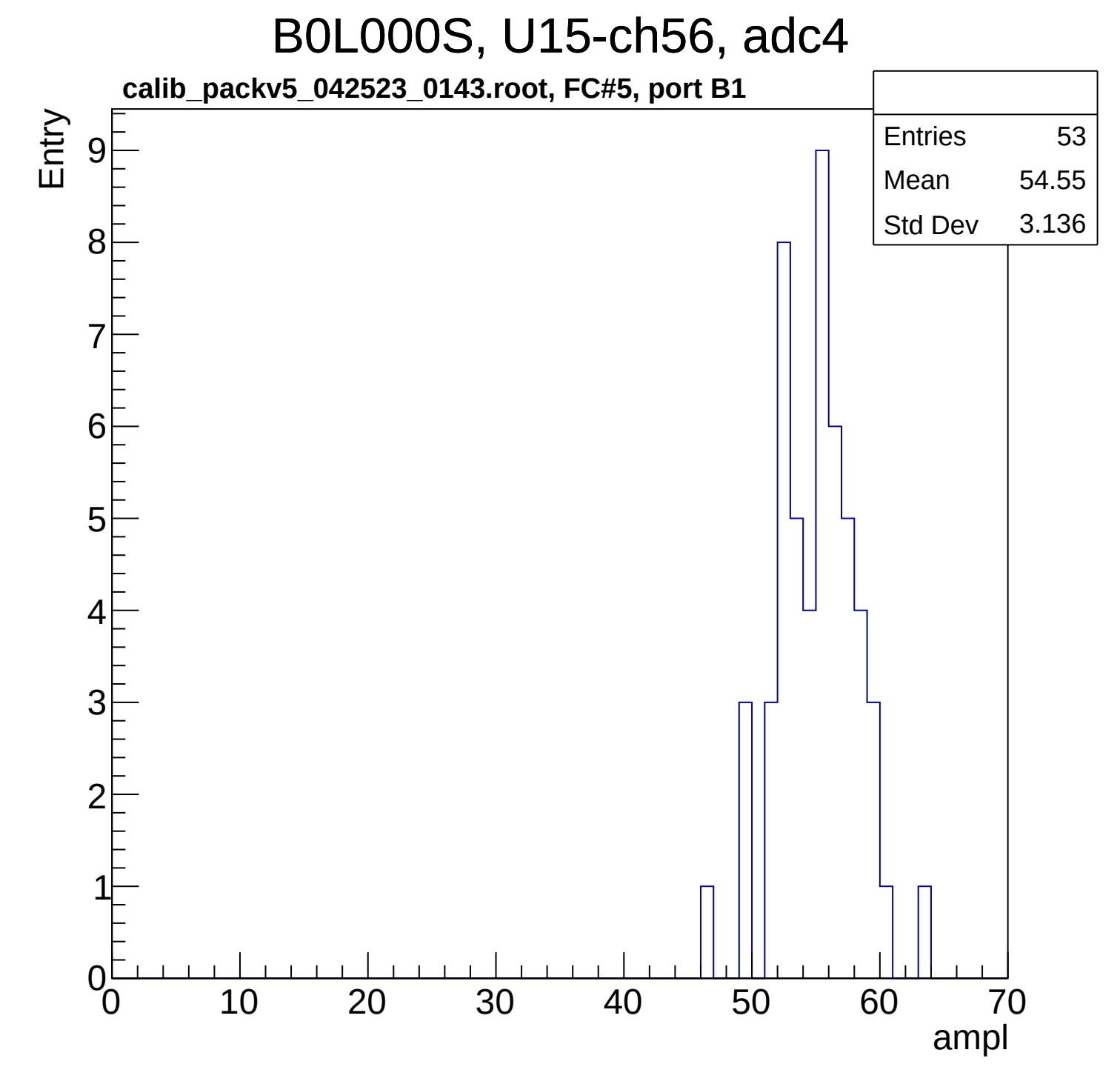
B0L000S, U15-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.55
Std Dev 3.136
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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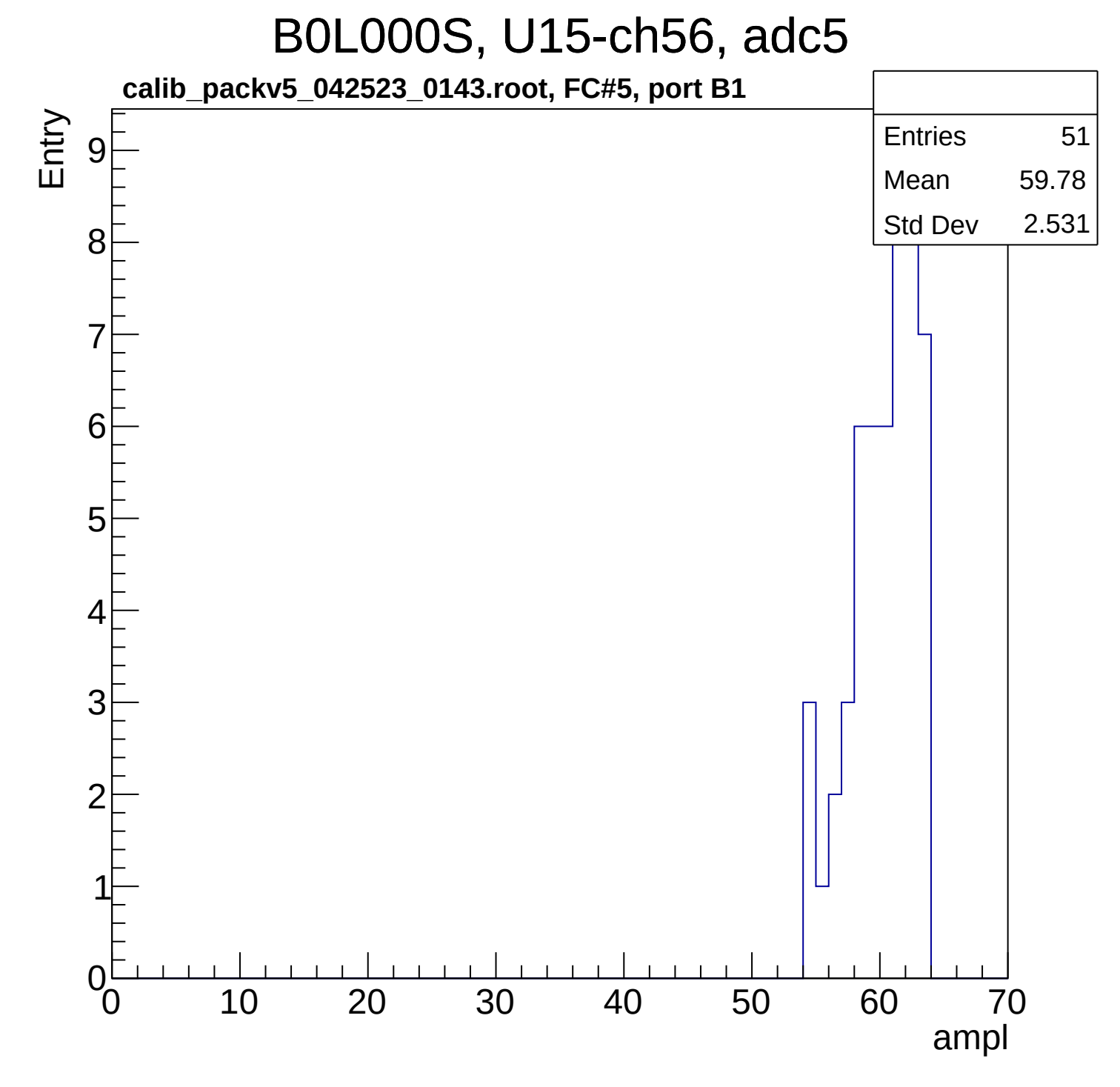
B0L000S, U15-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.78
Std Dev 2.531
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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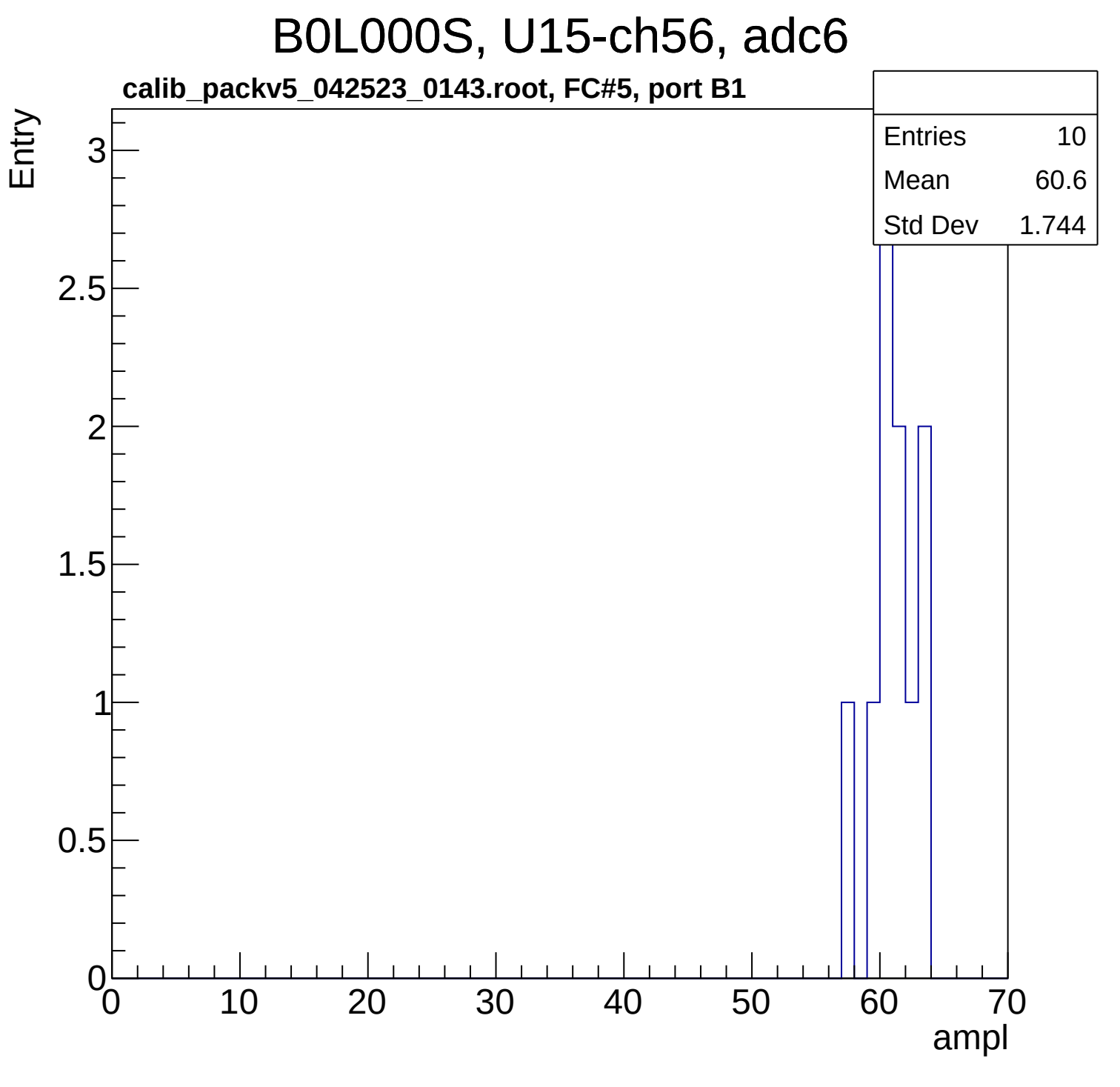
B0L000S, U15-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 60.6
Std Dev 1.744
Entry
ampl
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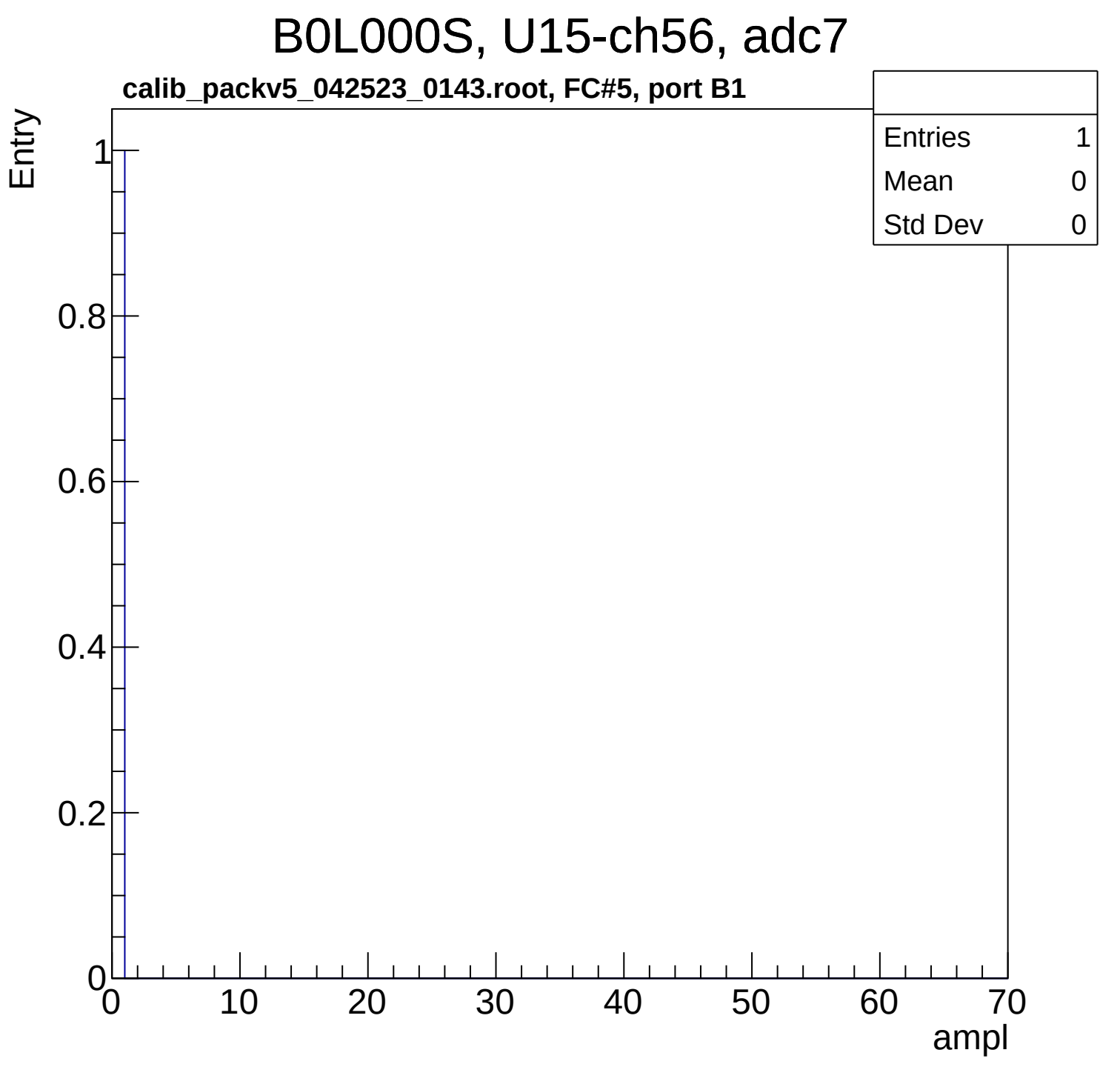
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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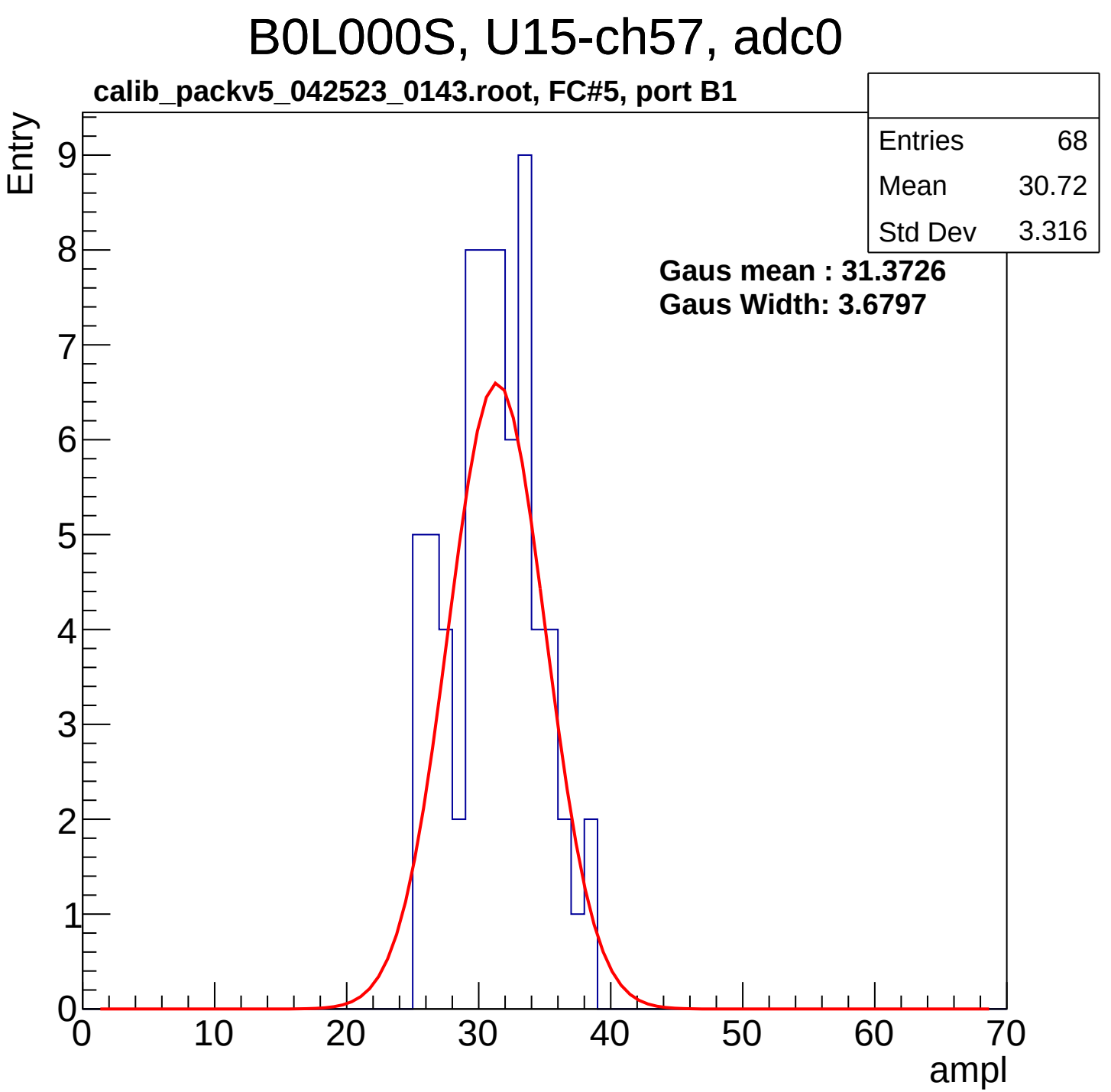
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.72
Std Dev 3.316
Gaus mean : 31.3726
Gaus Width: 3.6797
Entry
ampl
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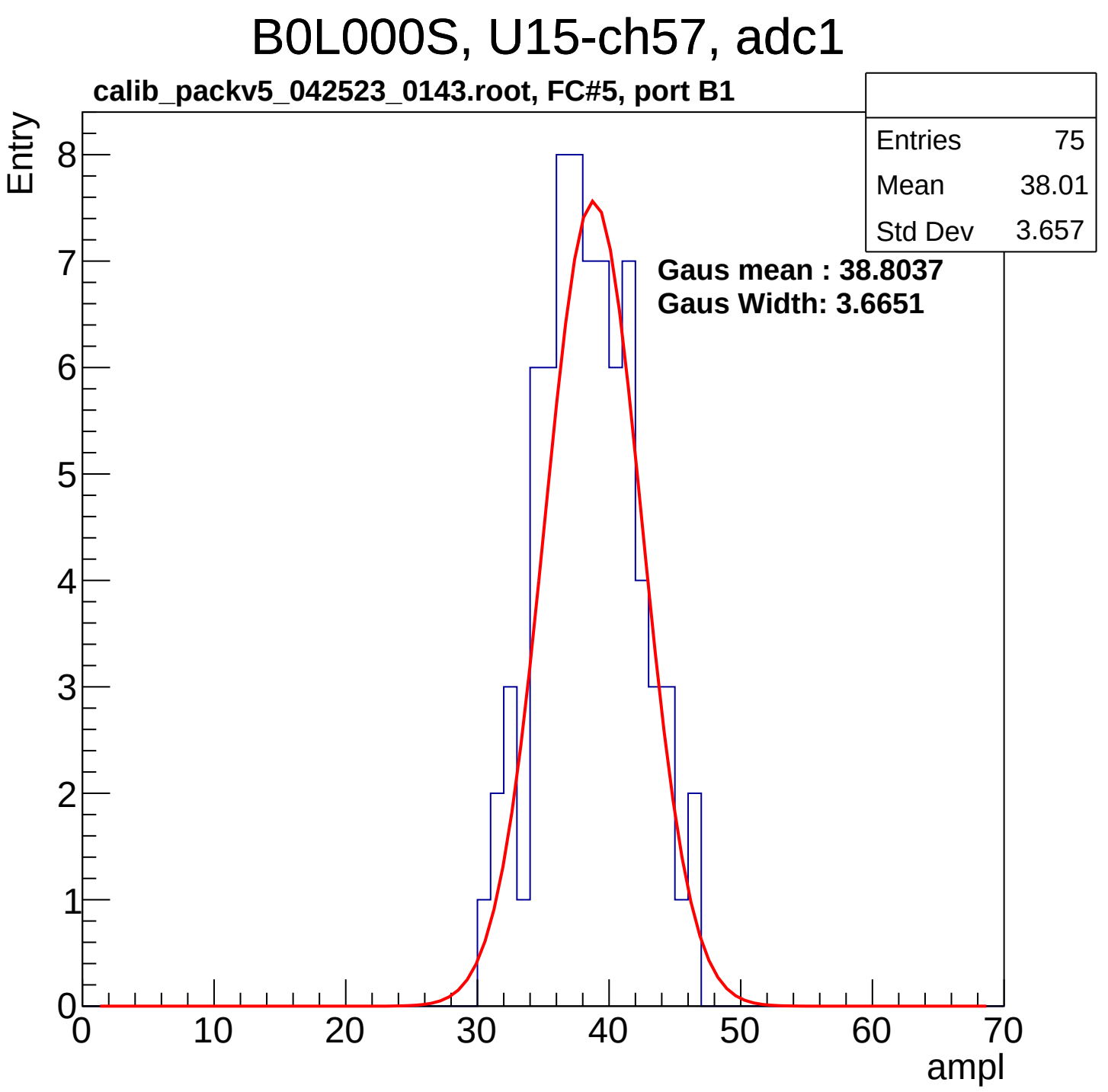
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 38.01
Std Dev 3.657
Gaus mean : 38.8037
Gaus Width: 3.6651
Entry
ampl
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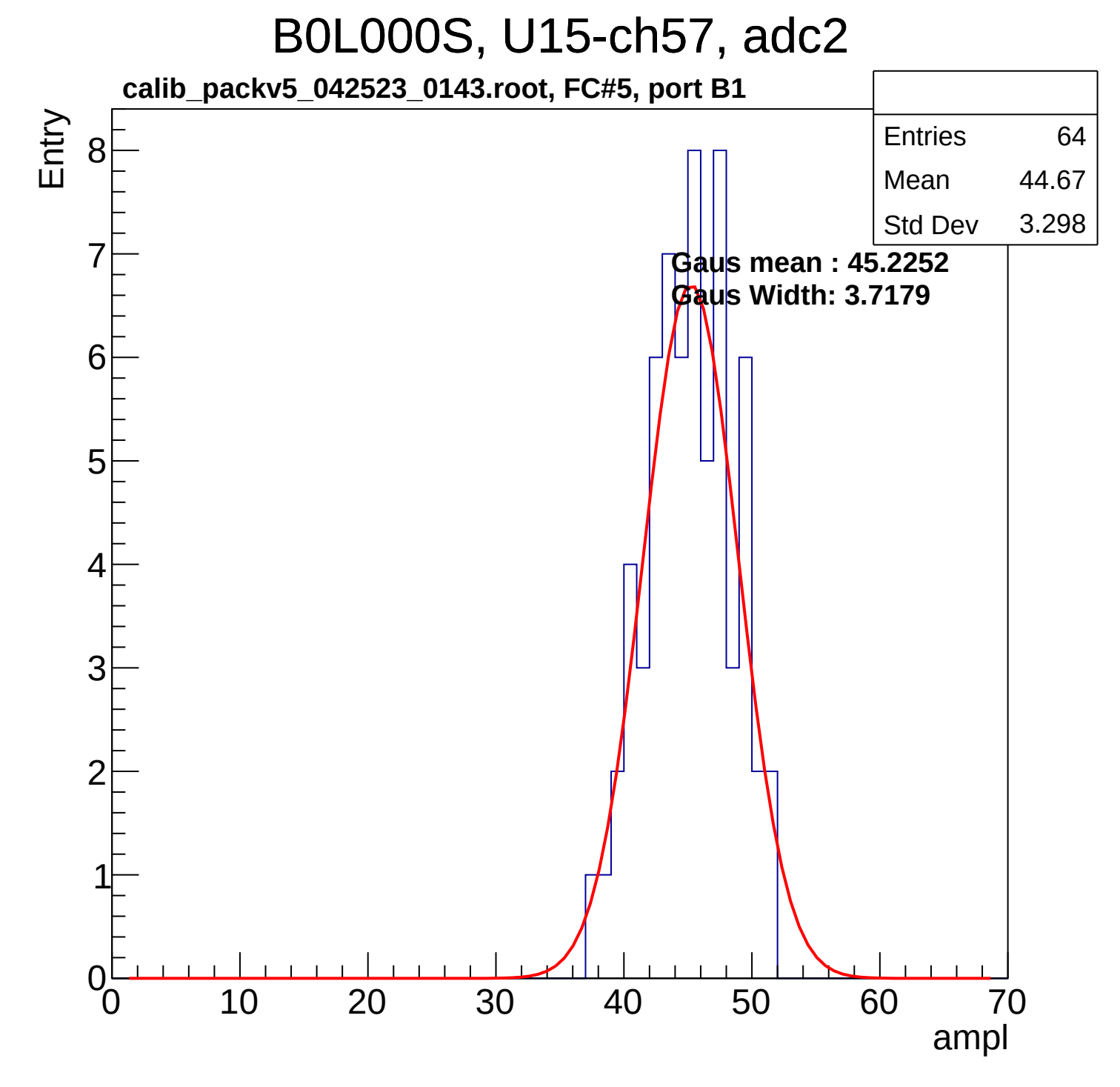
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.67
Std Dev 3.298
Gaus mean : 45.2252
Gaus Width: 3.7179
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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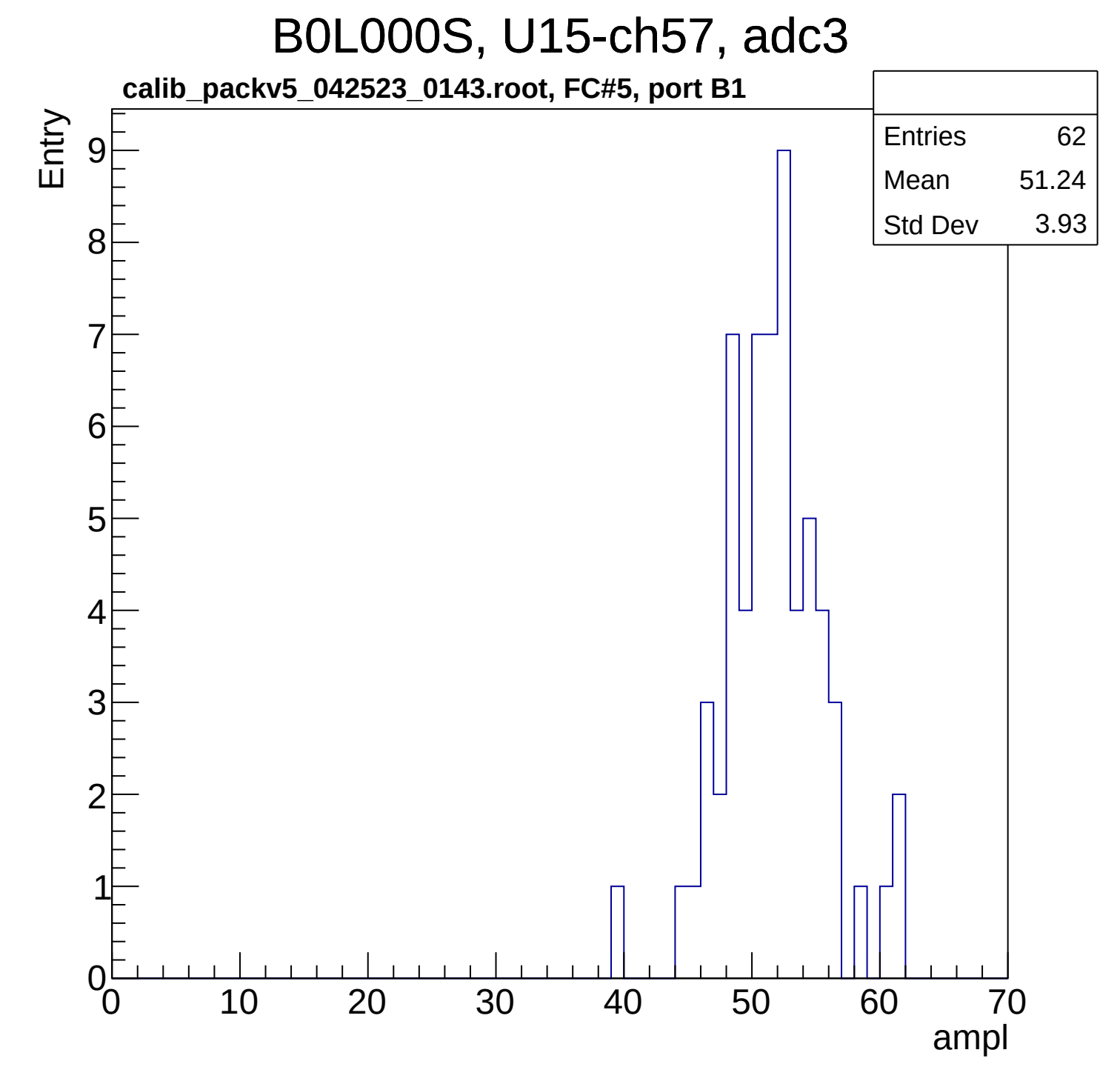
B0L000S, U15-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.24
Std Dev 3.93
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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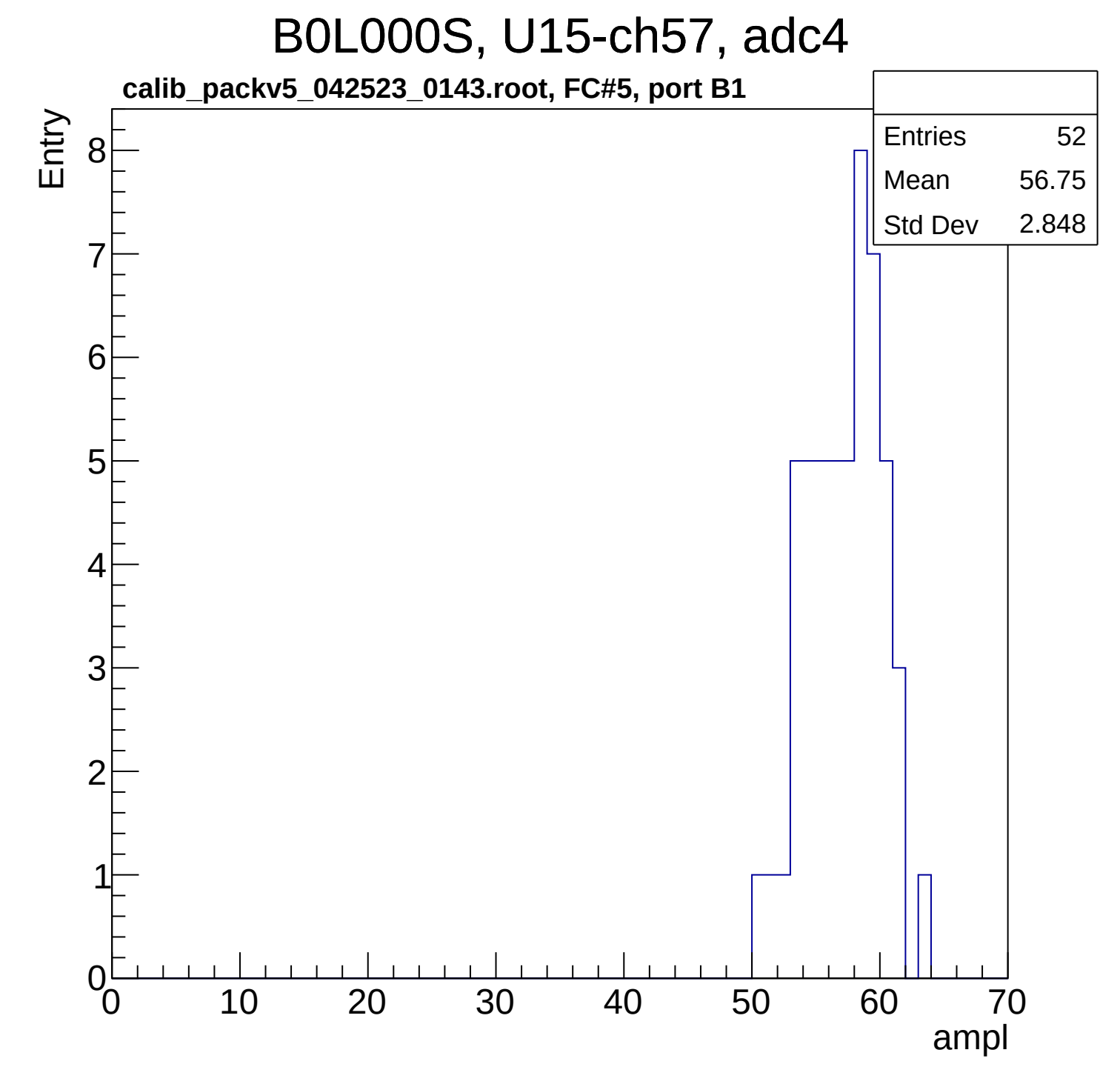
B0L000S, U15-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.75
Std Dev 2.848
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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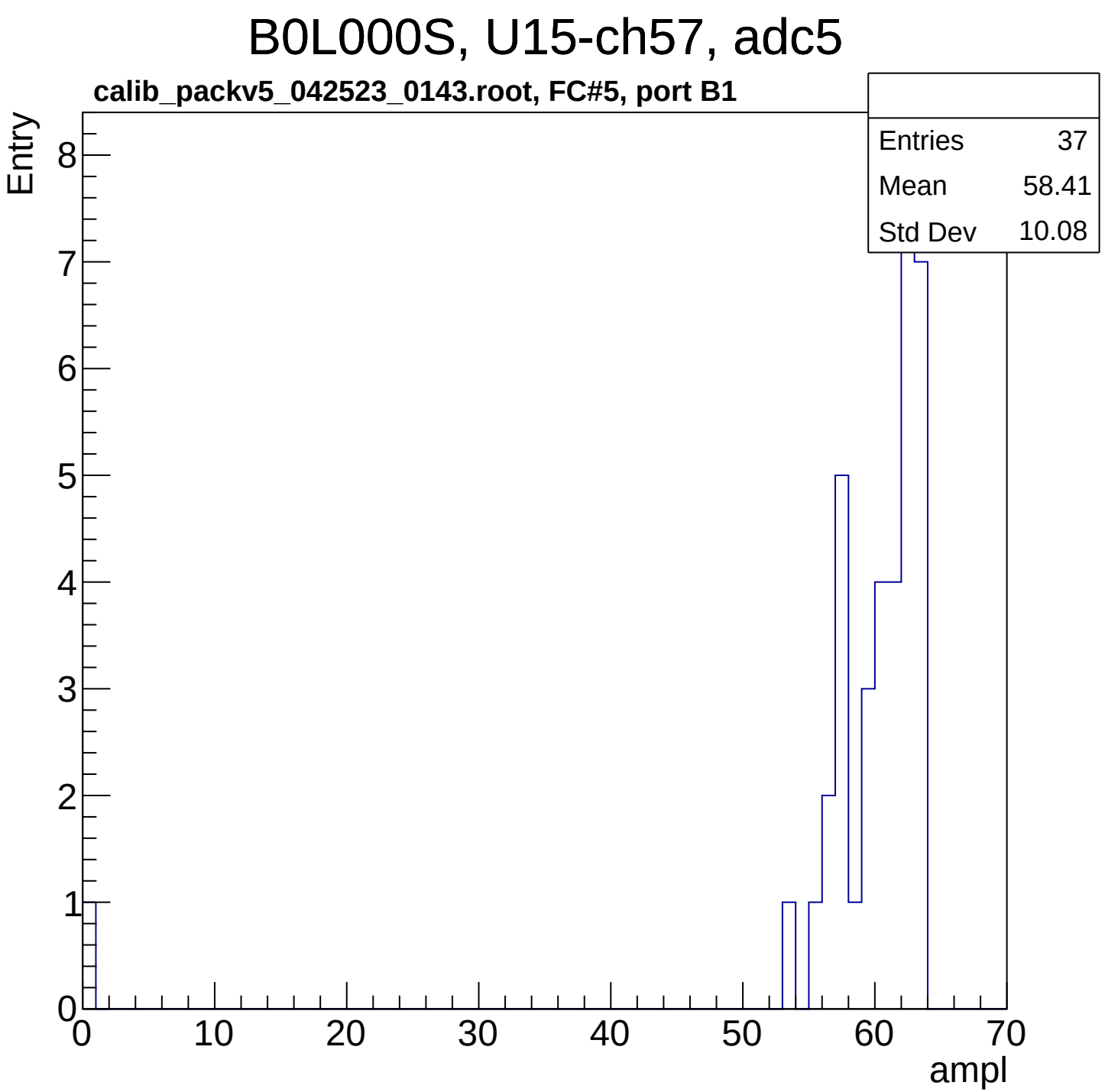
B0L000S, U15-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.41
Std Dev 10.08
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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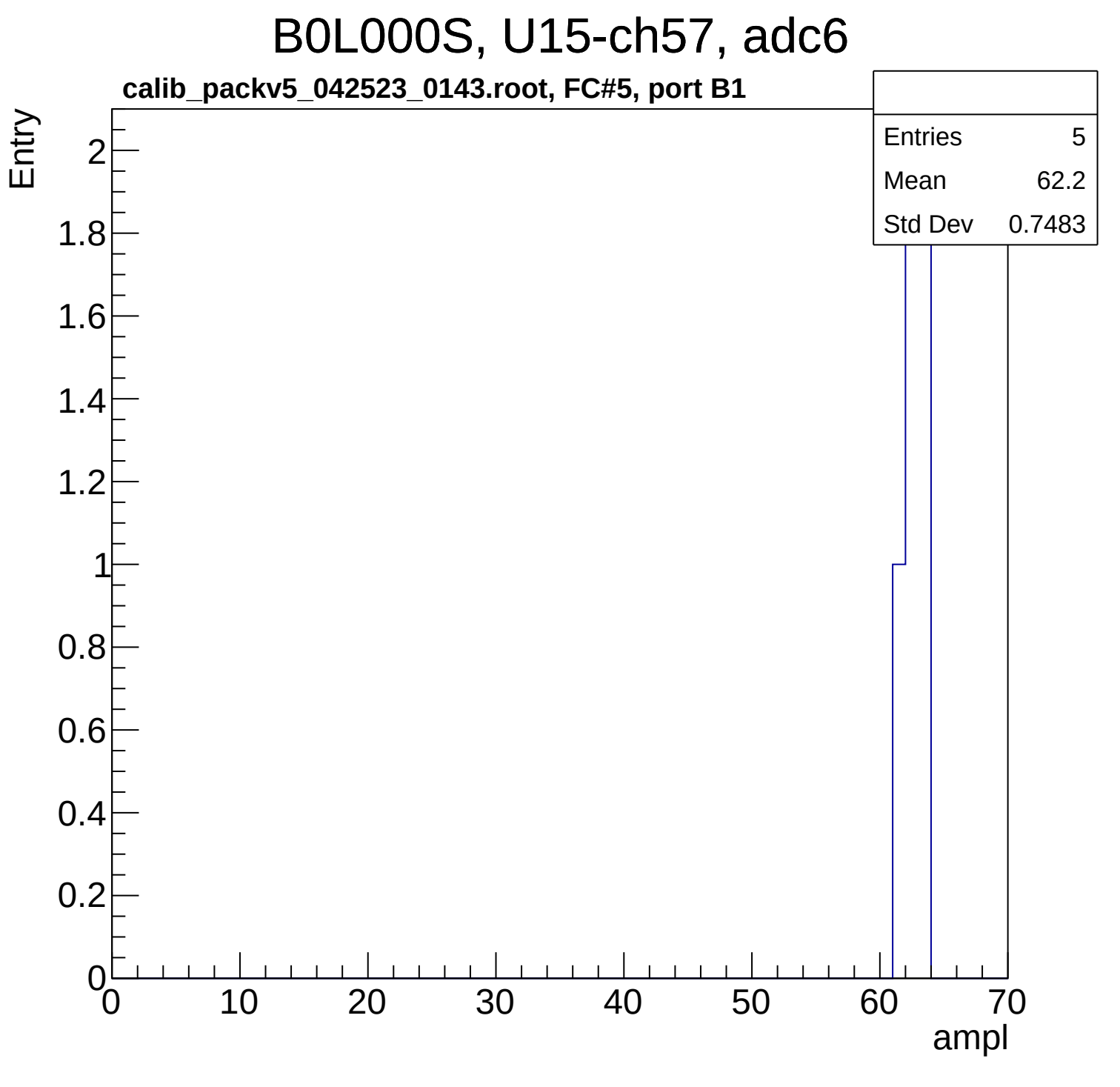
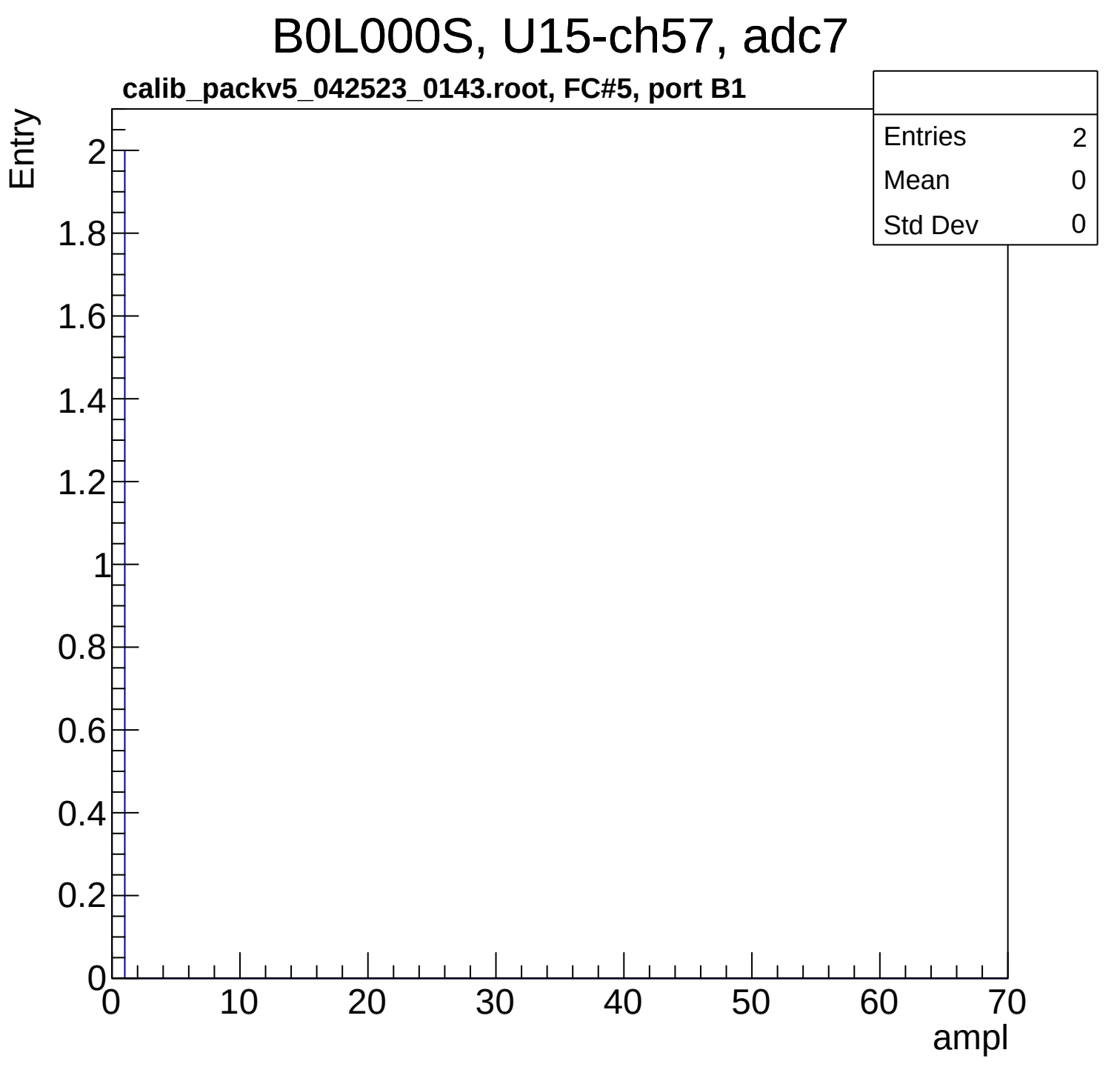

B0L000S, U15-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
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50
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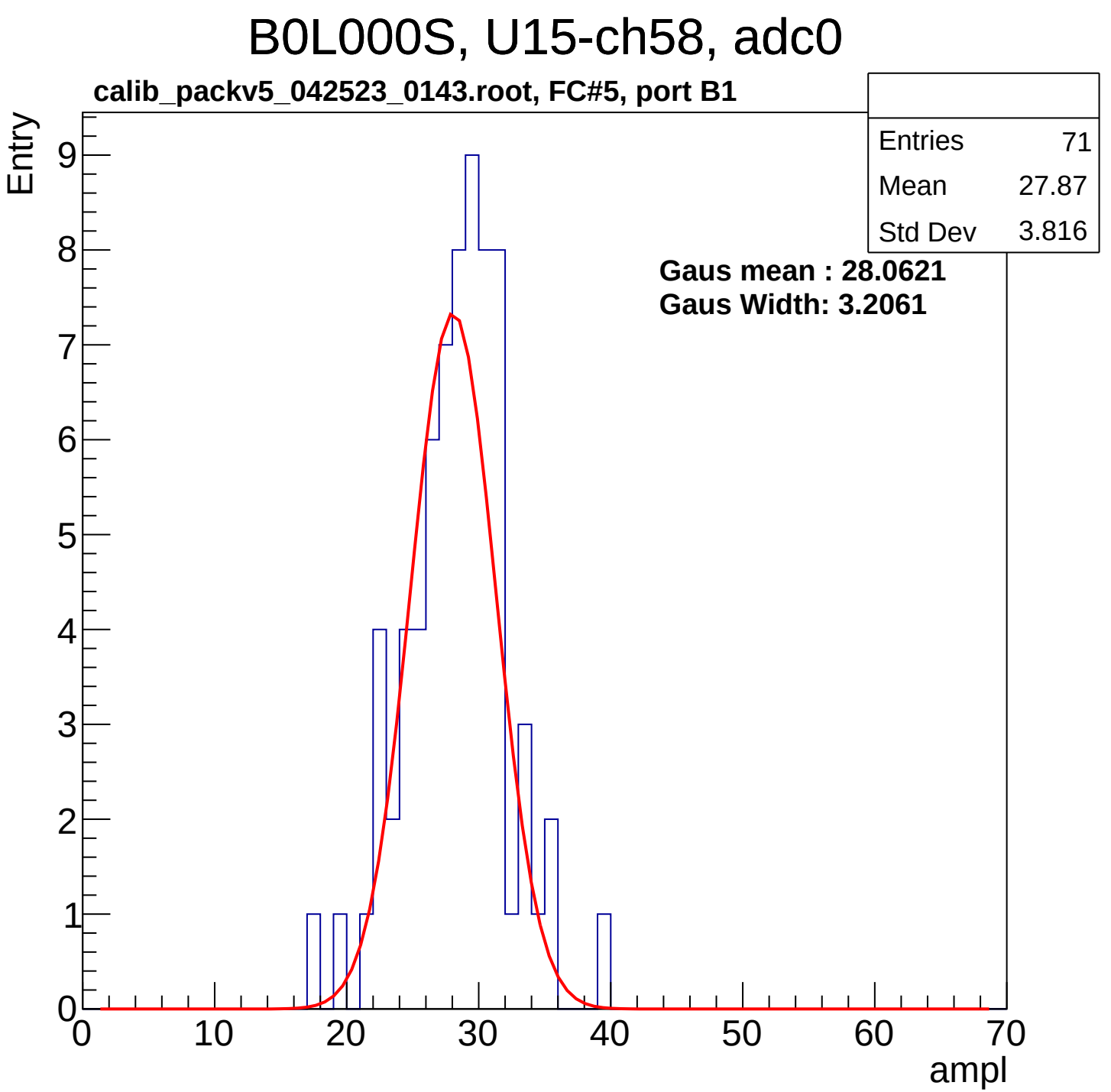
ampl


B0L000S, U15-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.87
Std Dev 3.816
Gaus mean : 28.0621
Gaus Width: 3.2061
Entry
ampl
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3
4
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6
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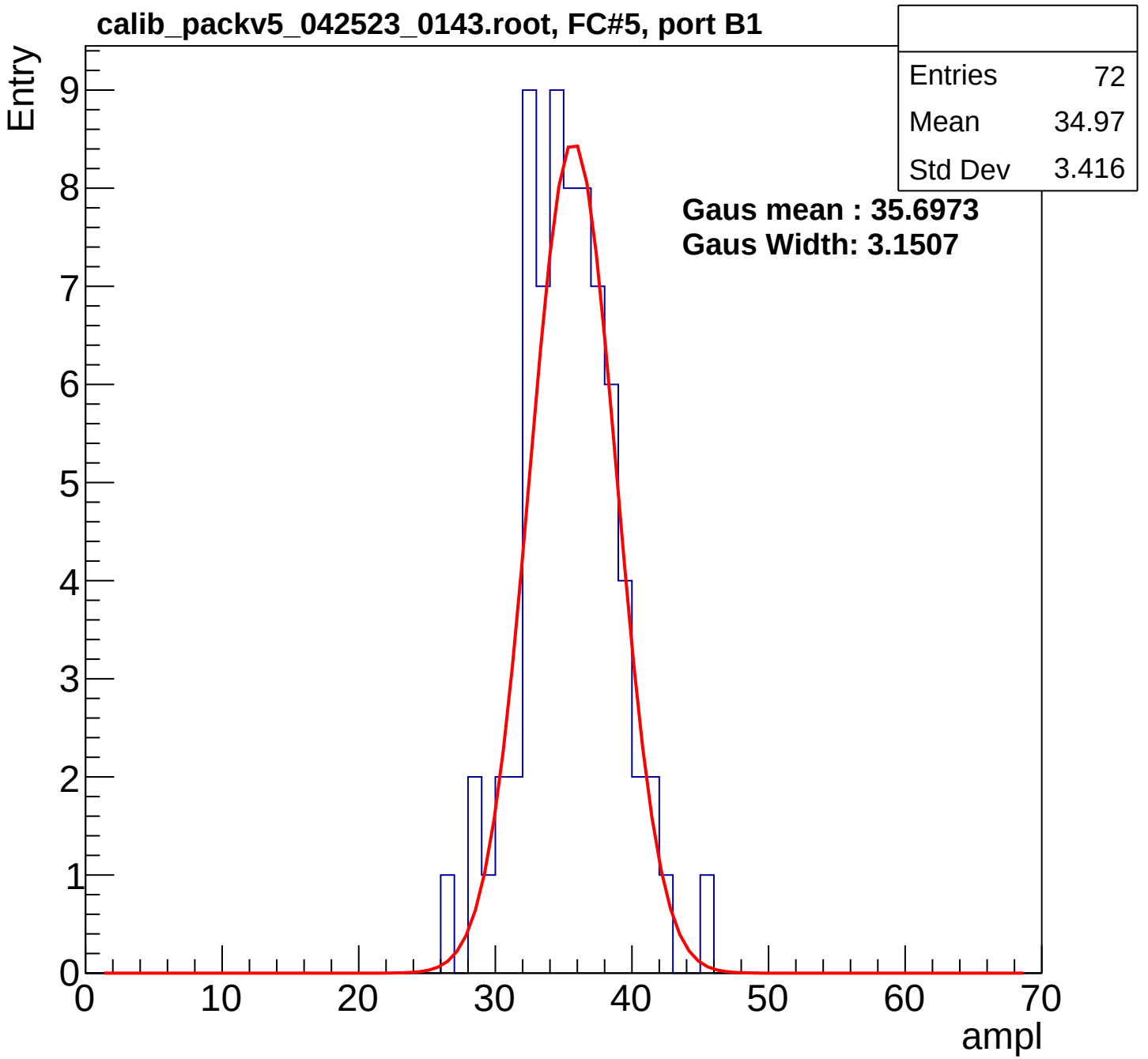
B0L000S, U15-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 34.97
Std Dev 3.416
Gaus mean : 35.6973
Gaus Width: 3.1507
Entry
ampl
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3
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5
6
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0
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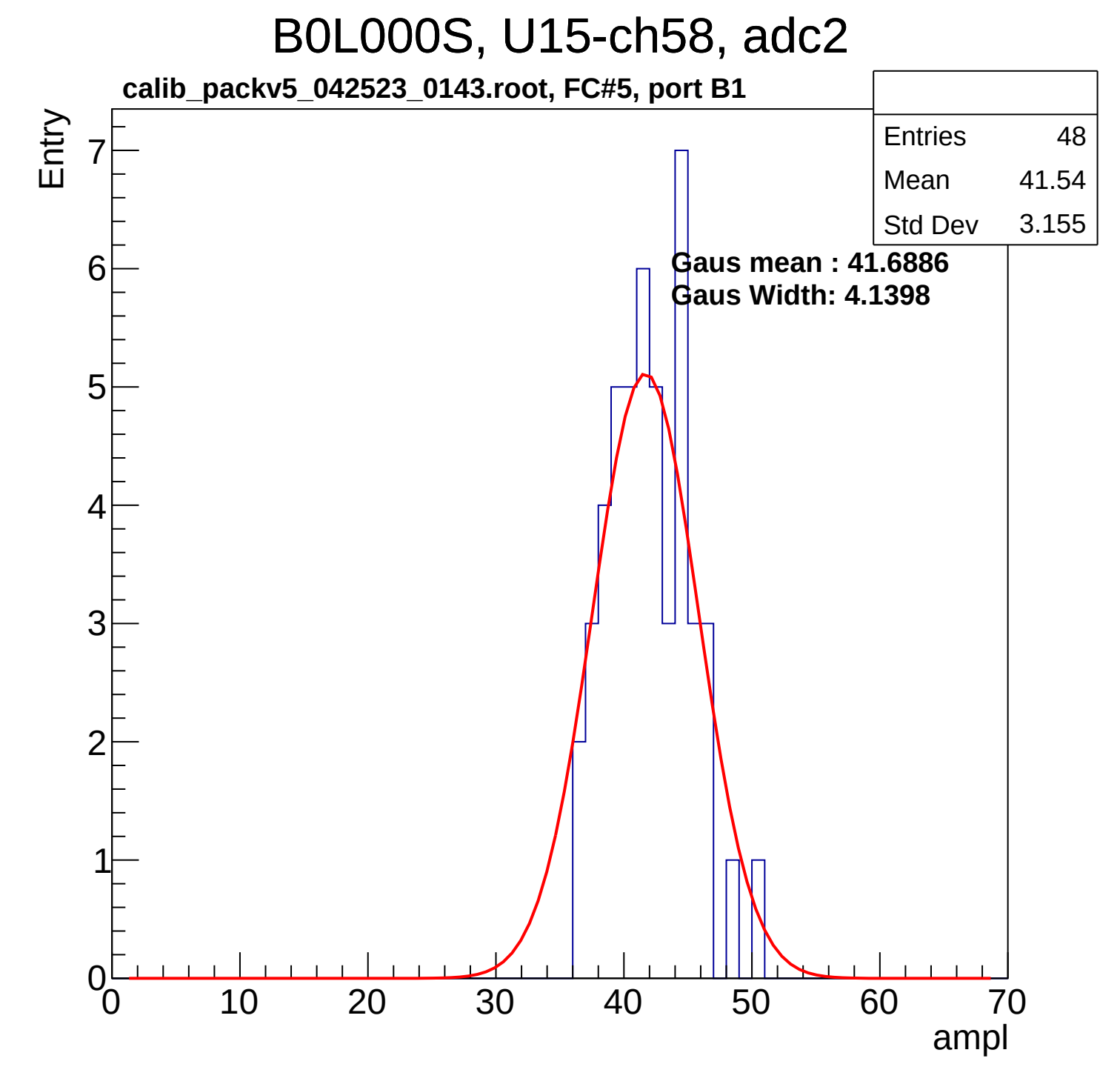
B0L000S, U15-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 41.54
Std Dev 3.155
Gaus mean : 41.6886
Gaus Width: 4.1398
Entry
ampl
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1
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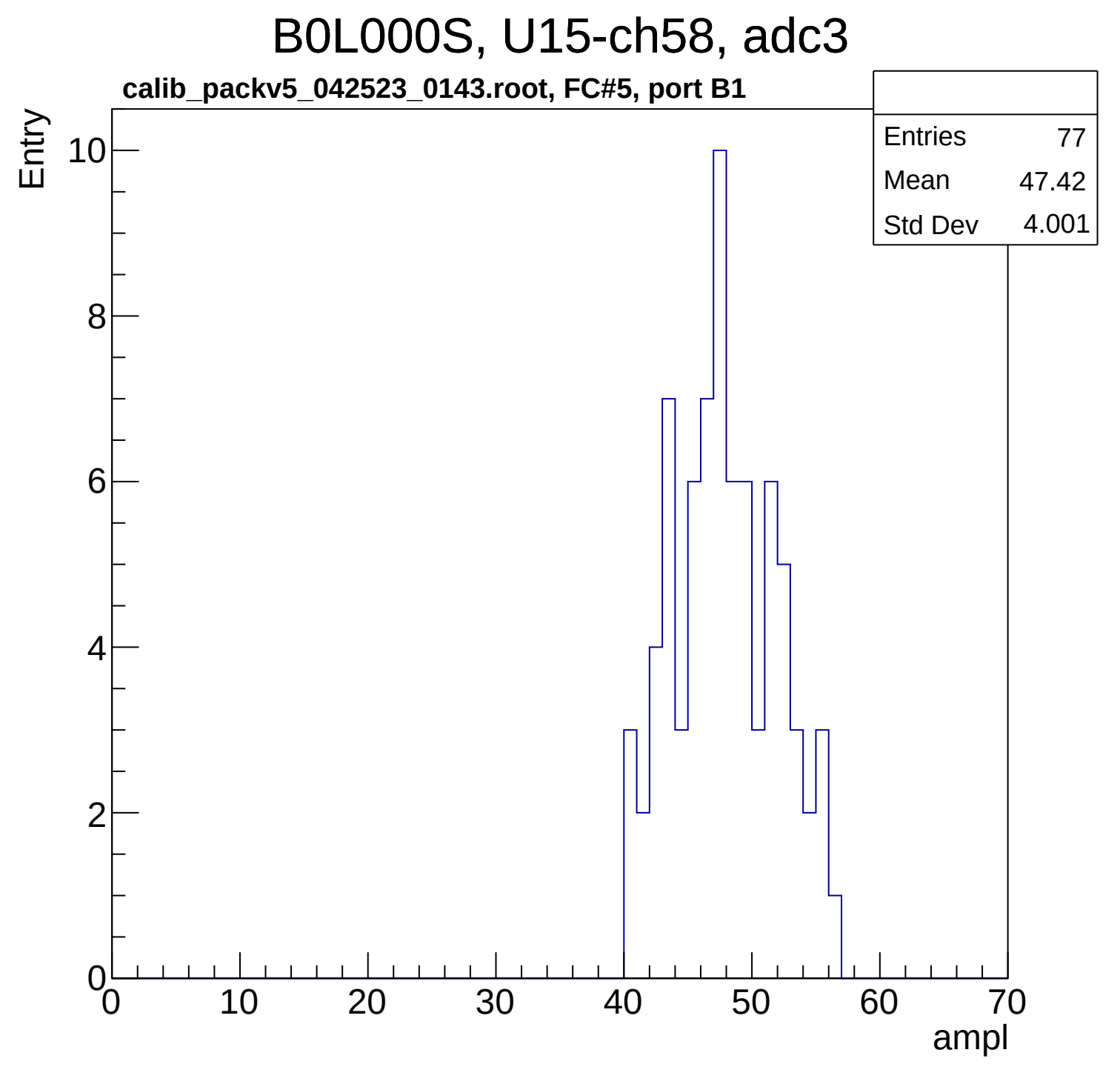
B0L000S, U15-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 47.42
Std Dev 4.001
Entry
0
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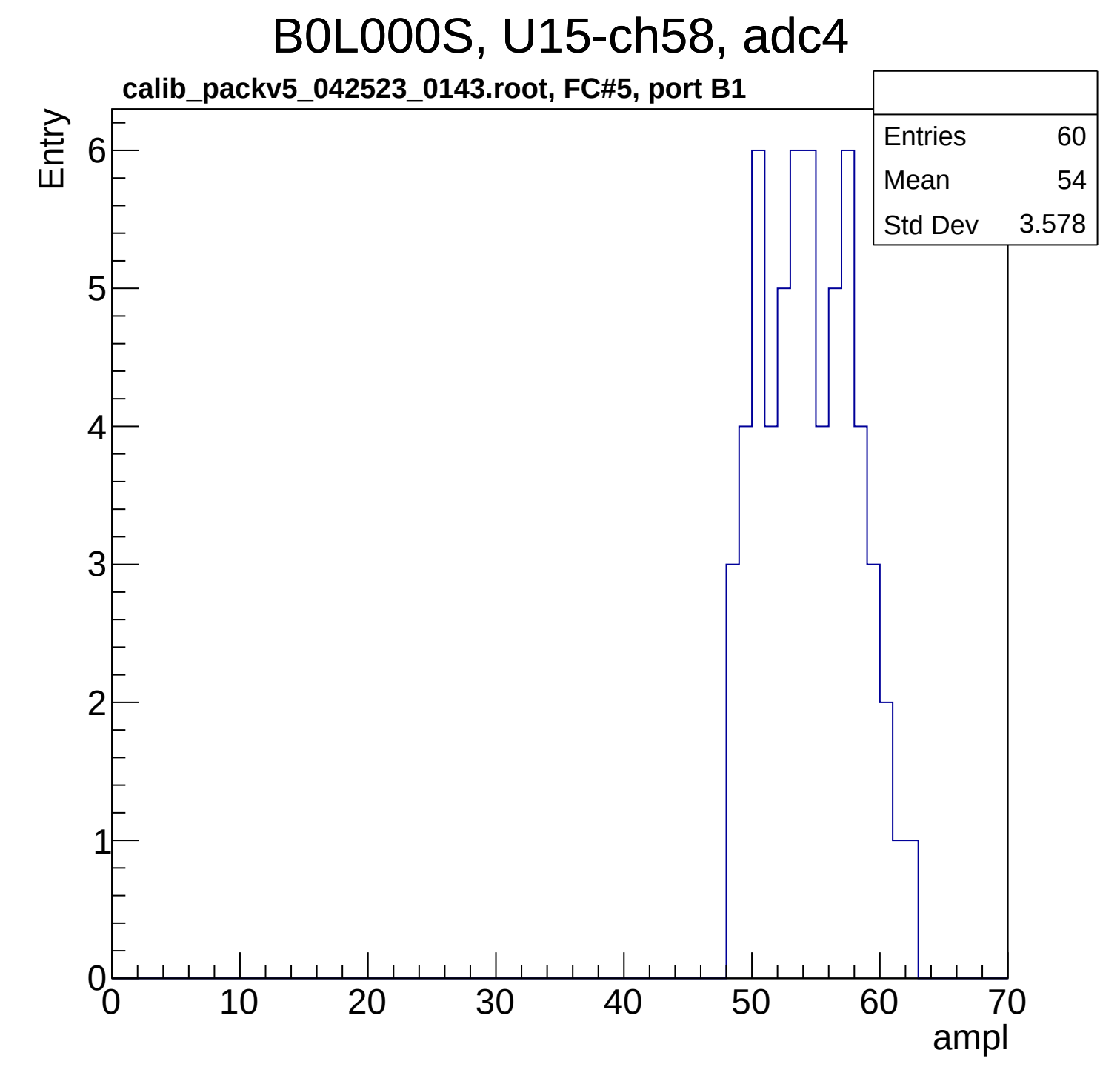
70
ampl


B0L000S, U15-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 54
Std Dev 3.578
Entry
0
1
2
3
4
5
6
0
10
20
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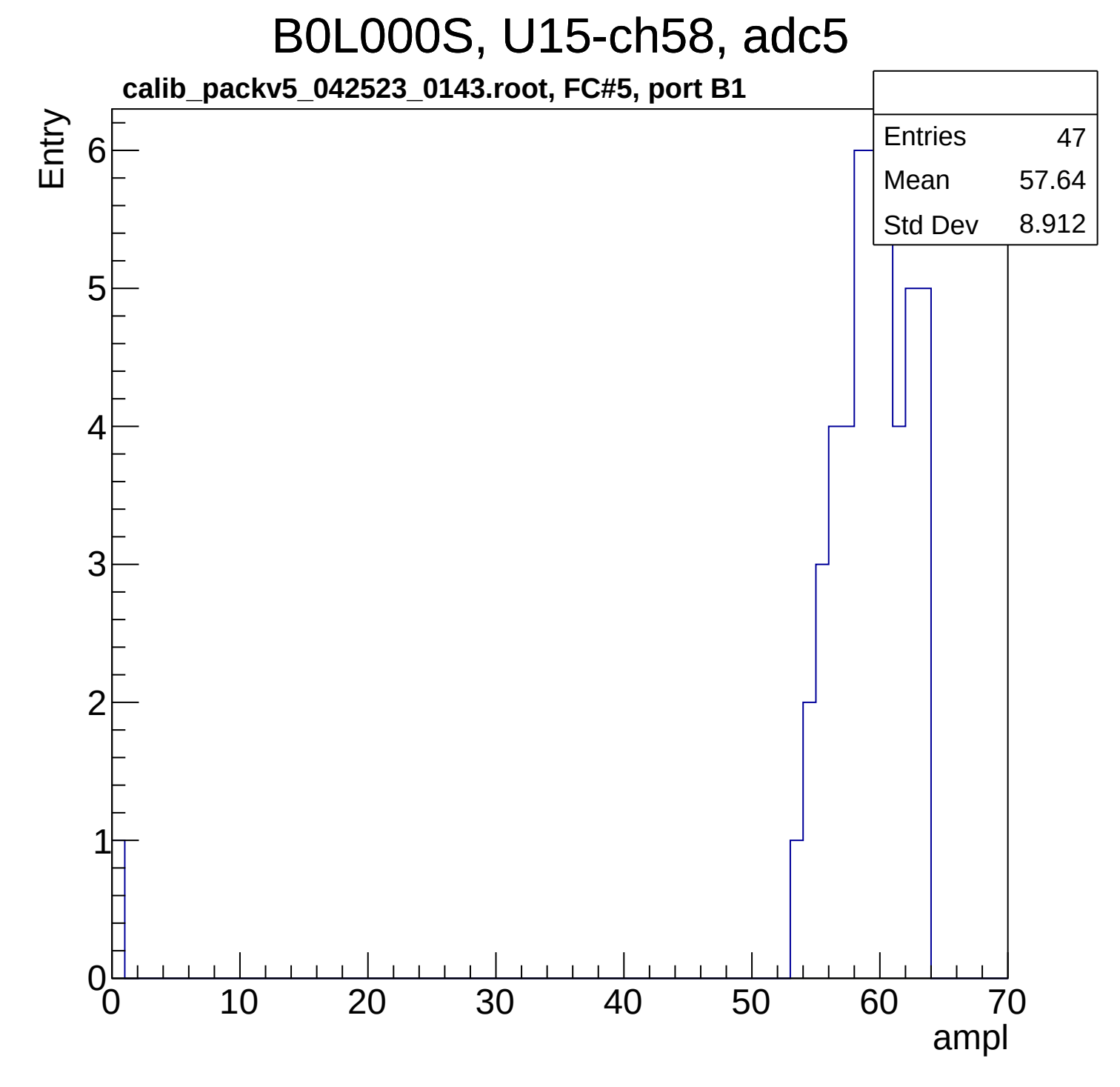
50
60
70
ampl


B0L000S, U15-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.64
Std Dev 8.912
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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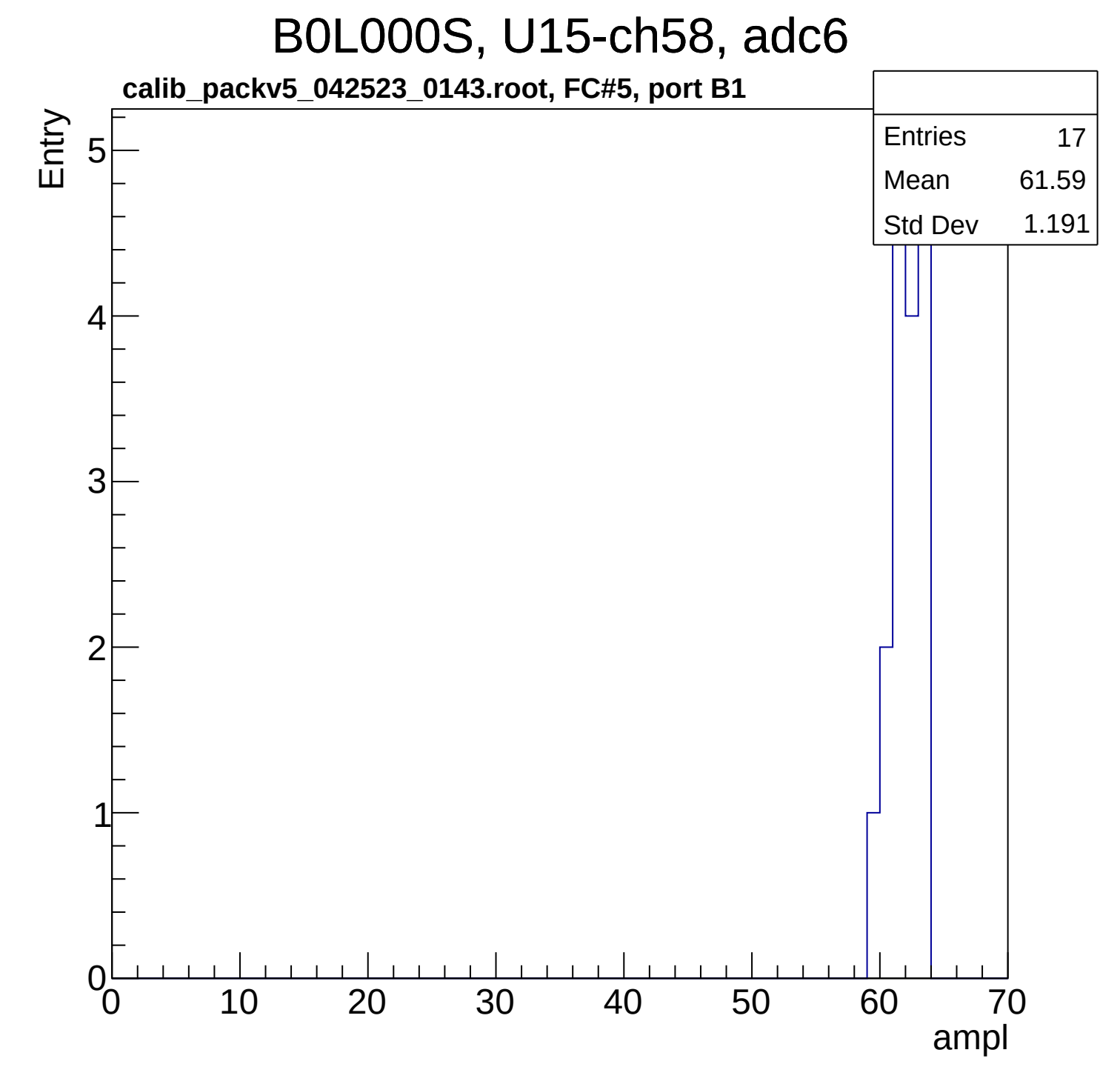
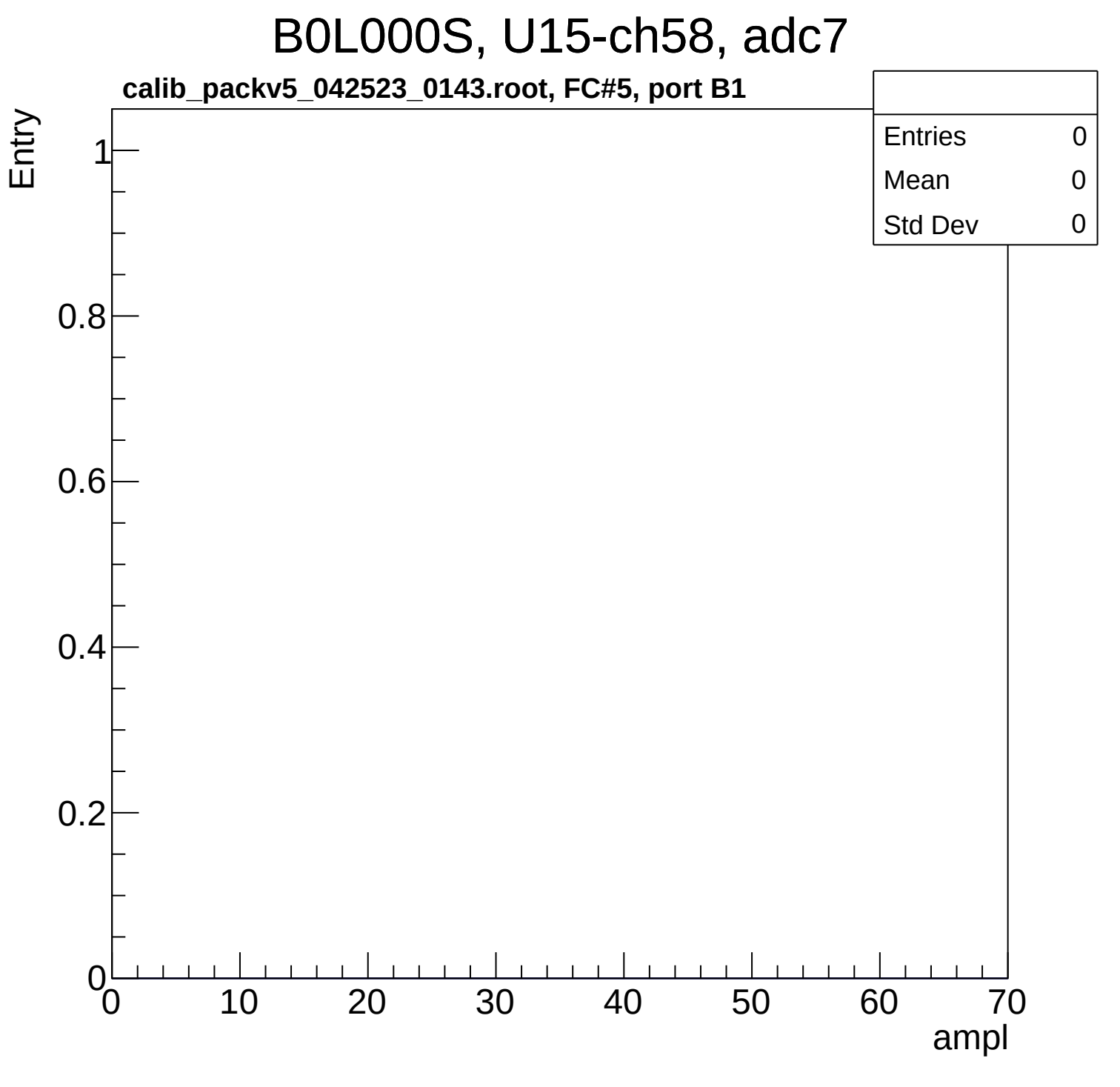

B0L000S, U15-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.59
Std Dev 1.191
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U15-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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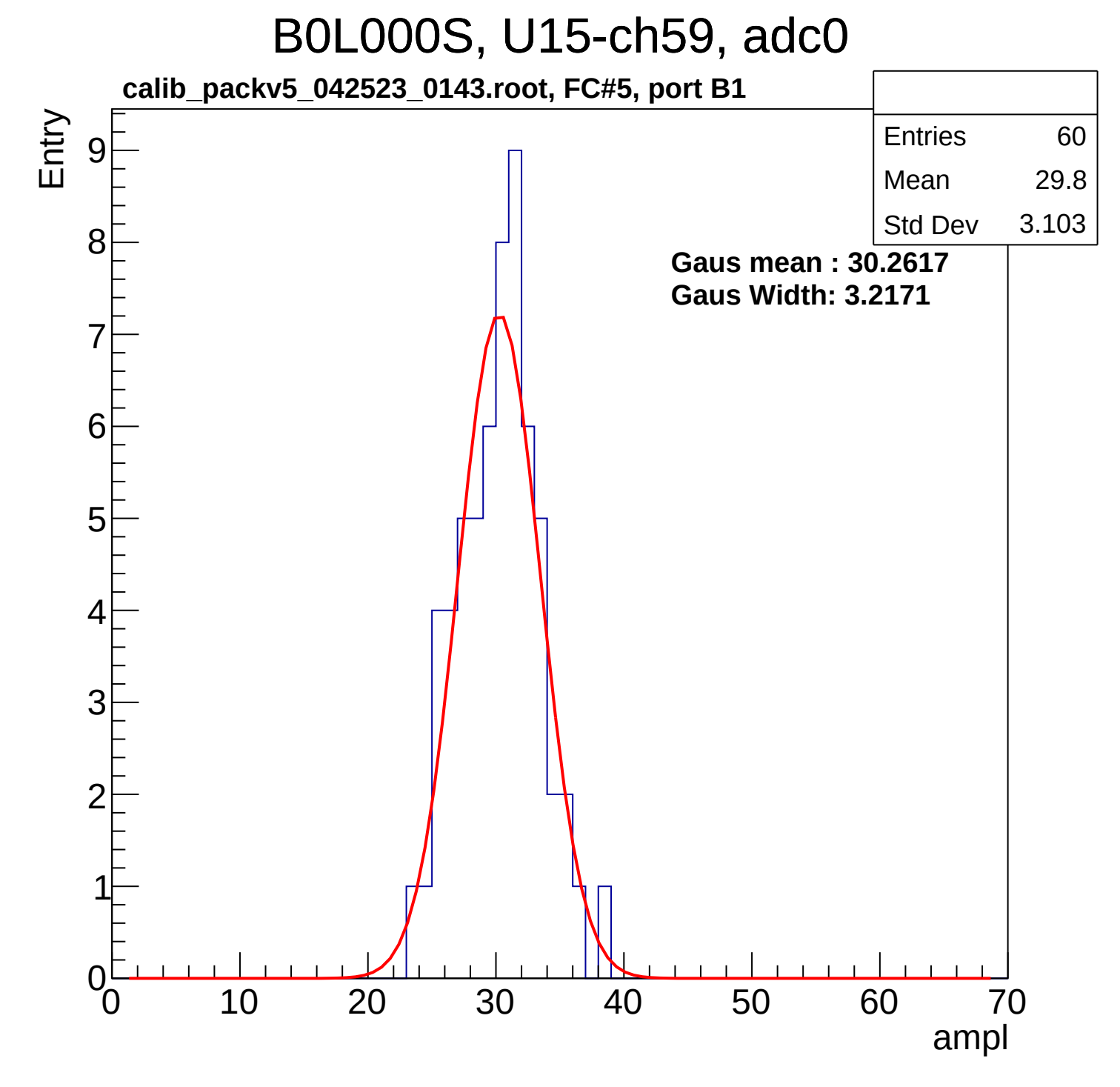
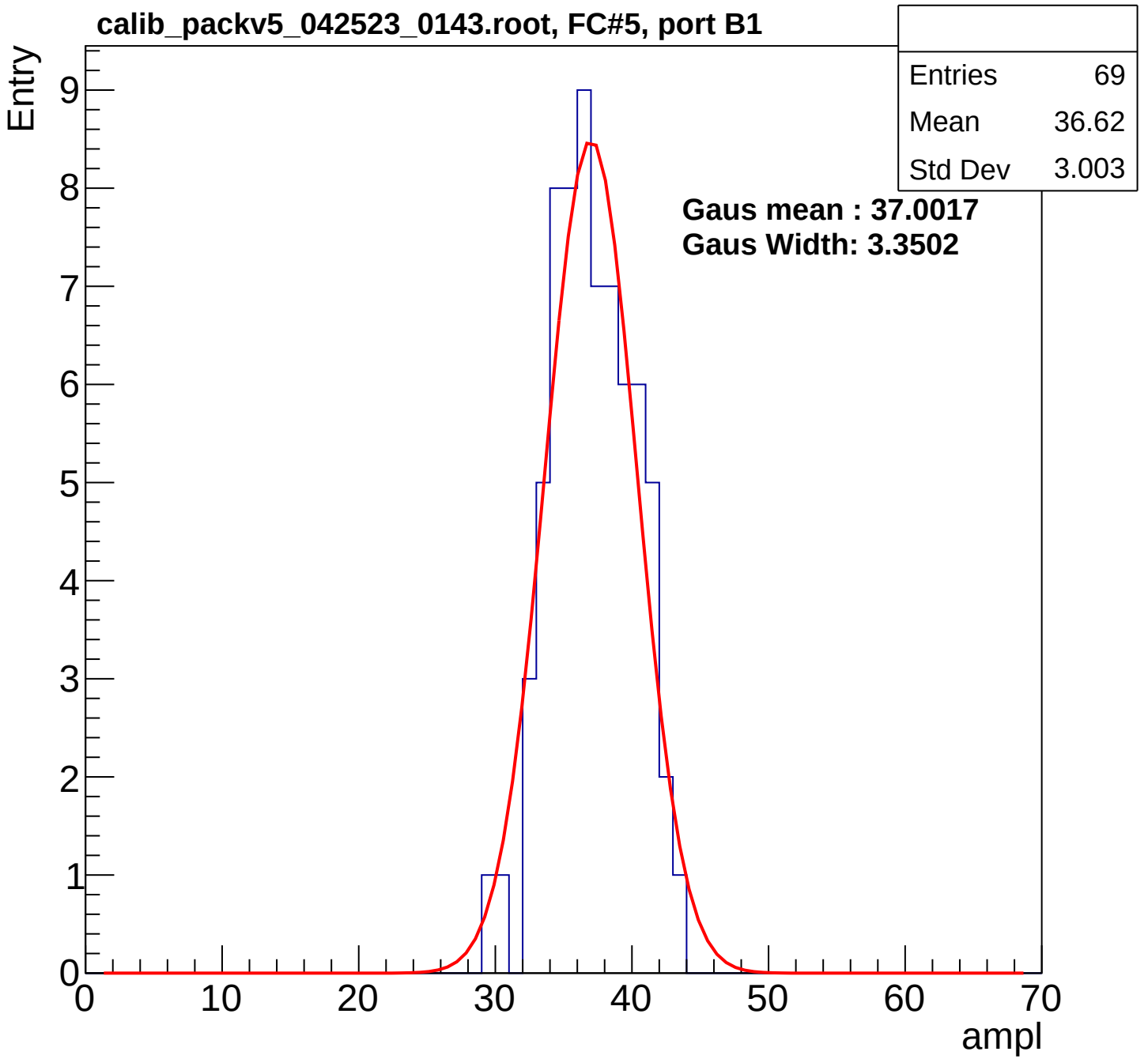
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.8
Std Dev 3.103
Gaus mean : 30.2617
Gaus Width: 3.2171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.62
Std Dev 3.003
Gaus mean : 37.0017
Gaus Width: 3.3502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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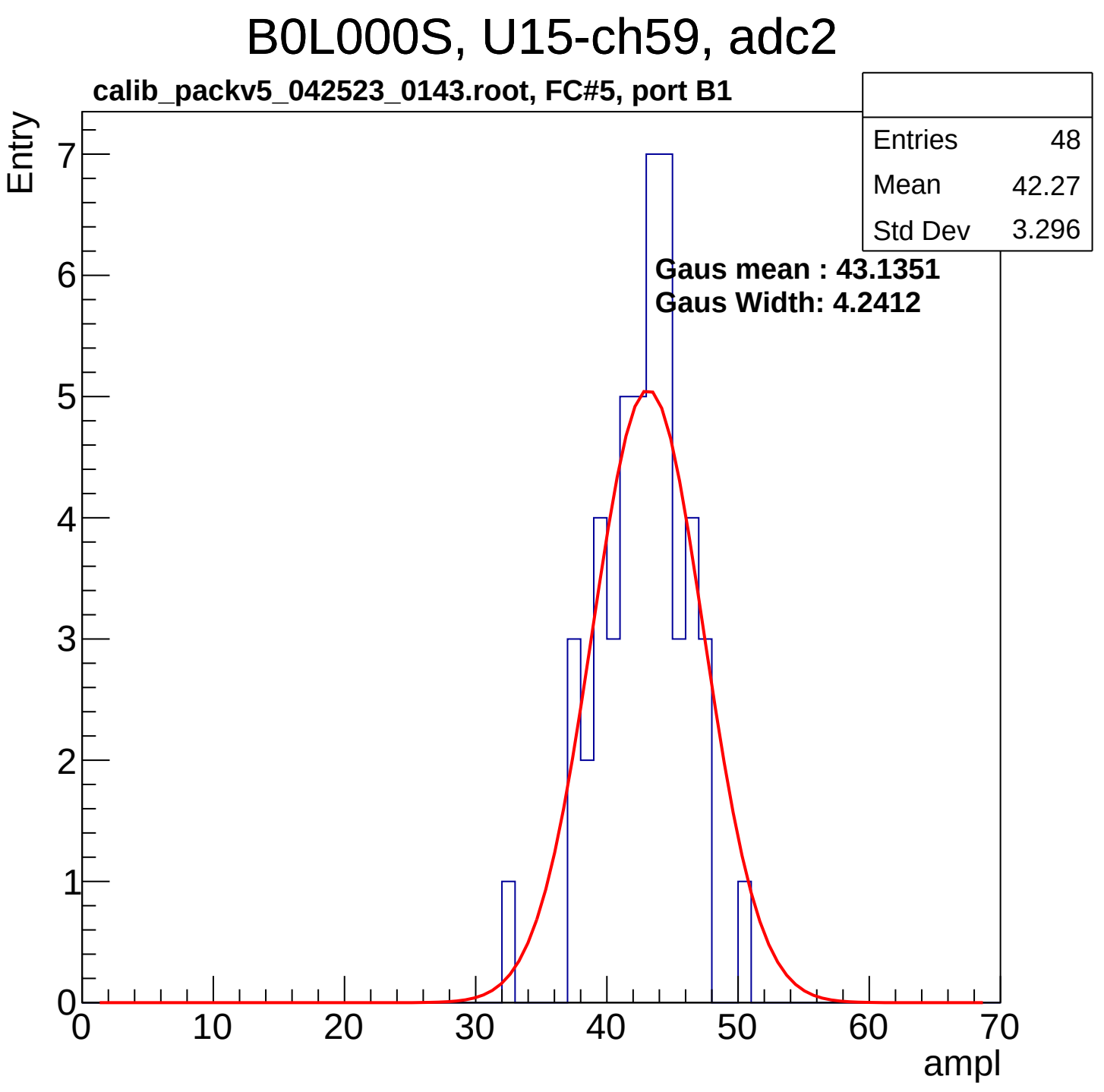
B0L000S, U15-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 42.27
Std Dev 3.296
Gaus mean : 43.1351
Gaus Width: 4.2412
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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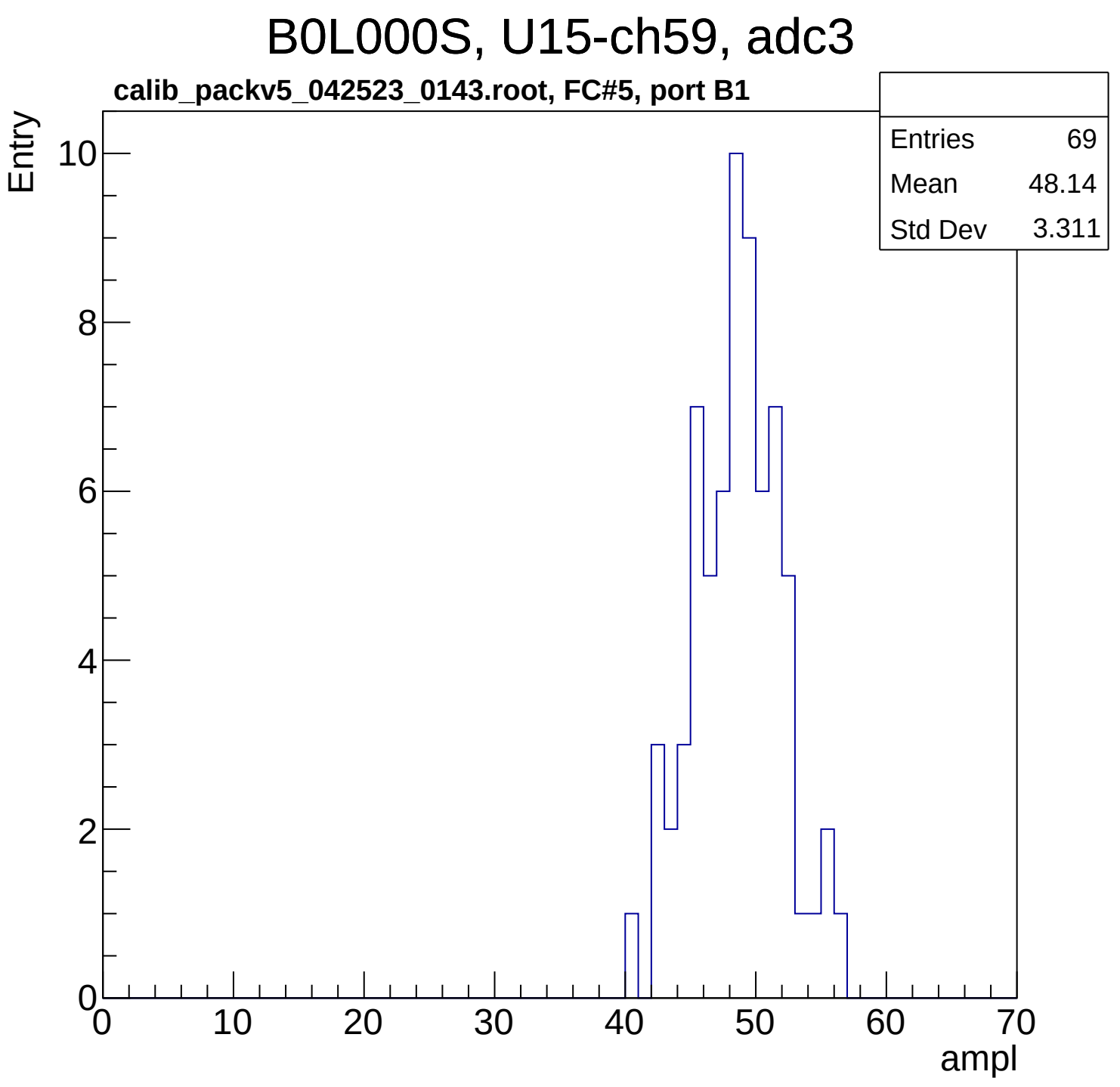
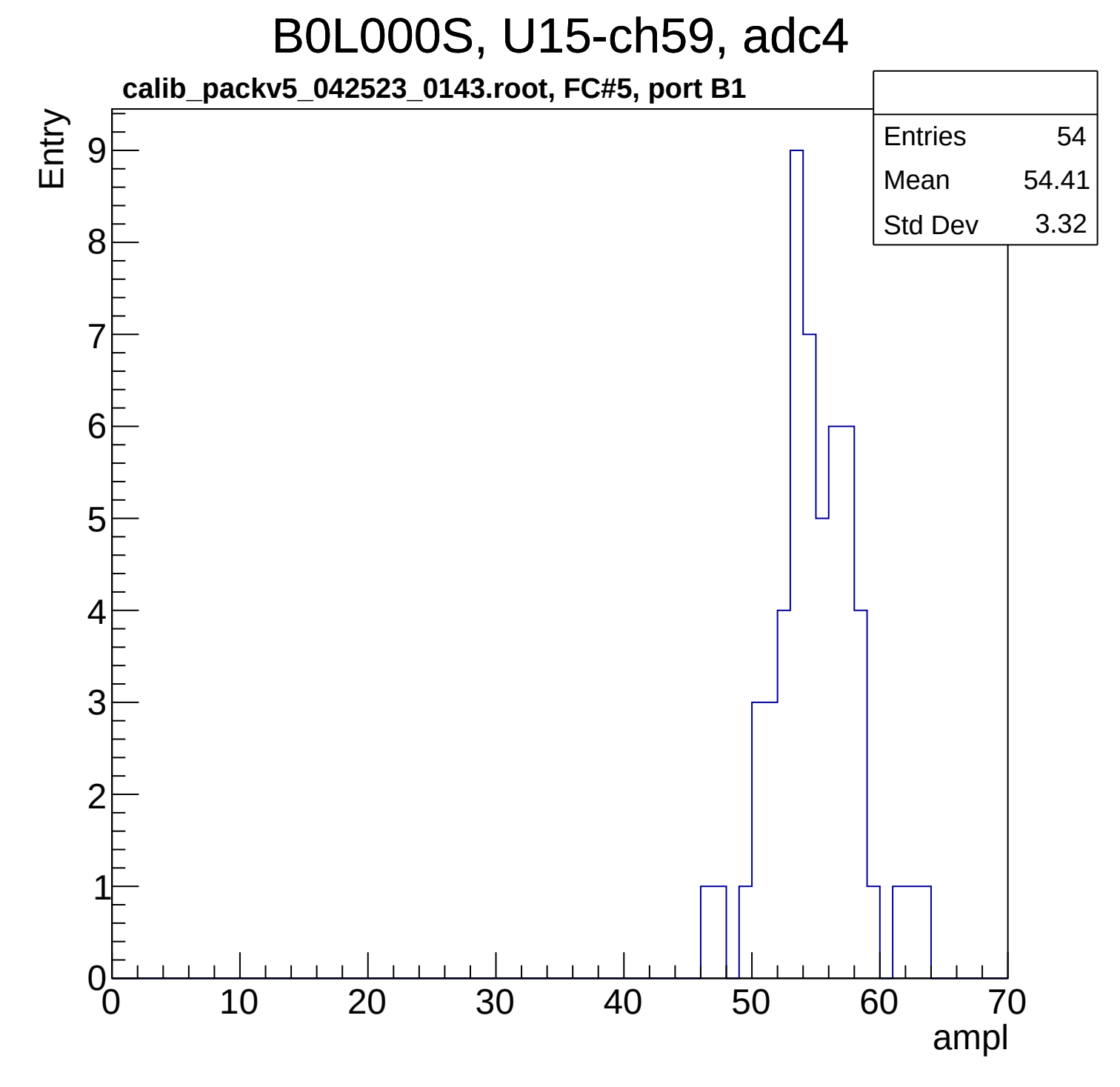

B0L000S, U15-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48.14
Std Dev 3.311
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.41
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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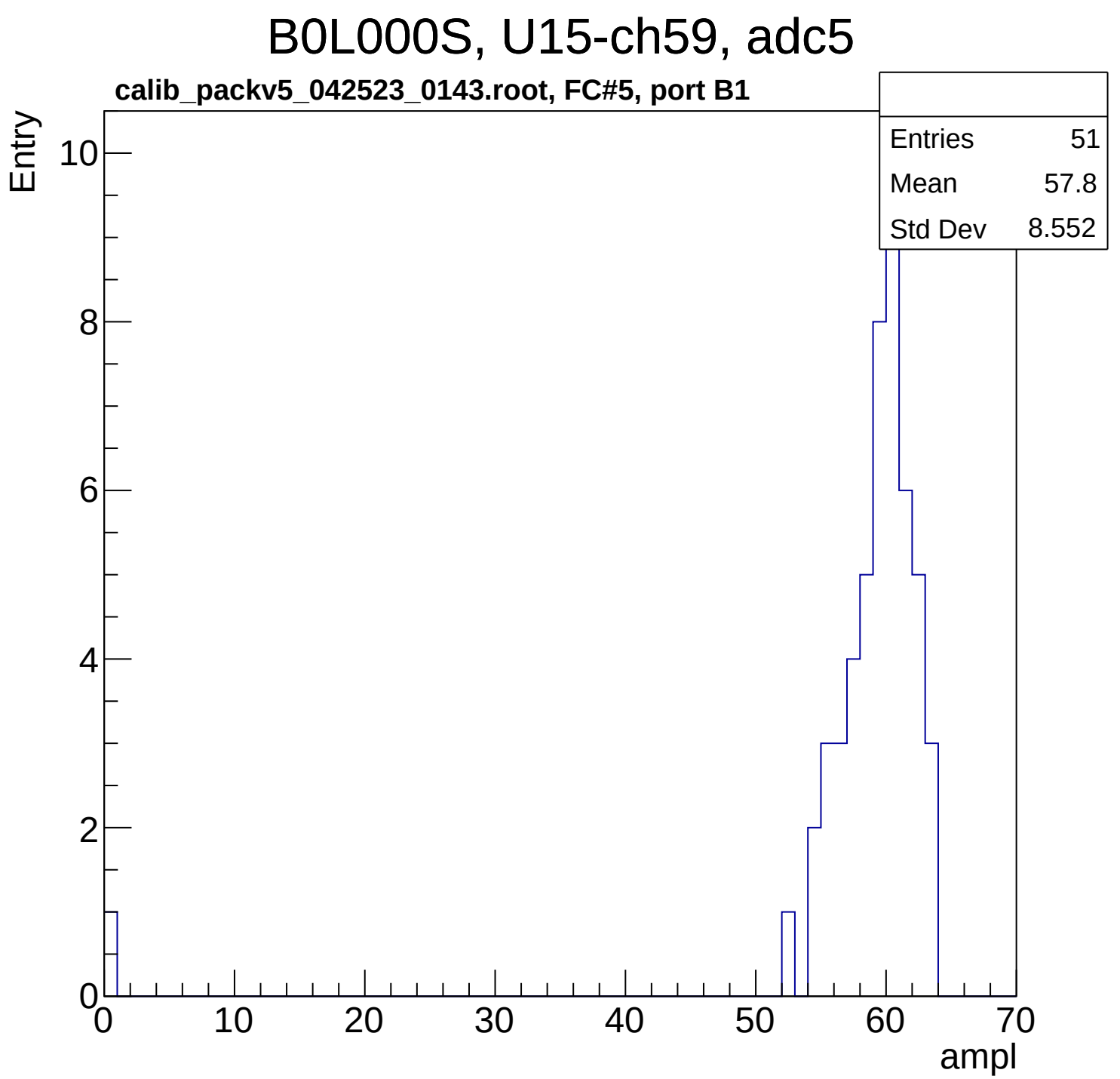
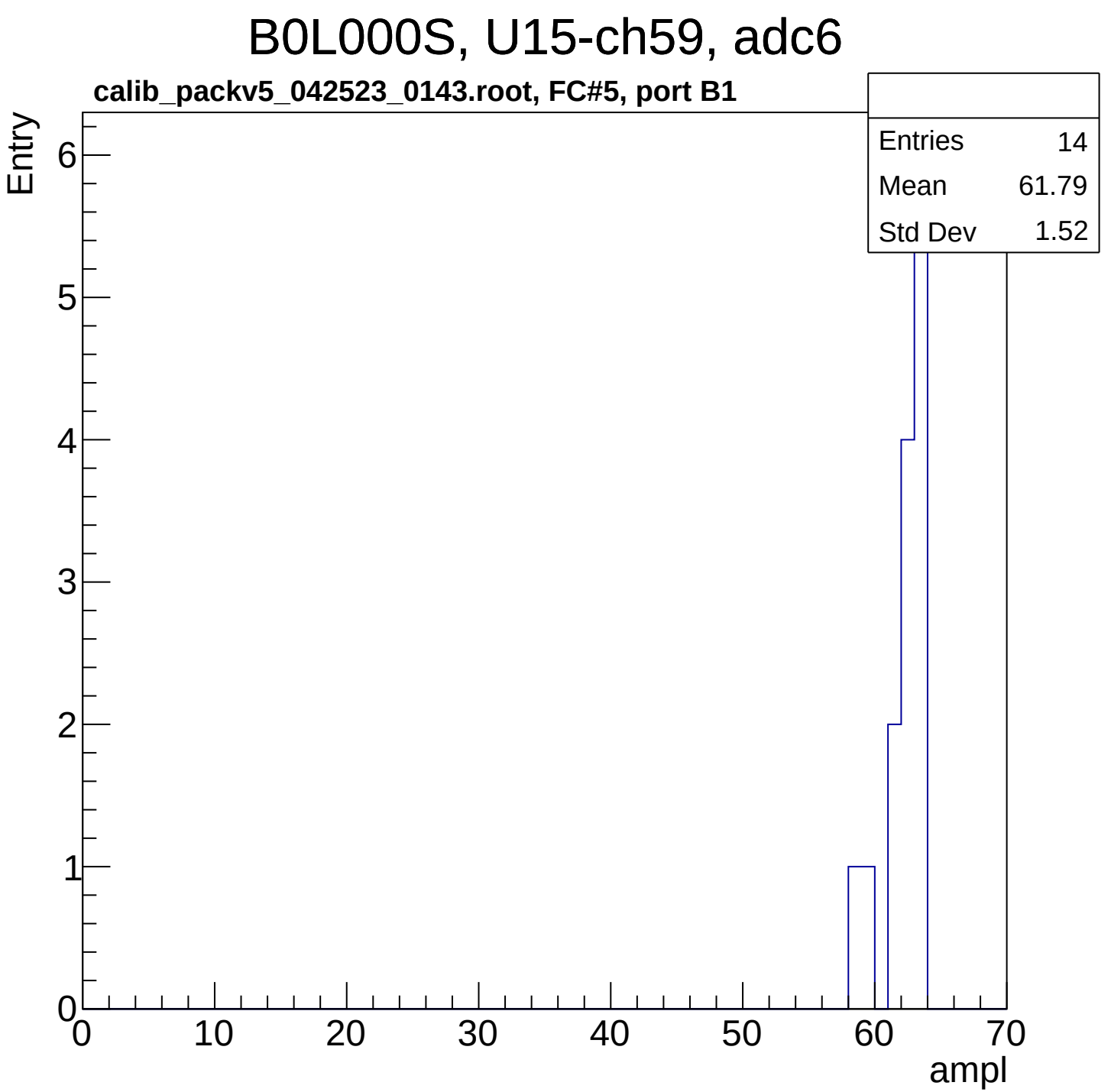
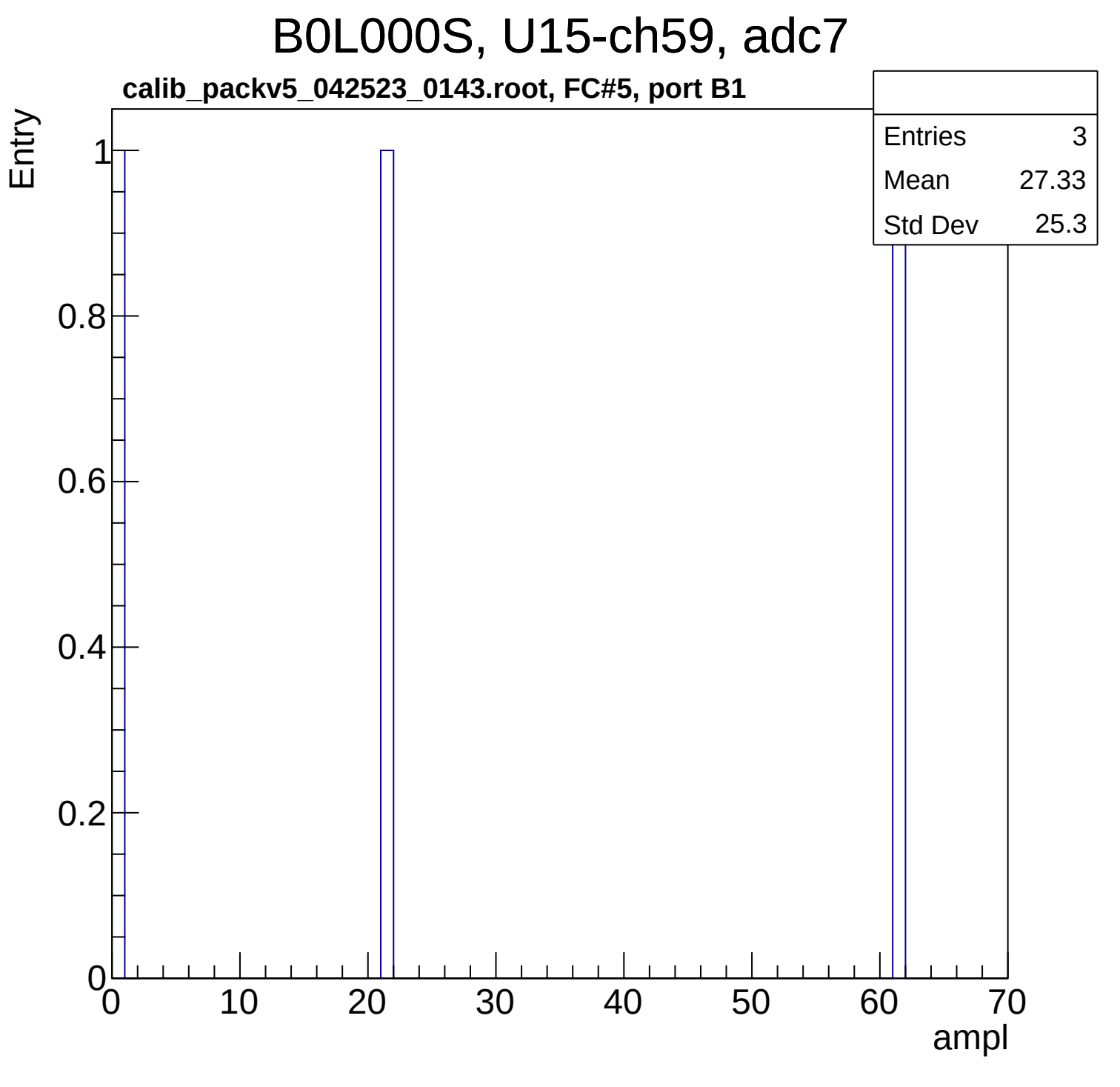
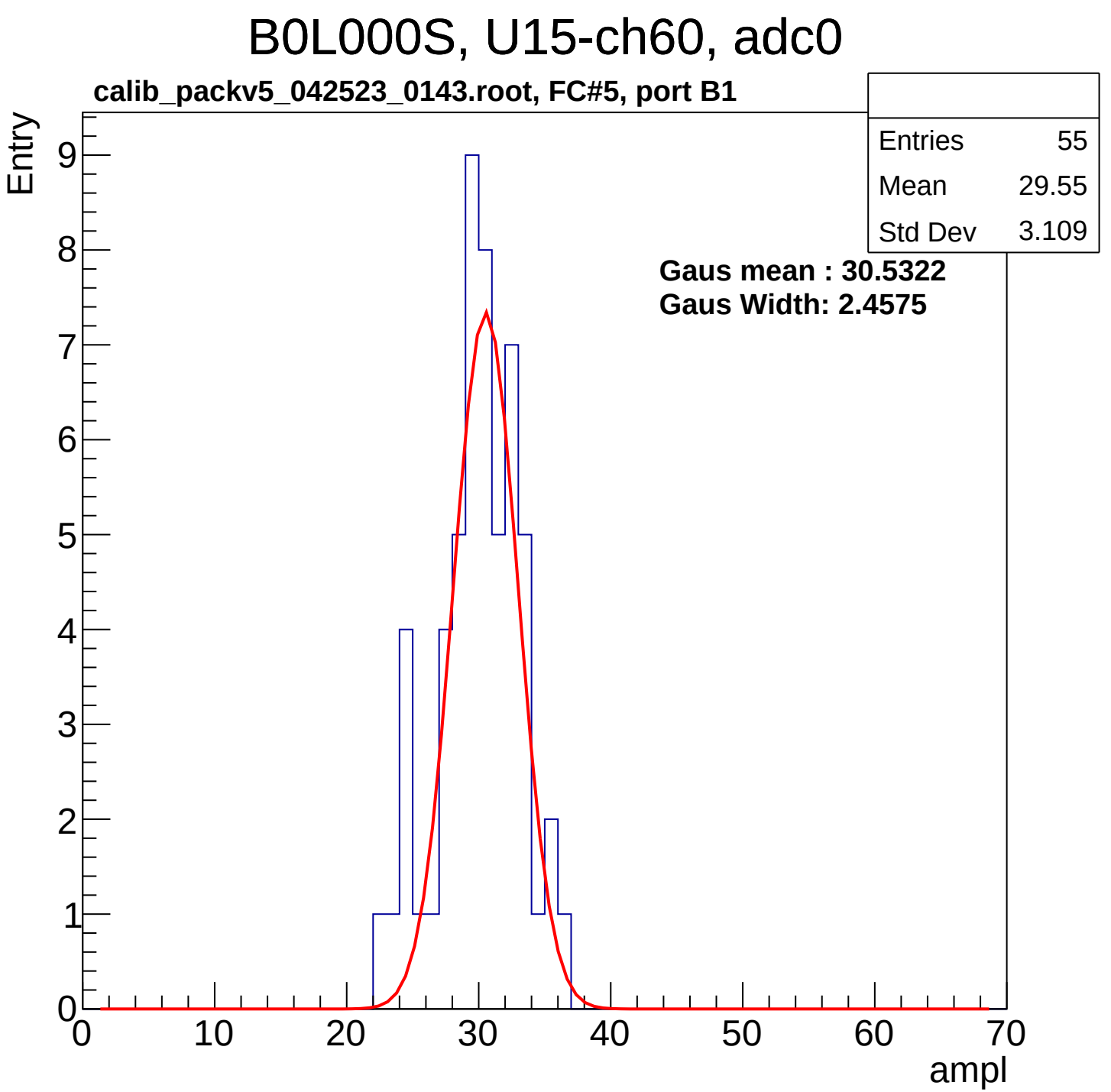
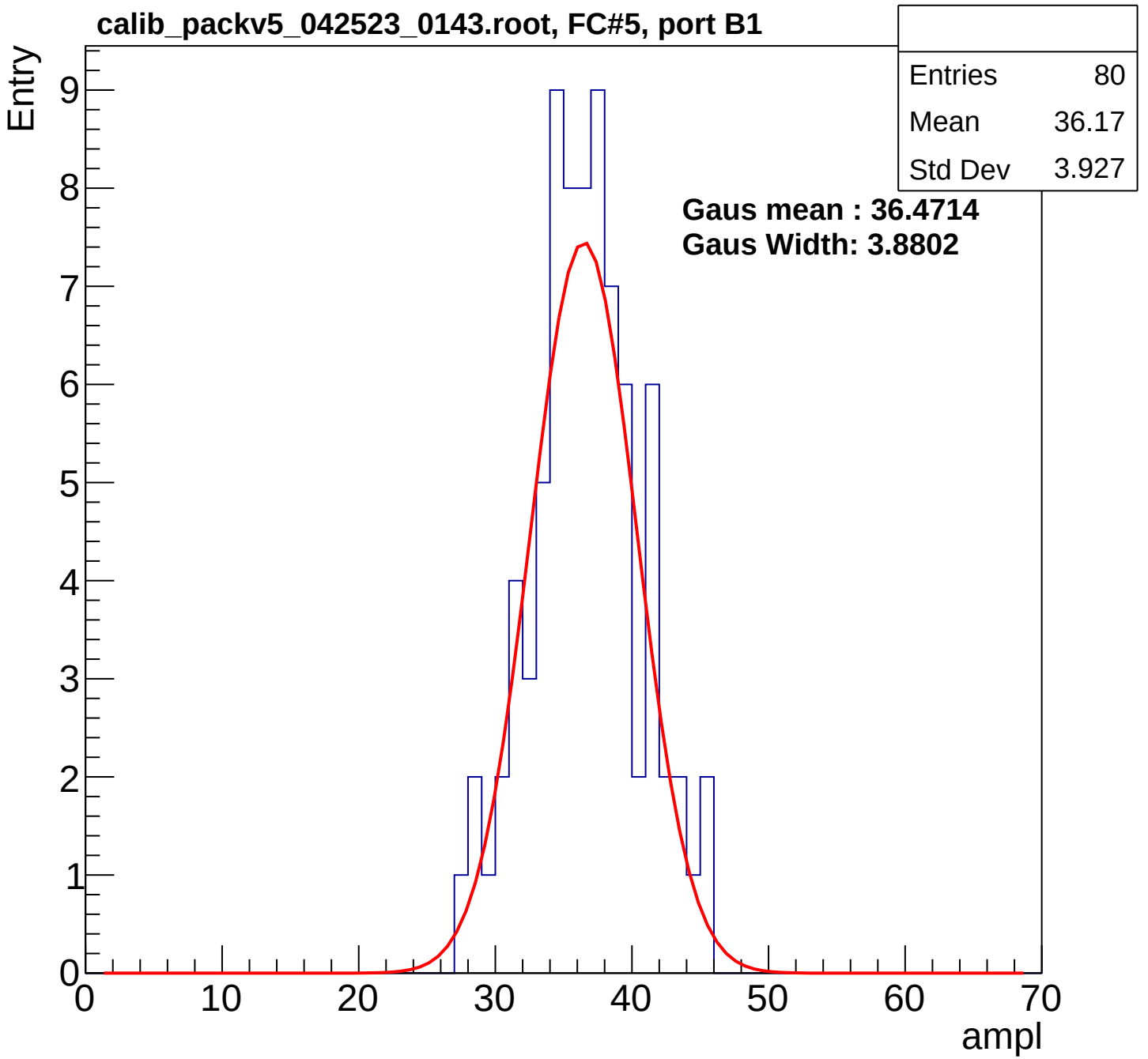

B0L000S, U15-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 57.8
Std Dev 8.552
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.79
Std Dev 1.52
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27.33
Std Dev 25.3
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U15-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.55
Std Dev 3.109
Gaus mean : 30.5322
Gaus Width: 2.4575
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.17
Std Dev 3.927
Gaus mean : 36.4714
Gaus Width: 3.8802
Entry
ampl
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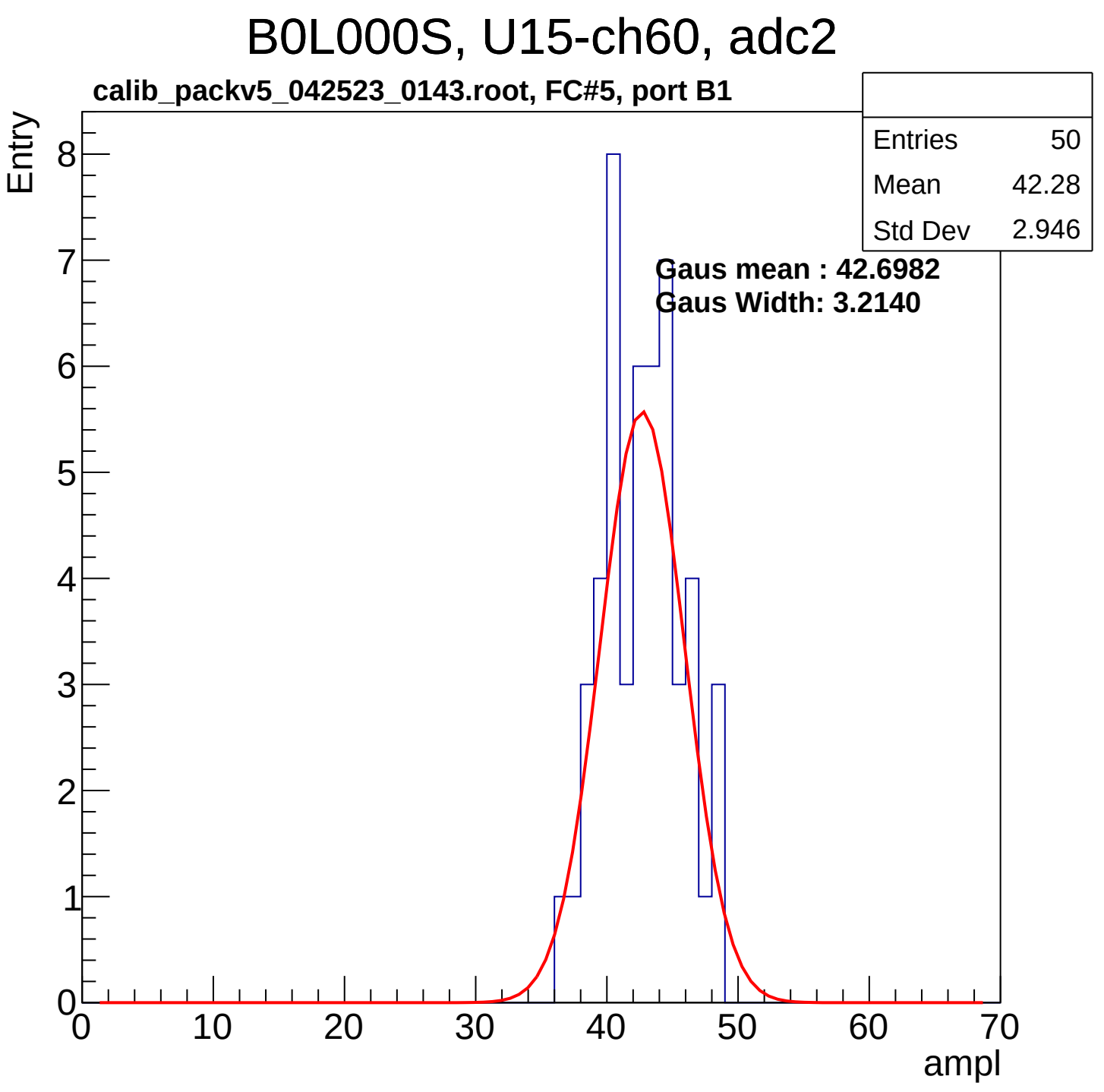
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.28
Std Dev 2.946
Gaus mean : 42.6982
Gaus Width: 3.2140
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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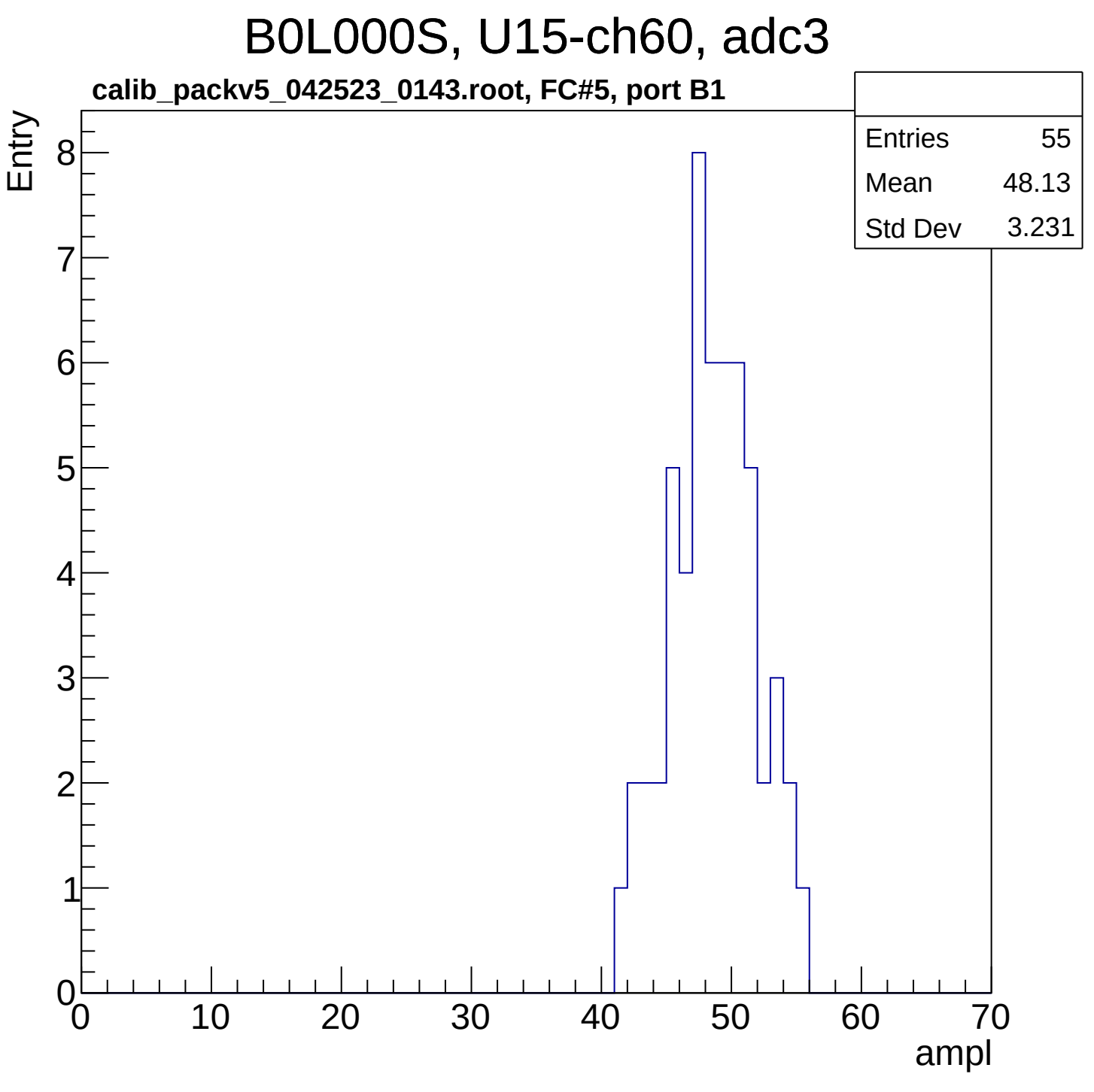
B0L000S, U15-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.13
Std Dev 3.231
Entry
ampl
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0
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40
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60
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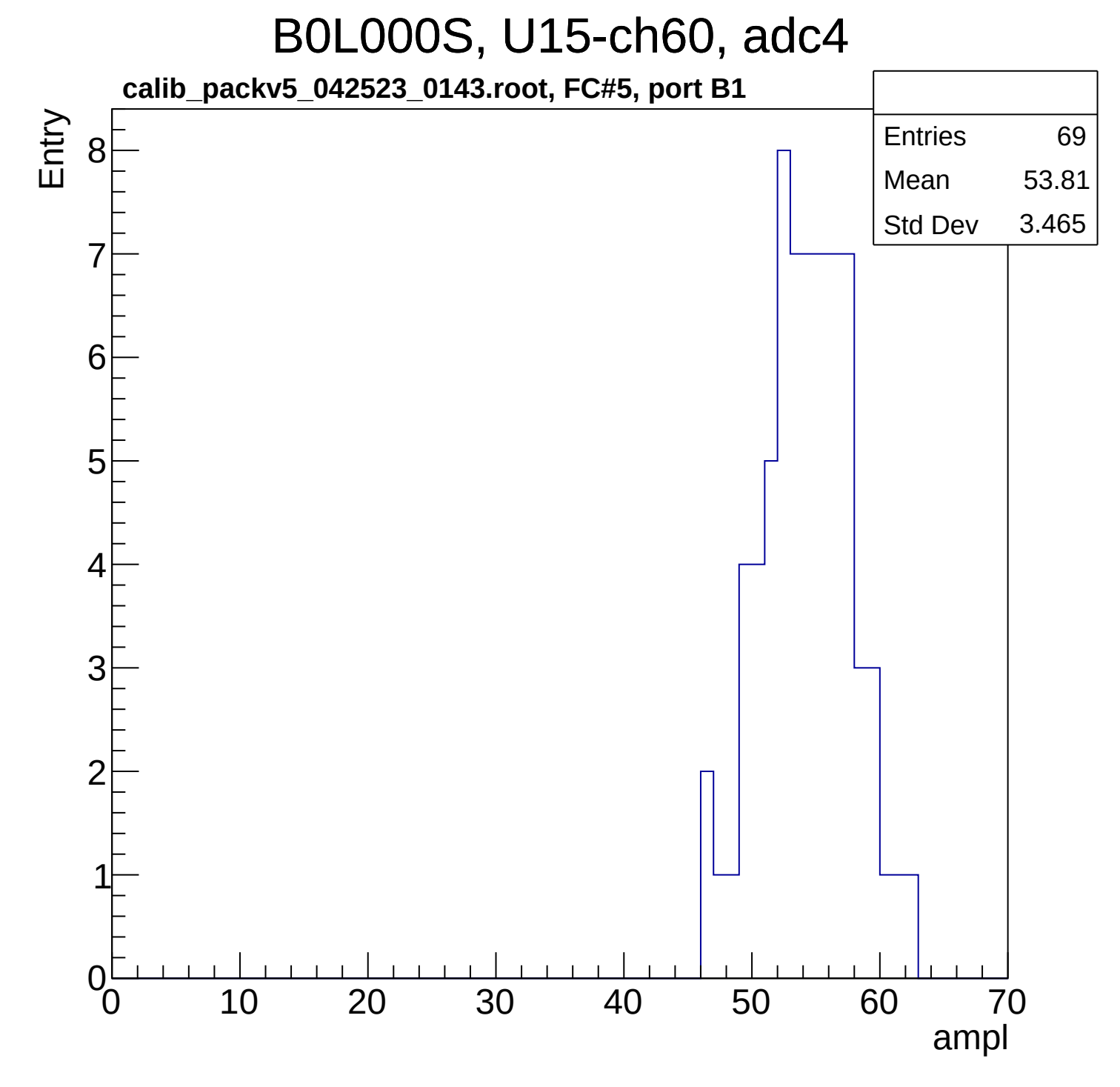
B0L000S, U15-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 53.81
Std Dev 3.465
Entry
ampl
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2
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7
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0
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40
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60
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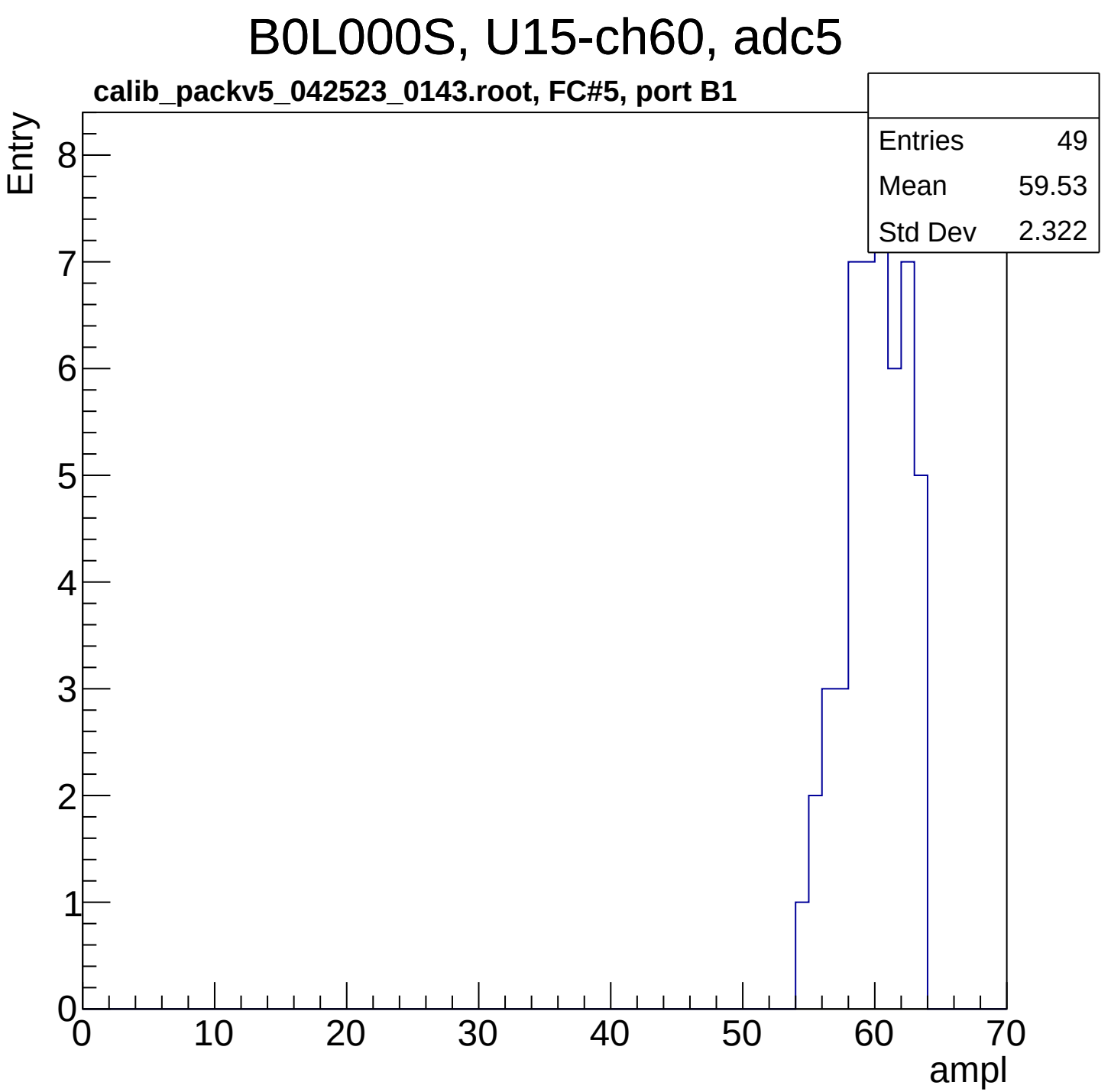
B0L000S, U15-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.53
Std Dev 2.322
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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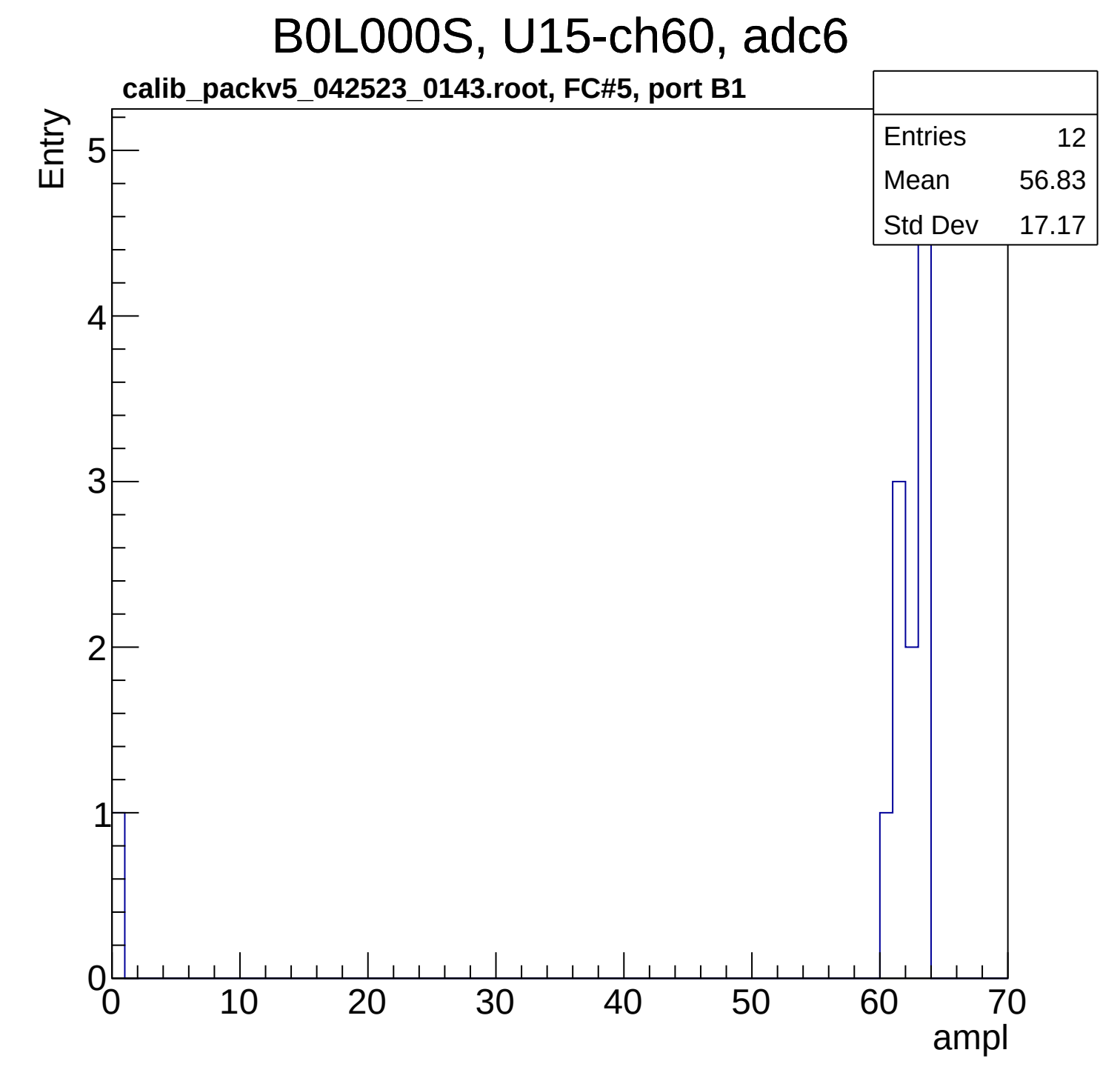
B0L000S, U15-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
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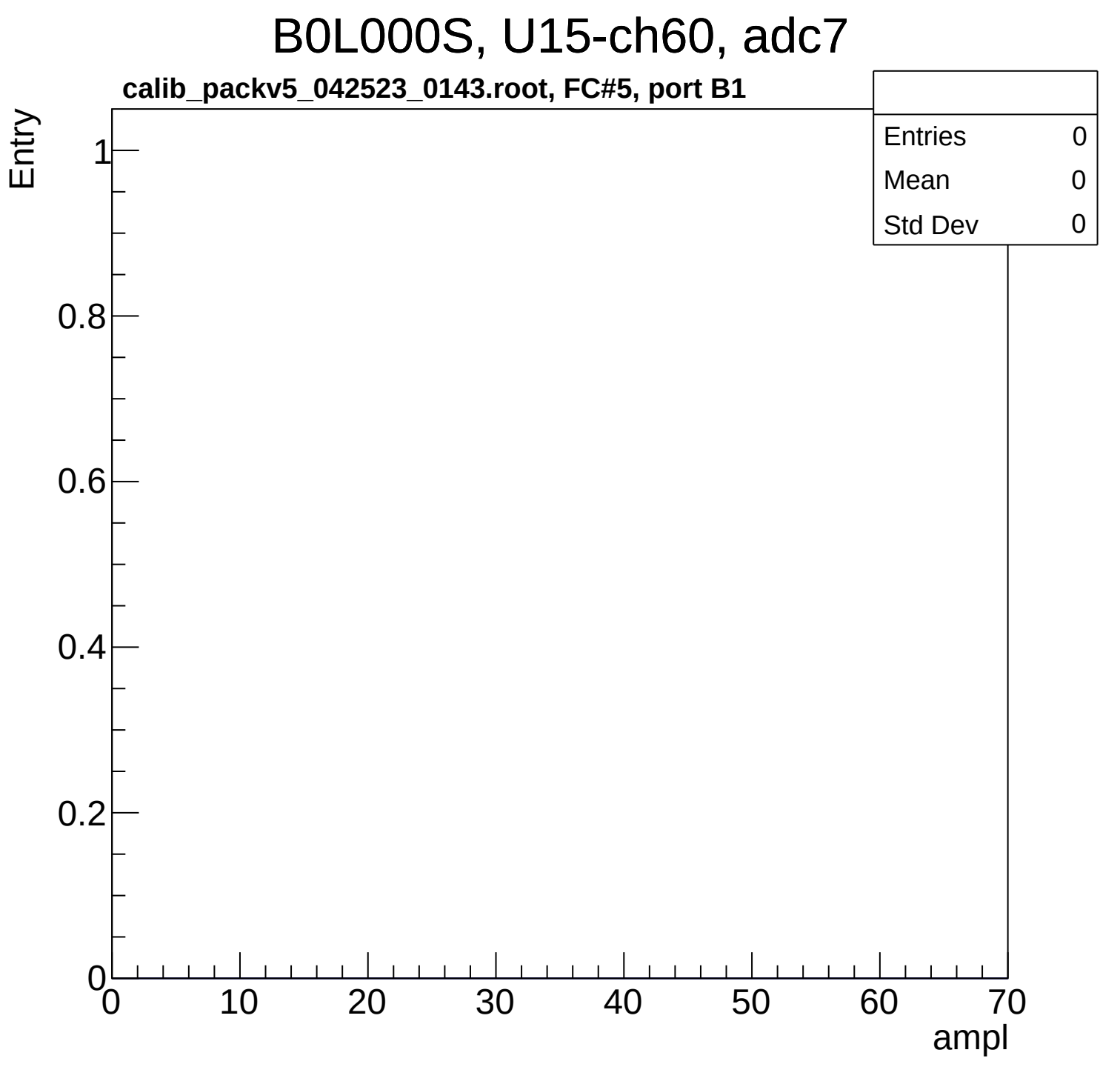
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
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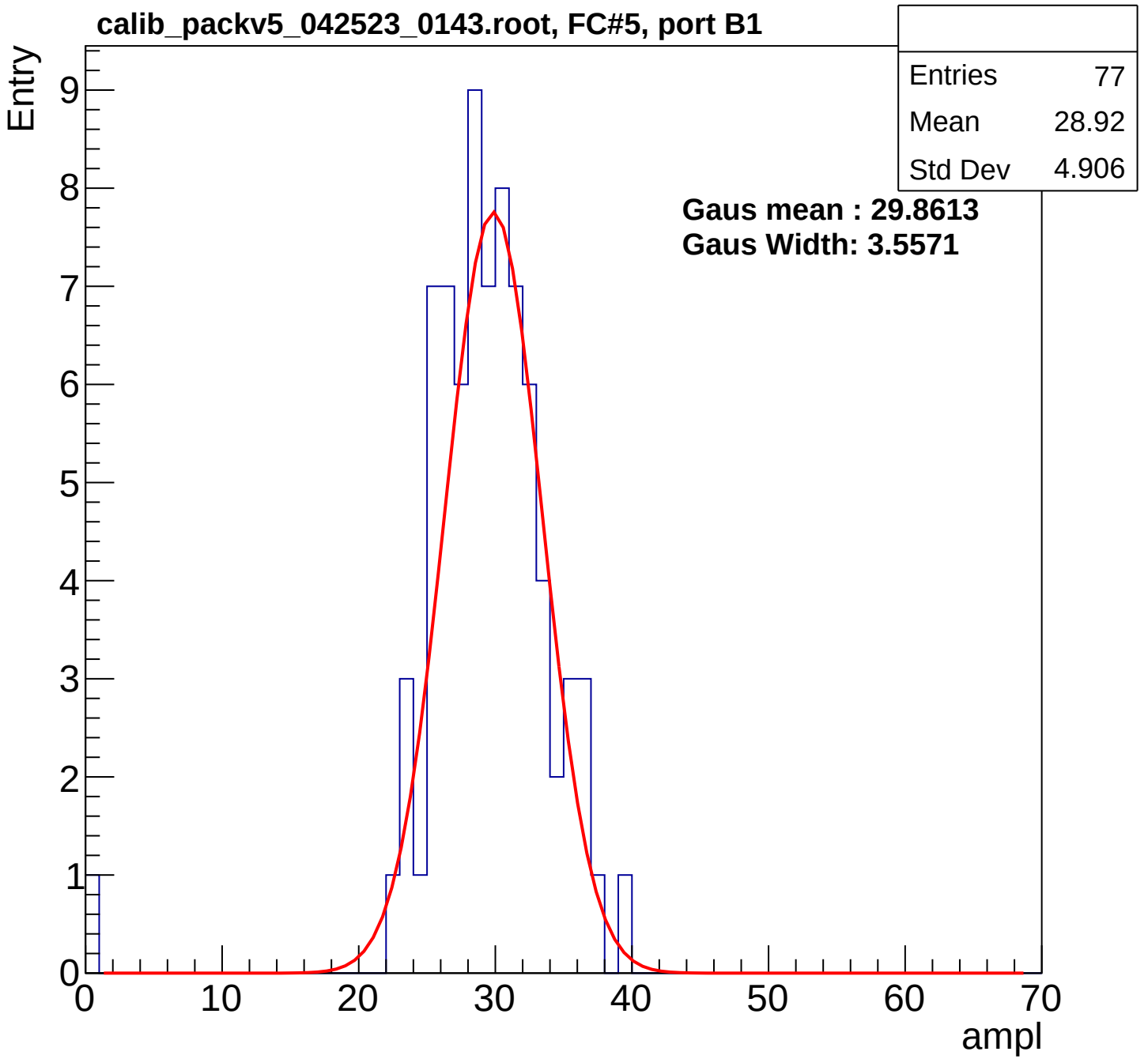
40
50
60
70
ampl


B0L000S, U15-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.92
Std Dev 4.906
Gaus mean : 29.8613
Gaus Width: 3.5571
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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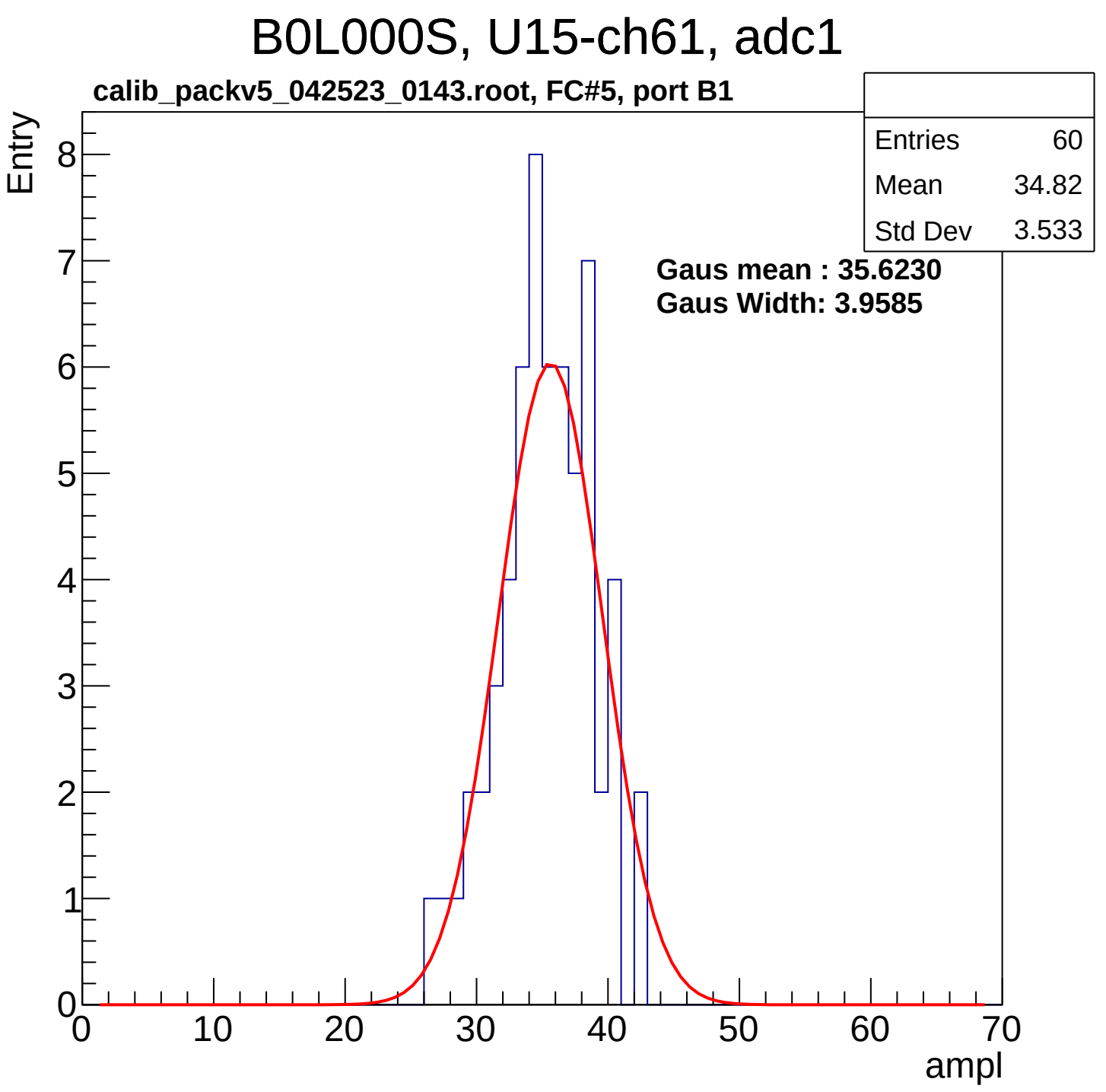
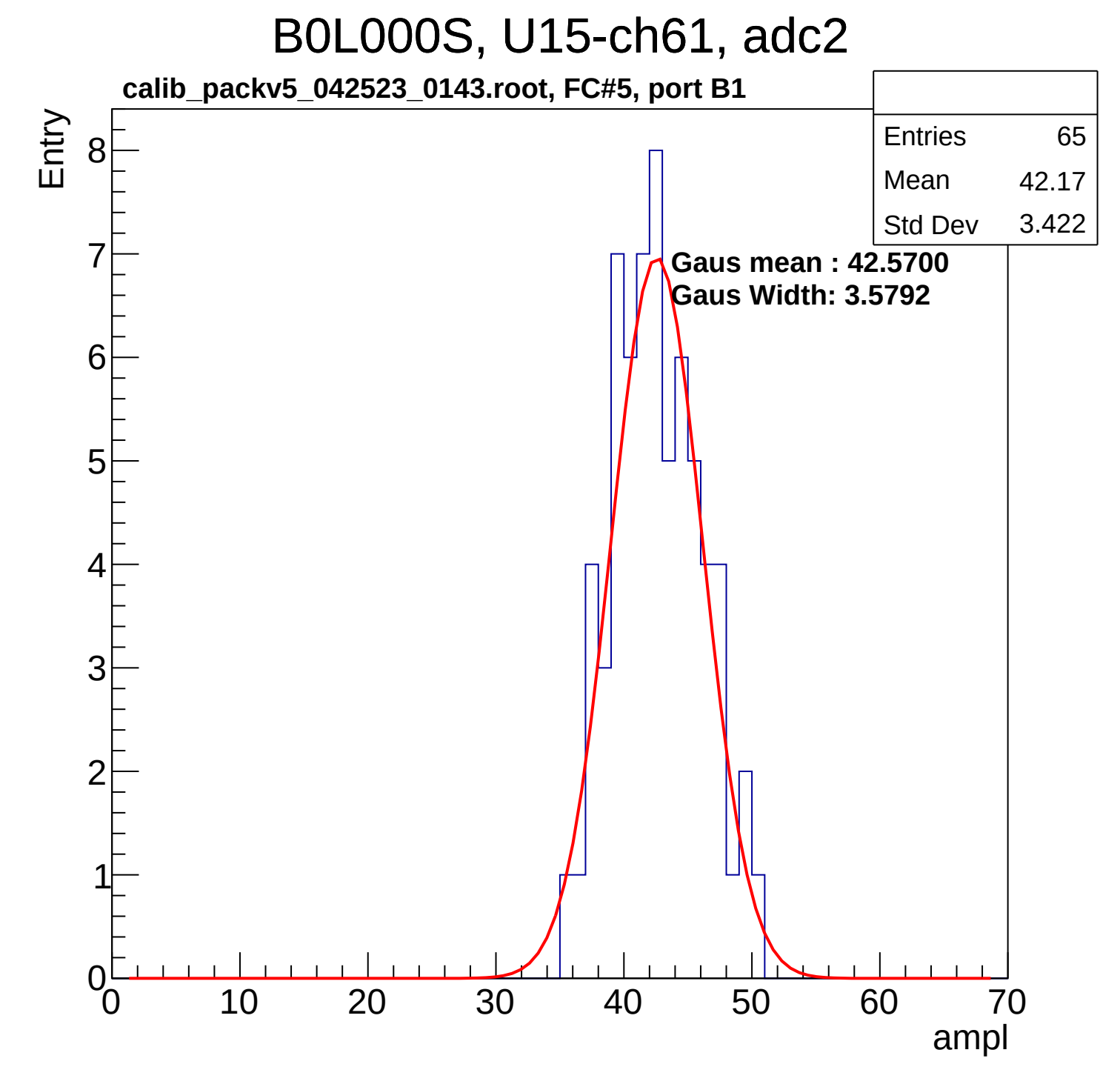
40
50
60
70


B0L000S, U15-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 34.82
Std Dev 3.533
Gaus mean : 35.6230
Gaus Width: 3.9585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.17
Std Dev 3.422
Gaus mean : 42.5700
Gaus Width: 3.5792
Entry
ampl
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2
3
4
5
6
7
8
0
10
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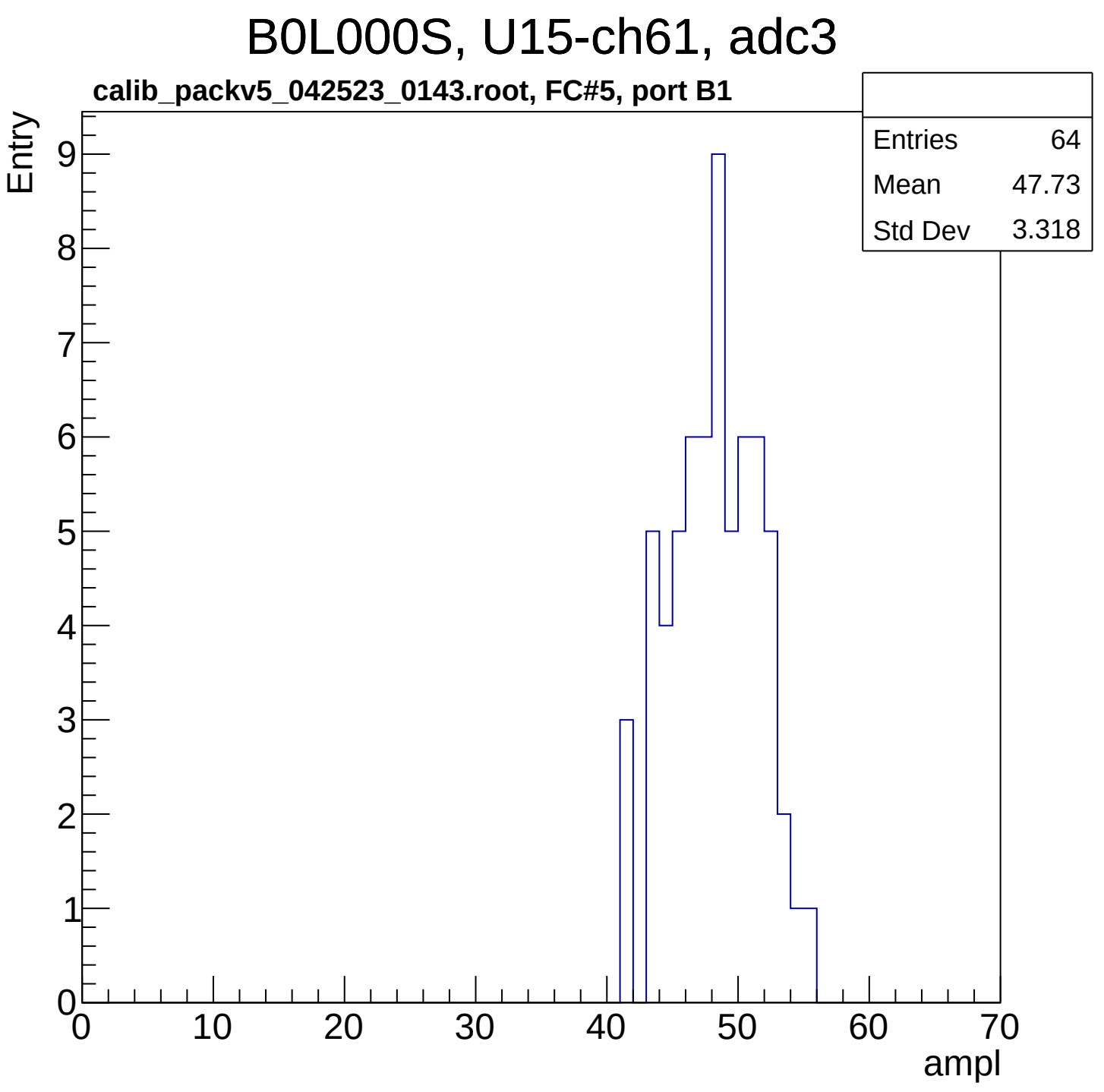
40
50
60
70


B0L000S, U15-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.73
Std Dev 3.318
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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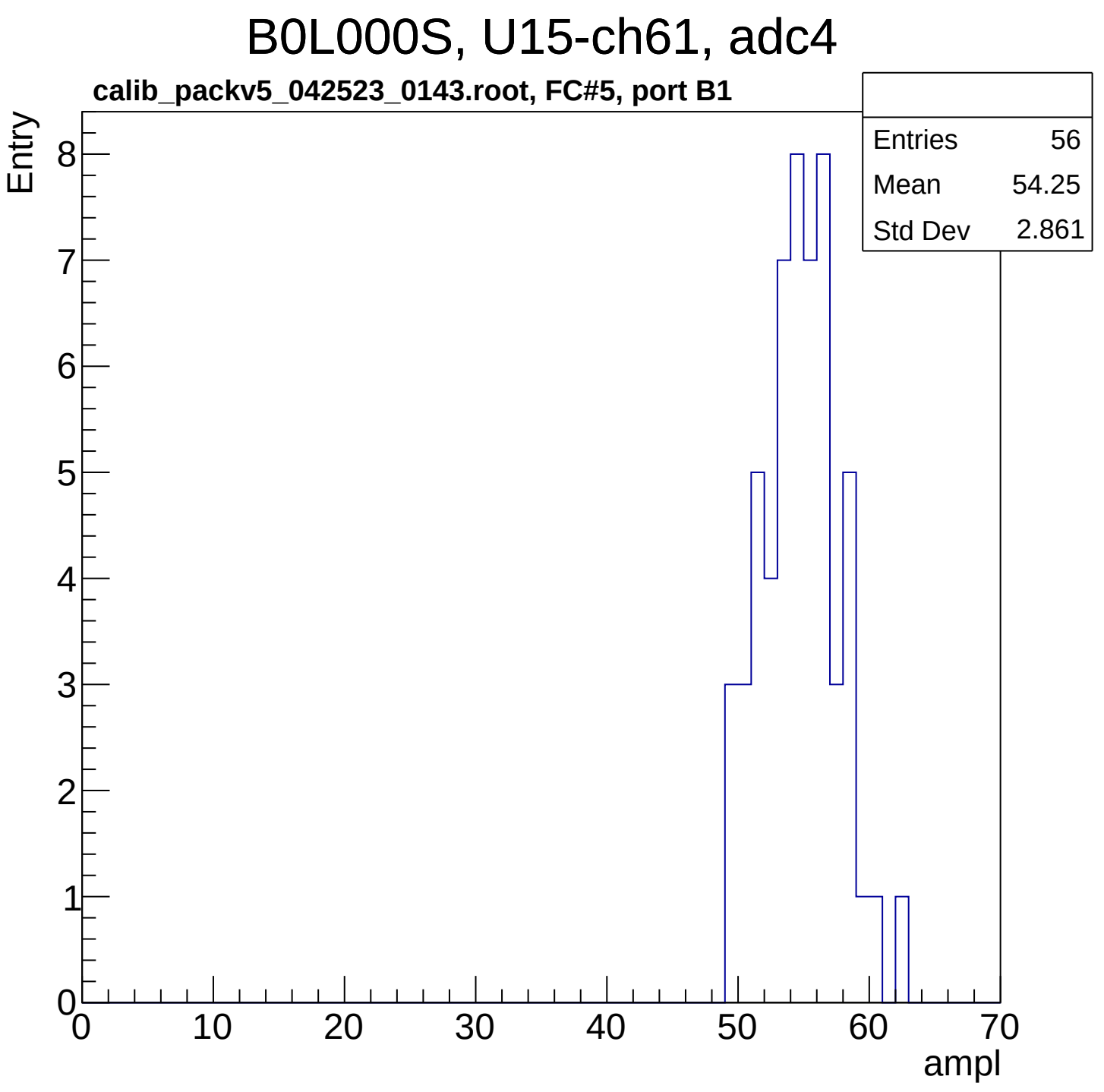
B0L000S, U15-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.25
Std Dev 2.861
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
50
60
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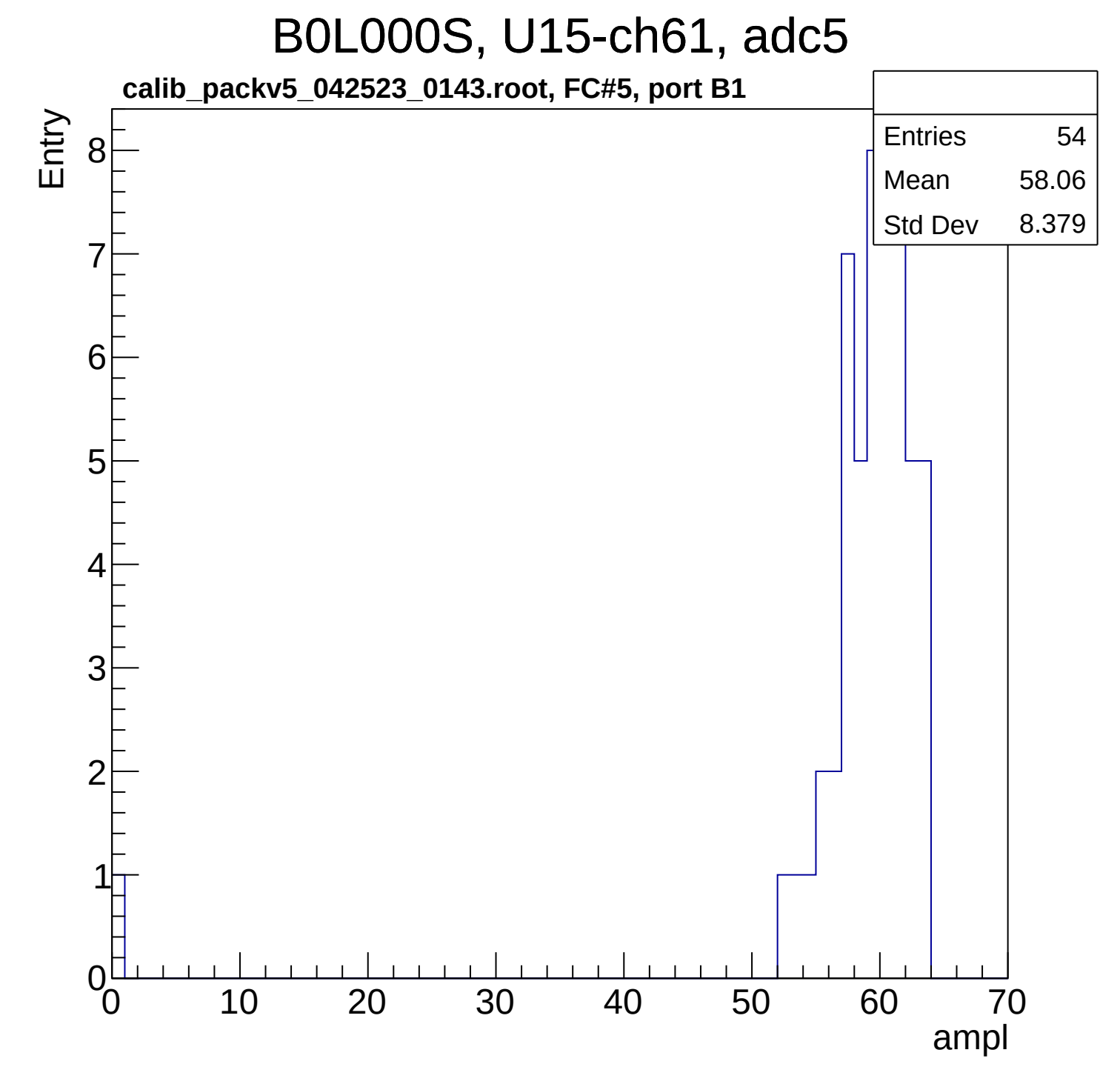
B0L000S, U15-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.06
Std Dev 8.379
Entry
ampl
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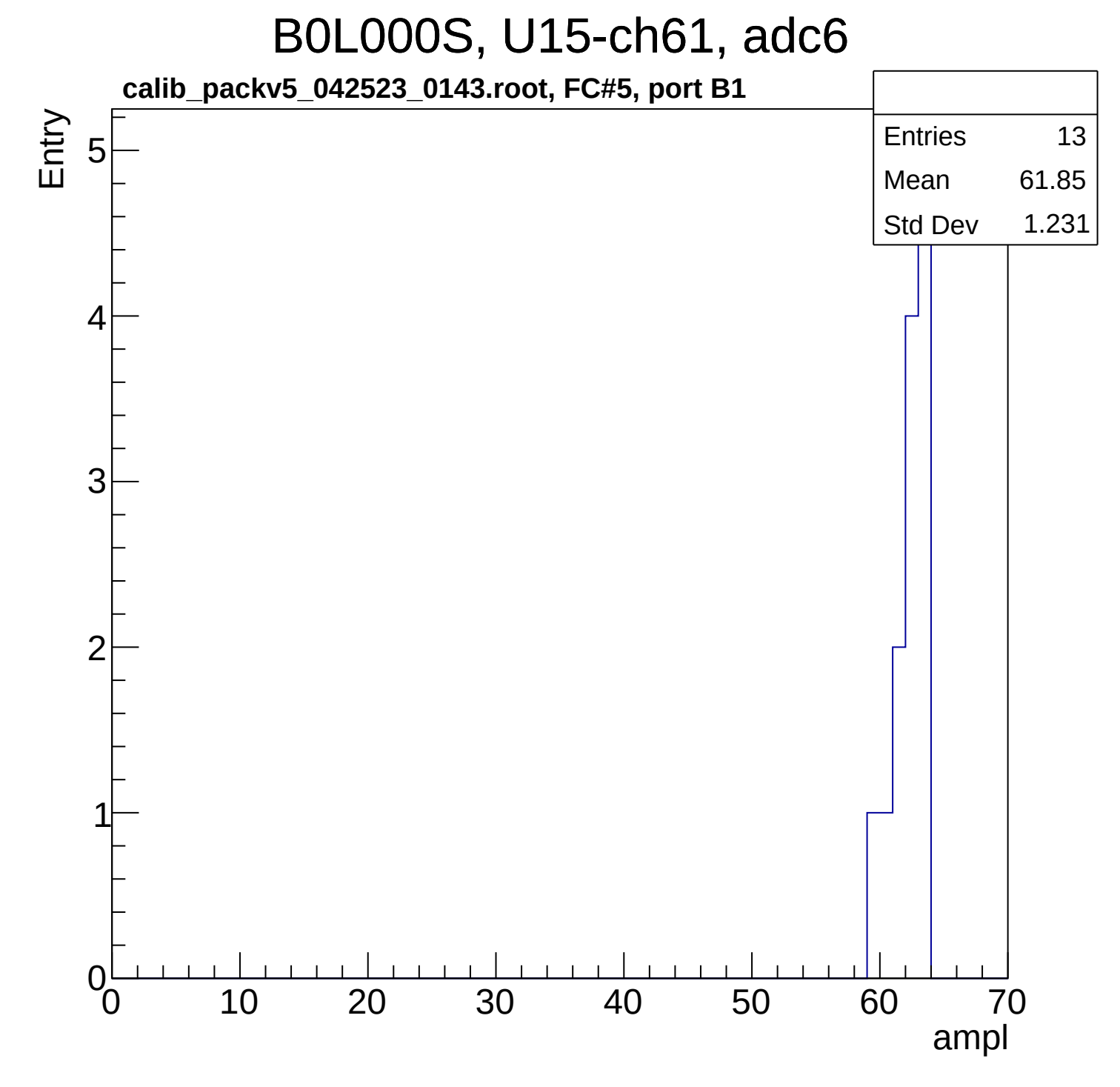
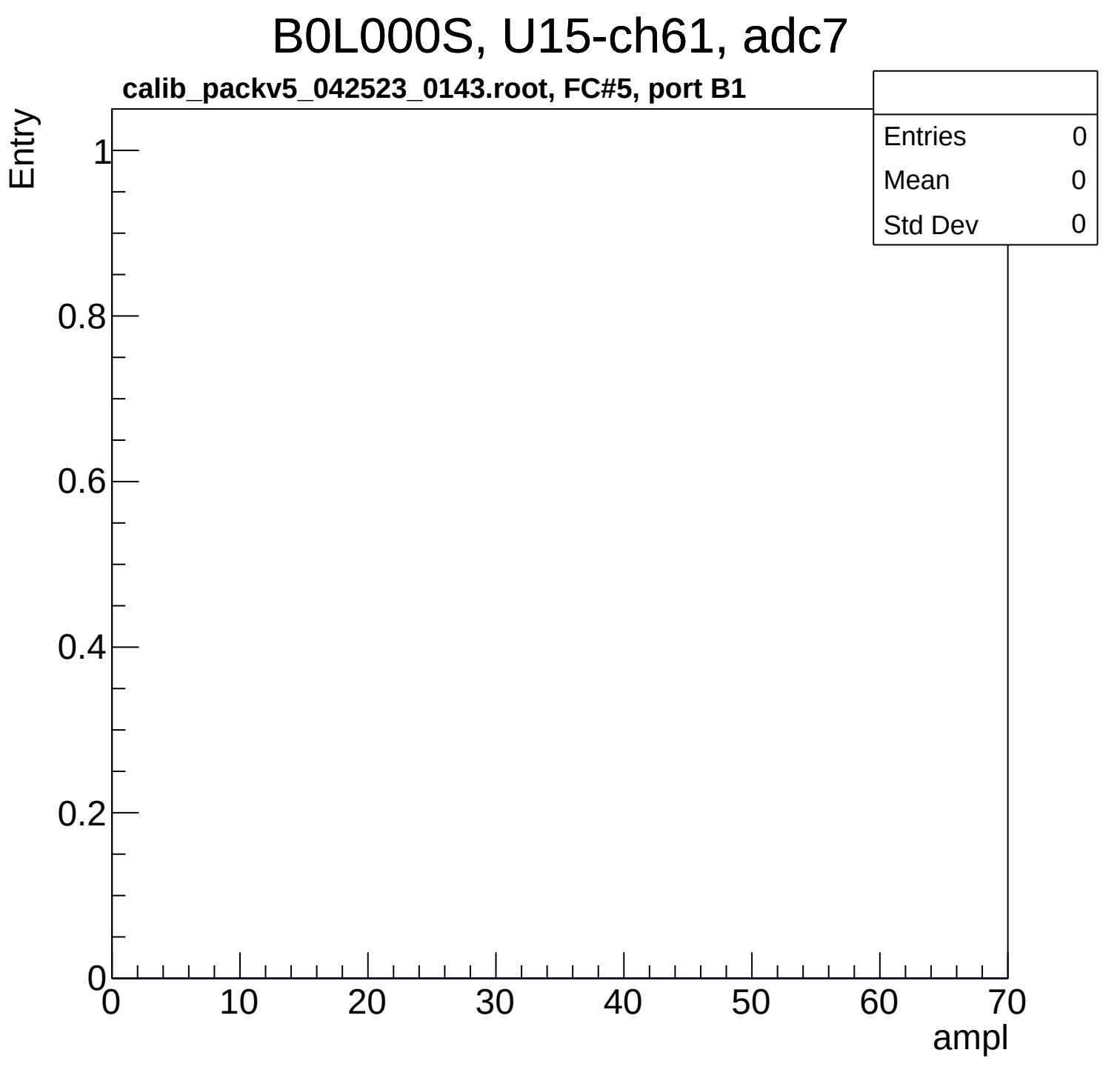
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.85
Std Dev 1.231
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U15-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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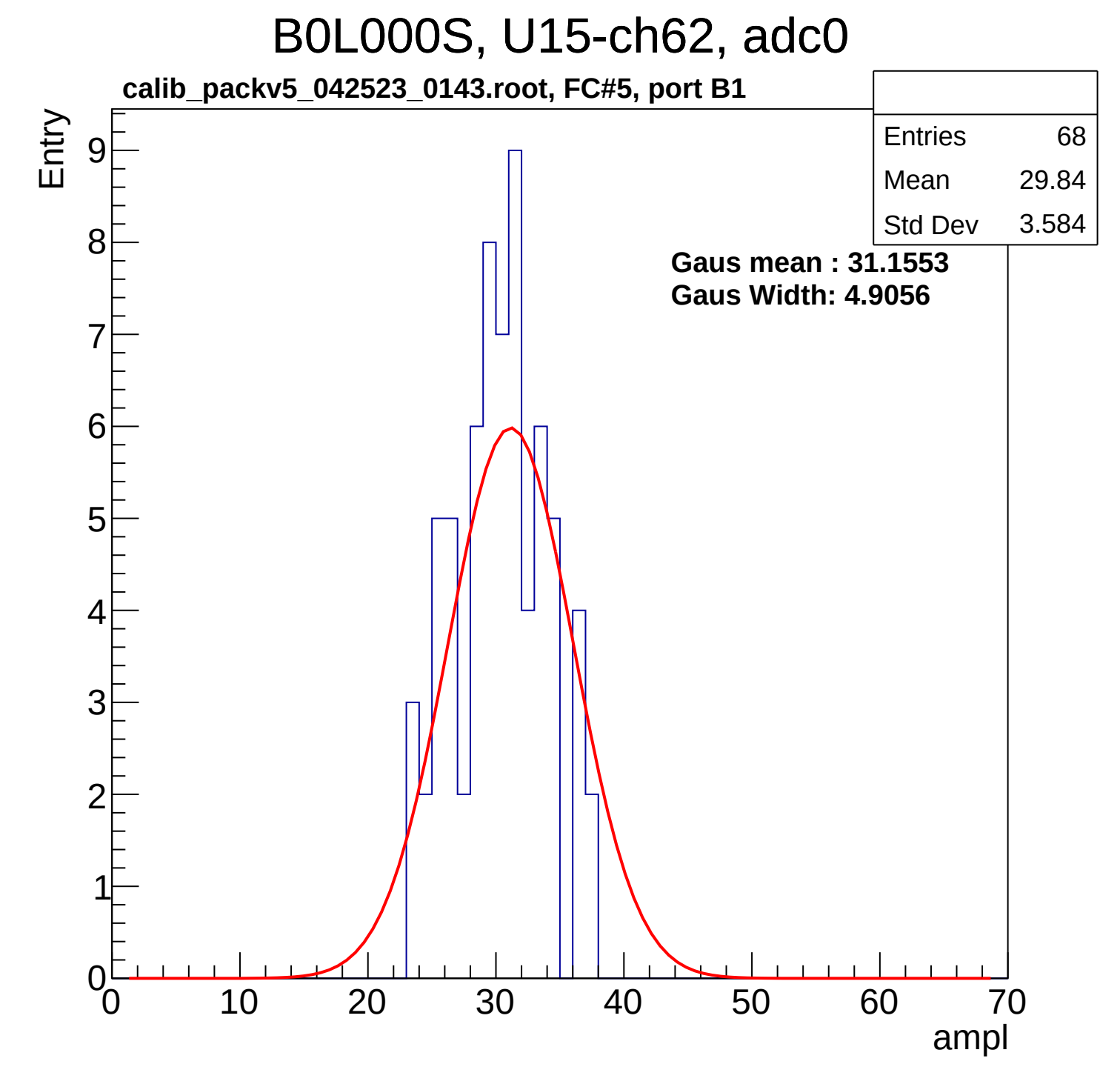
60
70
ampl


B0L000S, U15-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.84
Std Dev 3.584
Gaus mean : 31.1553
Gaus Width: 4.9056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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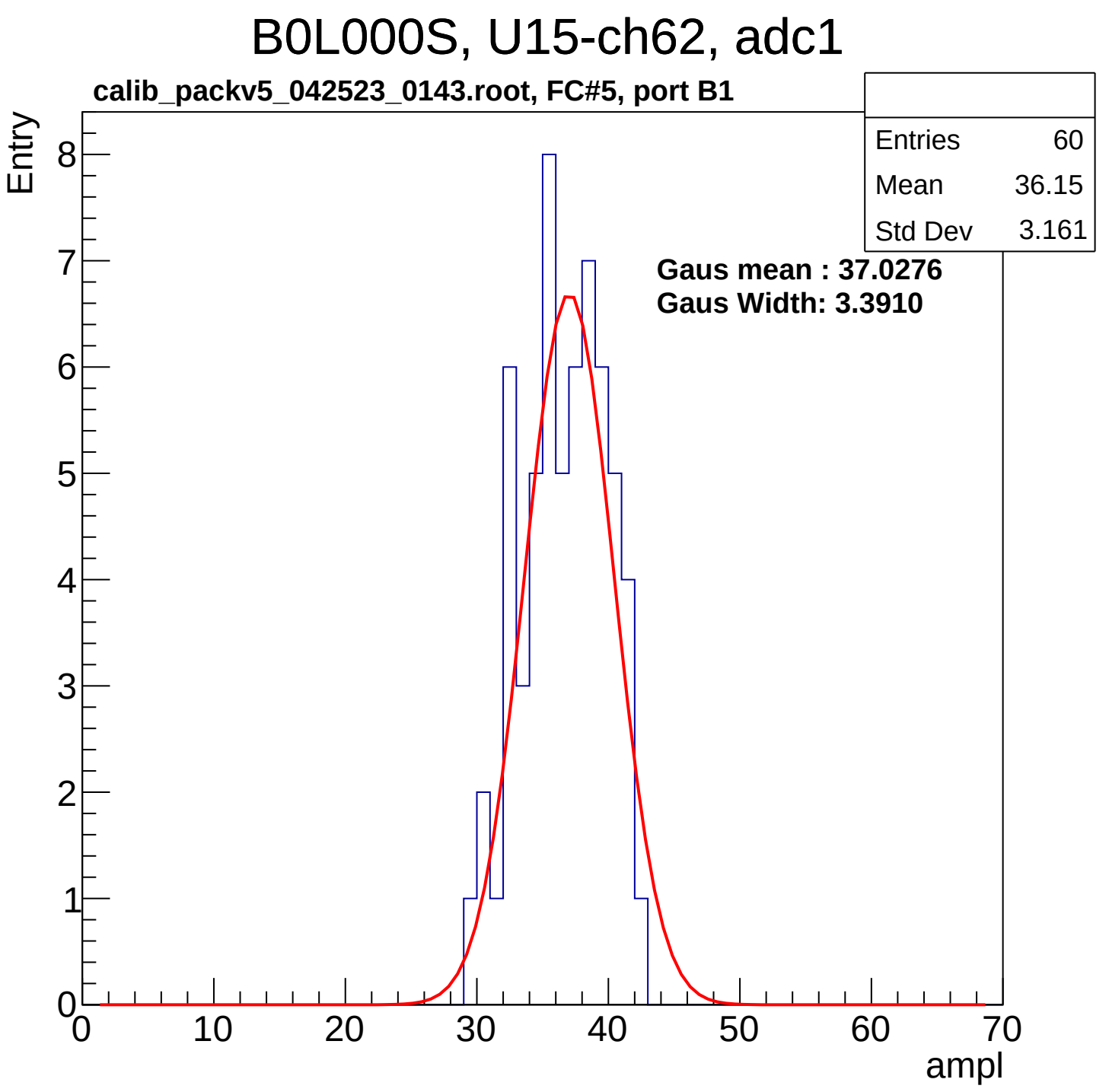
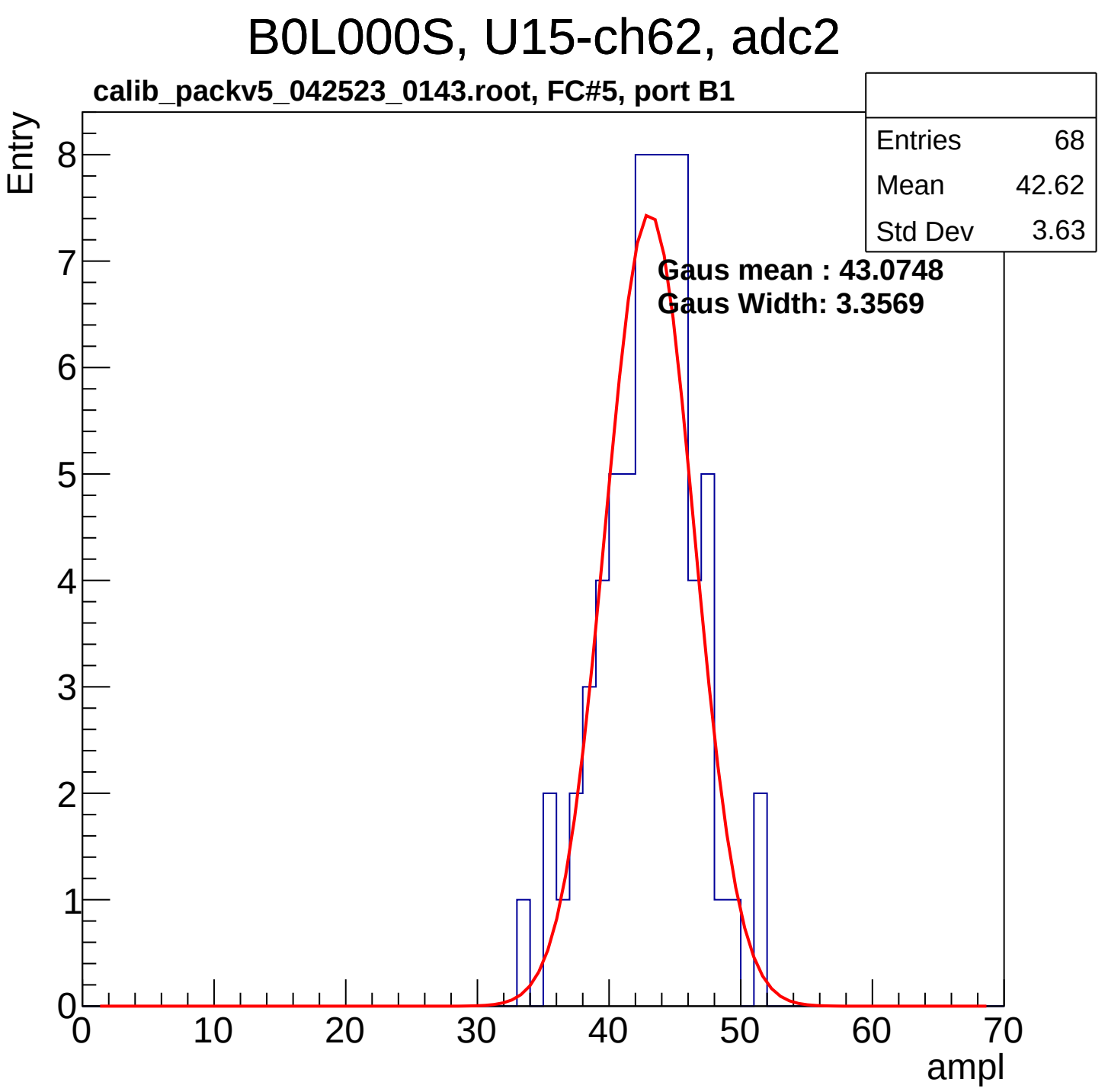
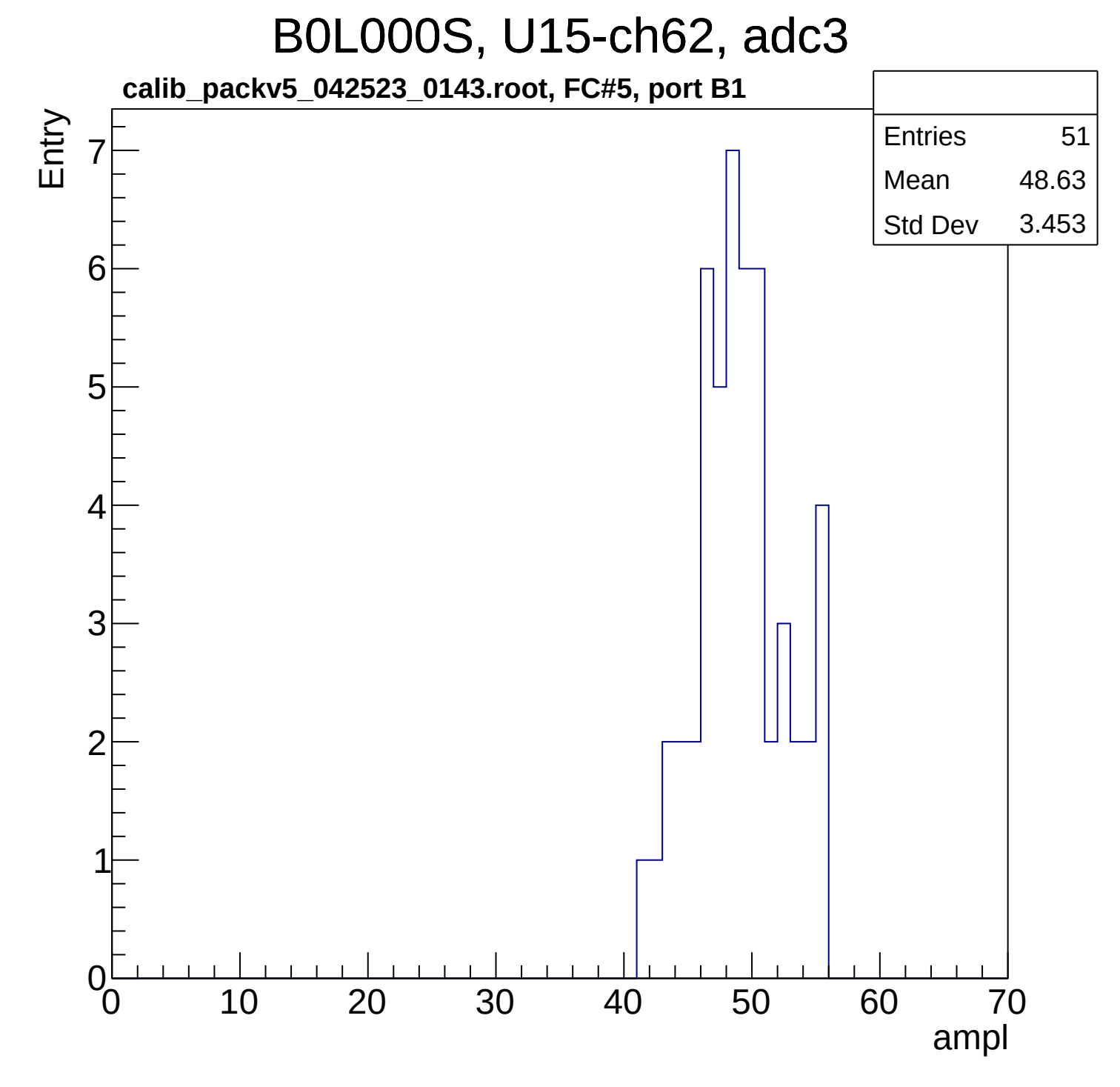
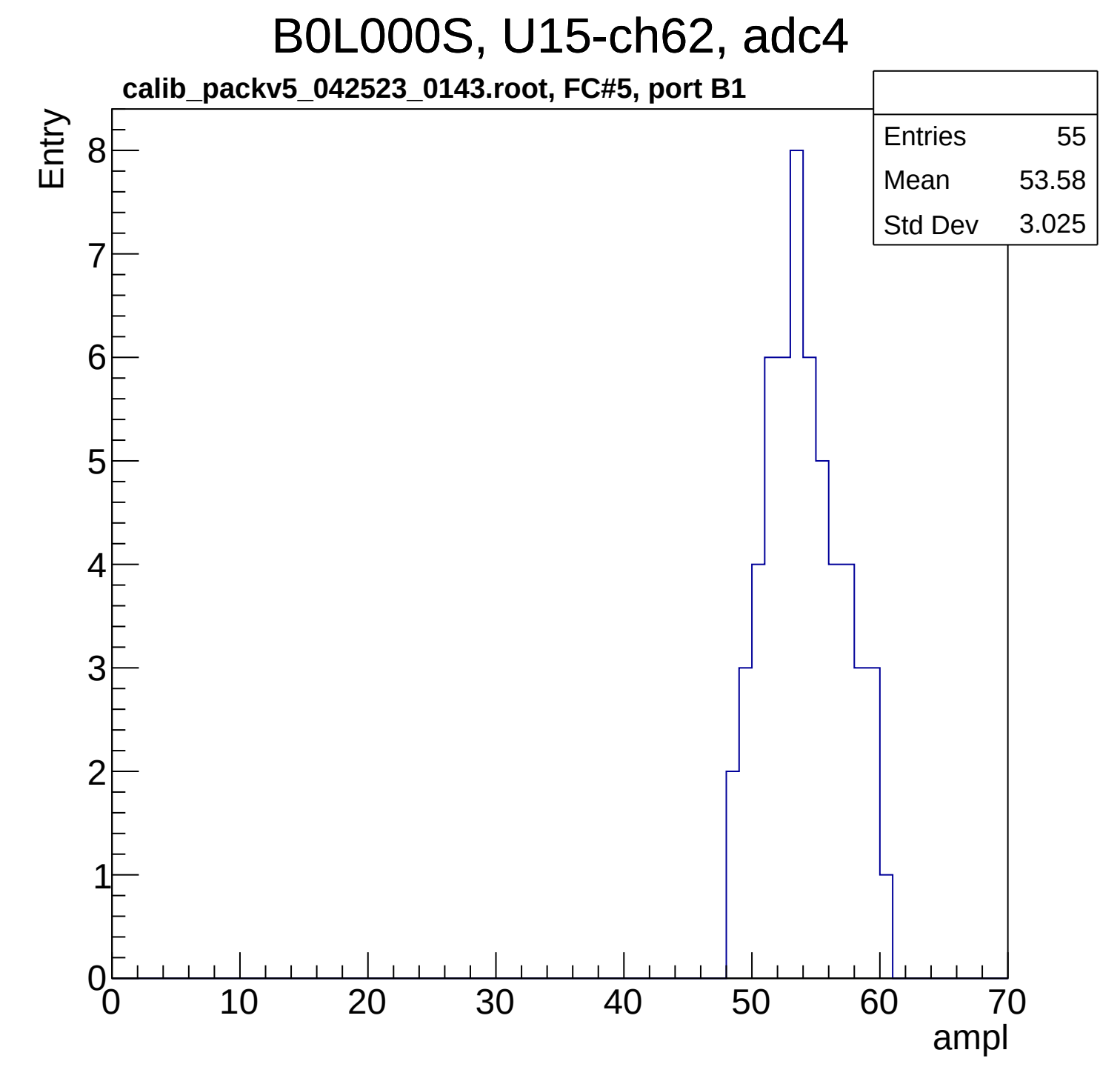
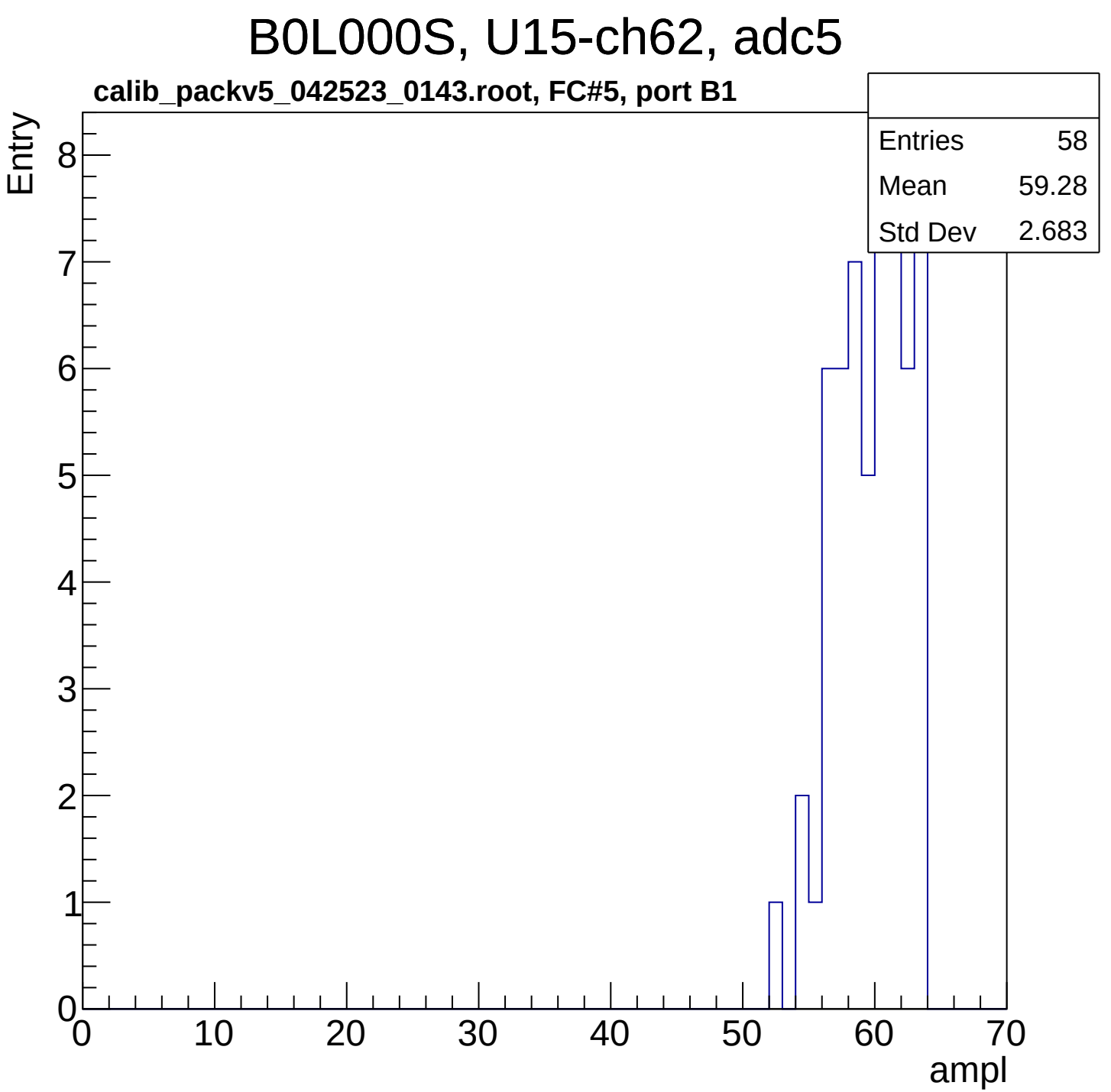

B0L000S, U15-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.15
Std Dev 3.161
Gaus mean : 37.0276
Gaus Width: 3.3910
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.62
Std Dev 3.63
Gaus mean : 43.0748
Gaus Width: 3.3569
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 48.63
Std Dev 3.453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.58
Std Dev 3.025
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 59.28
Std Dev 2.683
Entry
ampl
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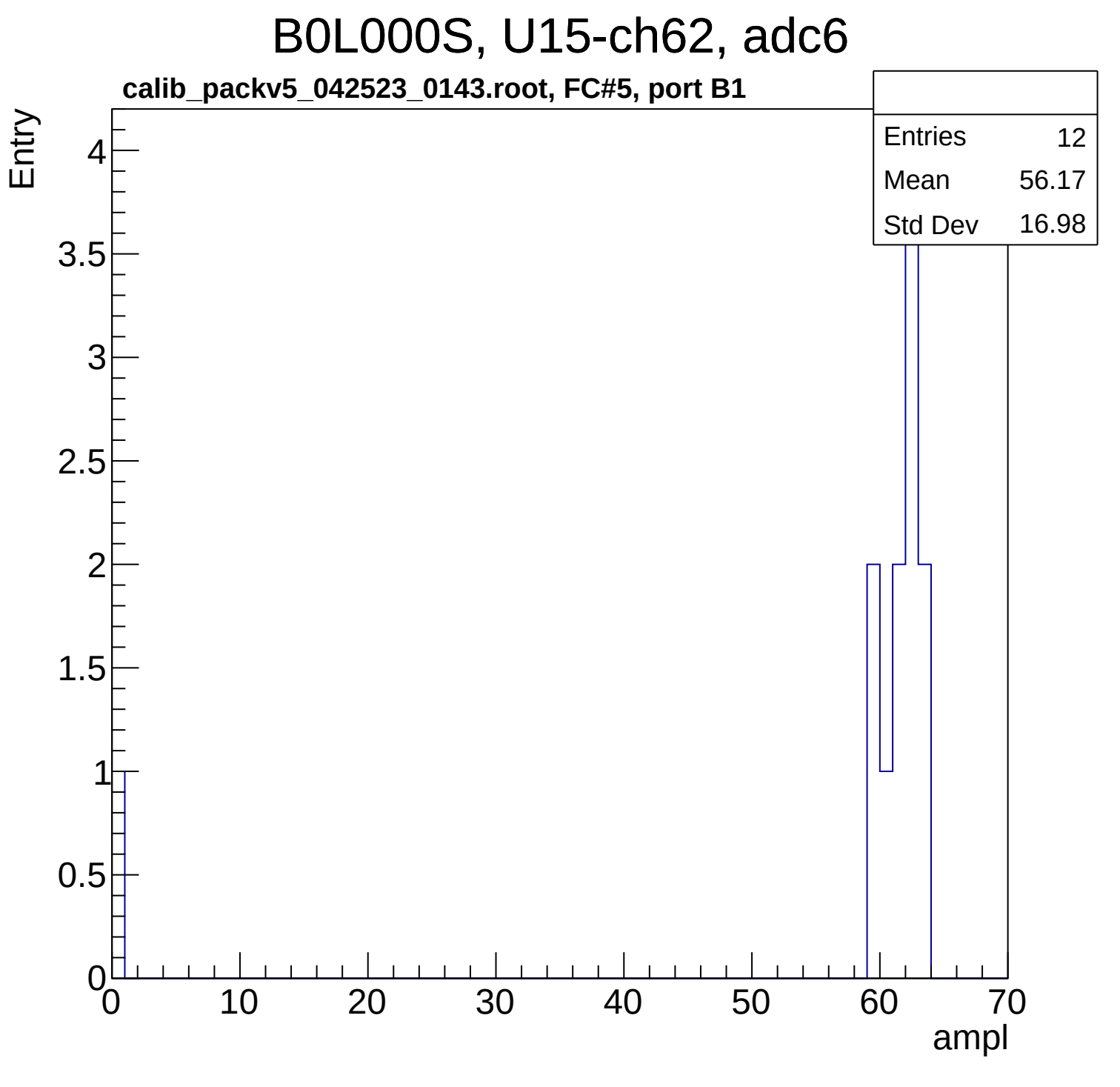
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.17
Std Dev 16.98
Entry
ampl
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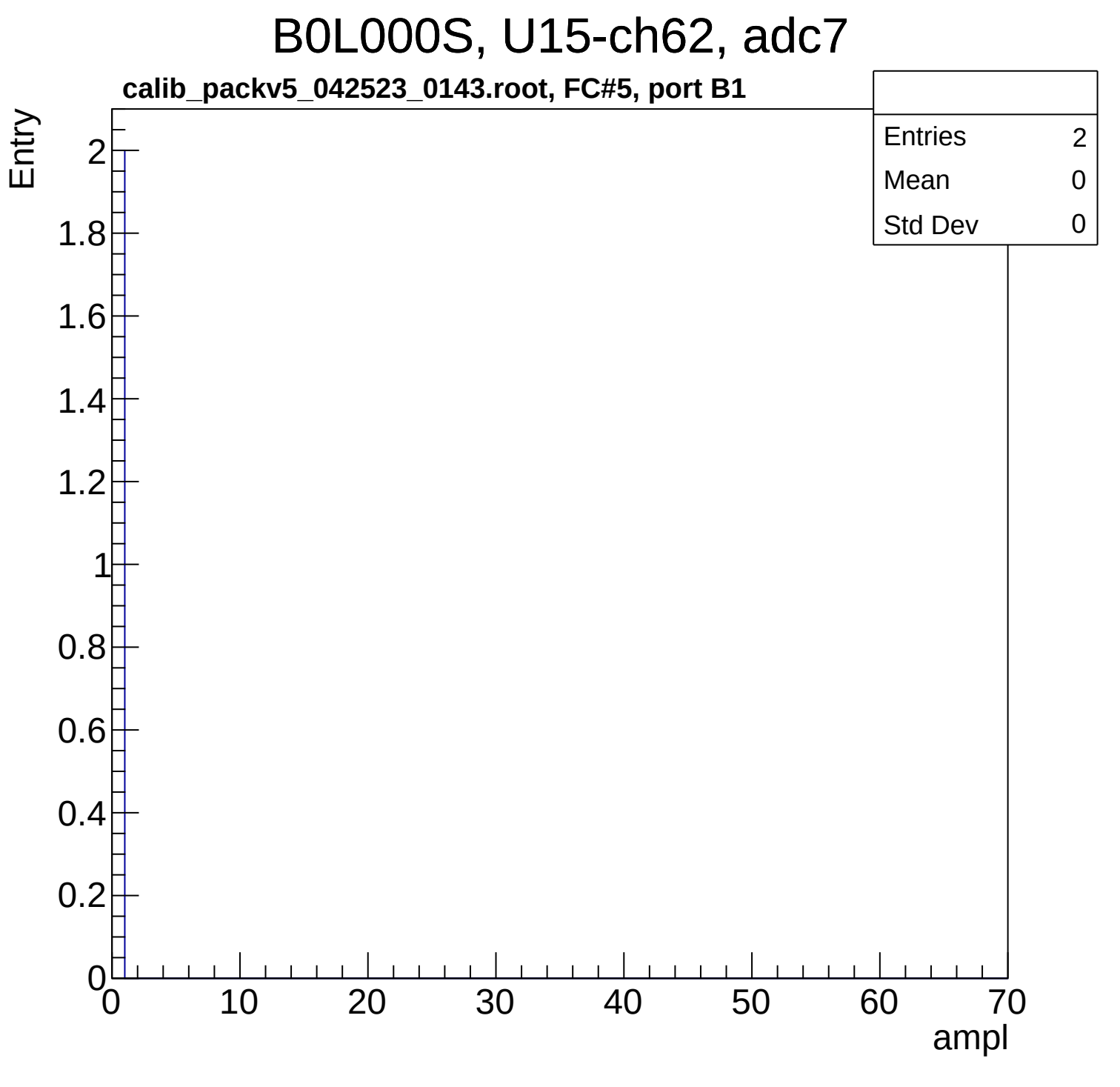
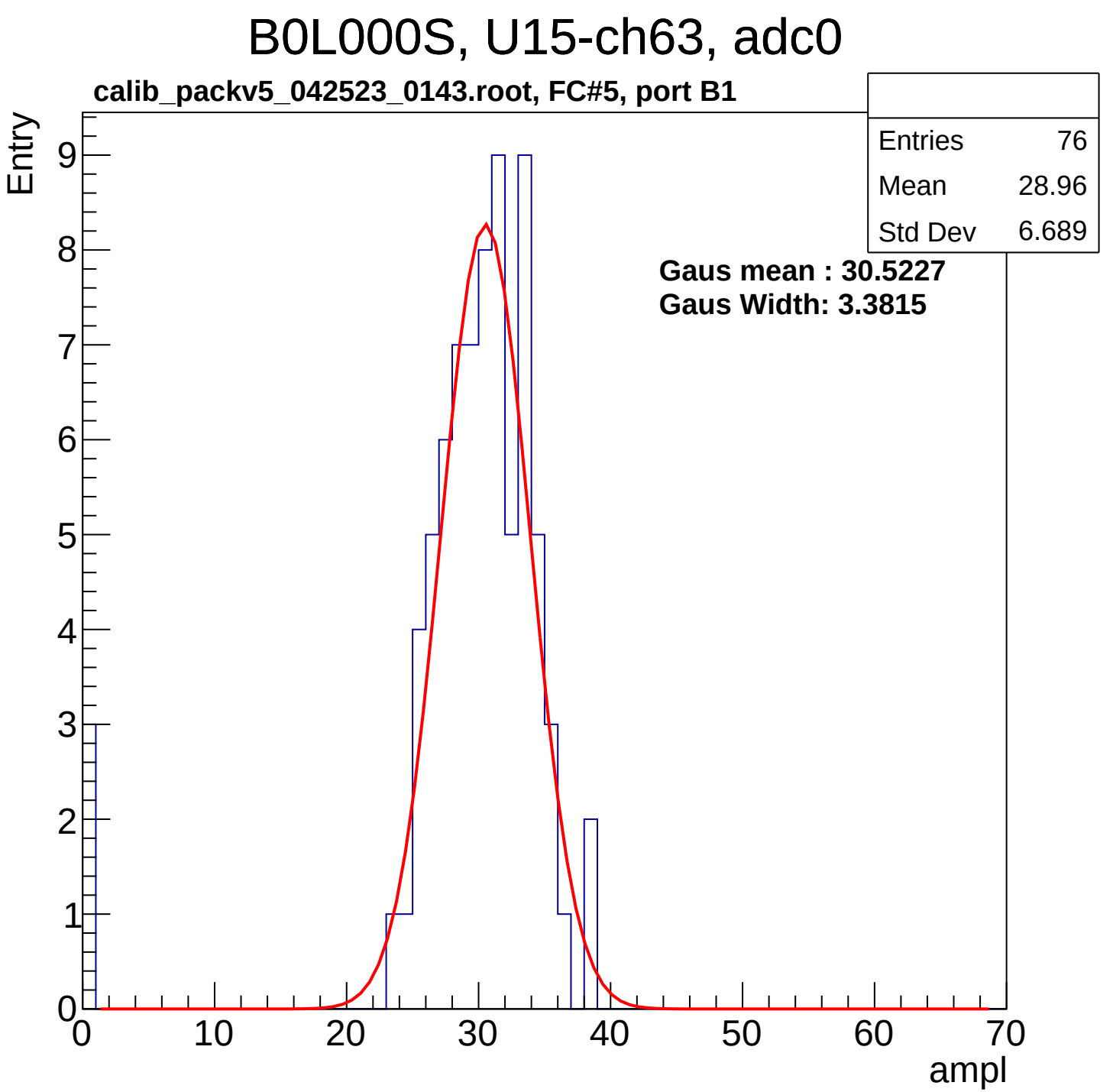
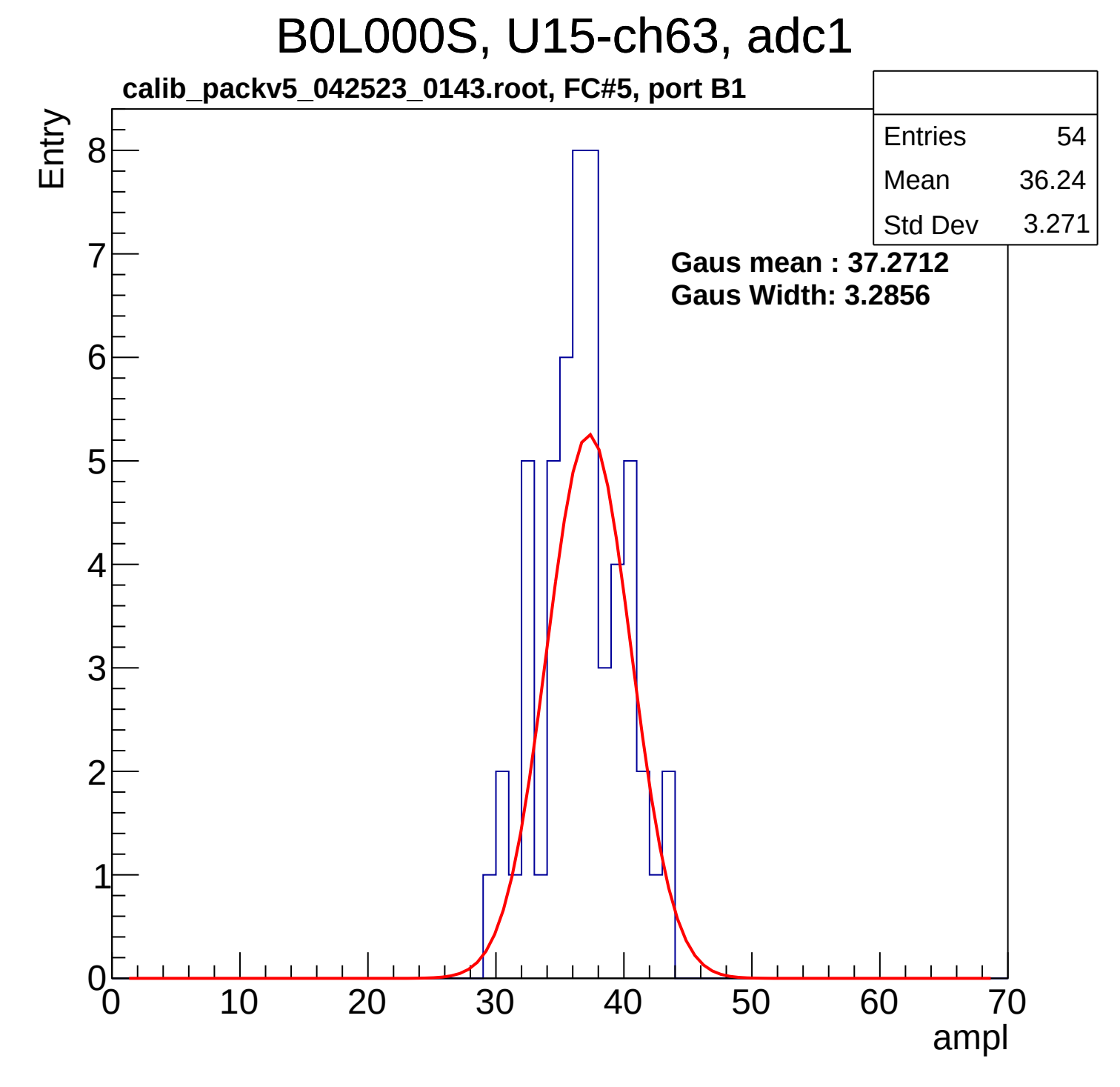
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.96
Std Dev 6.689
Gaus mean : 30.5227
Gaus Width: 3.3815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 36.24
Std Dev 3.271
Gaus mean : 37.2712
Gaus Width: 3.2856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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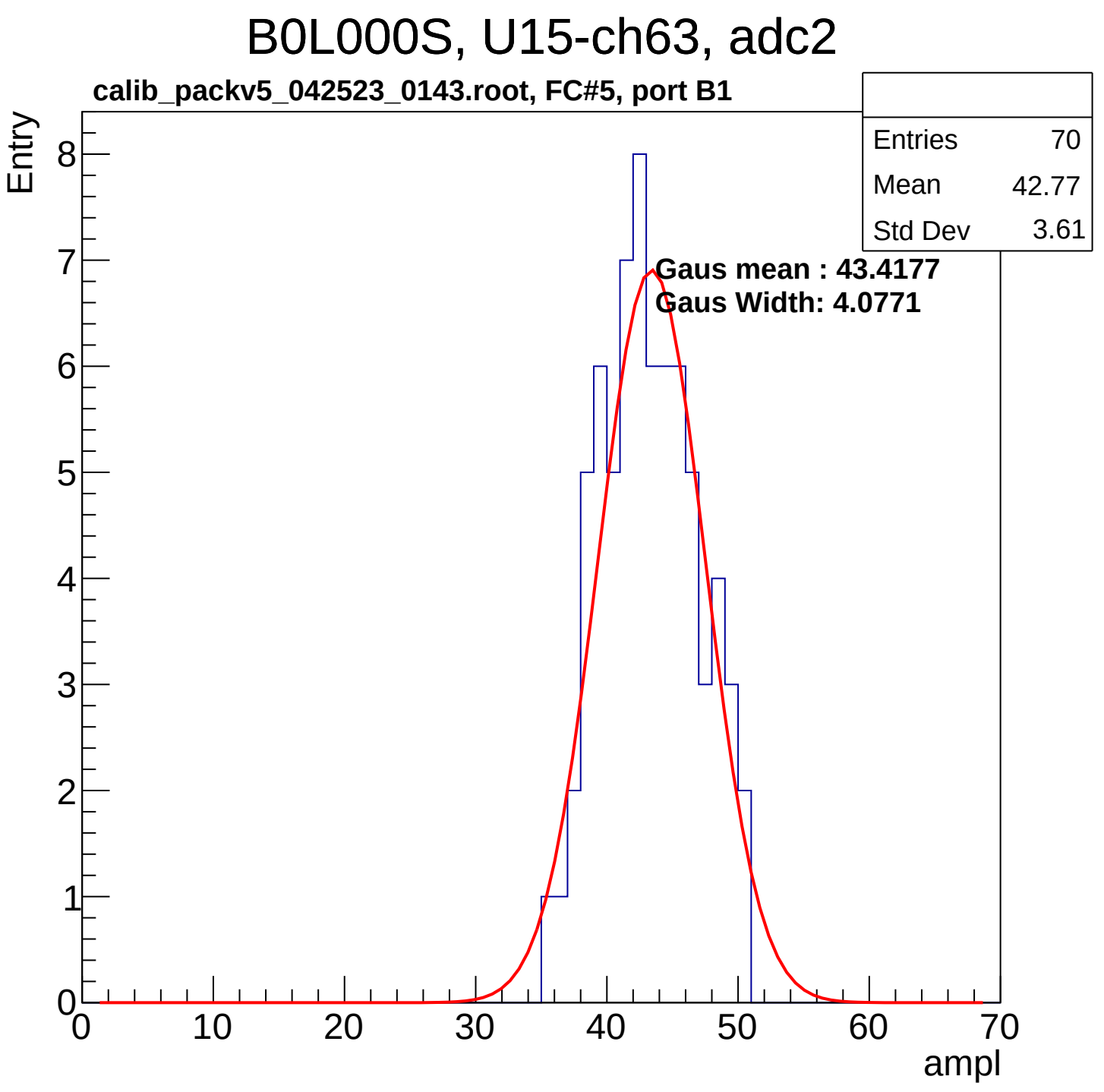
B0L000S, U15-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.77
Std Dev 3.61
Gaus mean : 43.4177
Gaus Width: 4.0771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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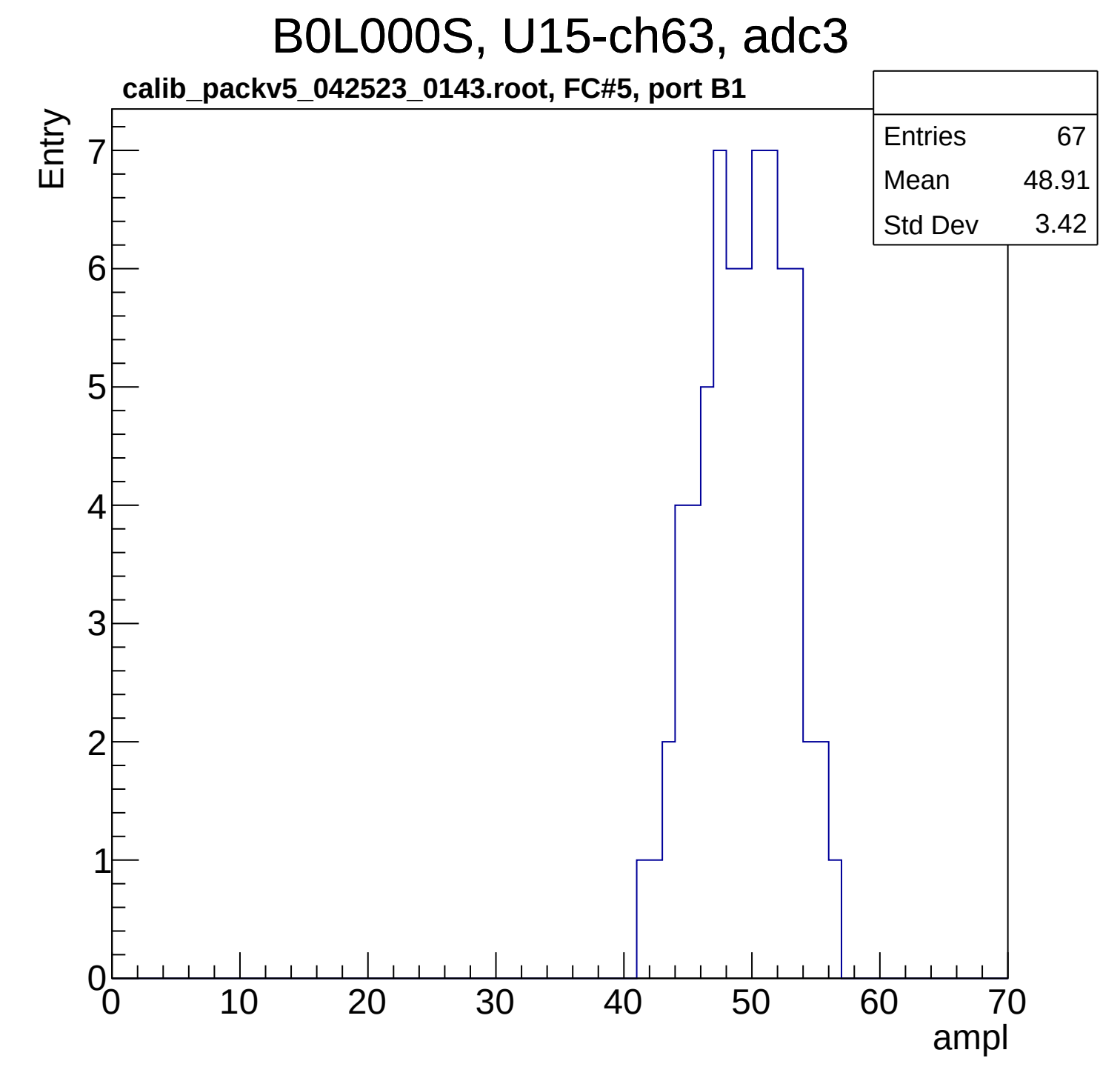
B0L000S, U15-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.91
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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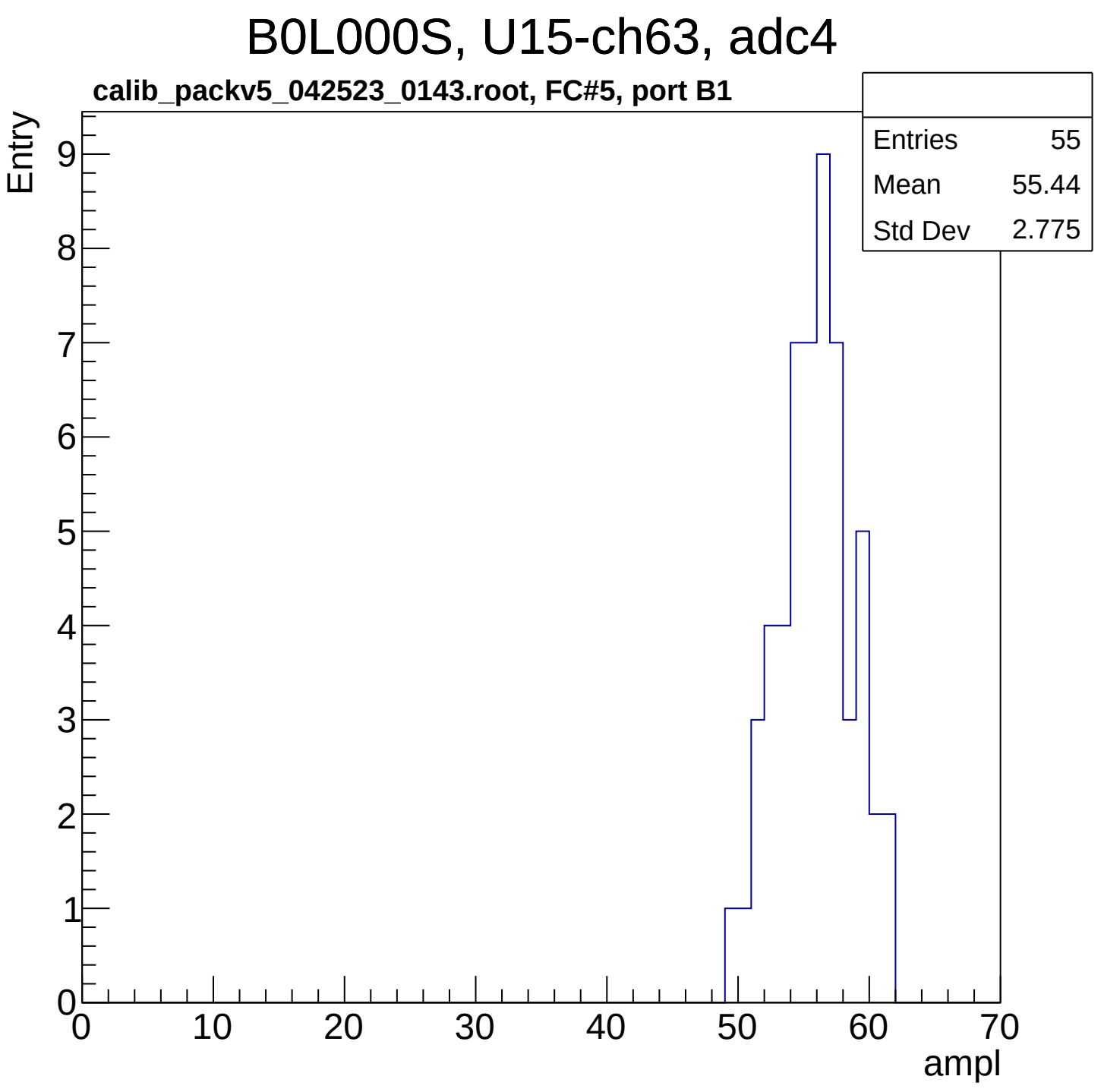
40
50
60
70


B0L000S, U15-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.44
Std Dev 2.775
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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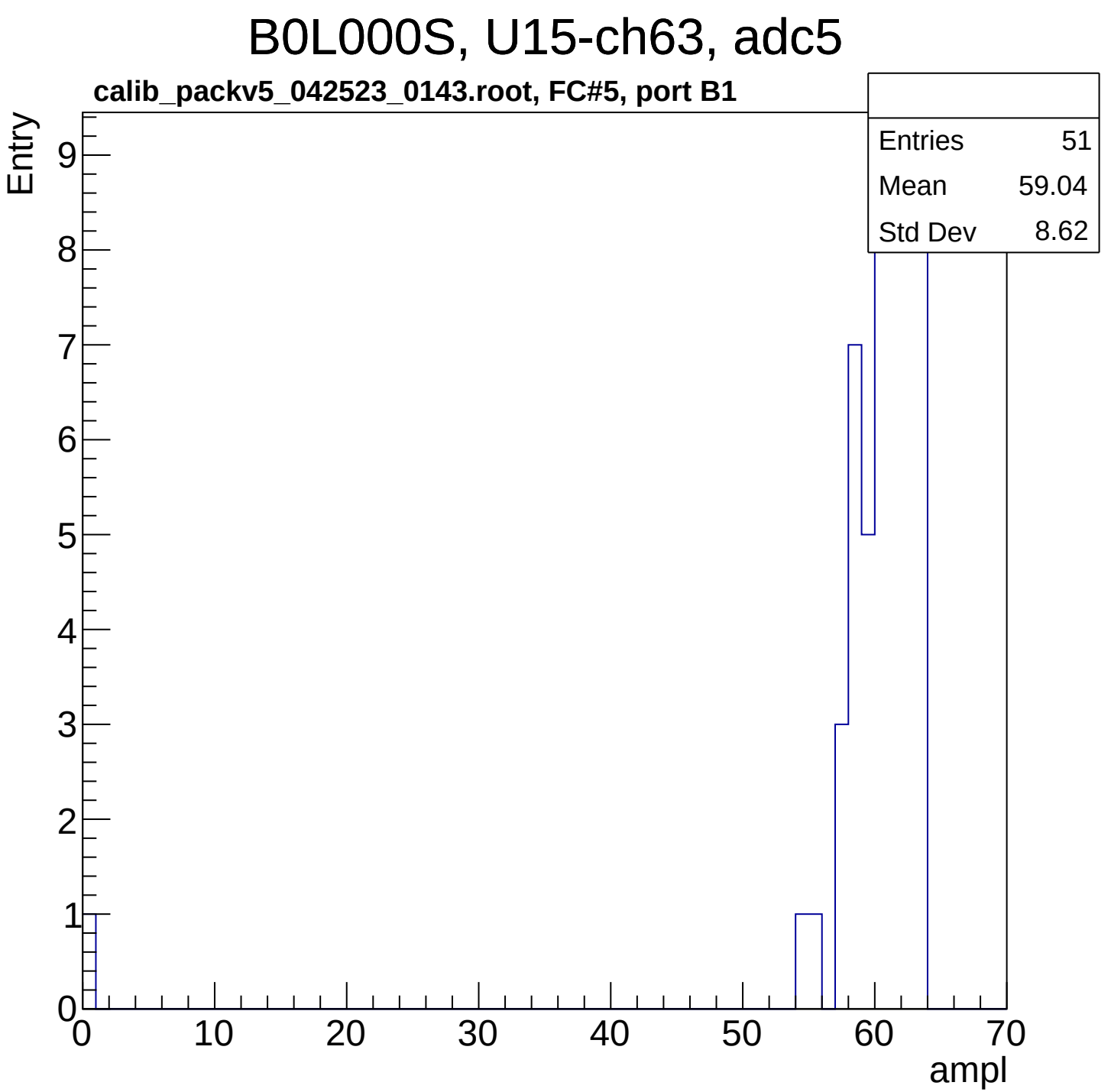
B0L000S, U15-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.04
Std Dev 8.62
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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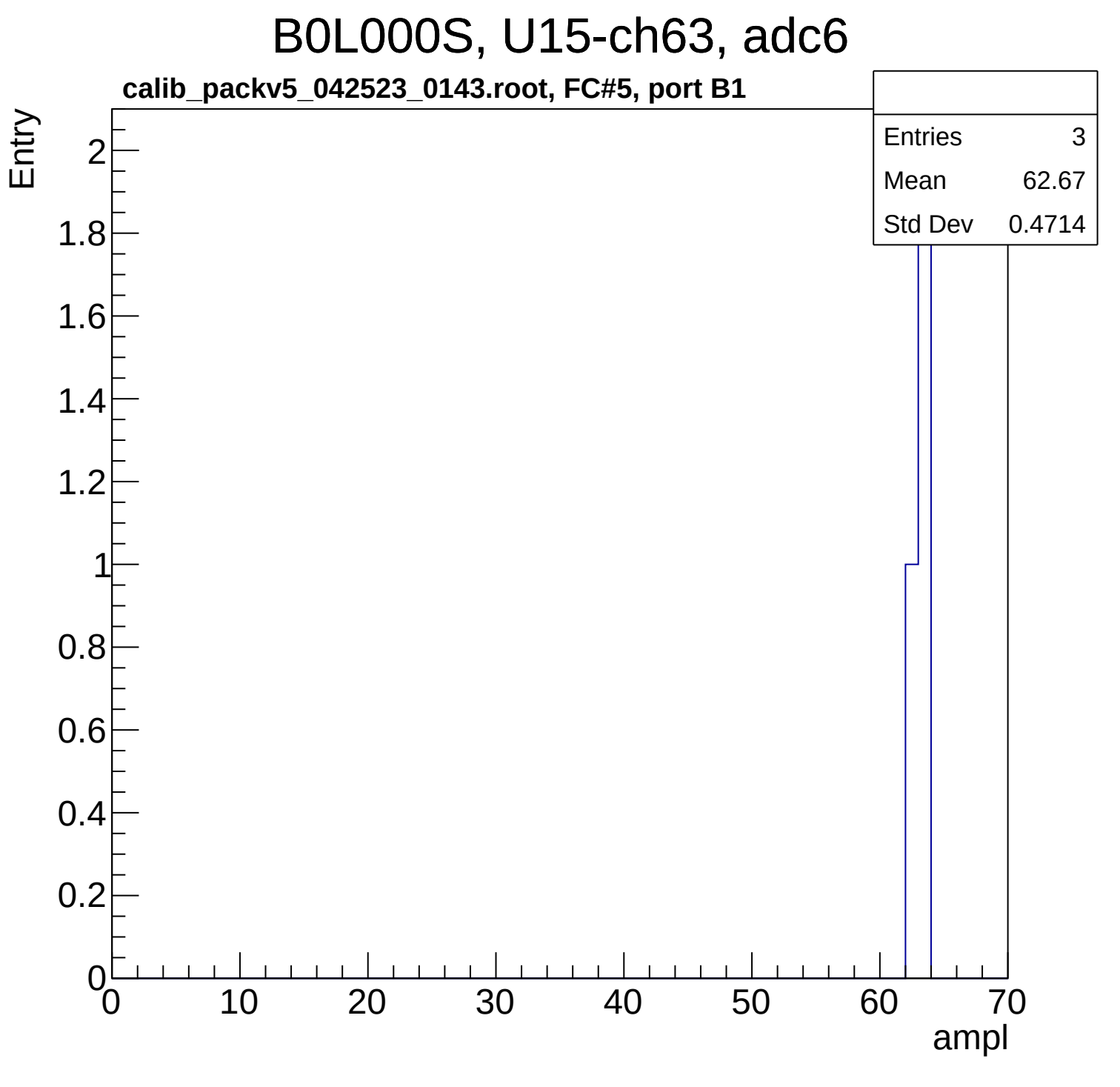
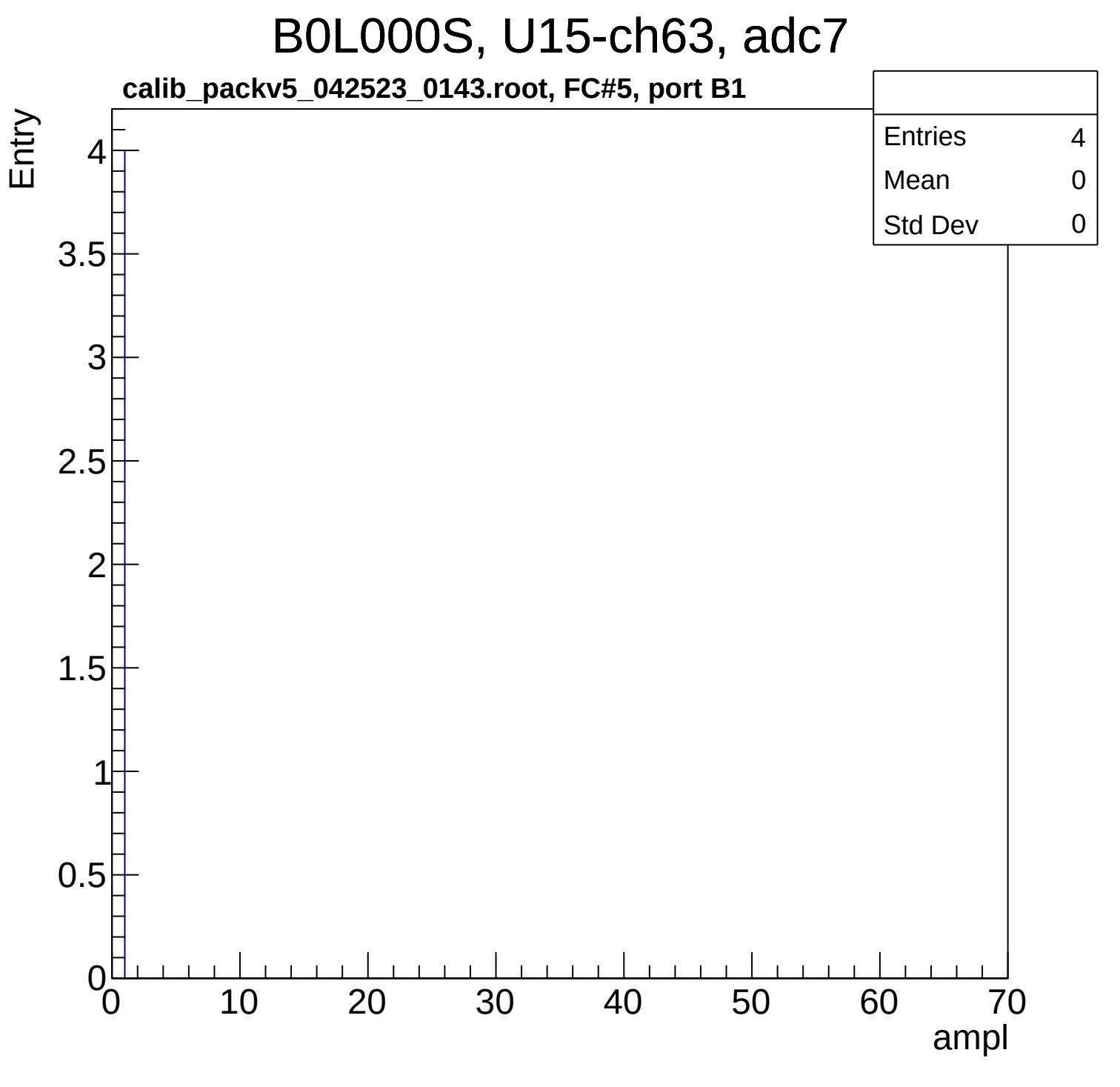

B0L000S, U15-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
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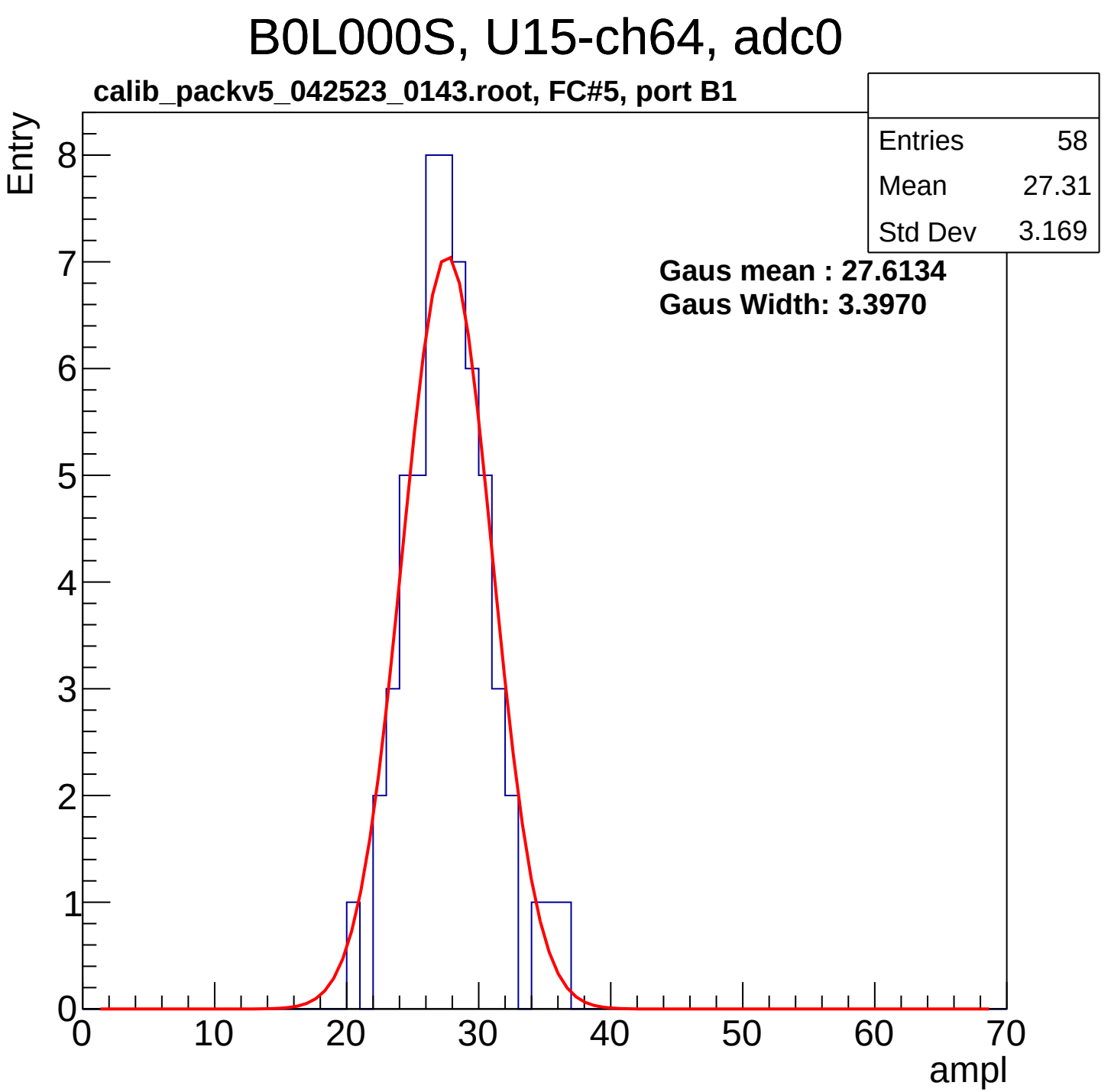
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 27.31
Std Dev 3.169
Gaus mean : 27.6134
Gaus Width: 3.3970
Entry
ampl
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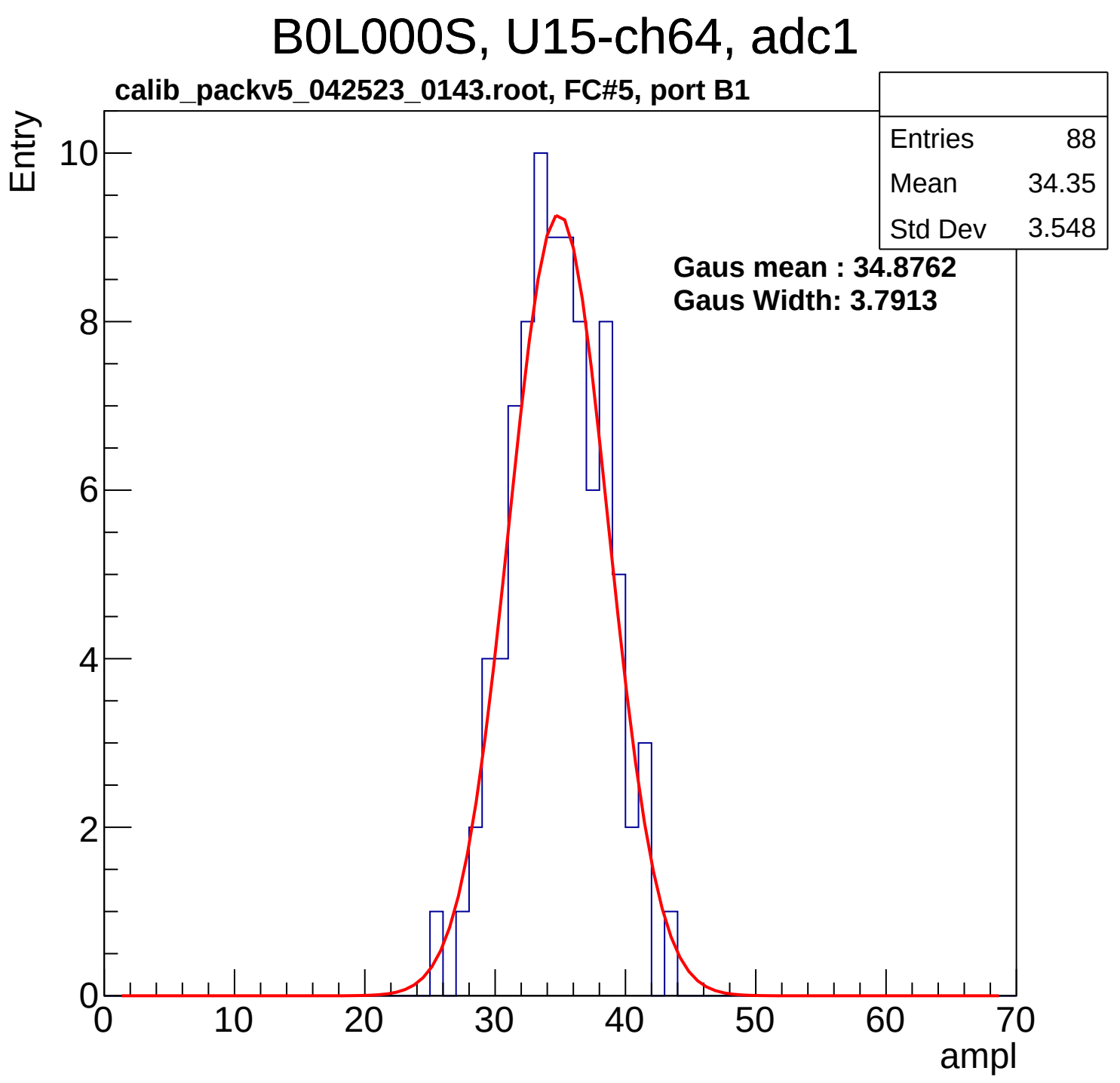
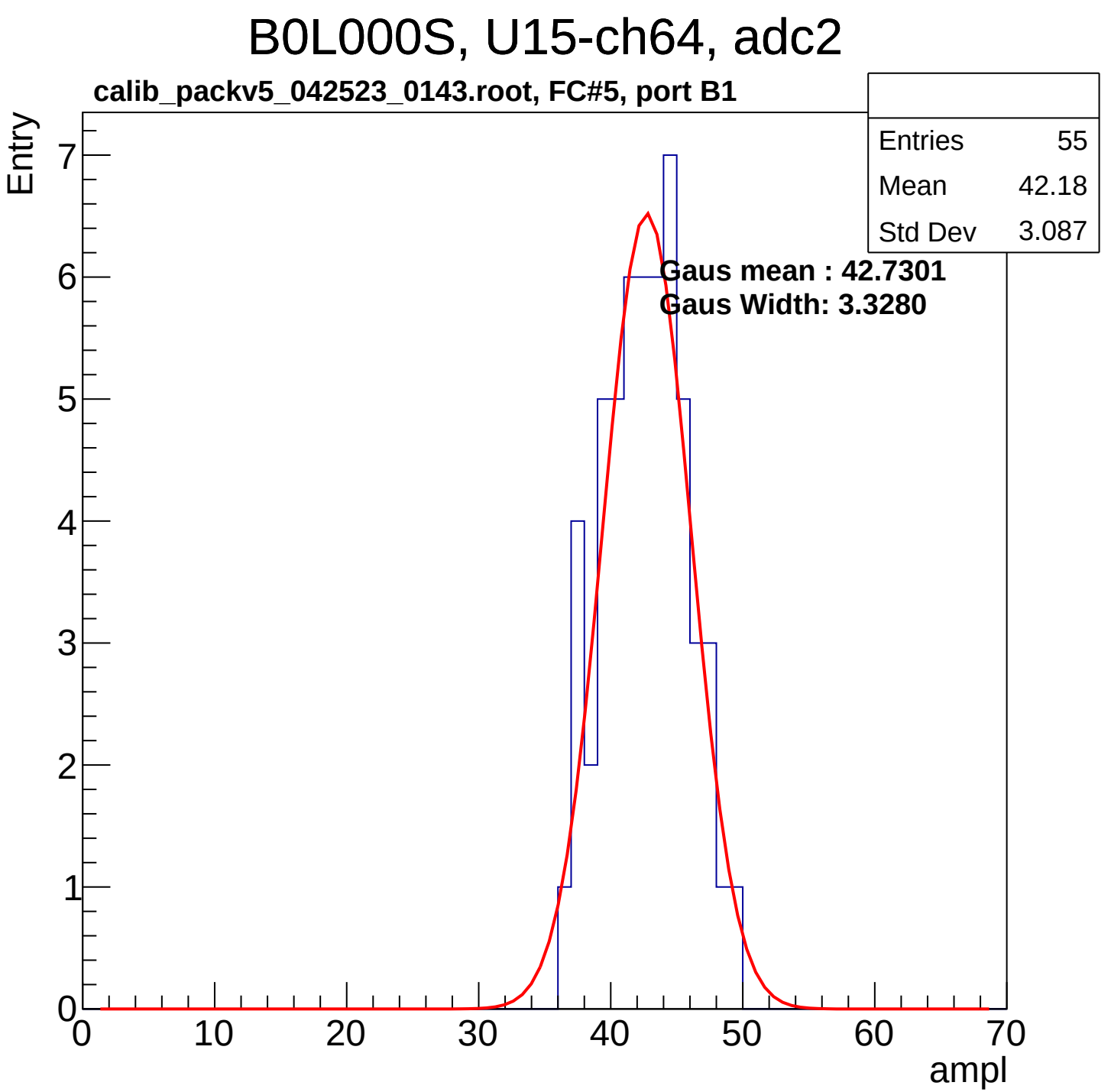
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 34.35
Std Dev 3.548
Gaus mean : 34.8762
Gaus Width: 3.7913
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.18
Std Dev 3.087
Gaus mean : 42.7301
Gaus Width: 3.3280
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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60
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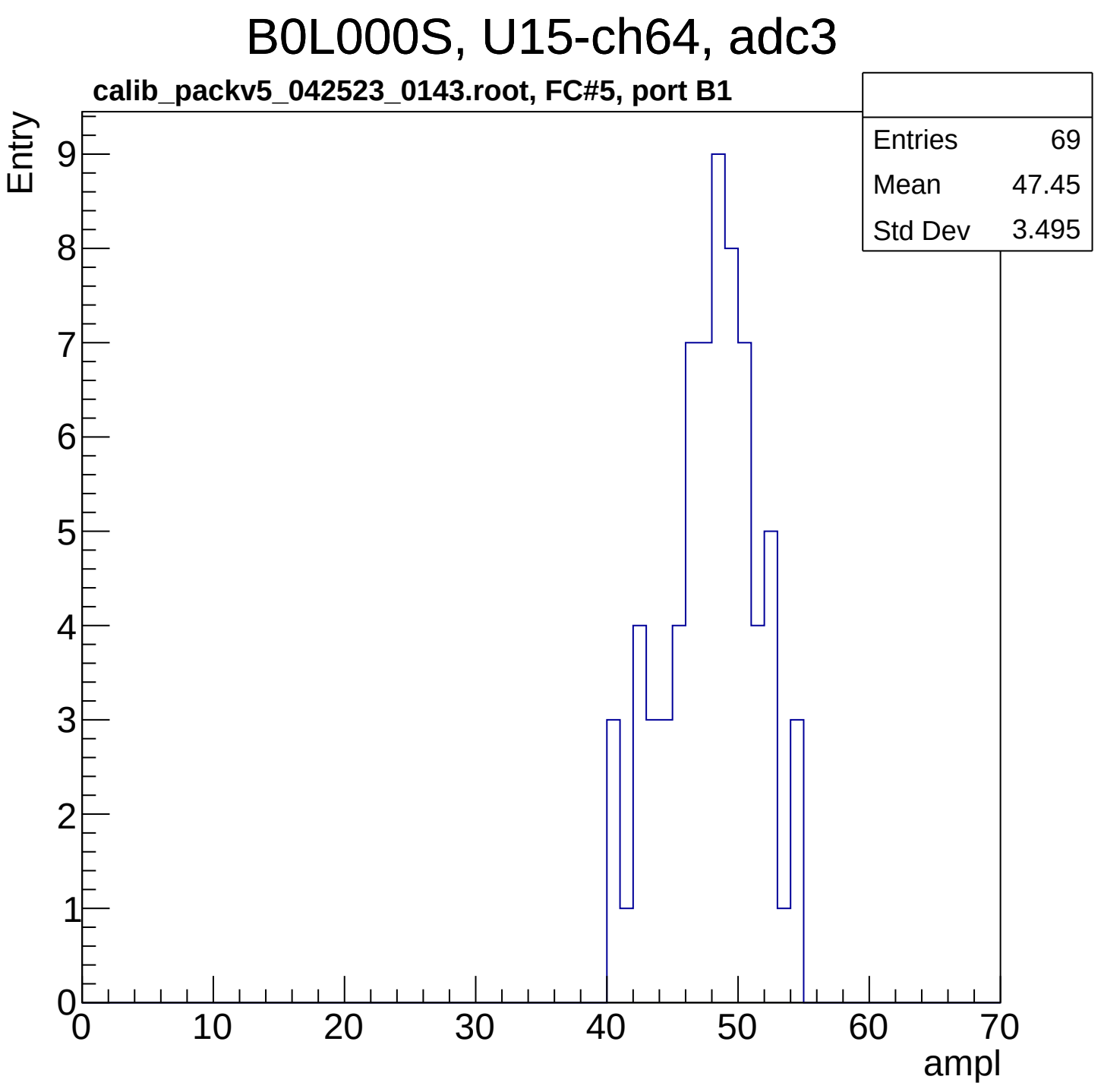
B0L000S, U15-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.45
Std Dev 3.495
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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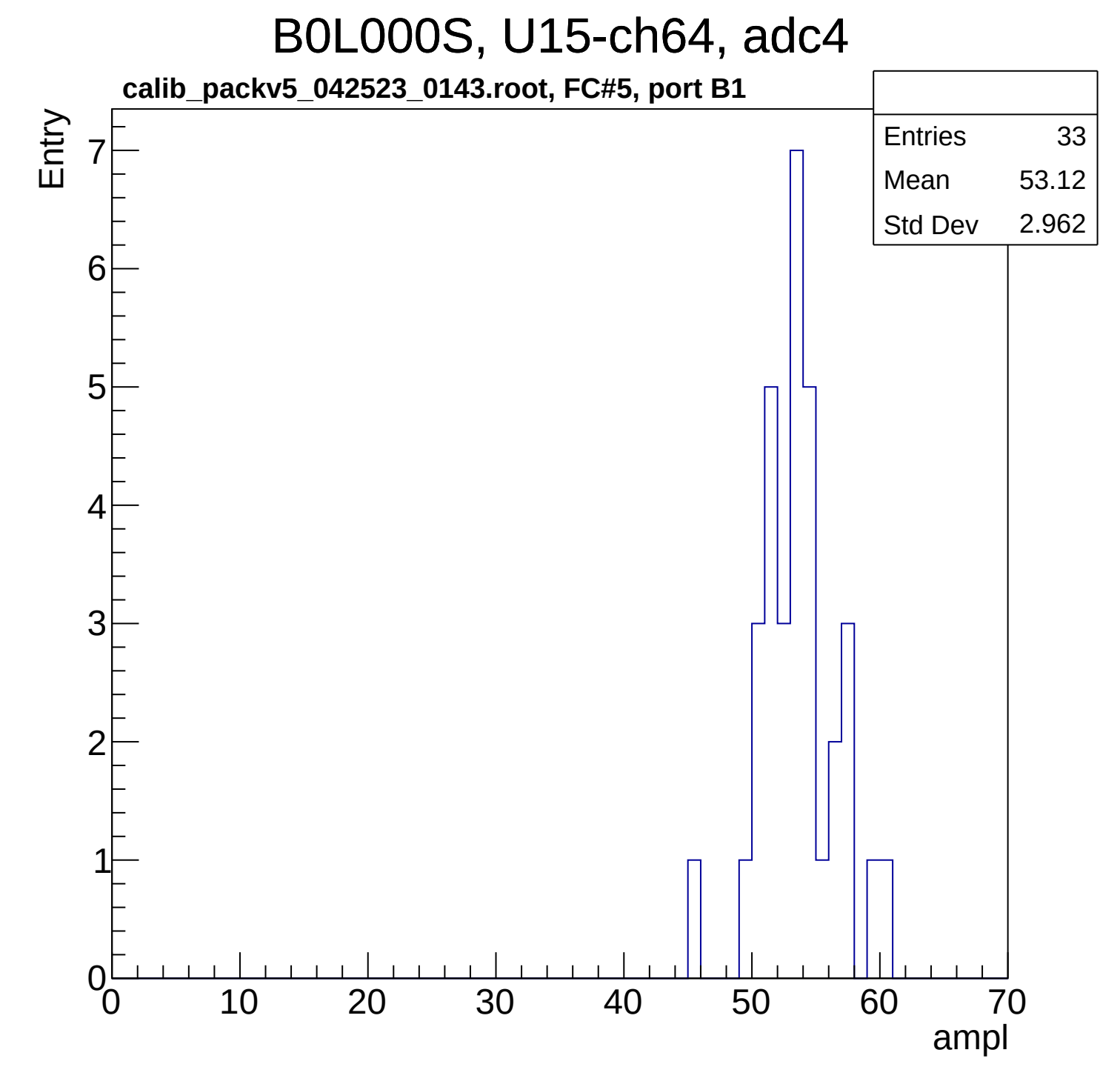
B0L000S, U15-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 53.12
Std Dev 2.962
Entry
ampl
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1
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3
4
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6
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0
10
20
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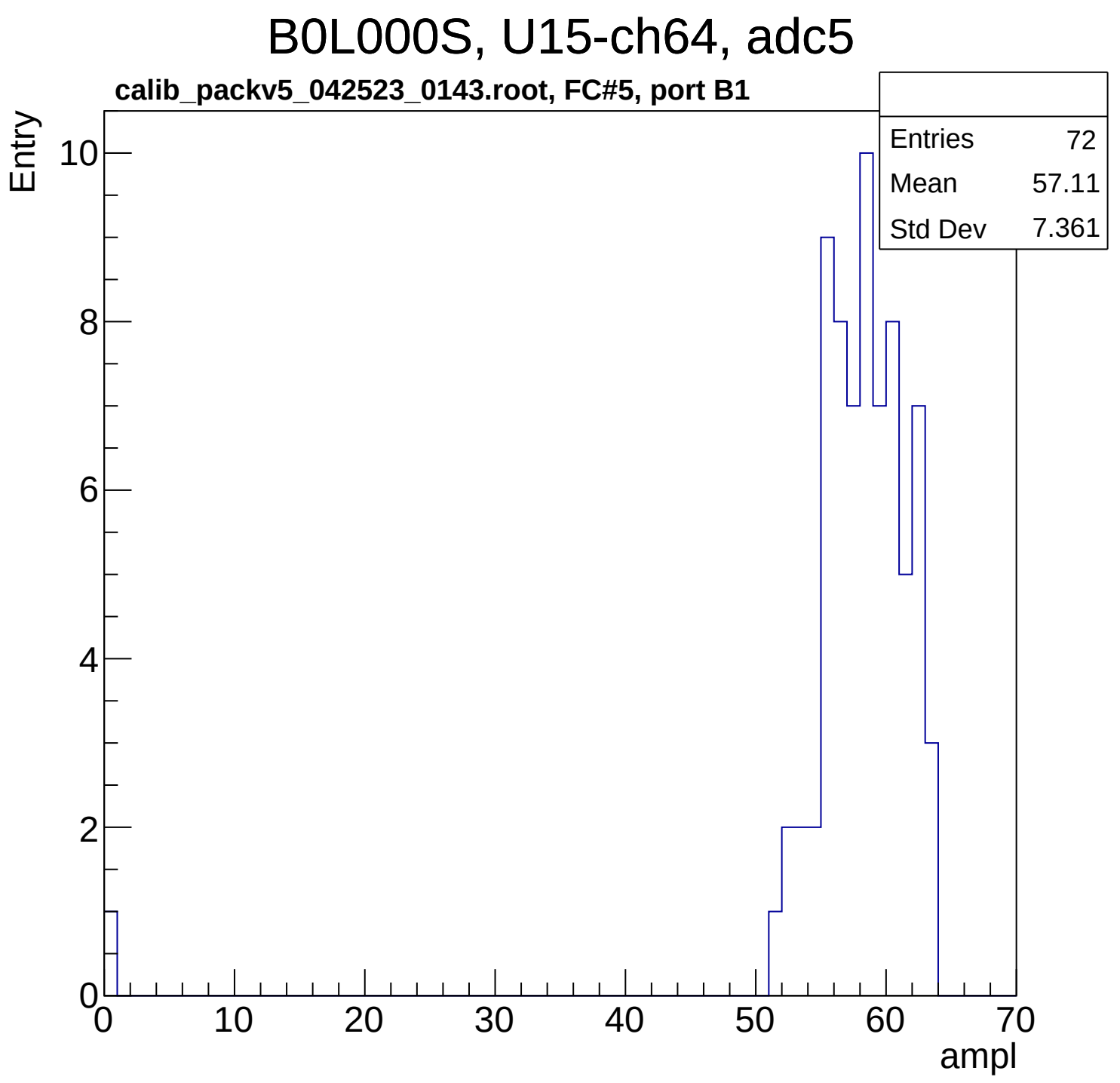
40
50
60
70


B0L000S, U15-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 57.11
Std Dev 7.361
Entry
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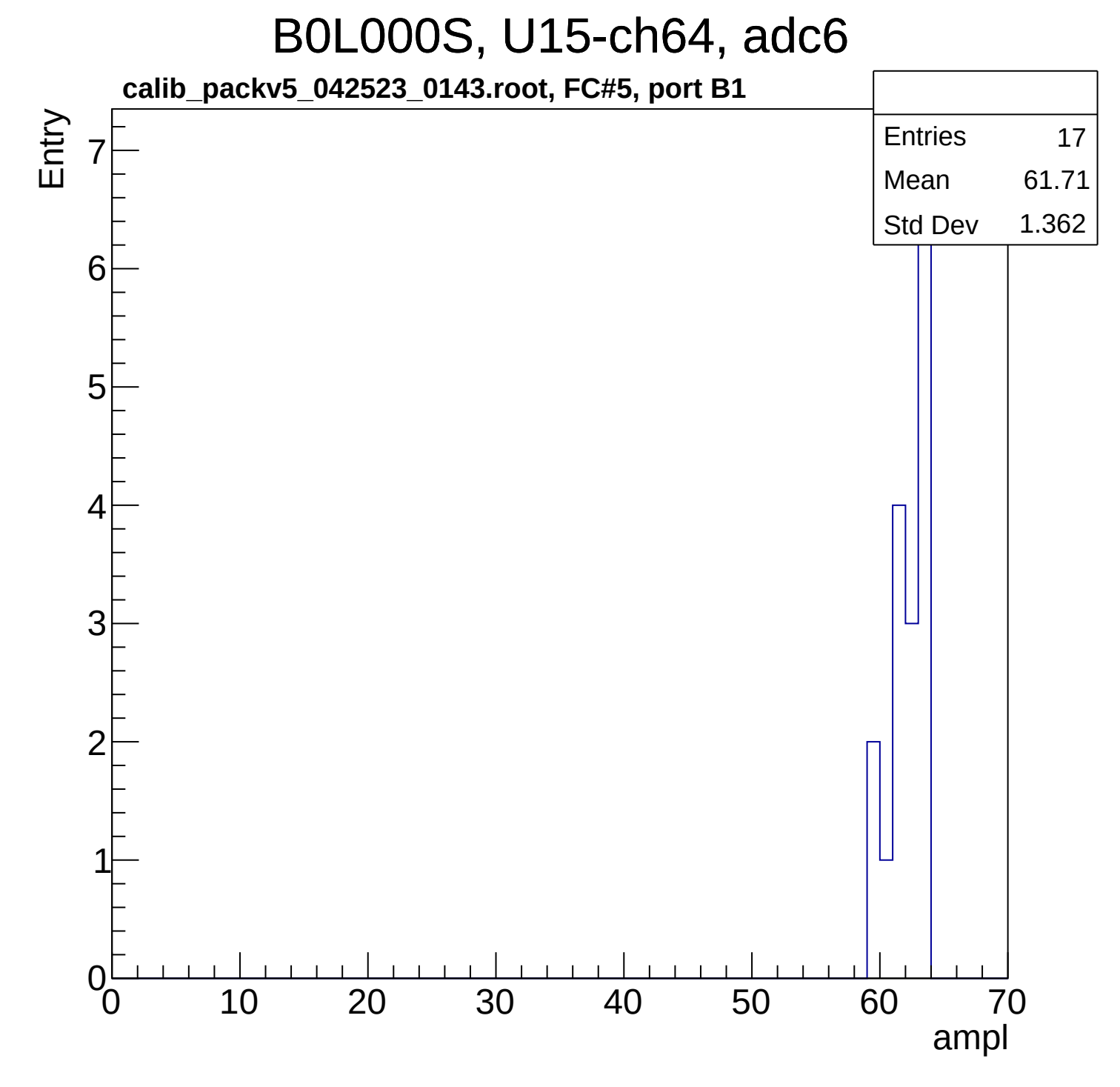
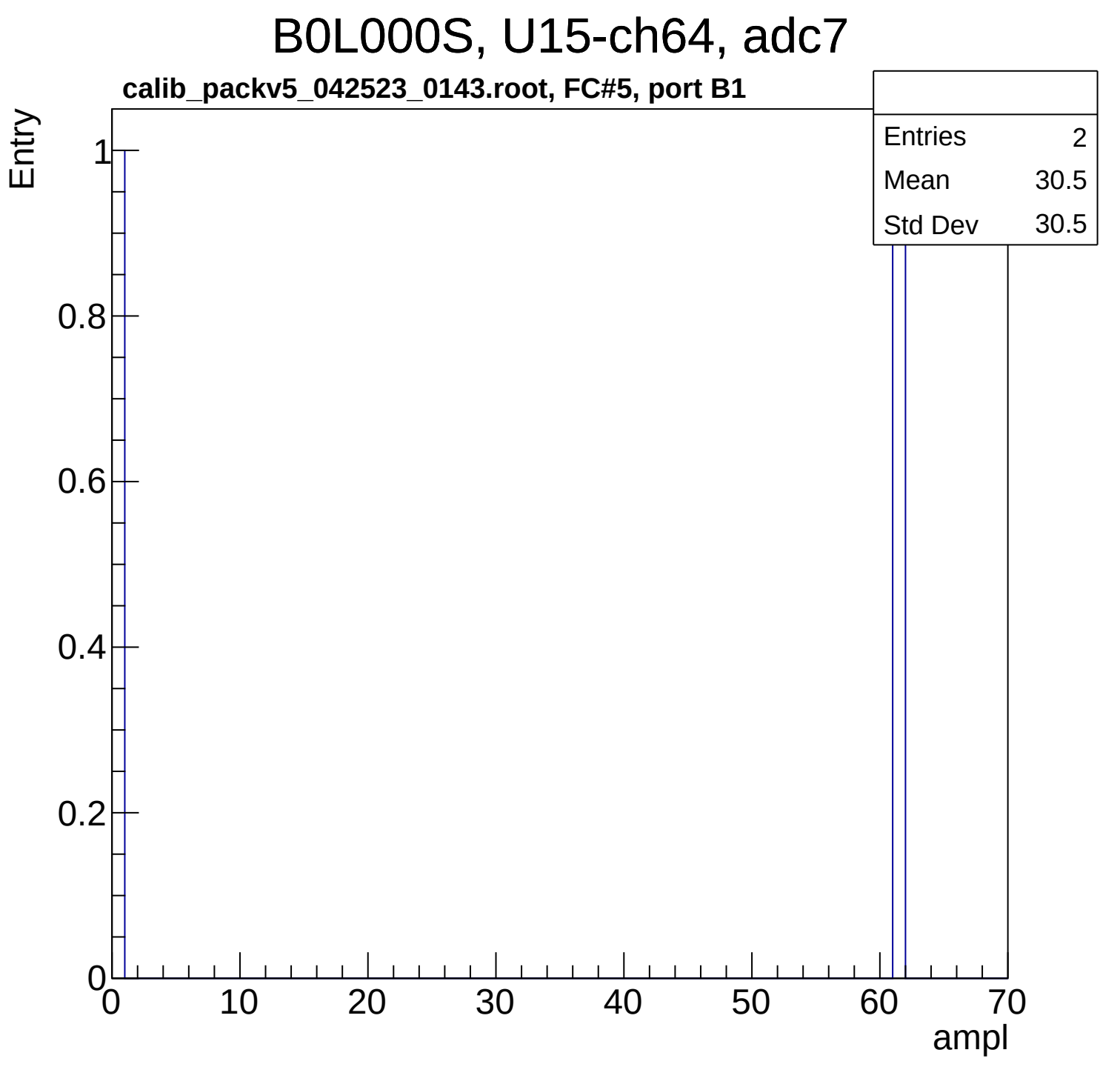
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.71
Std Dev 1.362
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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0.6
0.4
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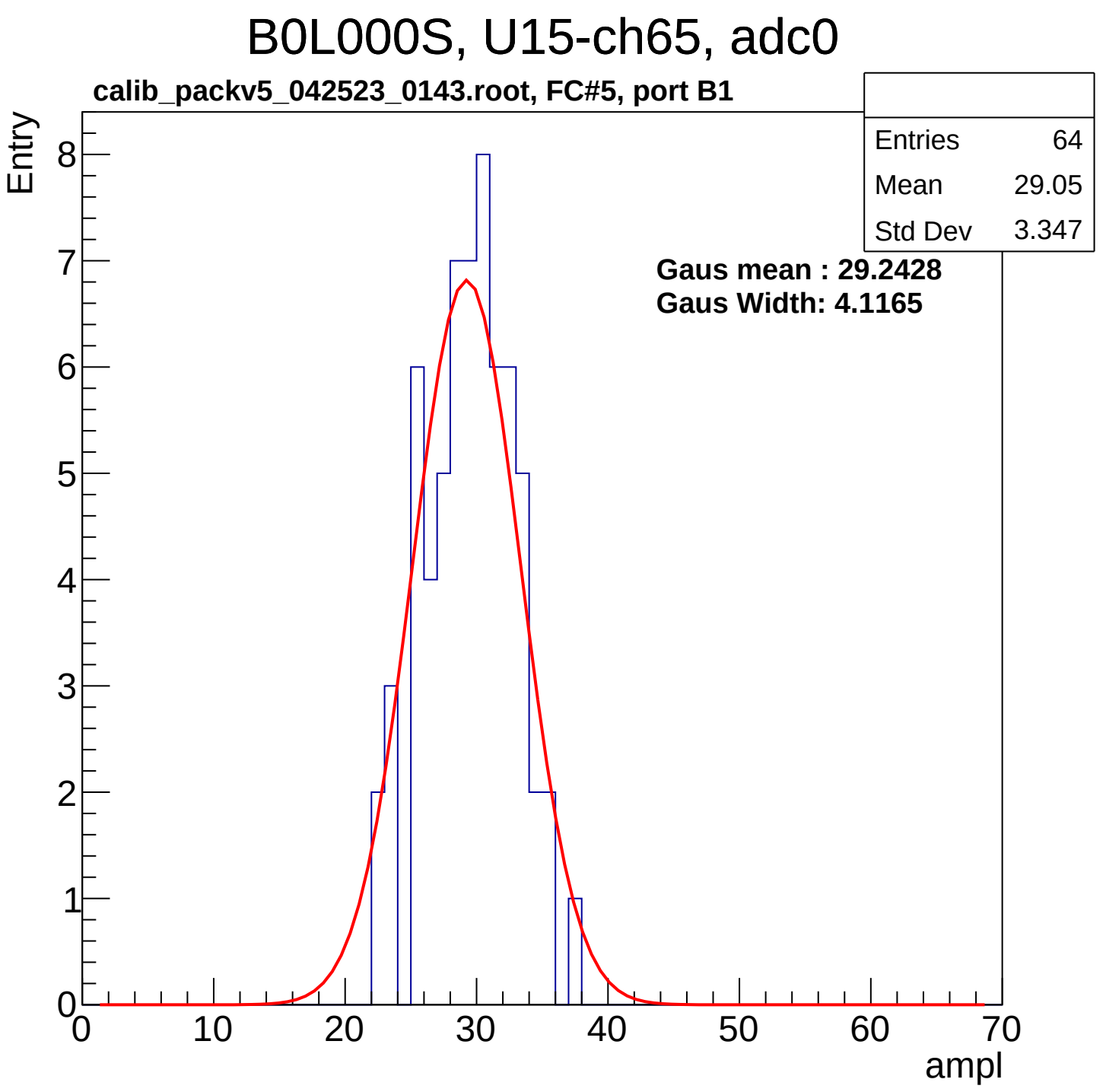
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.05
Std Dev 3.347
Gaus mean : 29.2428
Gaus Width: 4.1165
Entry
ampl
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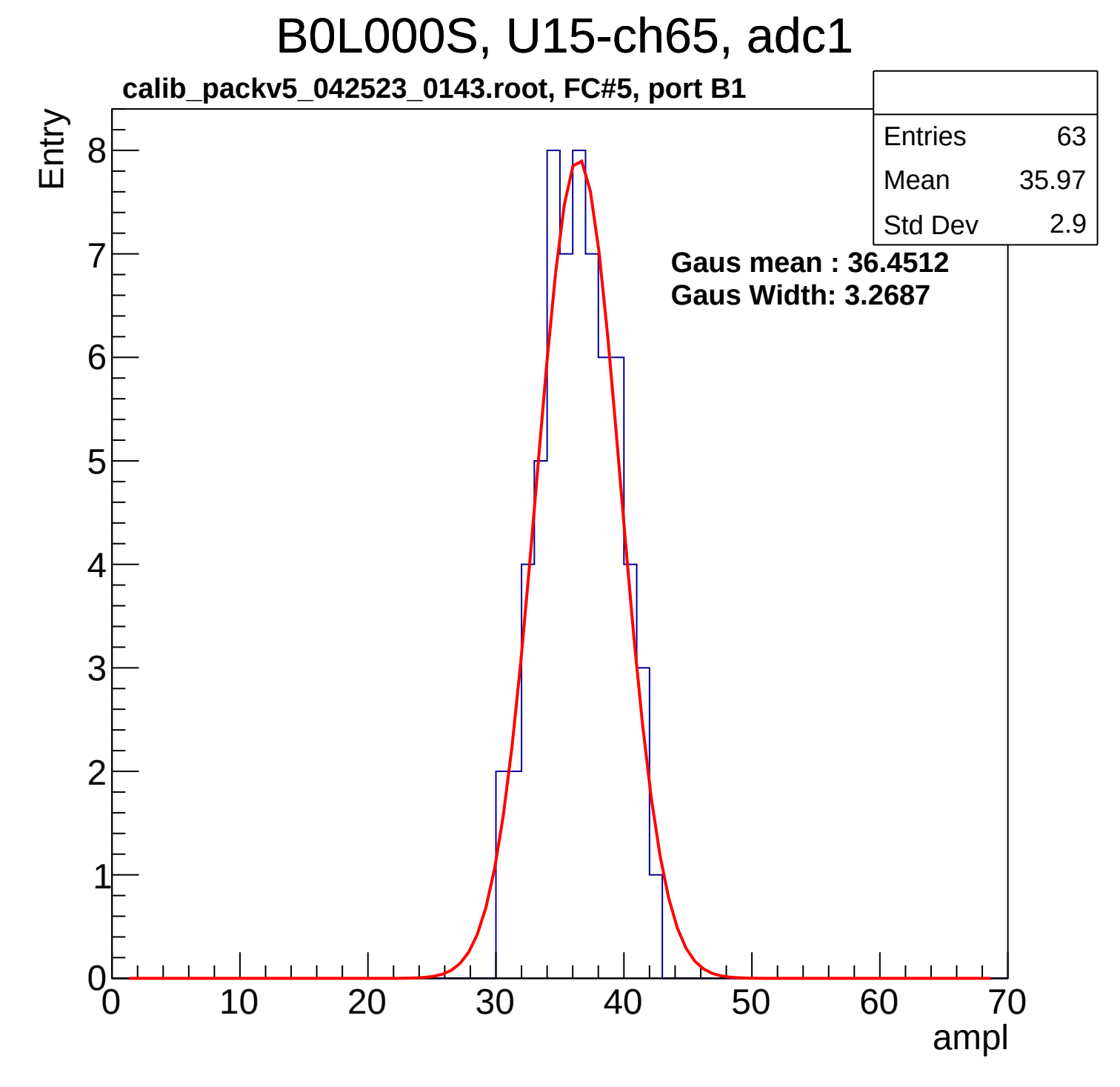
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.97
Std Dev 2.9
Gaus mean : 36.4512
Gaus Width: 3.2687
Entry
ampl
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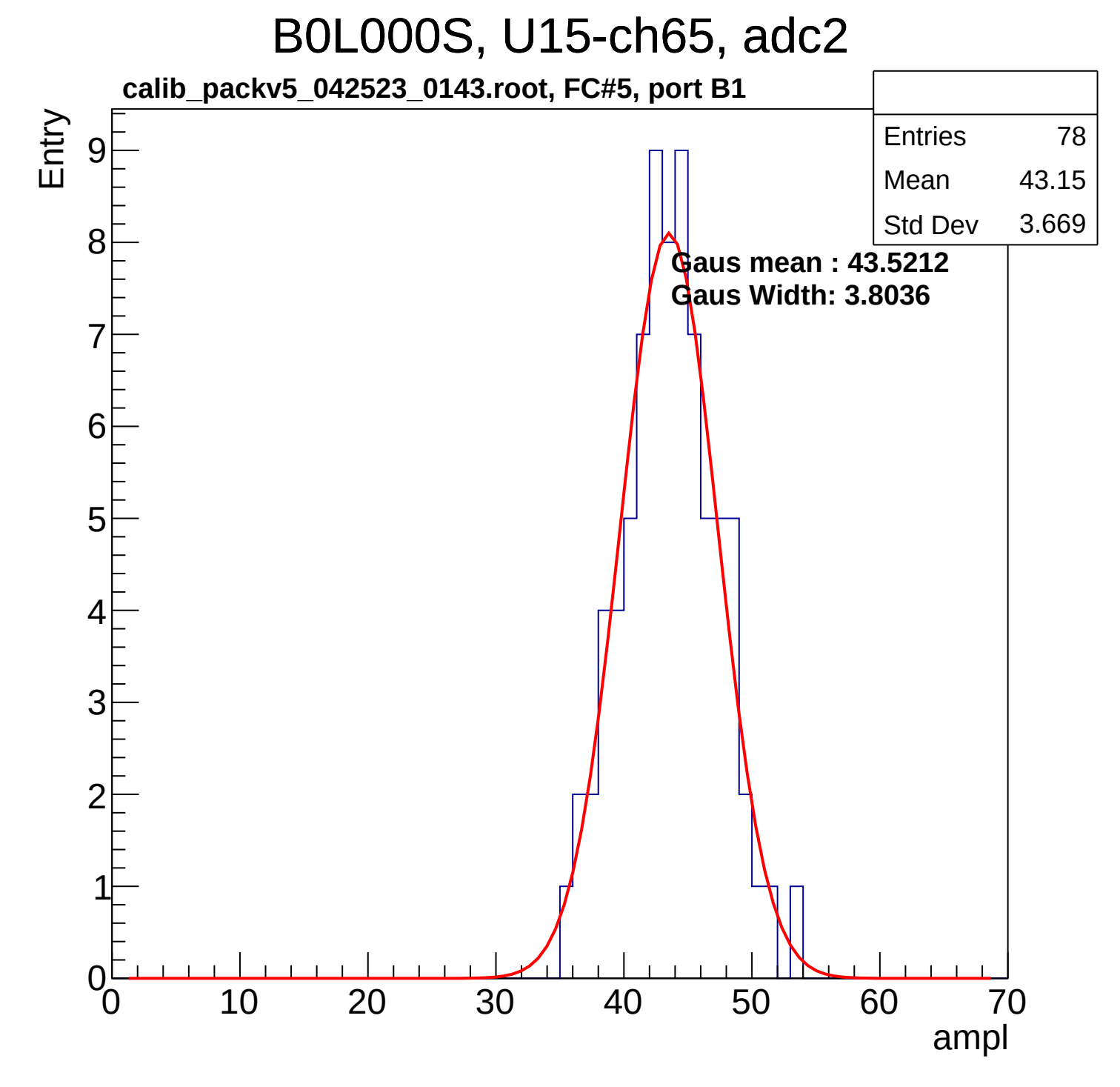
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.15
Std Dev 3.669
Gaus mean : 43.5212
Gaus Width: 3.8036
Entry
ampl
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2
3
4
5
6
7
8
9
0
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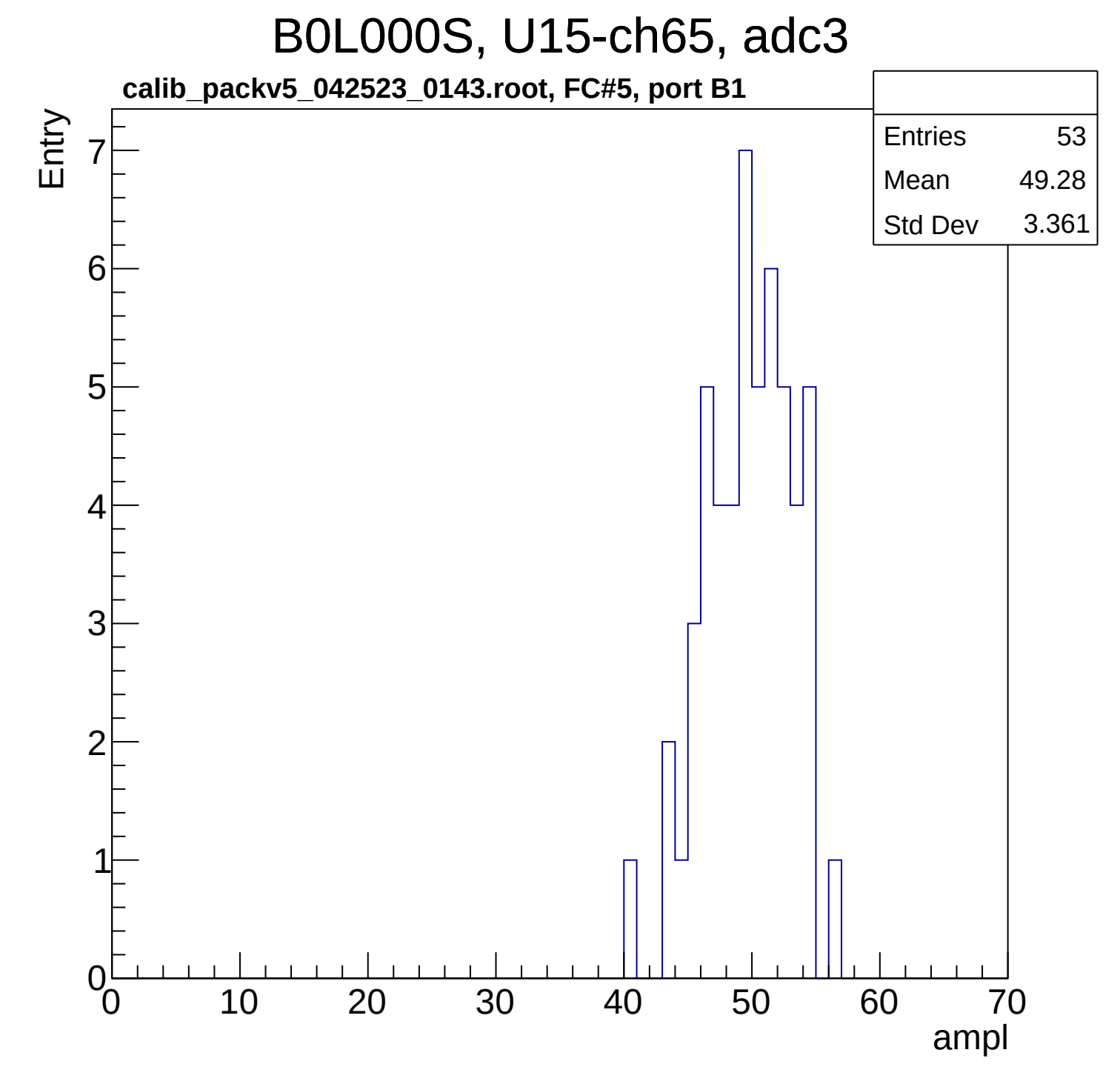
B0L000S, U15-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 49.28
Std Dev 3.361
Entry
ampl
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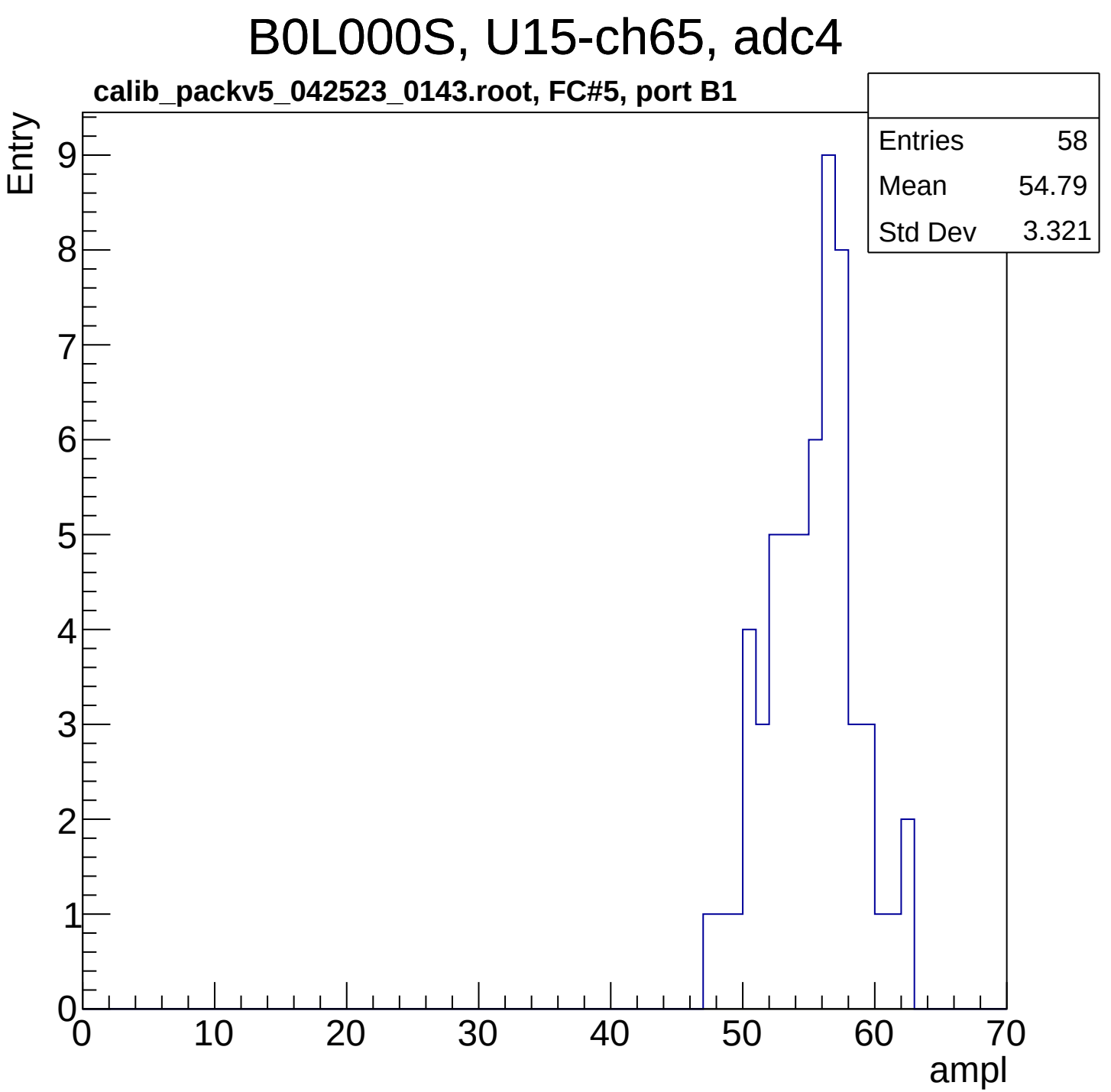
B0L000S, U15-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.79
Std Dev 3.321
Entry
ampl
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3
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5
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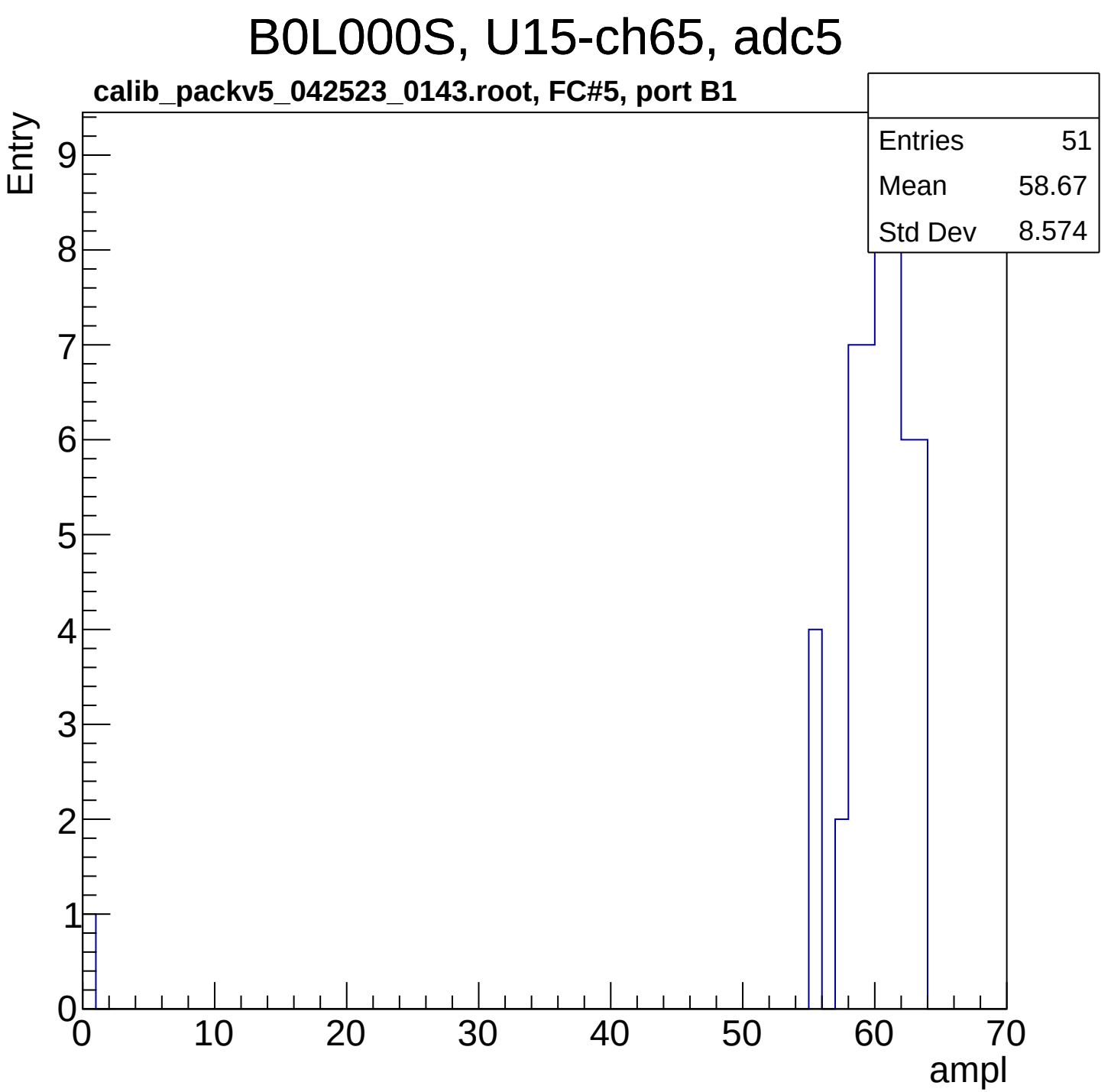
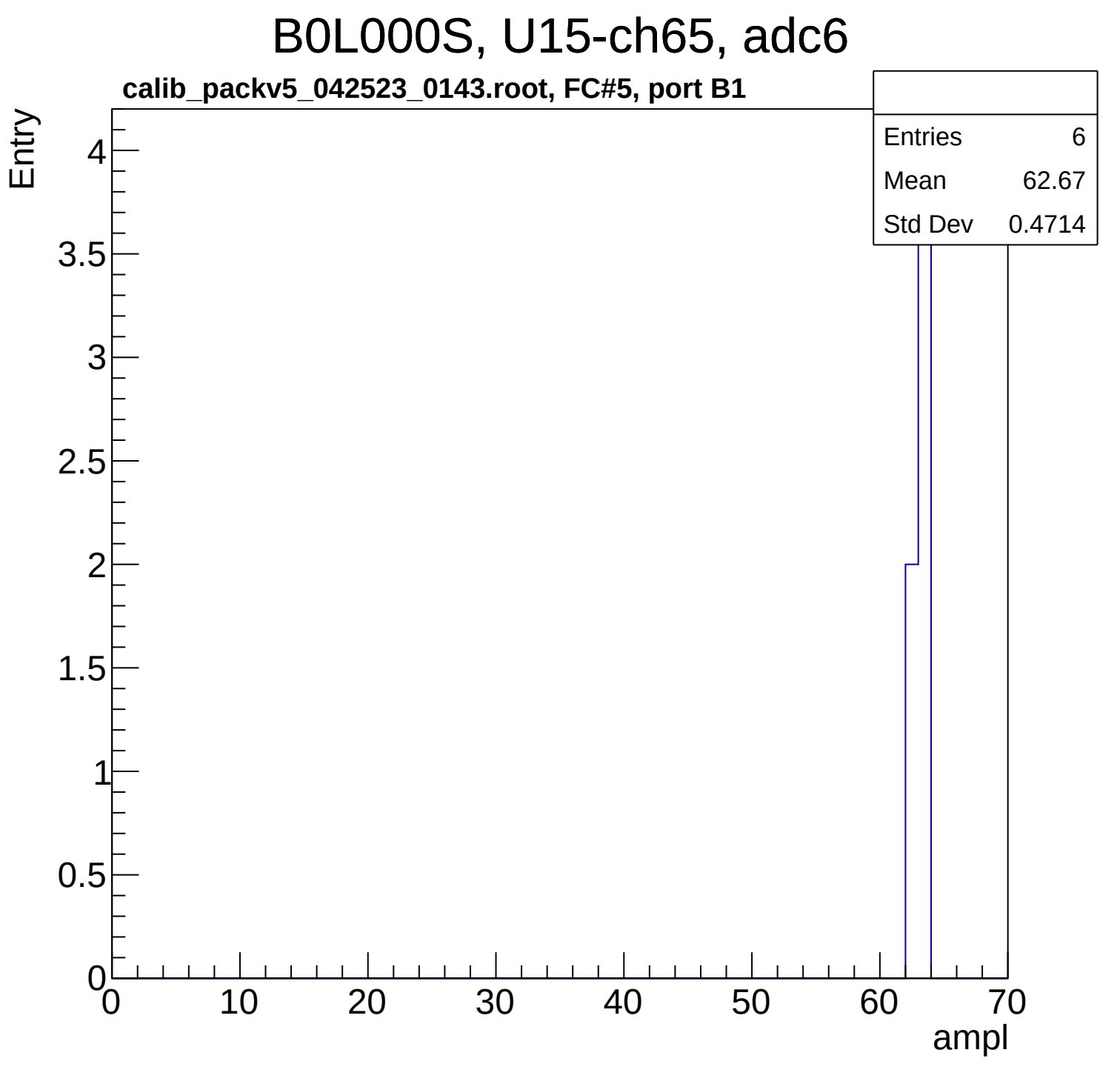

B0L000S, U15-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.67
Std Dev 8.574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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60
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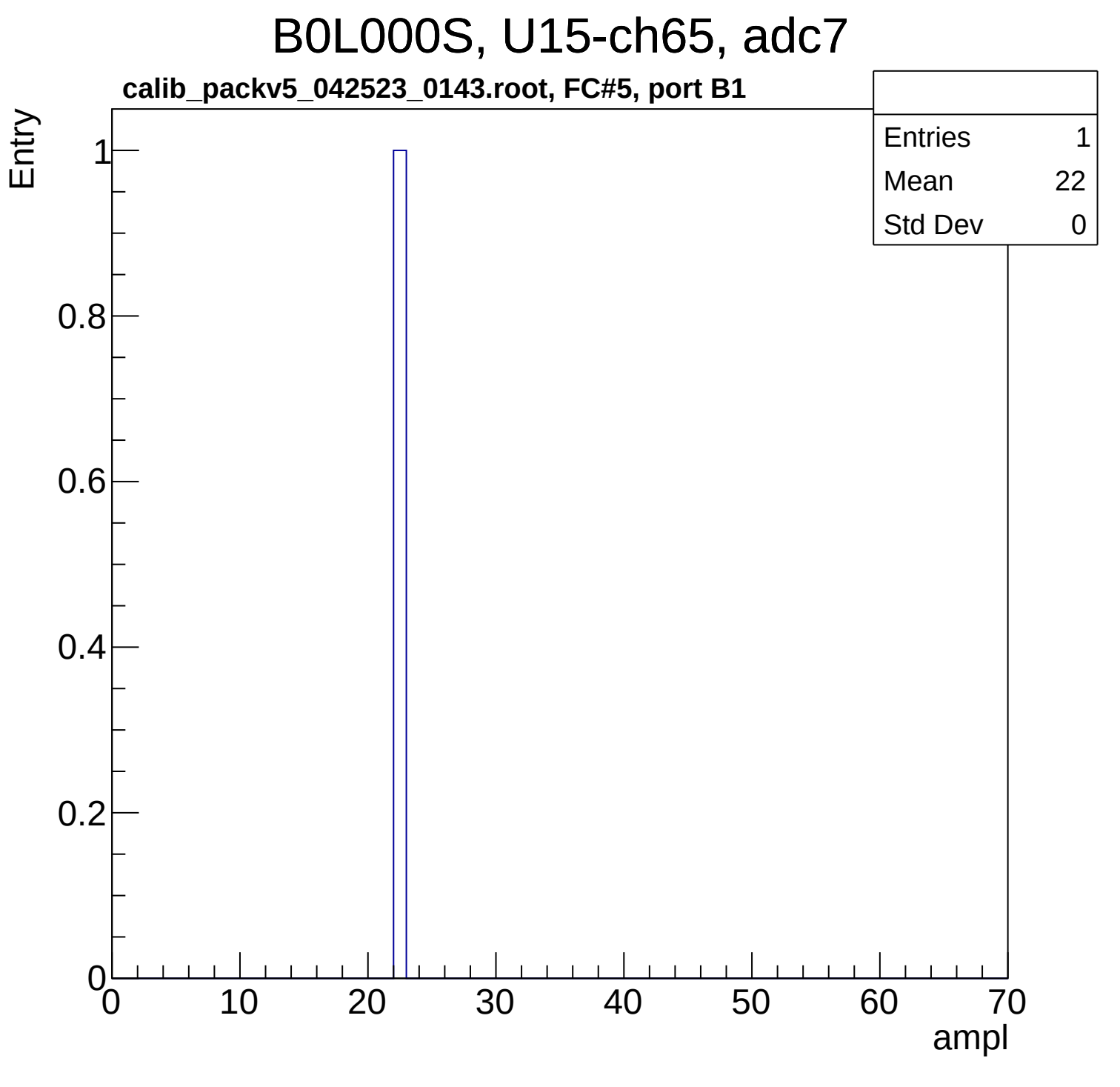
B0L000S, U15-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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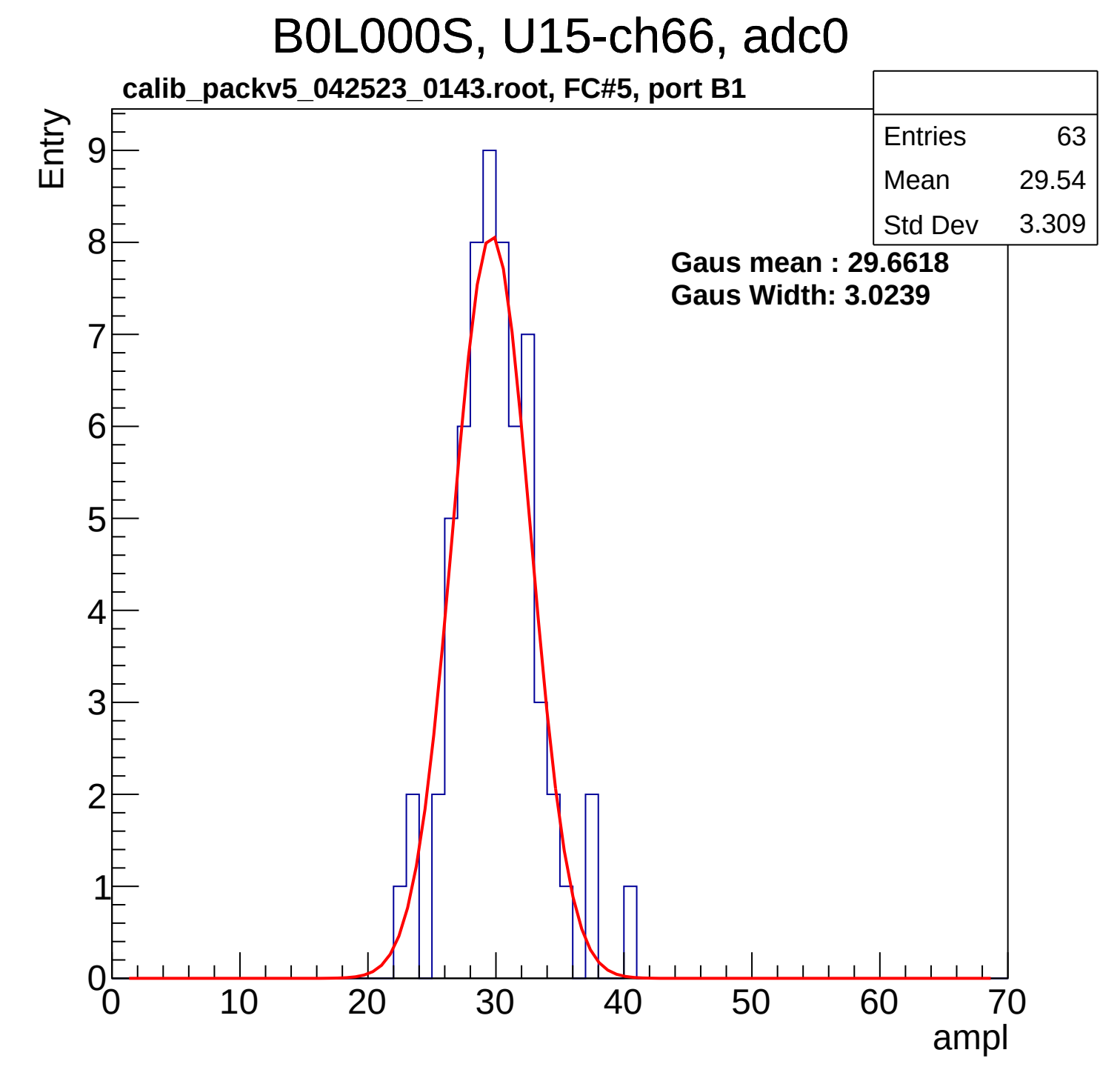
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.54
Std Dev 3.309
Gaus mean : 29.6618
Gaus Width: 3.0239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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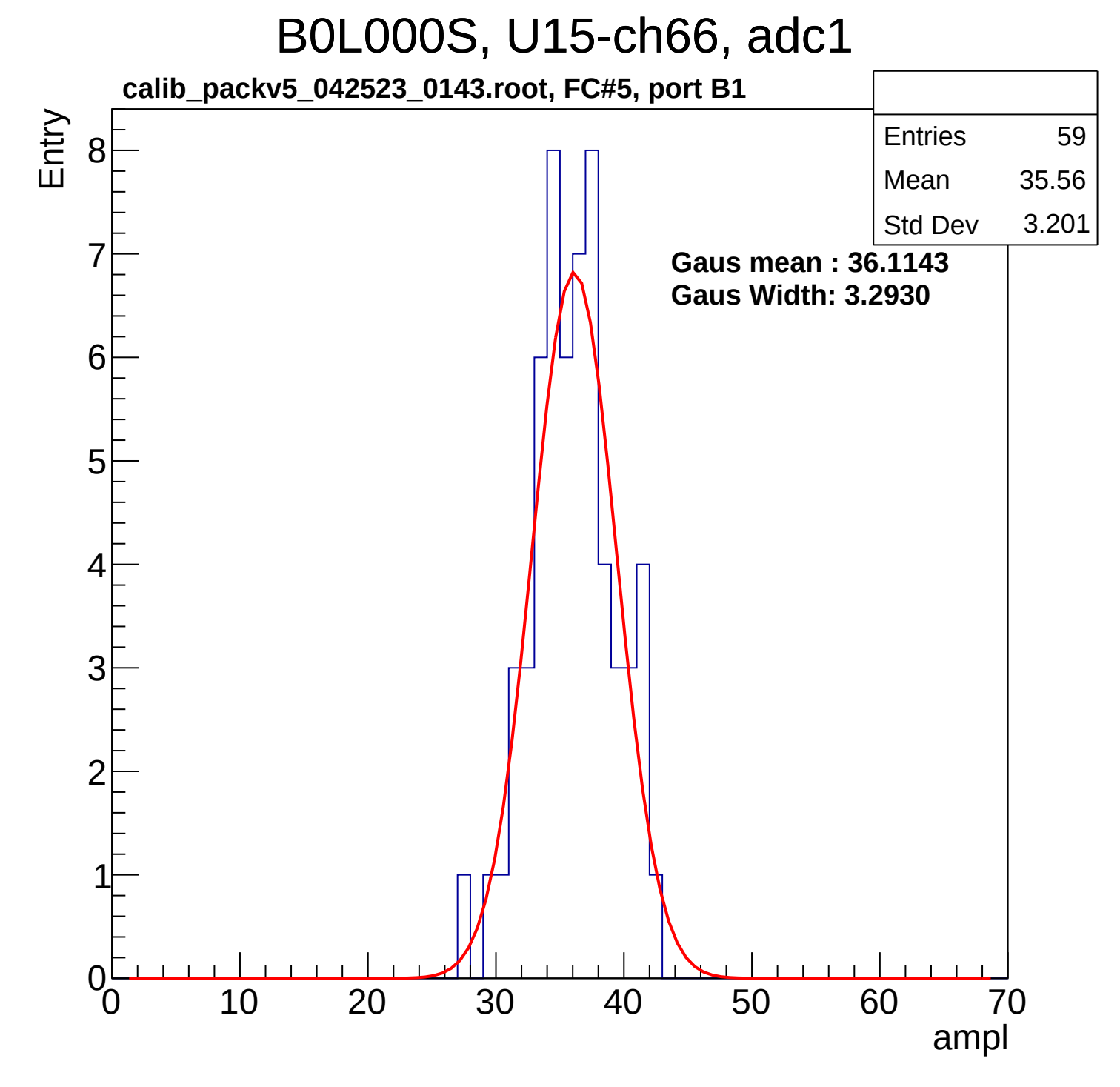
B0L000S, U15-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.56
Std Dev 3.201
Gaus mean : 36.1143
Gaus Width: 3.2930
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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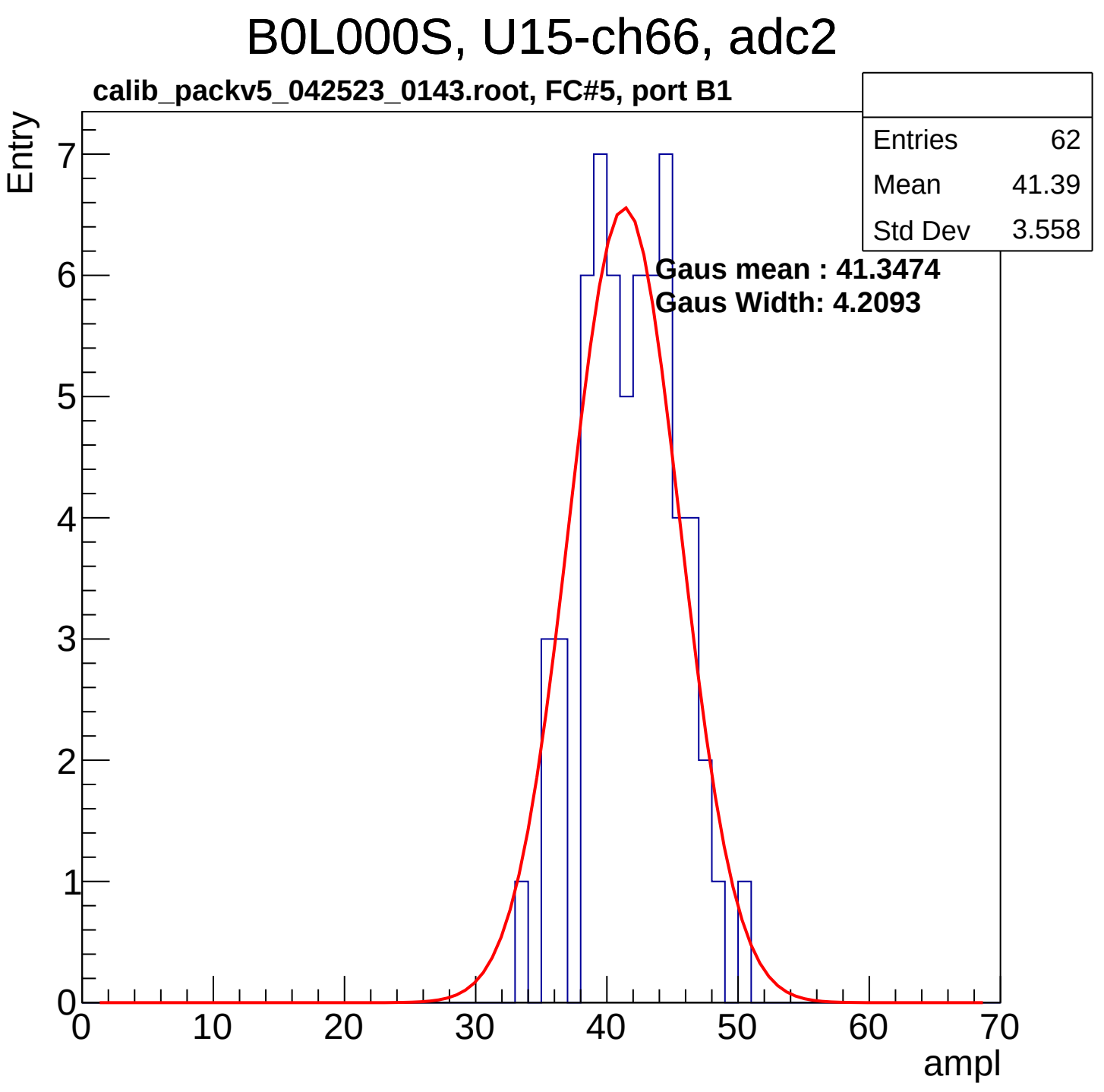
B0L000S, U15-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.39
Std Dev 3.558
Gaus mean : 41.3474
Gaus Width: 4.2093
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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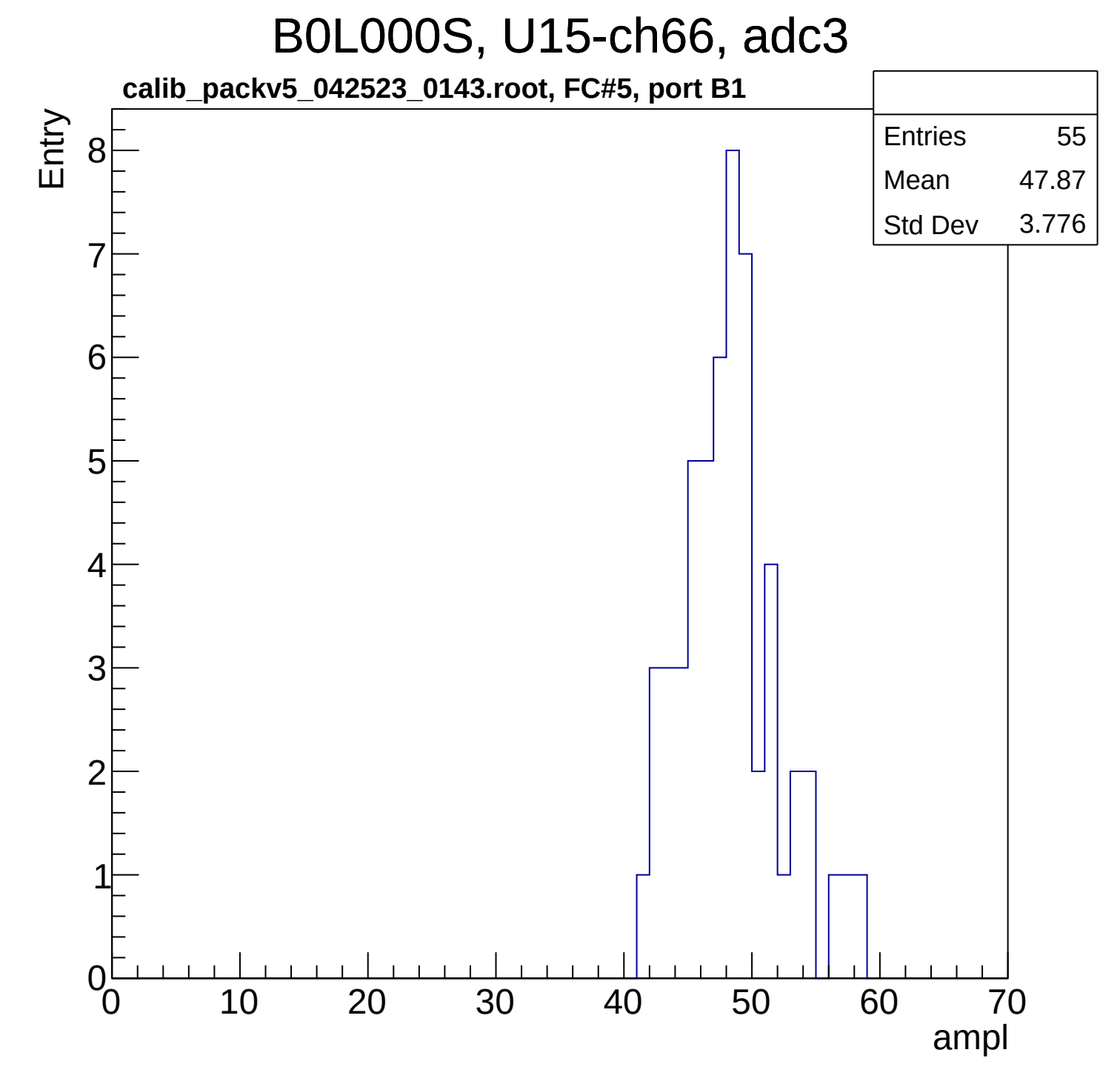
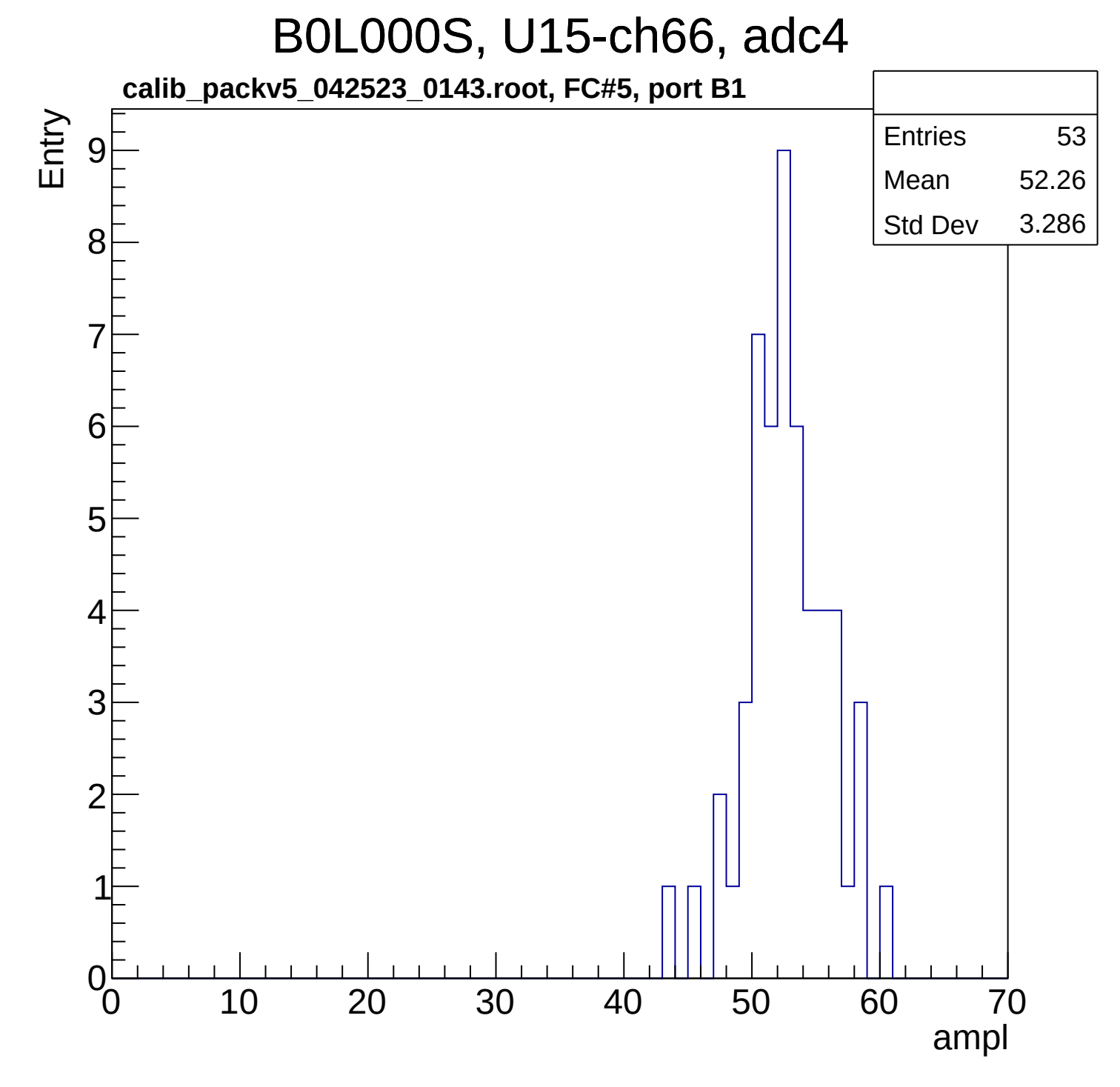
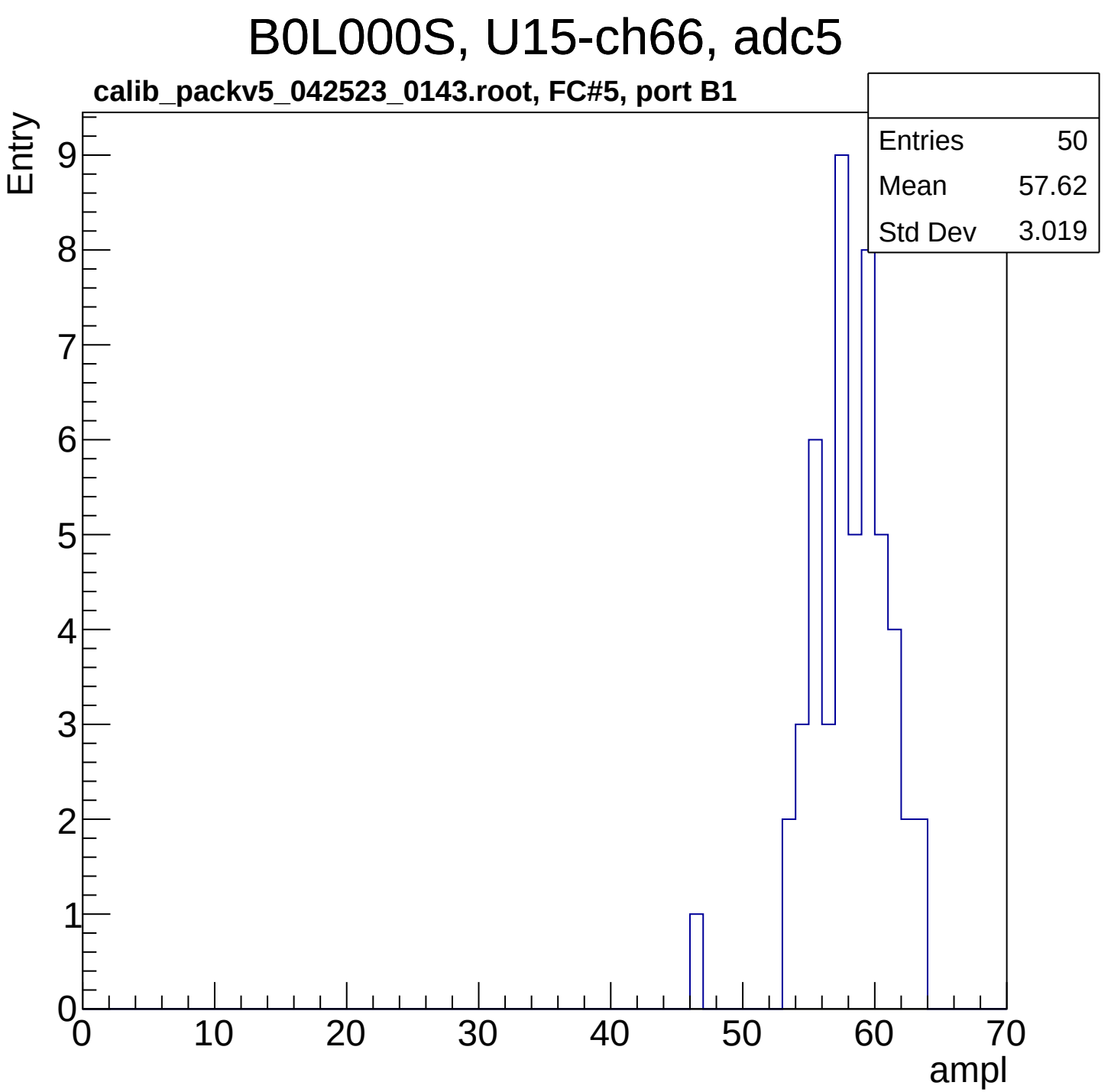

B0L000S, U15-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.87
Std Dev 3.776
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 52.26
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.62
Std Dev 3.019
Entry
ampl
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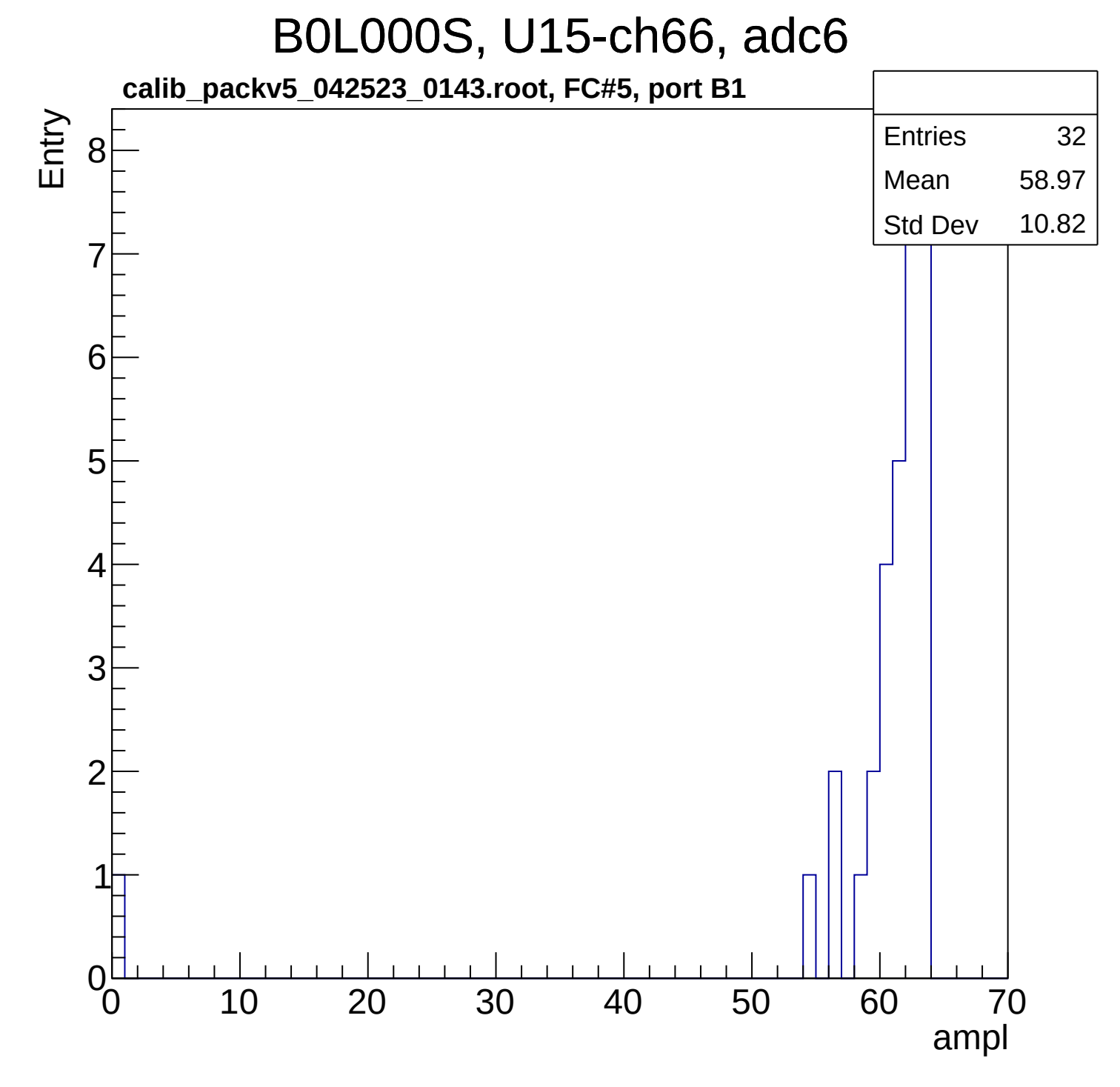
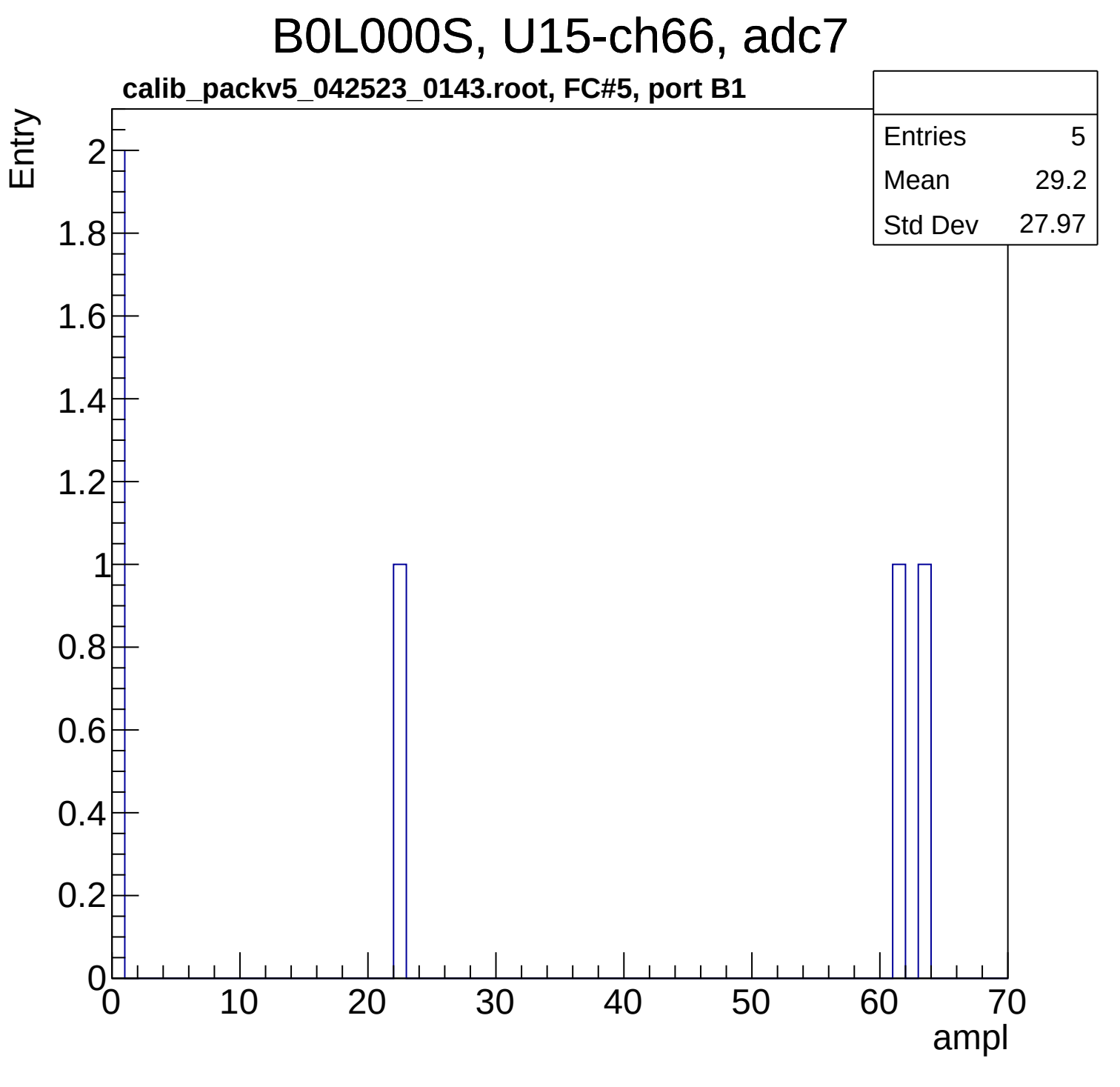
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.97
Std Dev 10.82
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 29.2
Std Dev 27.97
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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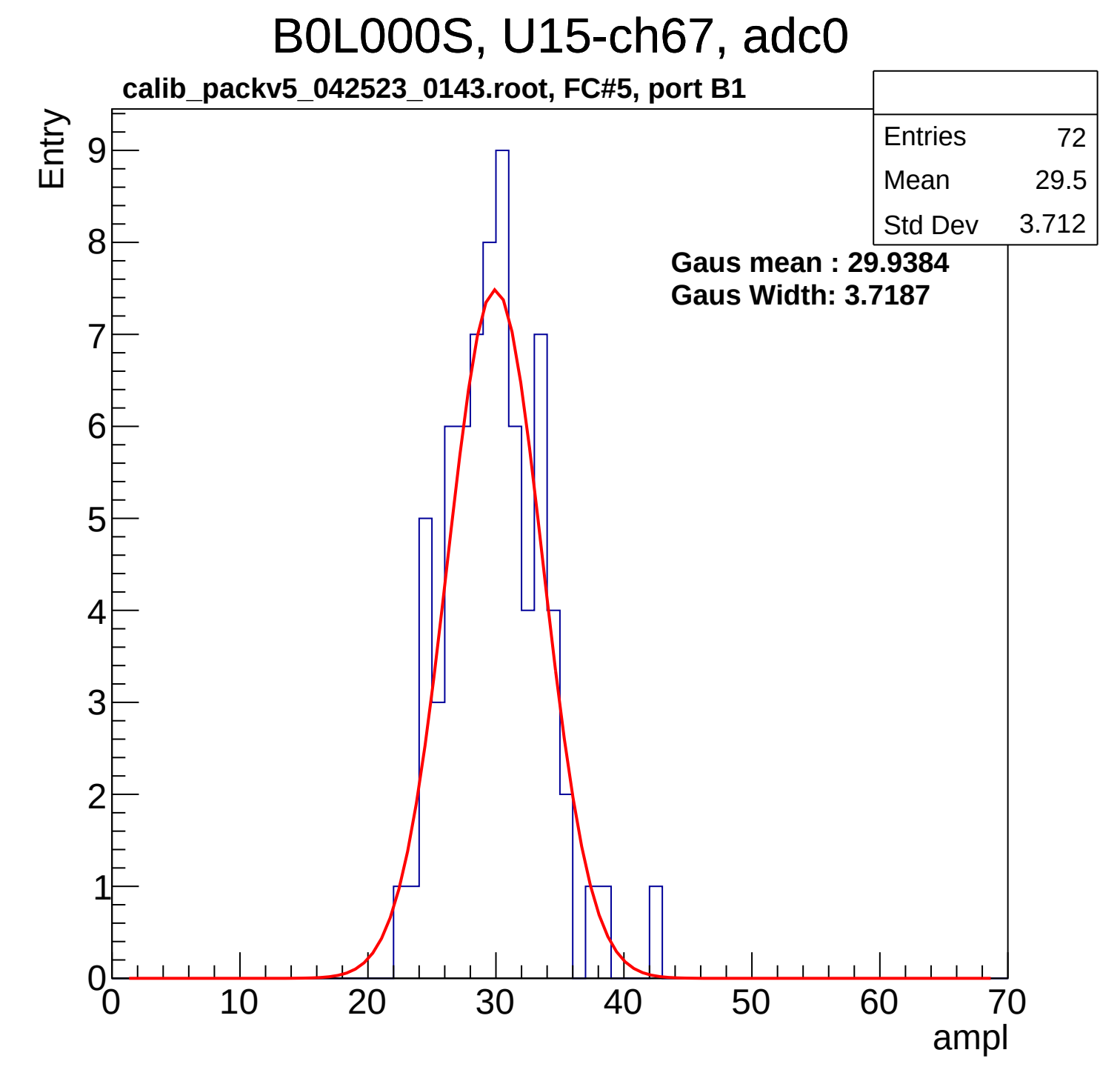
B0L000S, U15-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.5
Std Dev 3.712
Gaus mean : 29.9384
Gaus Width: 3.7187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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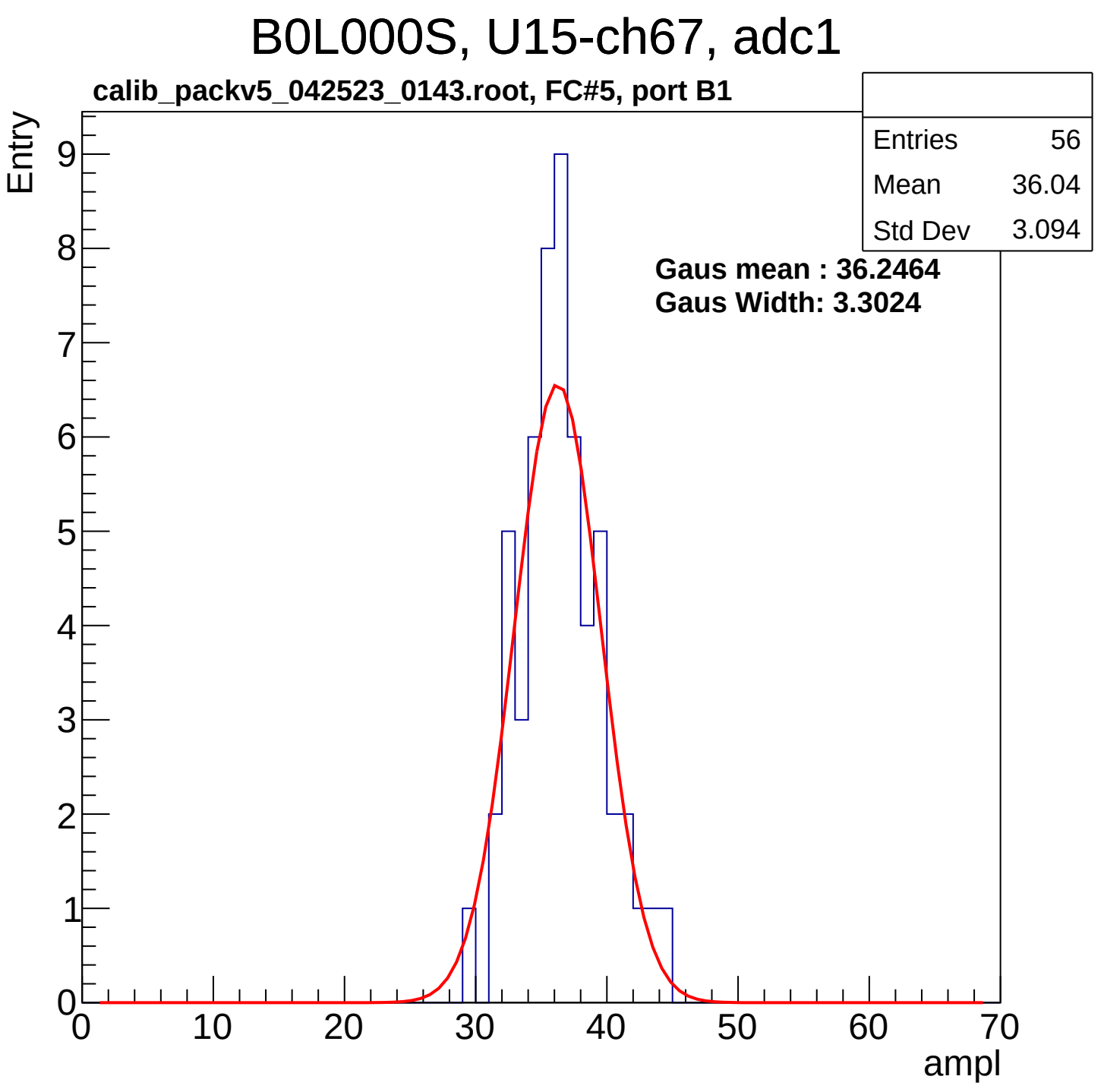
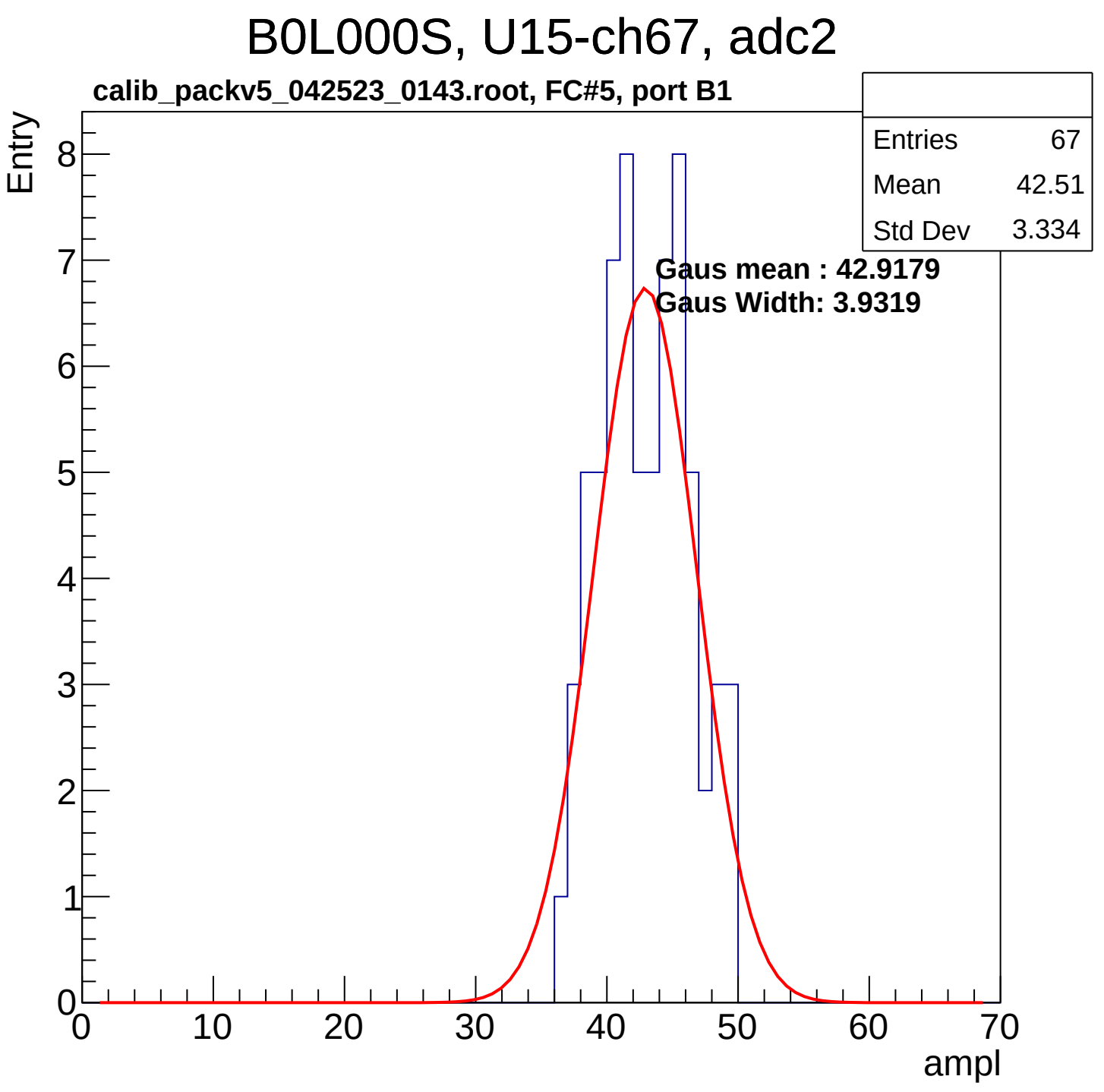
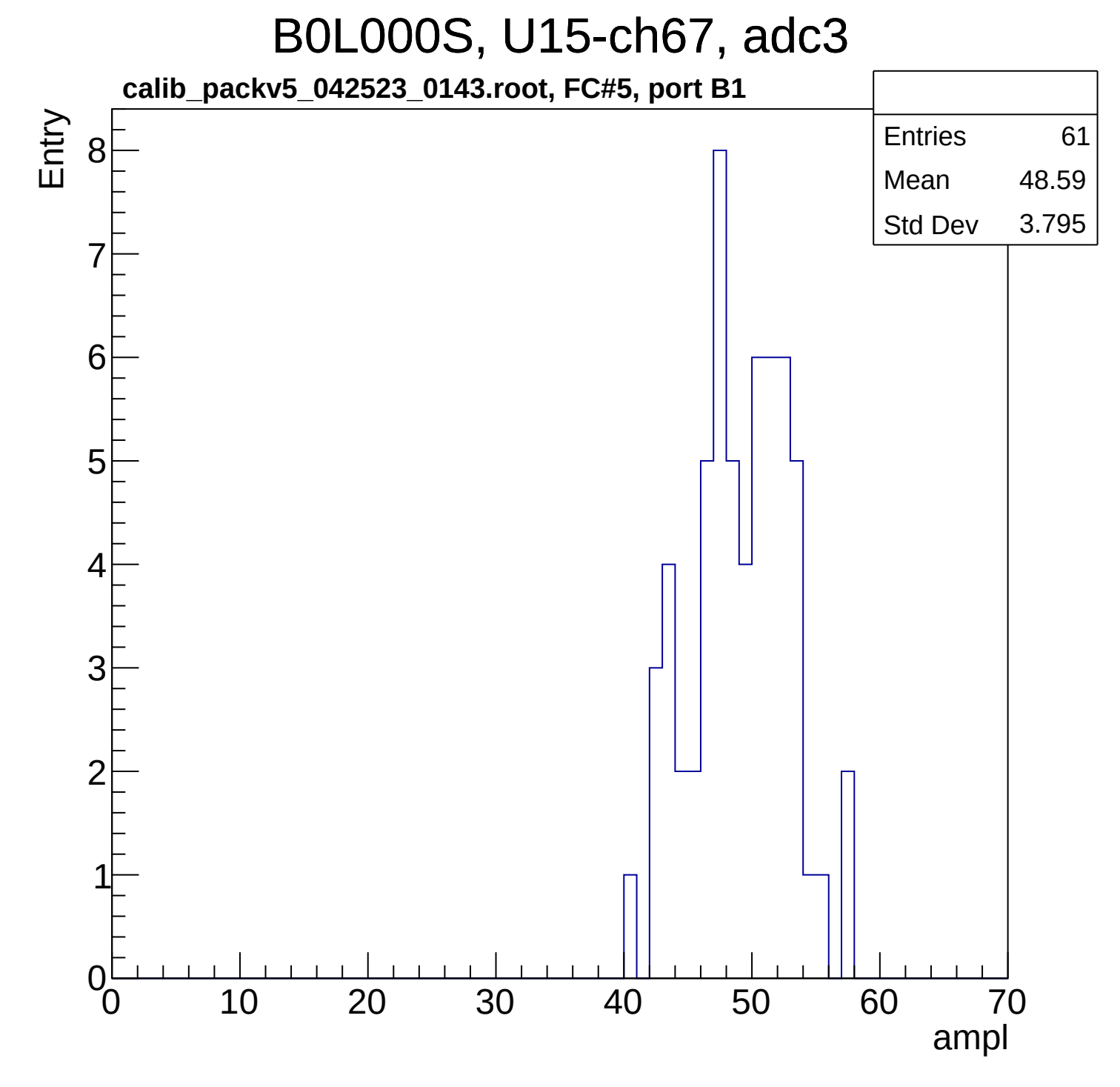

B0L000S, U15-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.04
Std Dev 3.094
Gaus mean : 36.2464
Gaus Width: 3.3024
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.51
Std Dev 3.334
Gaus mean : 42.9179
Gaus Width: 3.9319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.59
Std Dev 3.795
Entry
ampl
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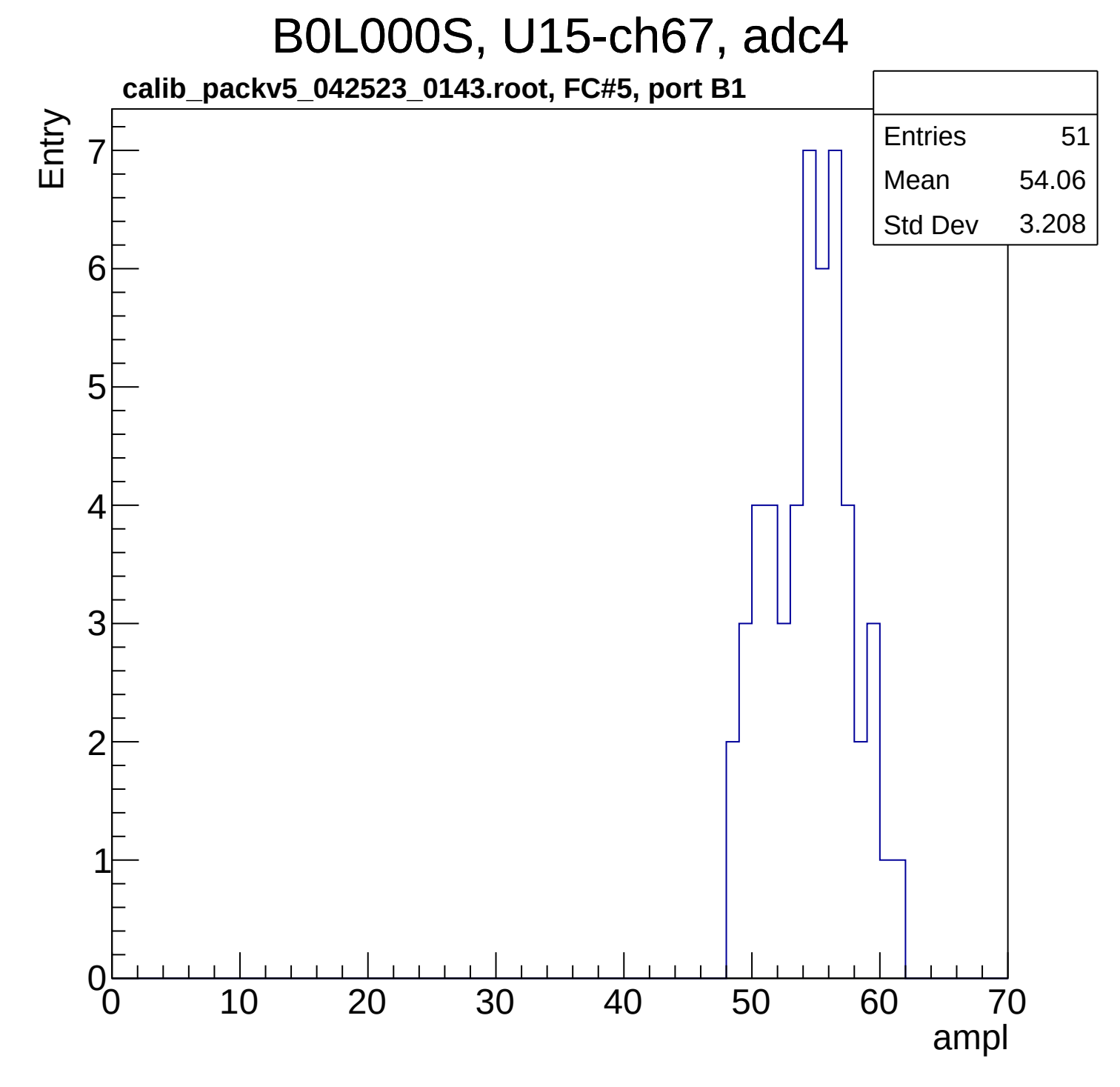
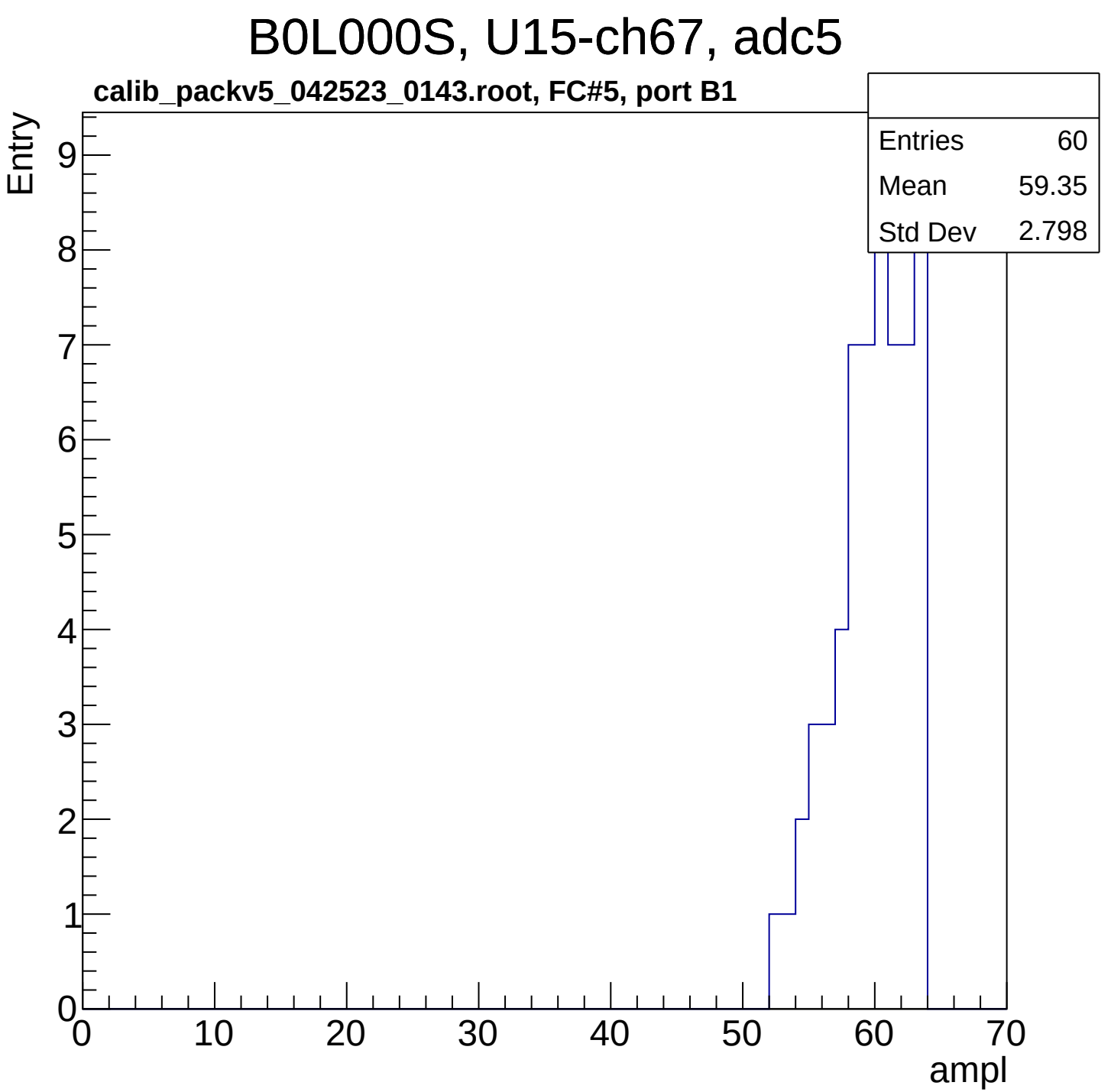
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.06
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 59.35
Std Dev 2.798
Entry
ampl
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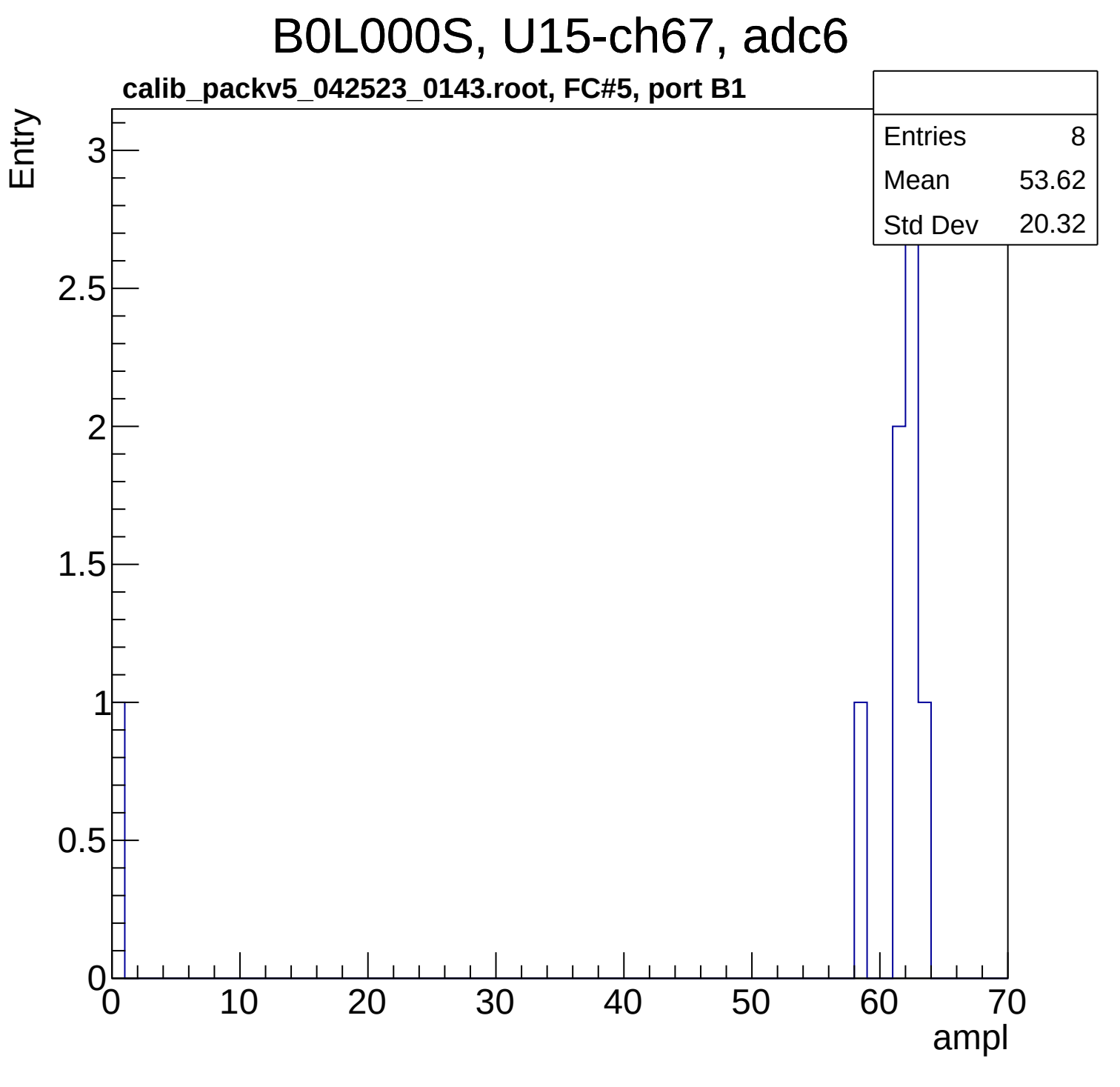
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.62
Std Dev 20.32
Entry
ampl
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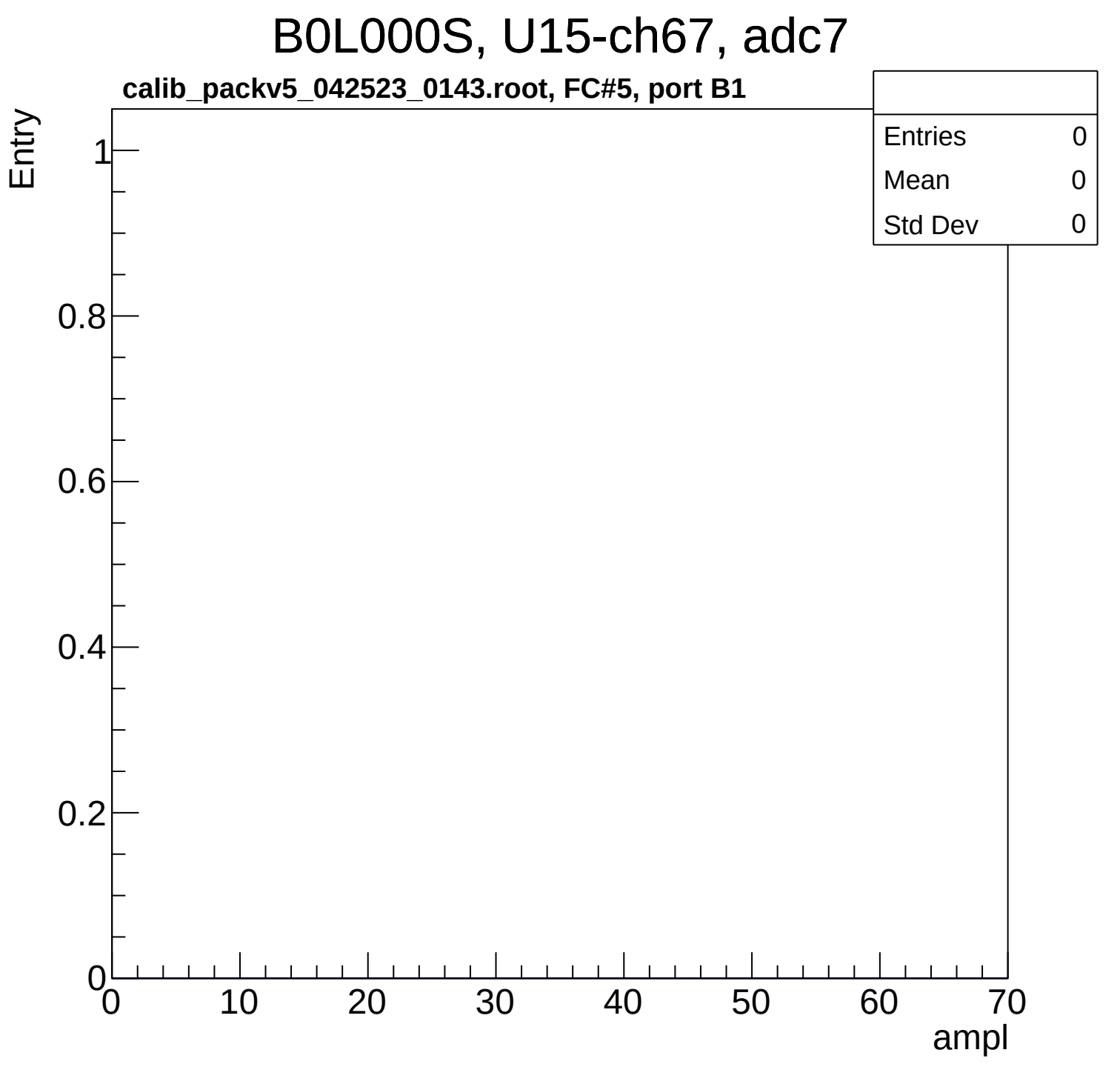
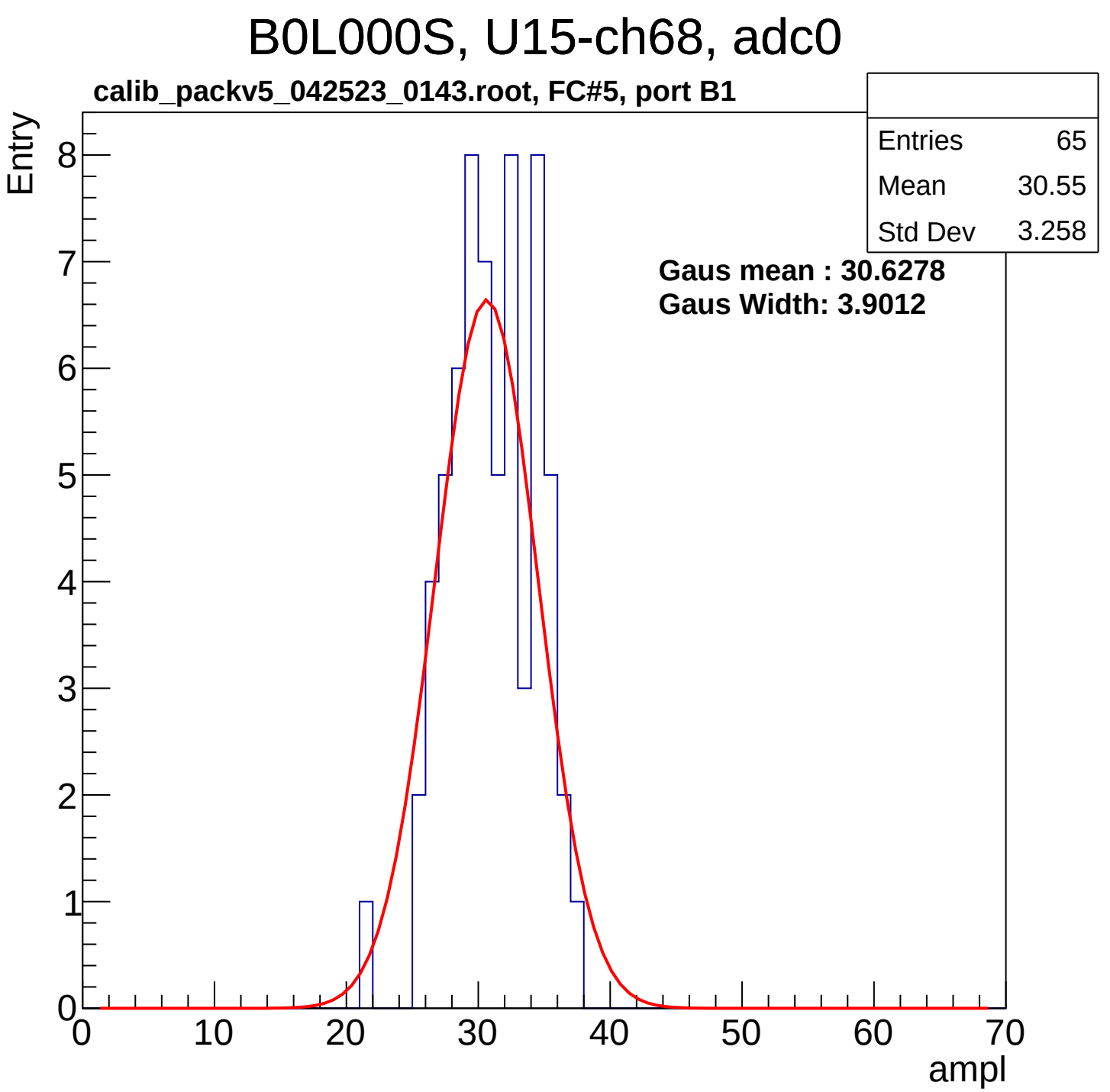
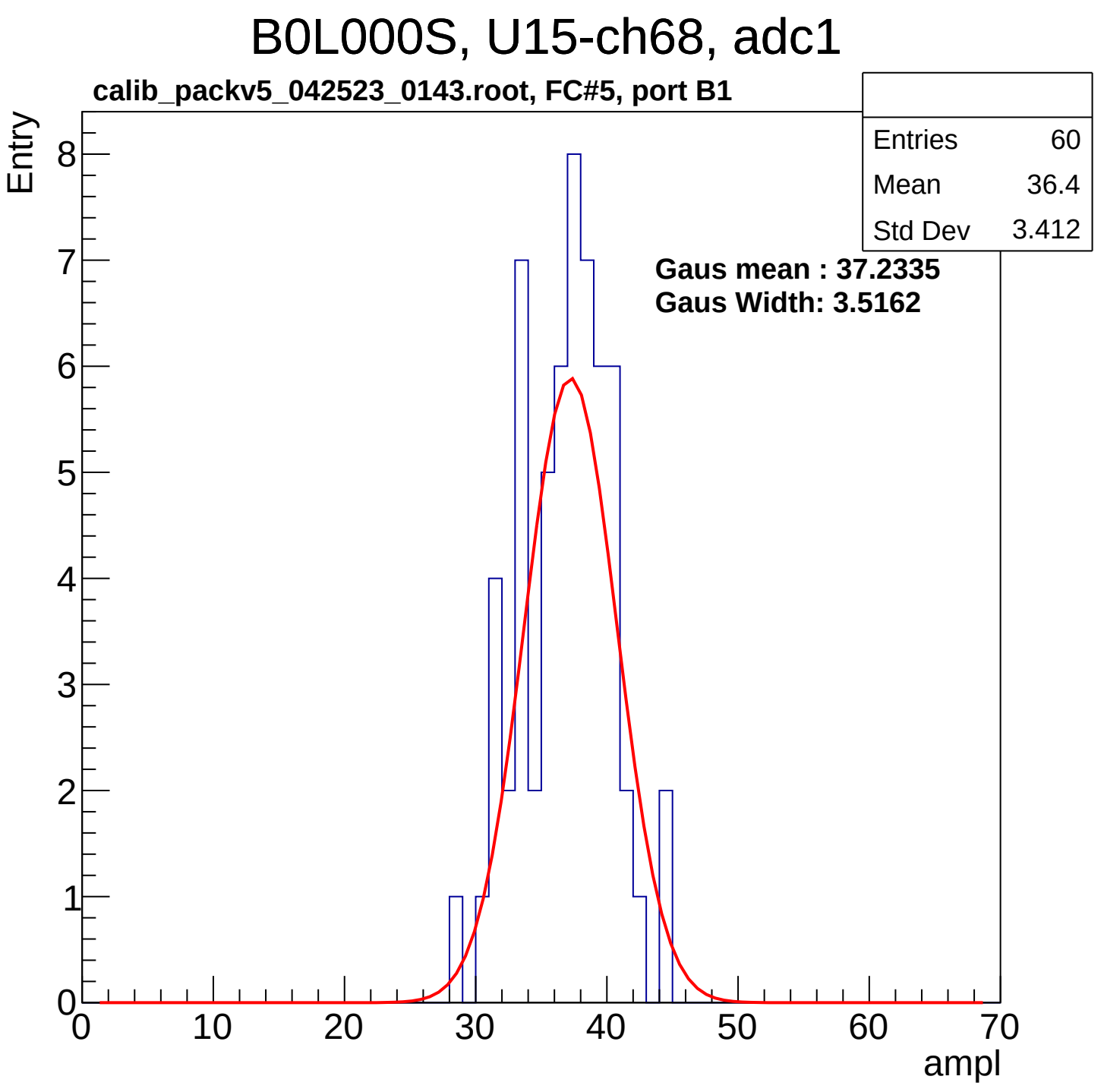
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.55
Std Dev 3.258
Gaus mean : 30.6278
Gaus Width: 3.9012
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.4
Std Dev 3.412
Gaus mean : 37.2335
Gaus Width: 3.5162
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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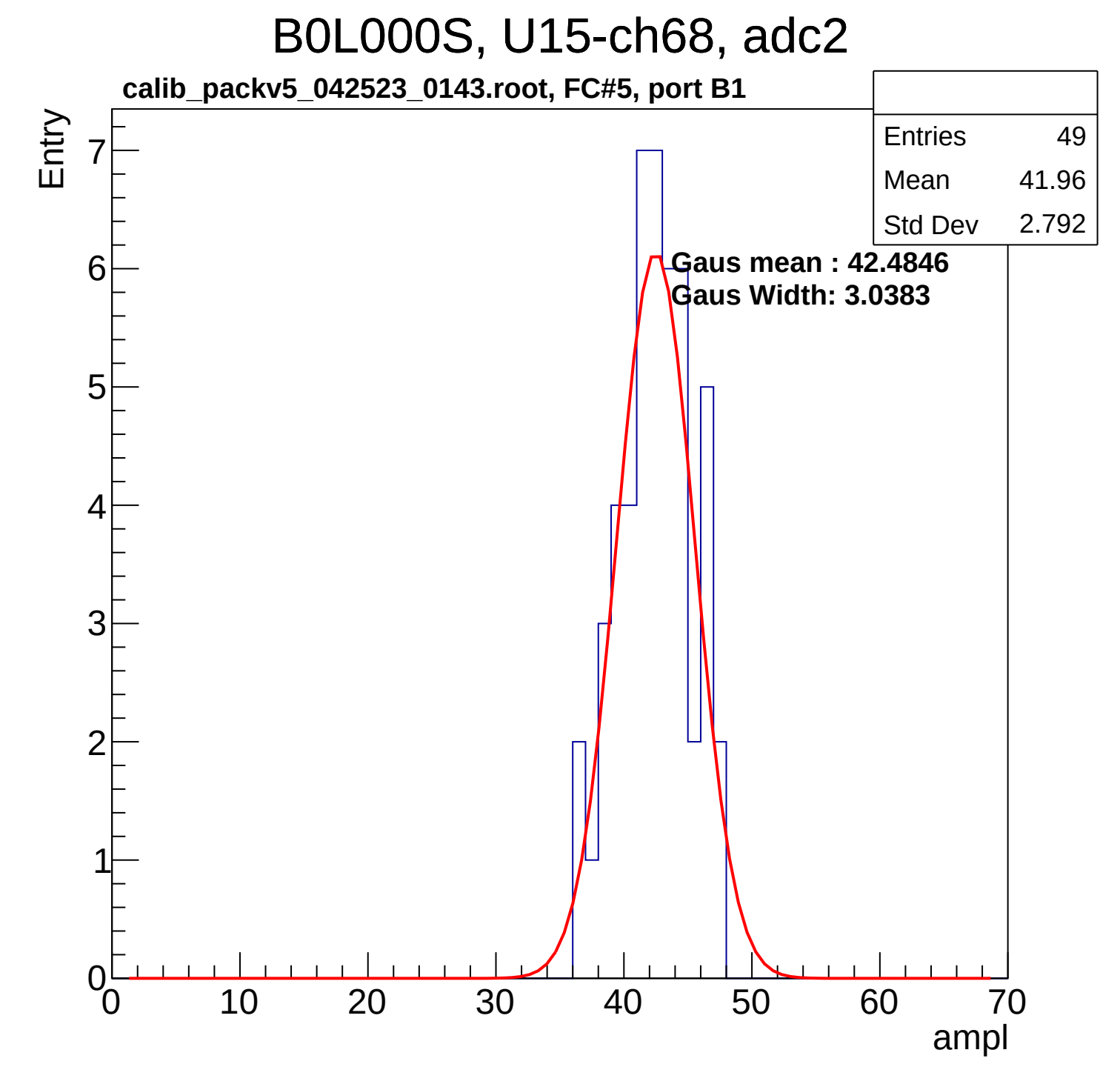
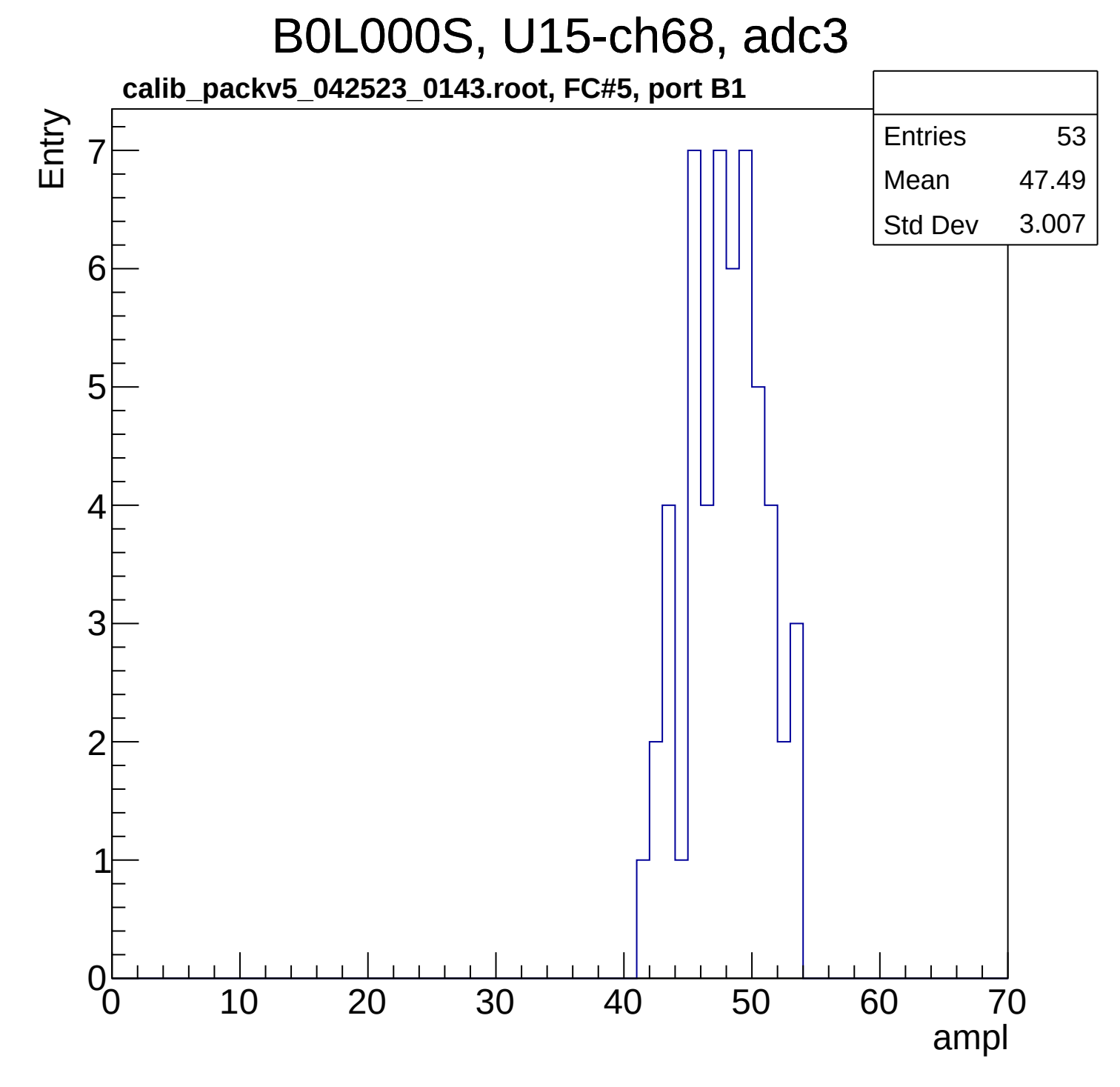

B0L000S, U15-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 41.96
Std Dev 2.792
Gaus mean : 42.4846
Gaus Width: 3.0383
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 47.49
Std Dev 3.007
Entry
ampl
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60
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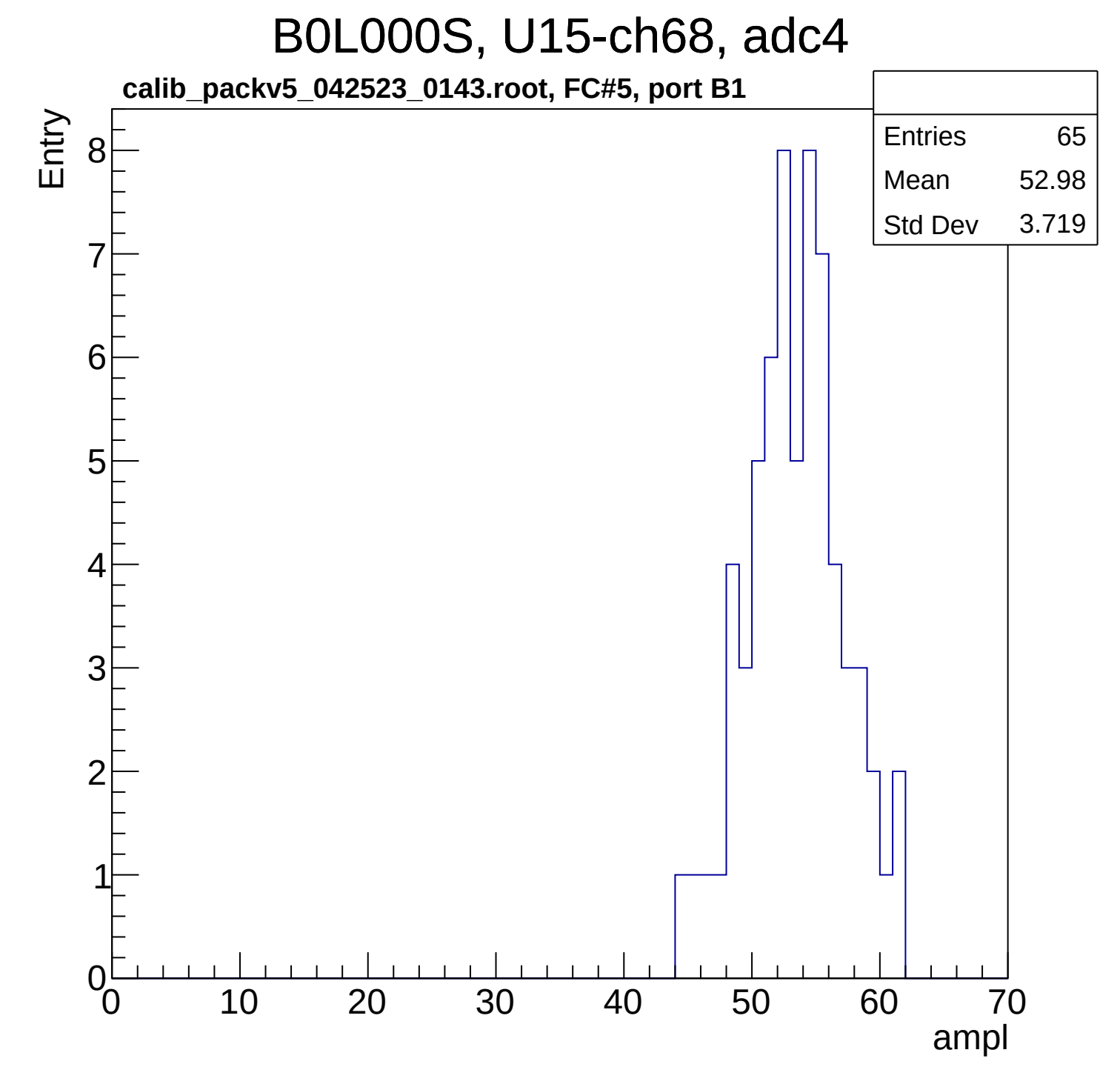
B0L000S, U15-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 52.98
Std Dev 3.719
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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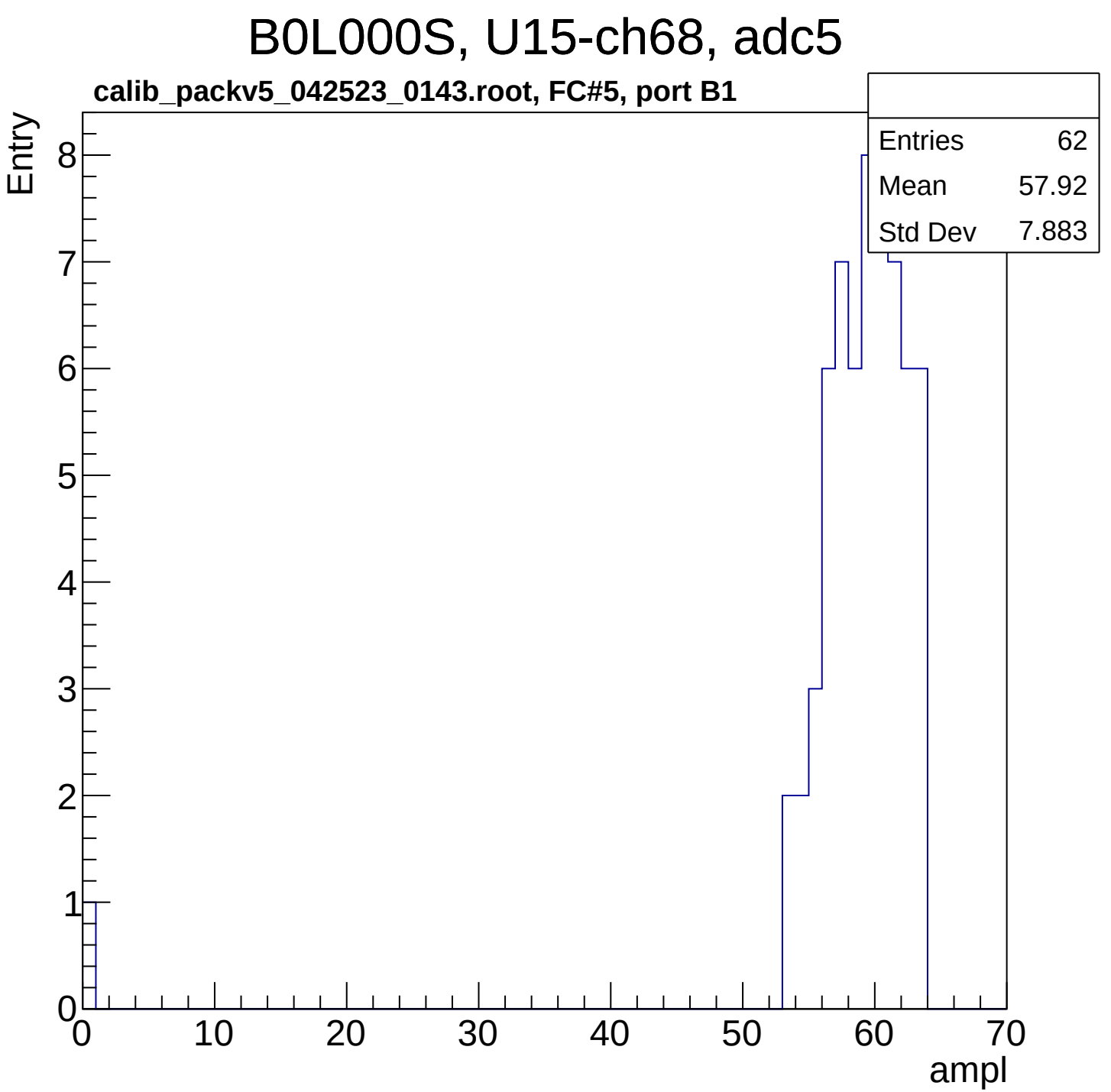
B0L000S, U15-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.92
Std Dev 7.883
Entry
ampl
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3
4
5
6
7
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0
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60
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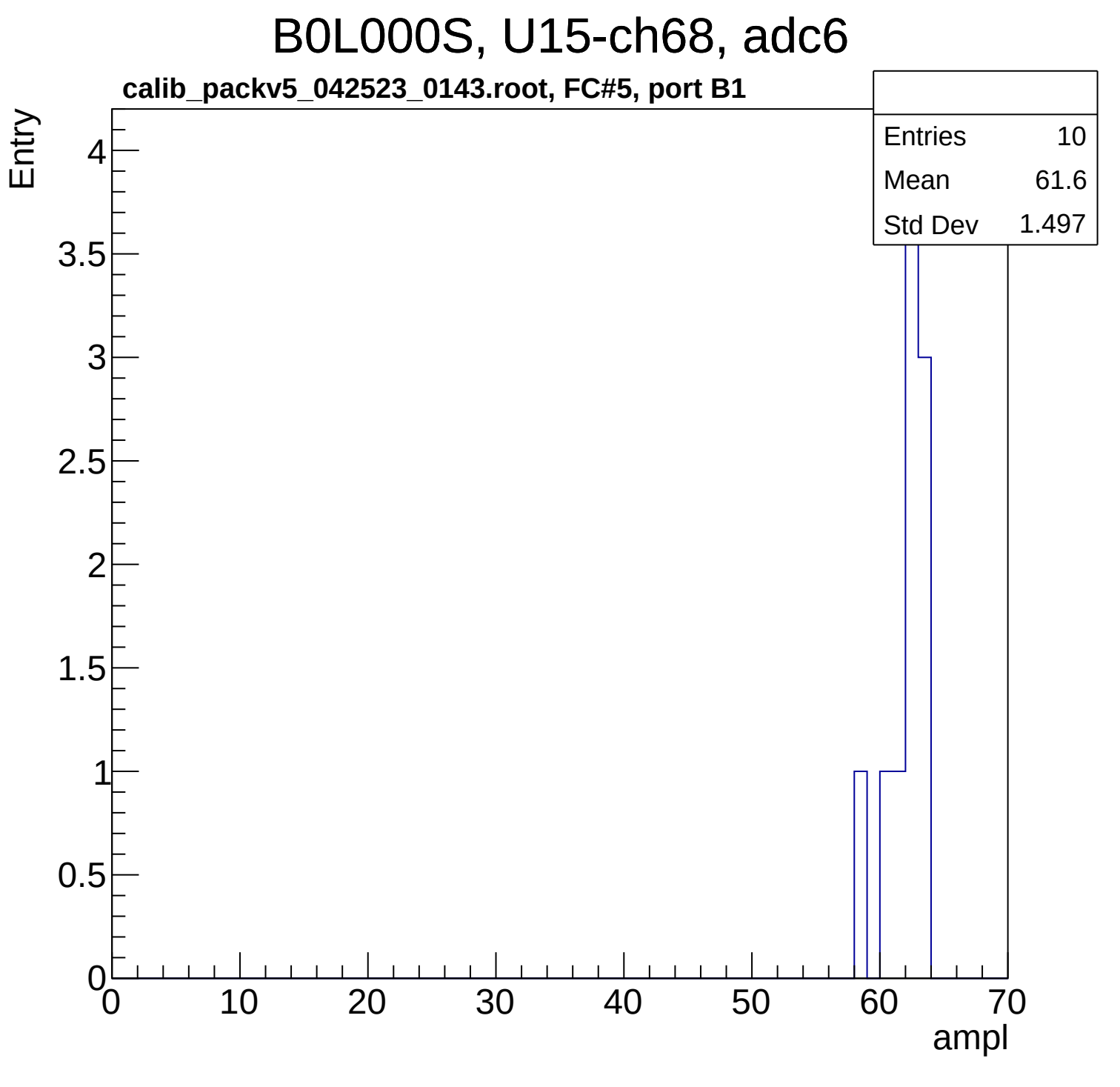
B0L000S, U15-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.6
Std Dev 1.497
Entry
ampl
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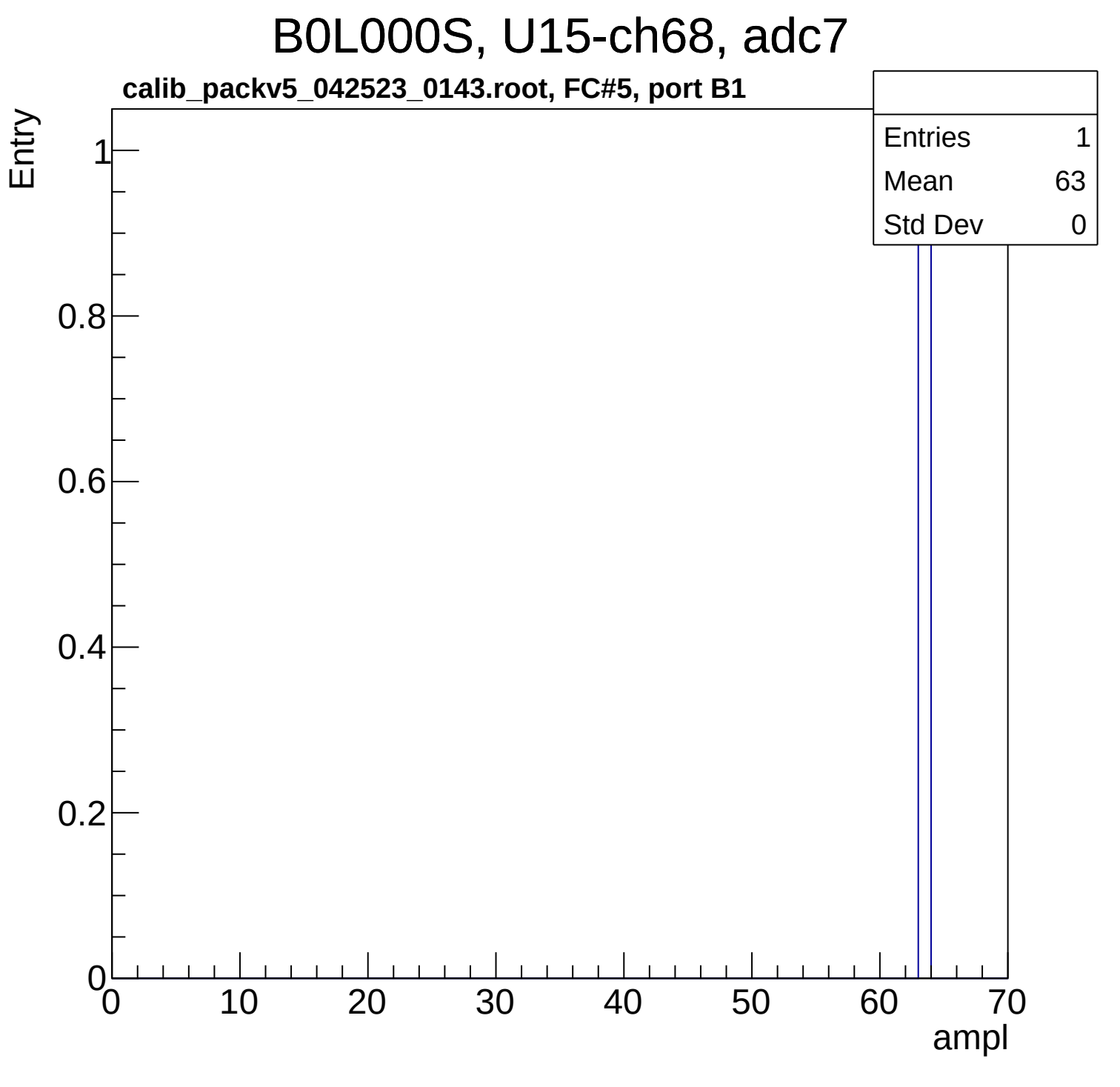
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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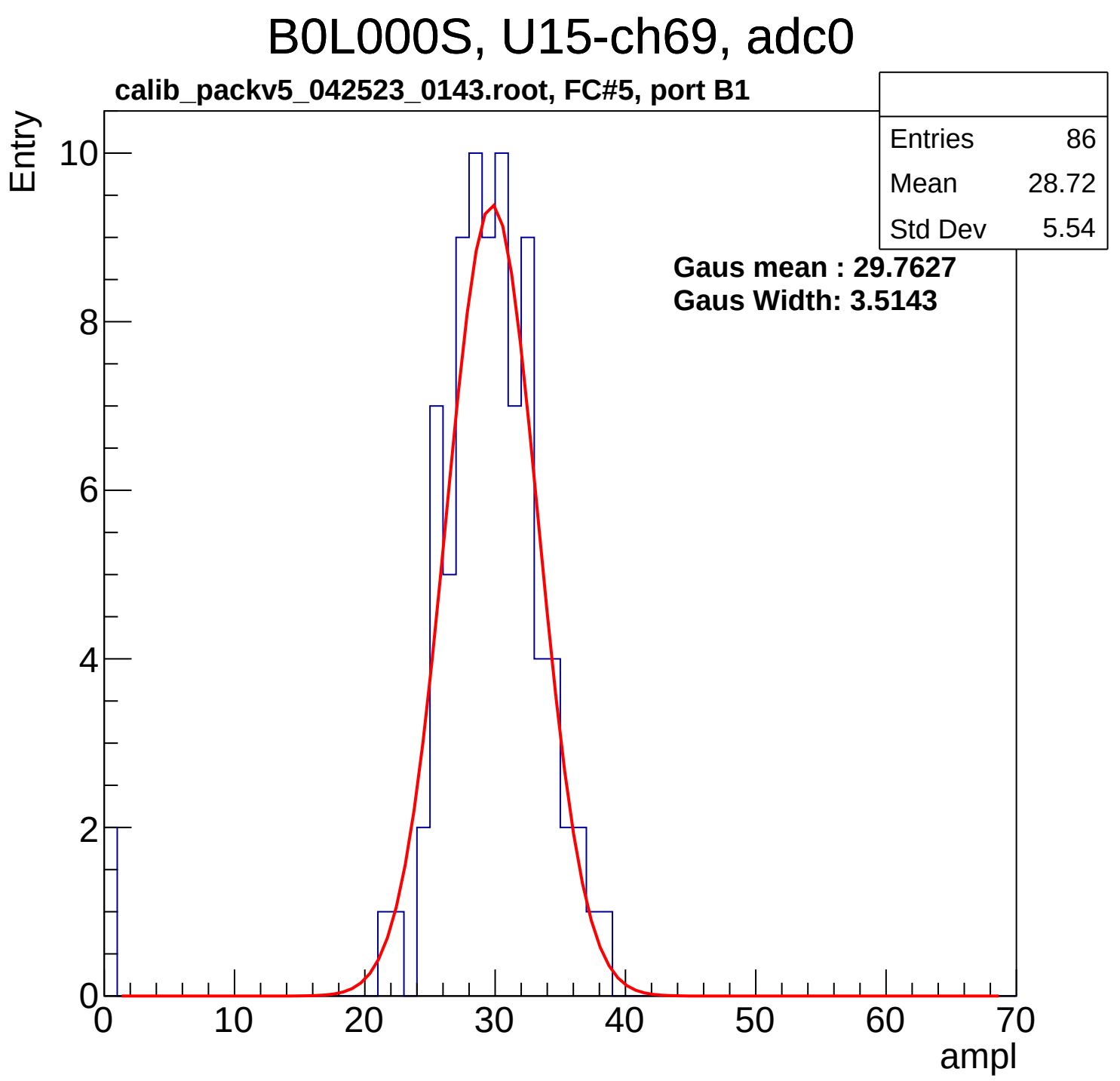
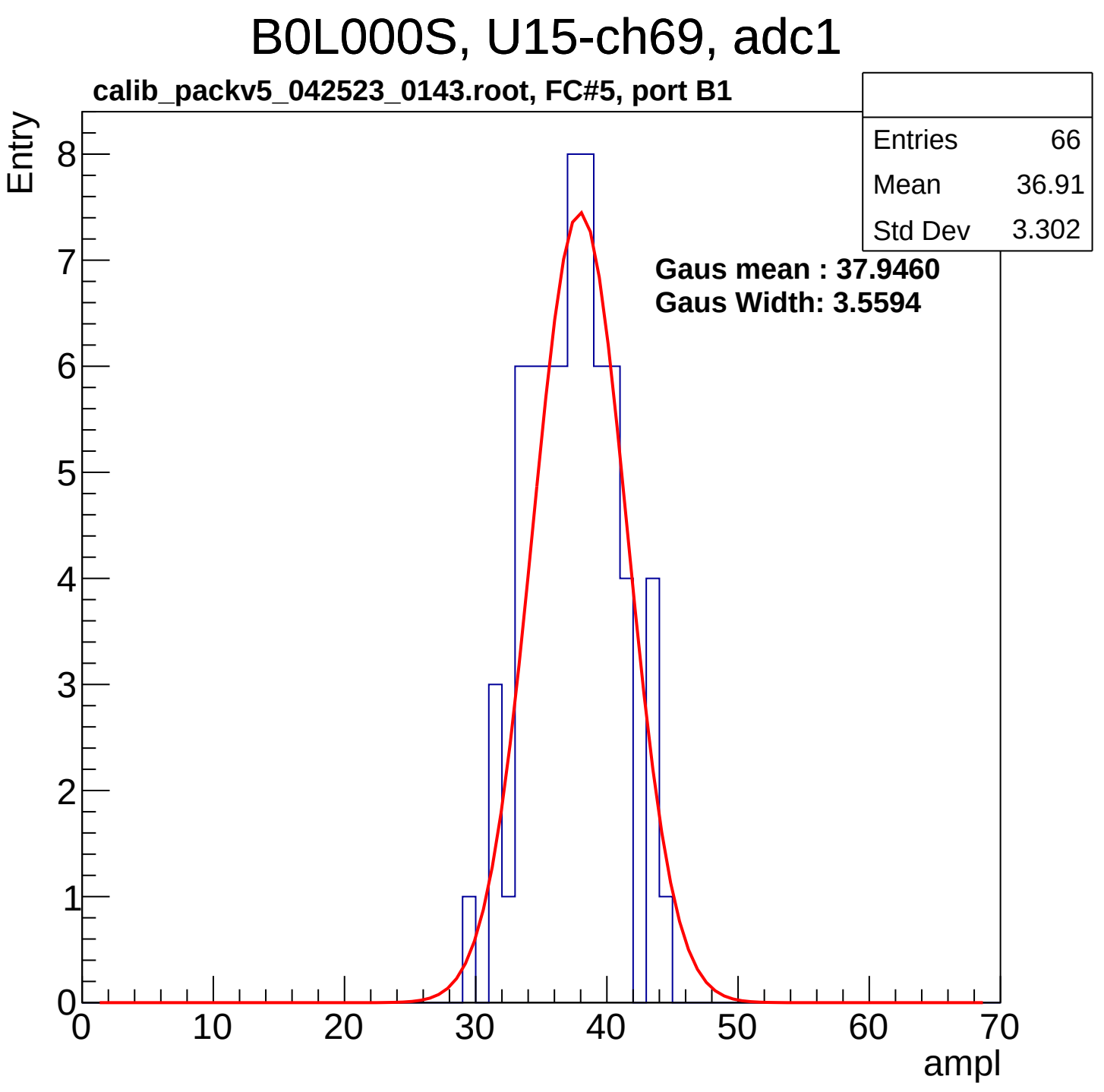
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 28.72
Std Dev 5.54
Gaus mean : 29.7627
Gaus Width: 3.5143
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.91
Std Dev 3.302
Gaus mean : 37.9460
Gaus Width: 3.5594
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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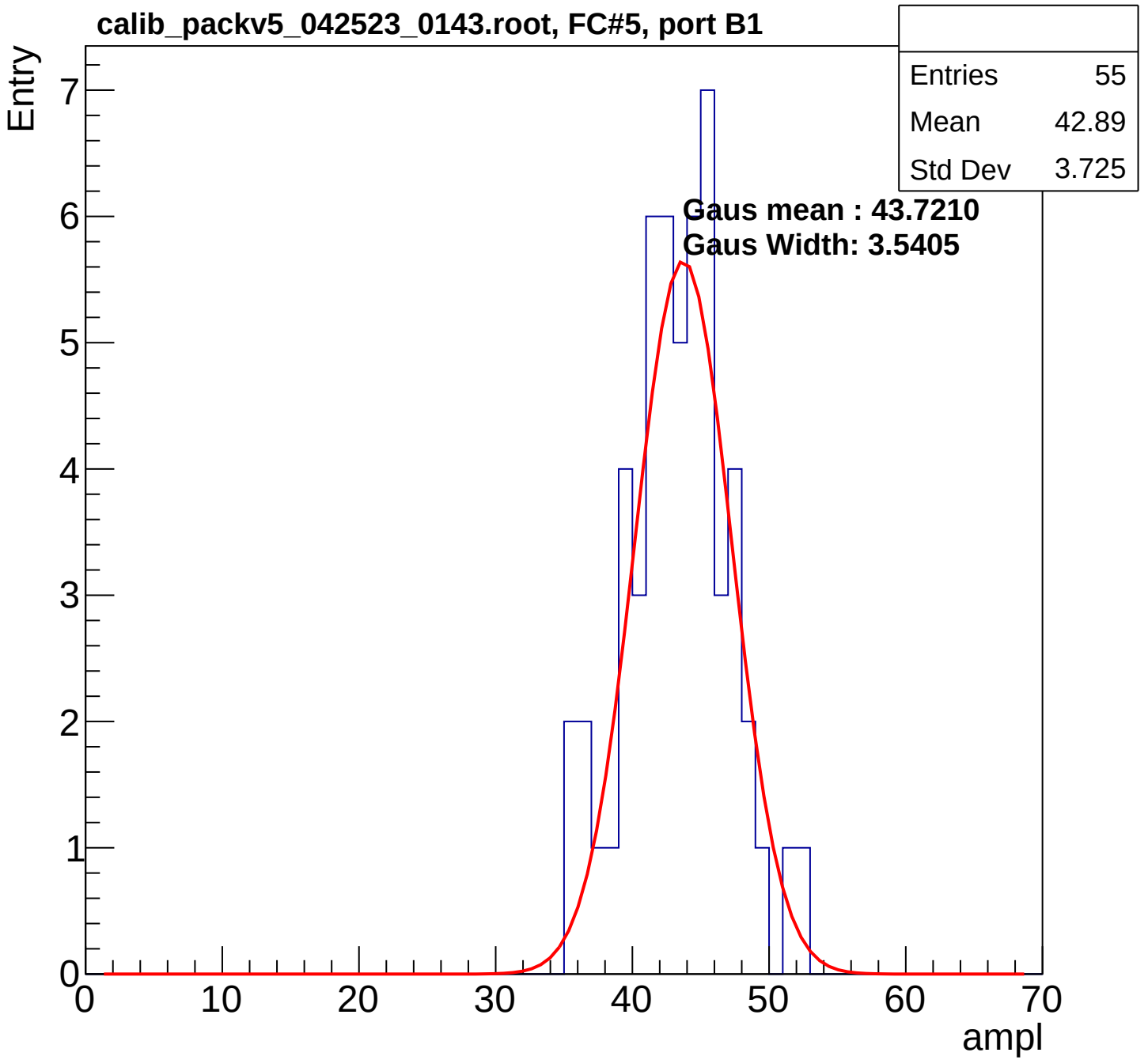
B0L000S, U15-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.89
Std Dev 3.725
Gaus mean : 43.7210
Gaus Width: 3.5405
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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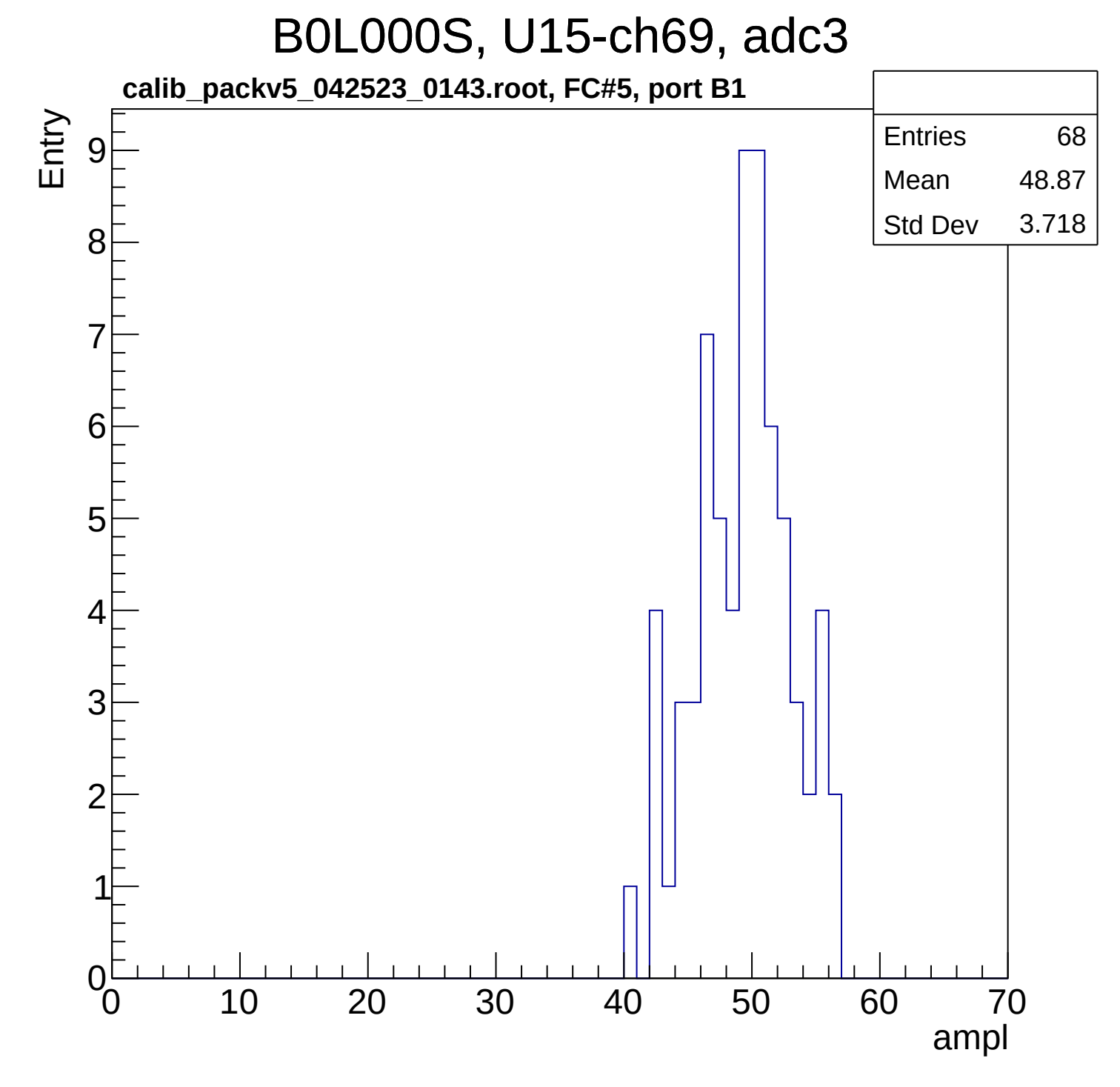
B0L000S, U15-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.87
Std Dev 3.718
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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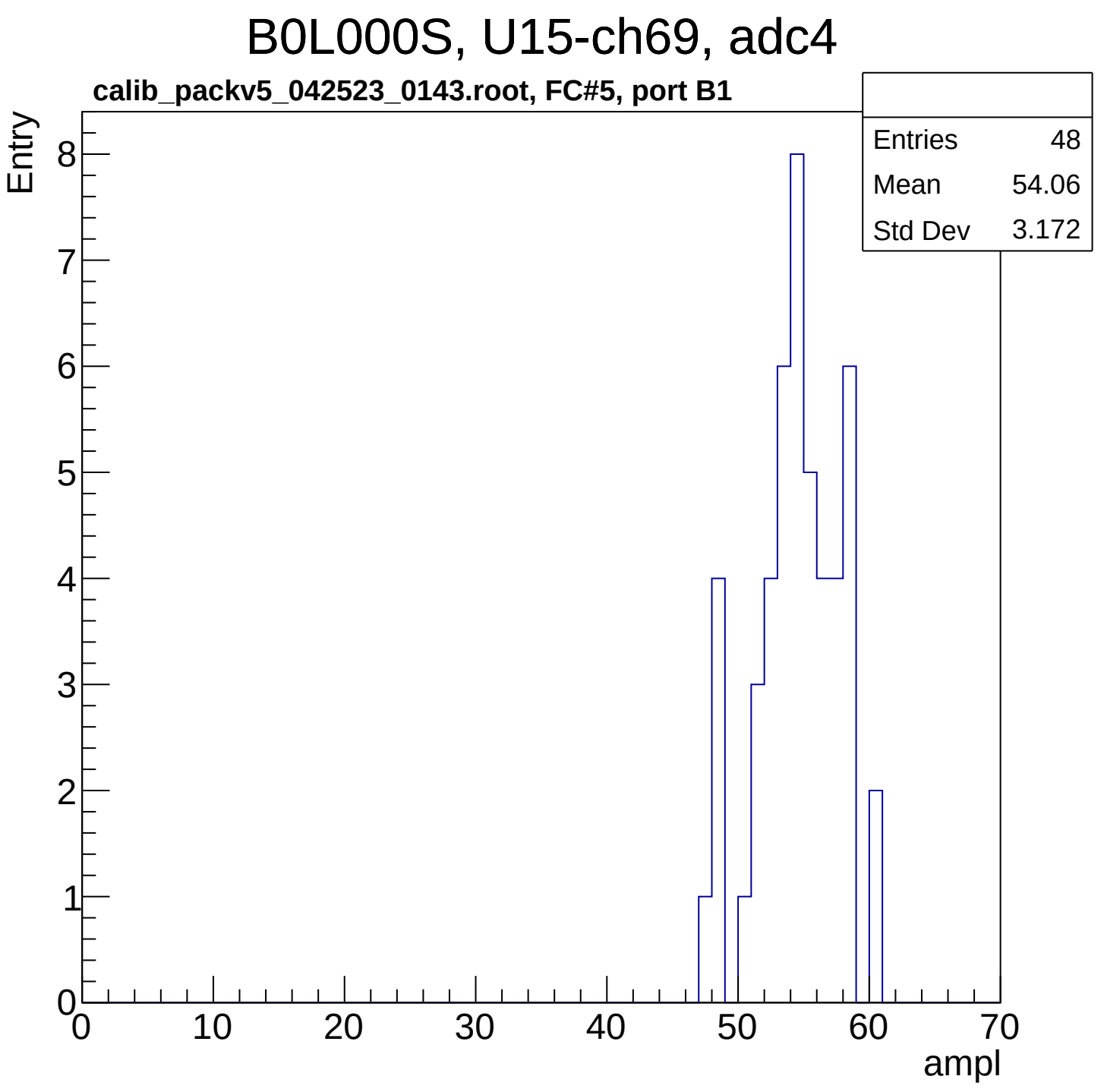
B0L000S, U15-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.06
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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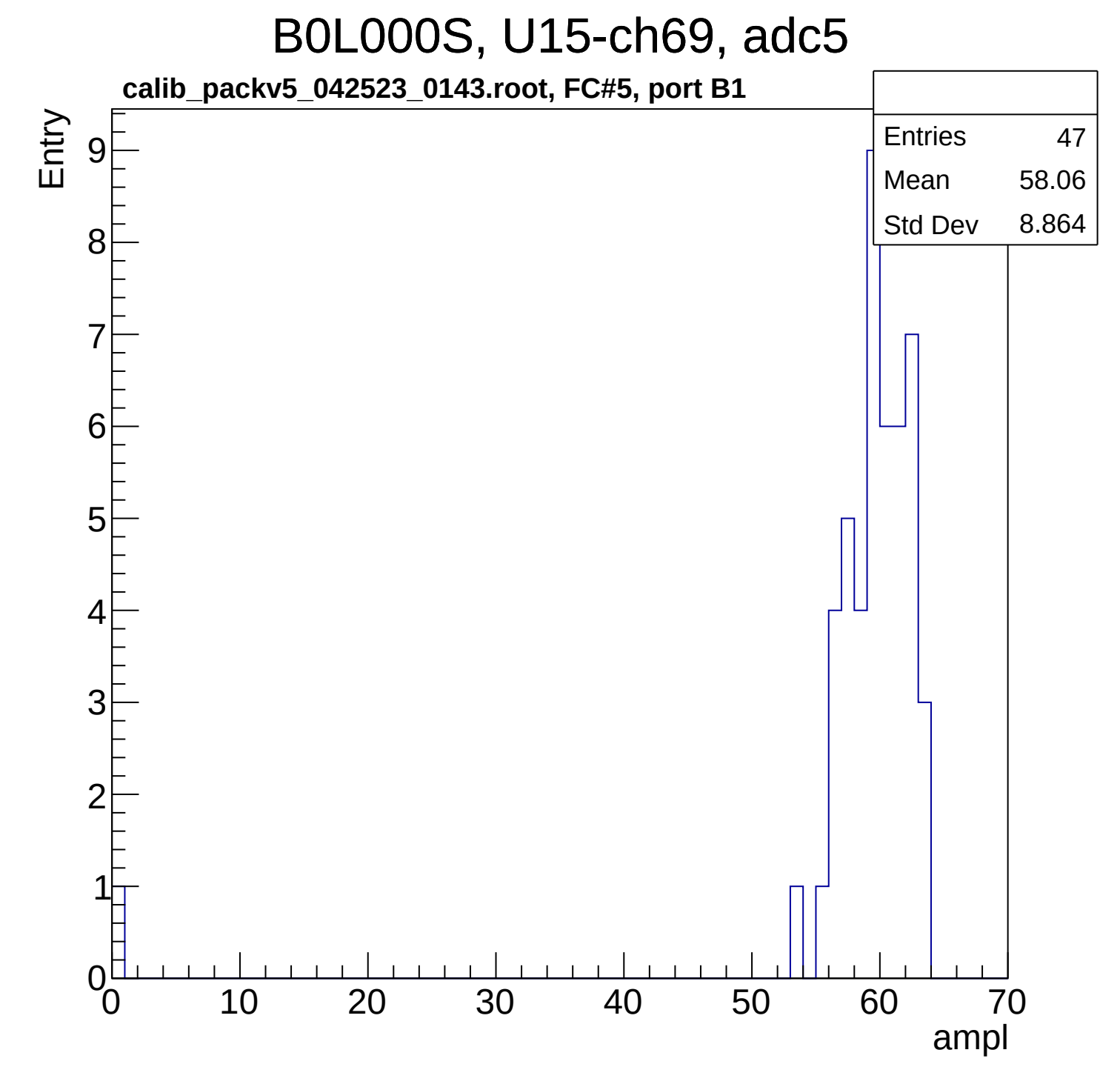
B0L000S, U15-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.06
Std Dev 8.864
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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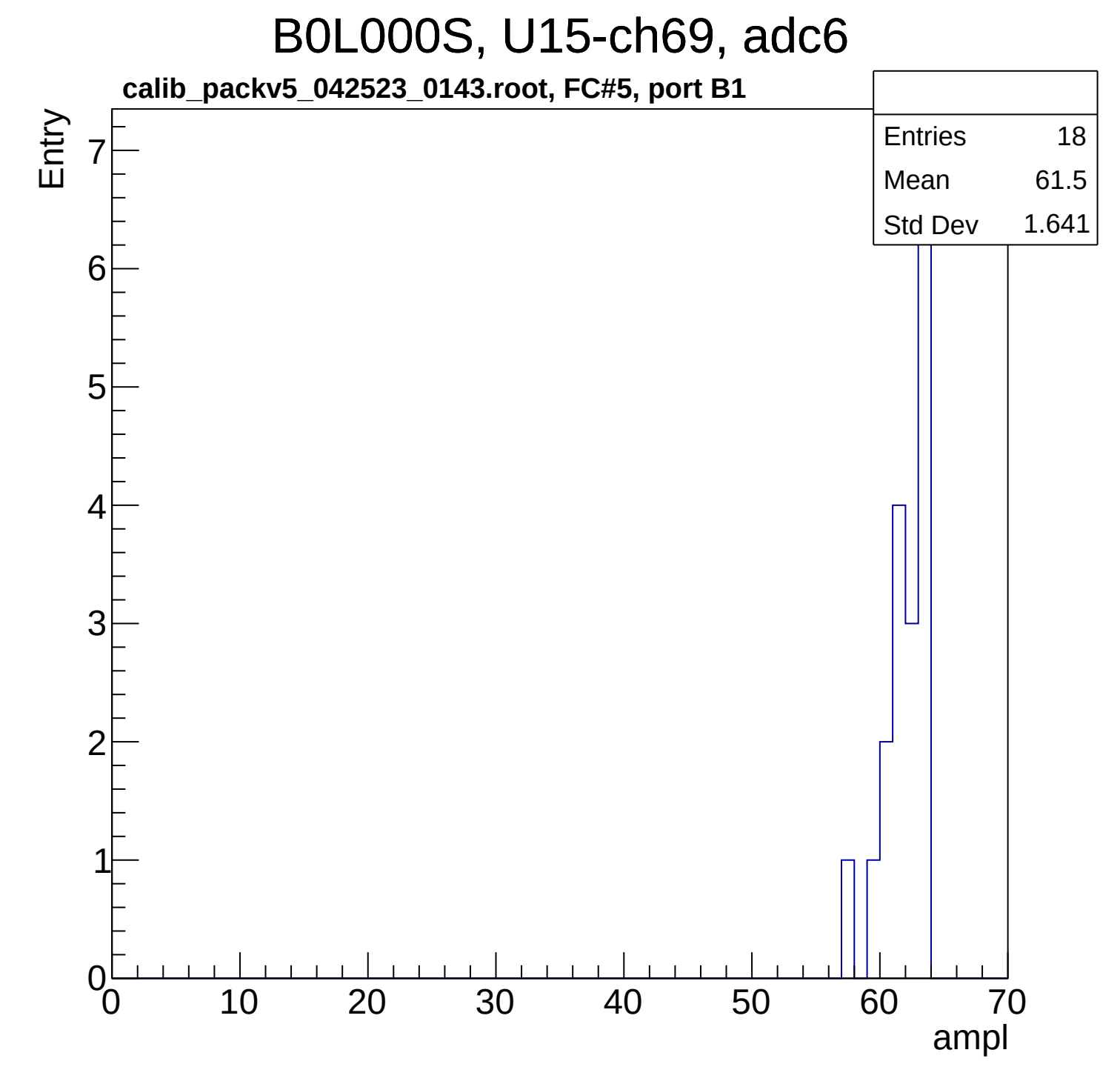
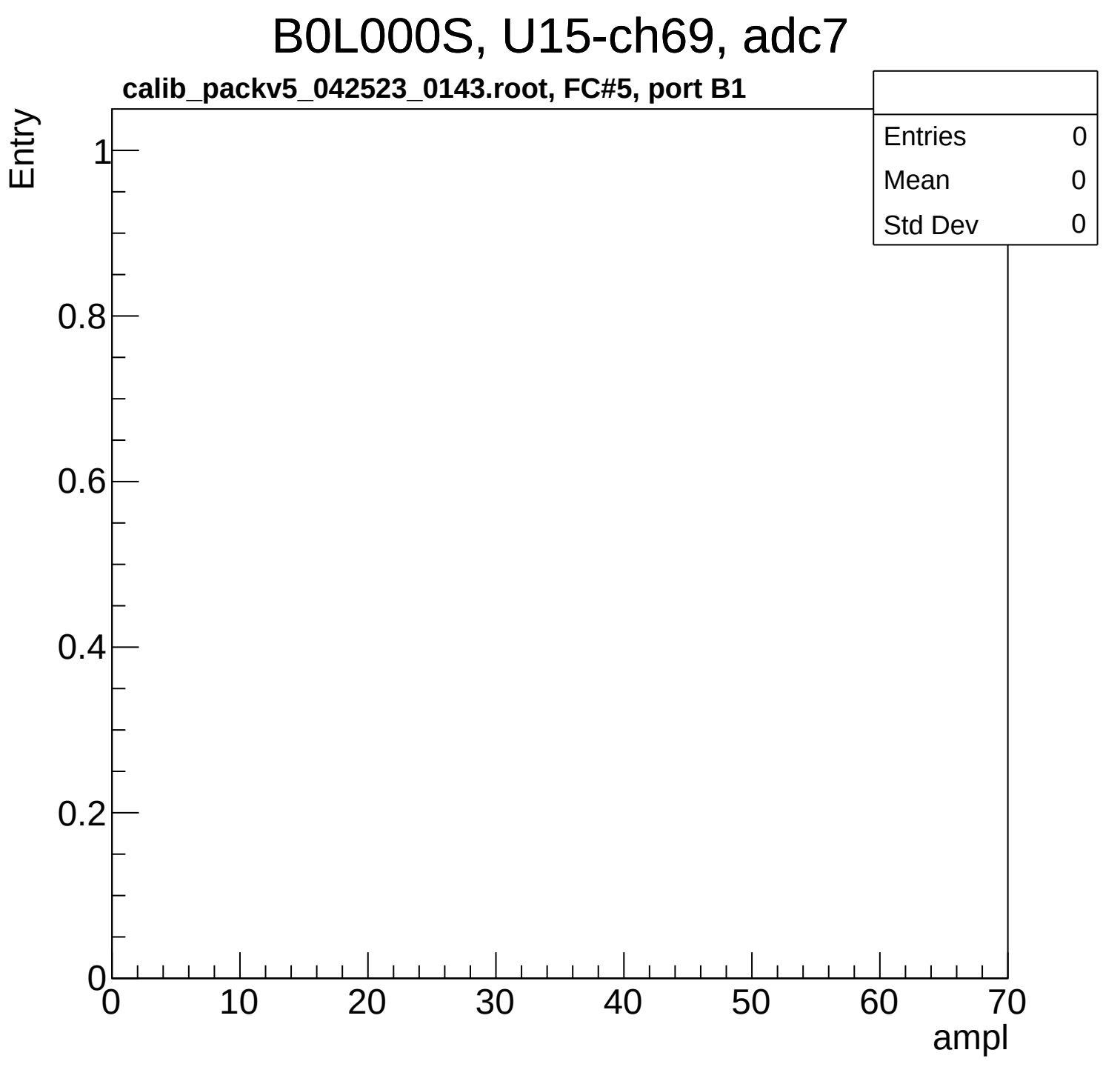
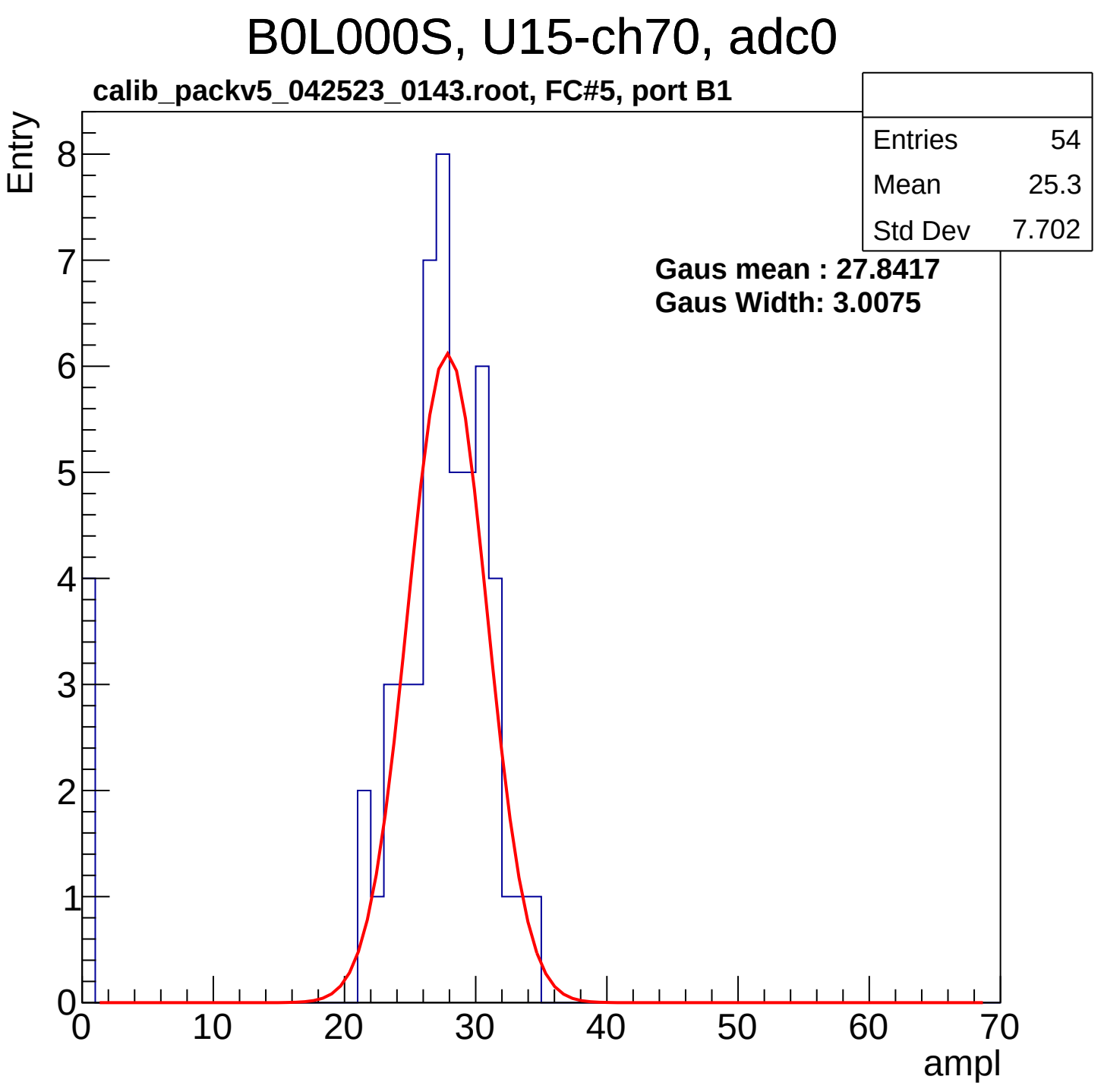

B0L000S, U15-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.5
Std Dev 1.641
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 25.3
Std Dev 7.702
Gaus mean : 27.8417
Gaus Width: 3.0075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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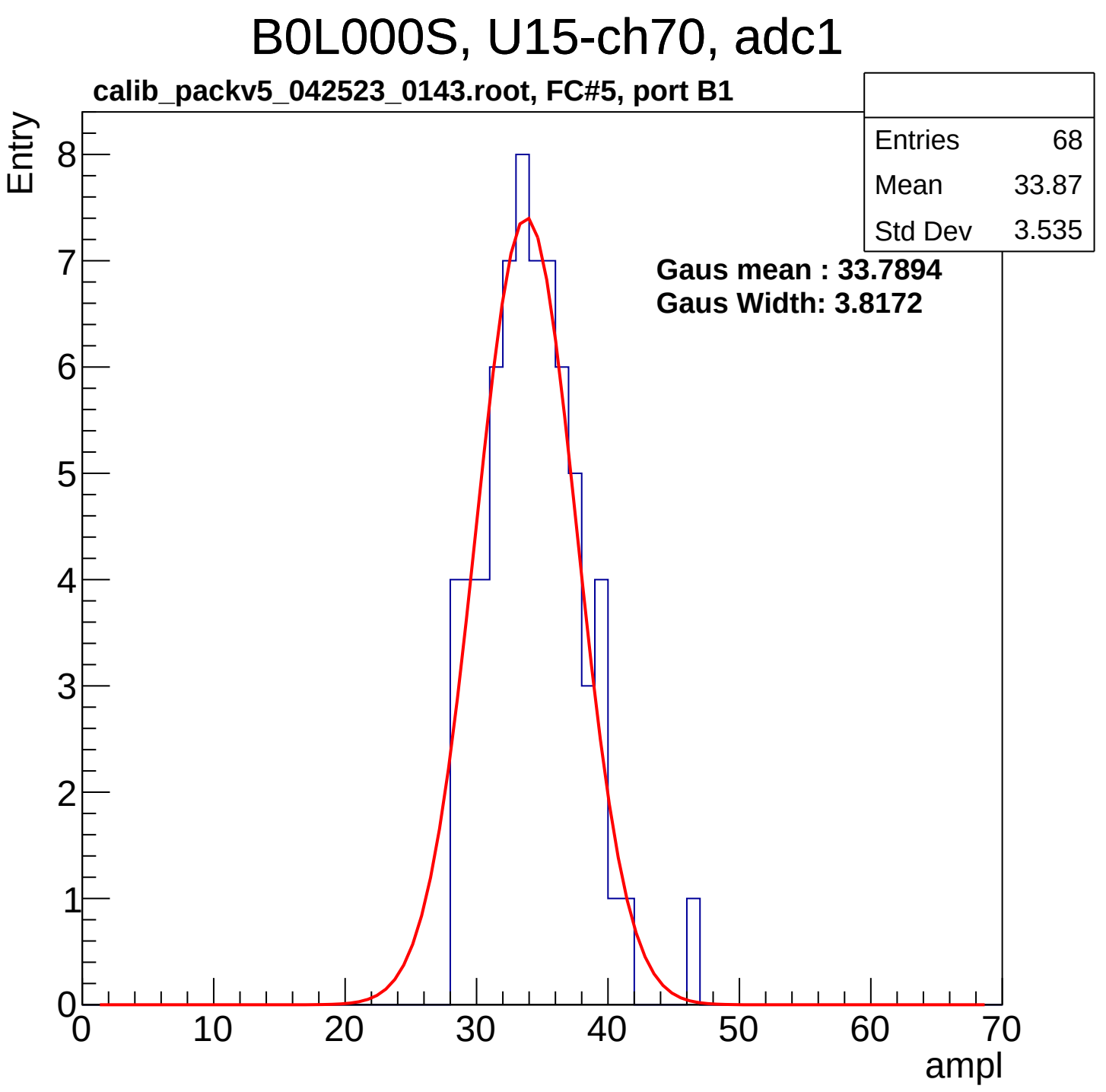
B0L000S, U15-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 33.87
Std Dev 3.535
Gaus mean : 33.7894
Gaus Width: 3.8172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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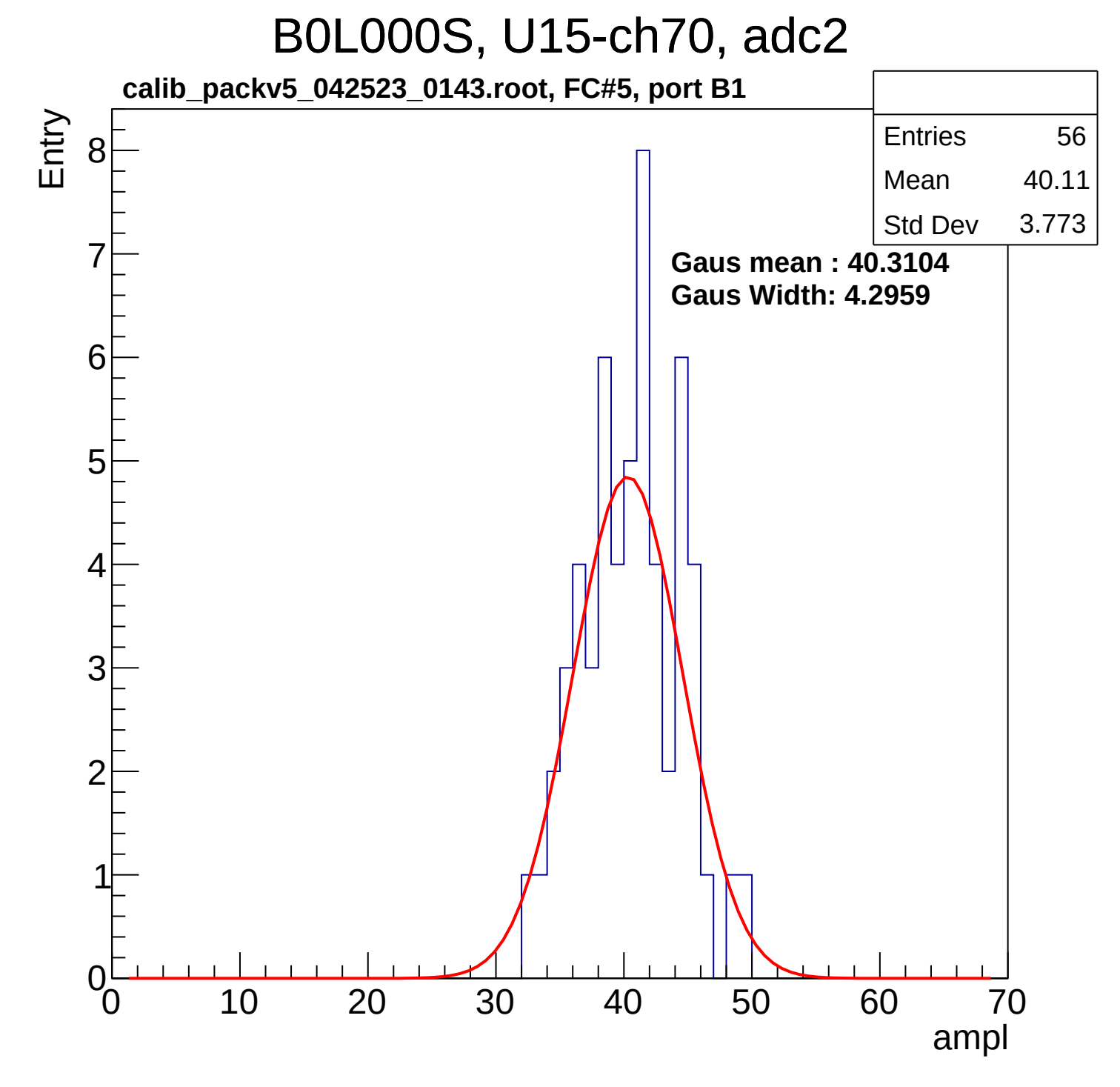
B0L000S, U15-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 40.11
Std Dev 3.773
Gaus mean : 40.3104
Gaus Width: 4.2959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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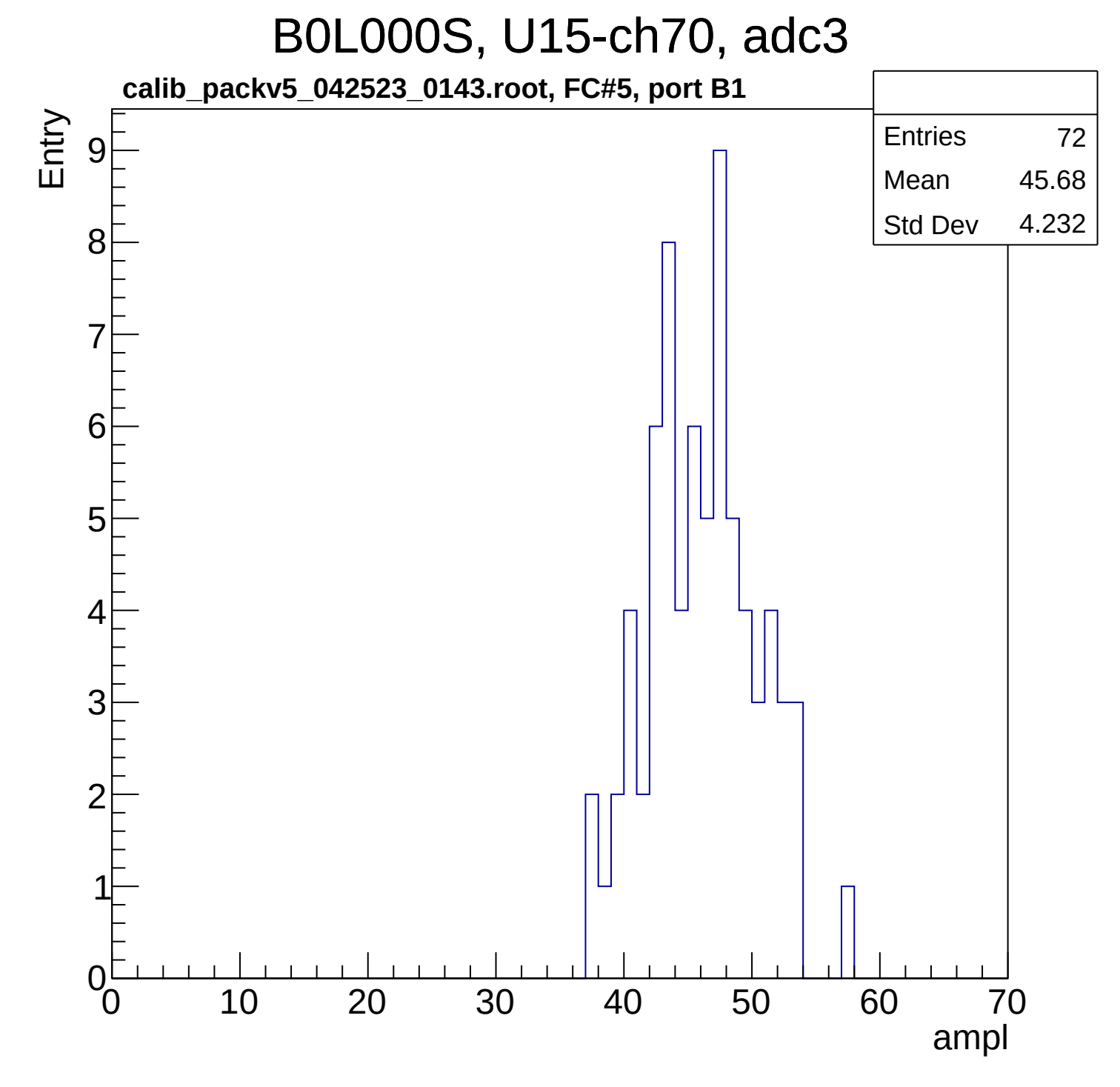
B0L000S, U15-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 45.68
Std Dev 4.232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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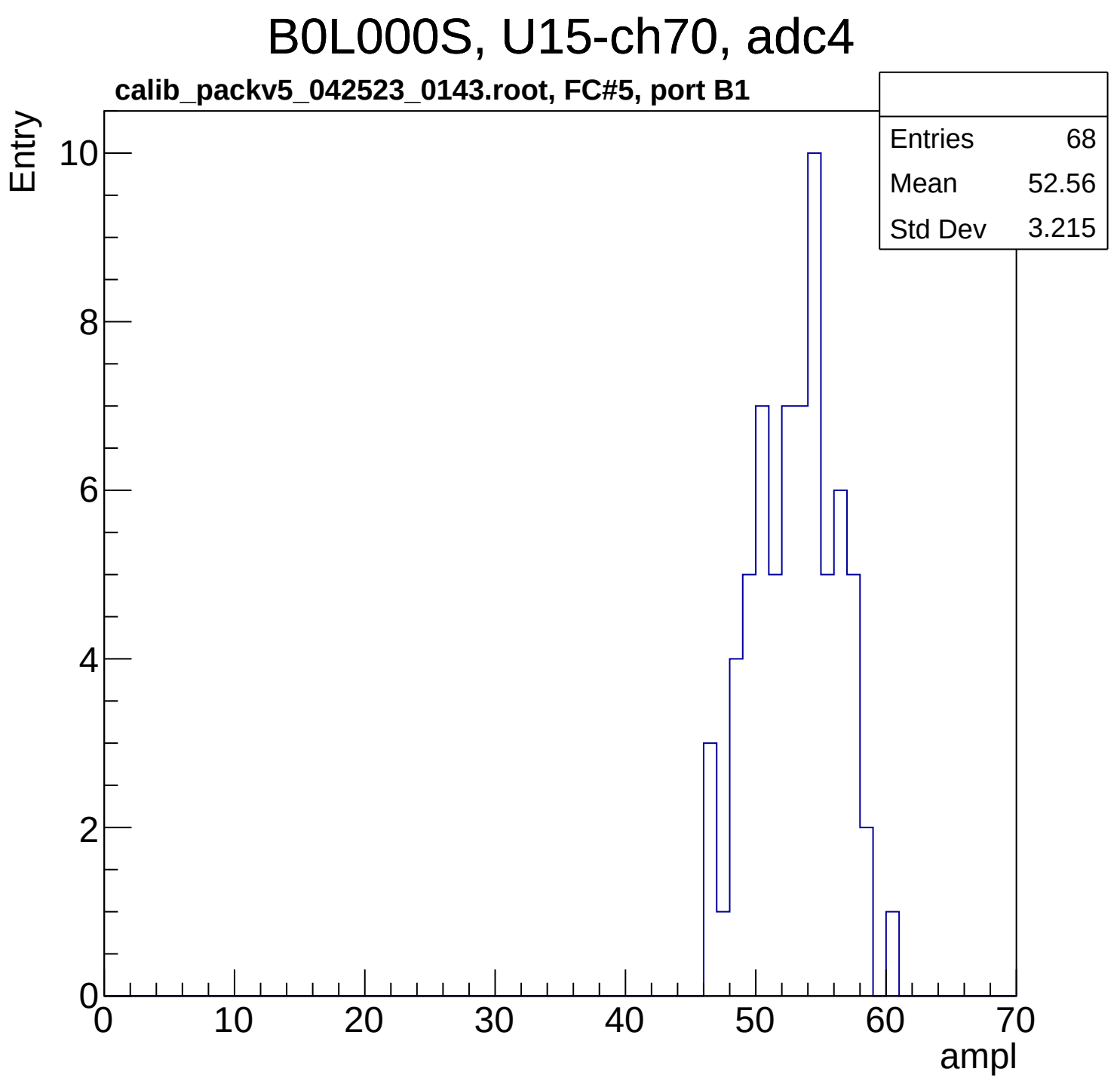
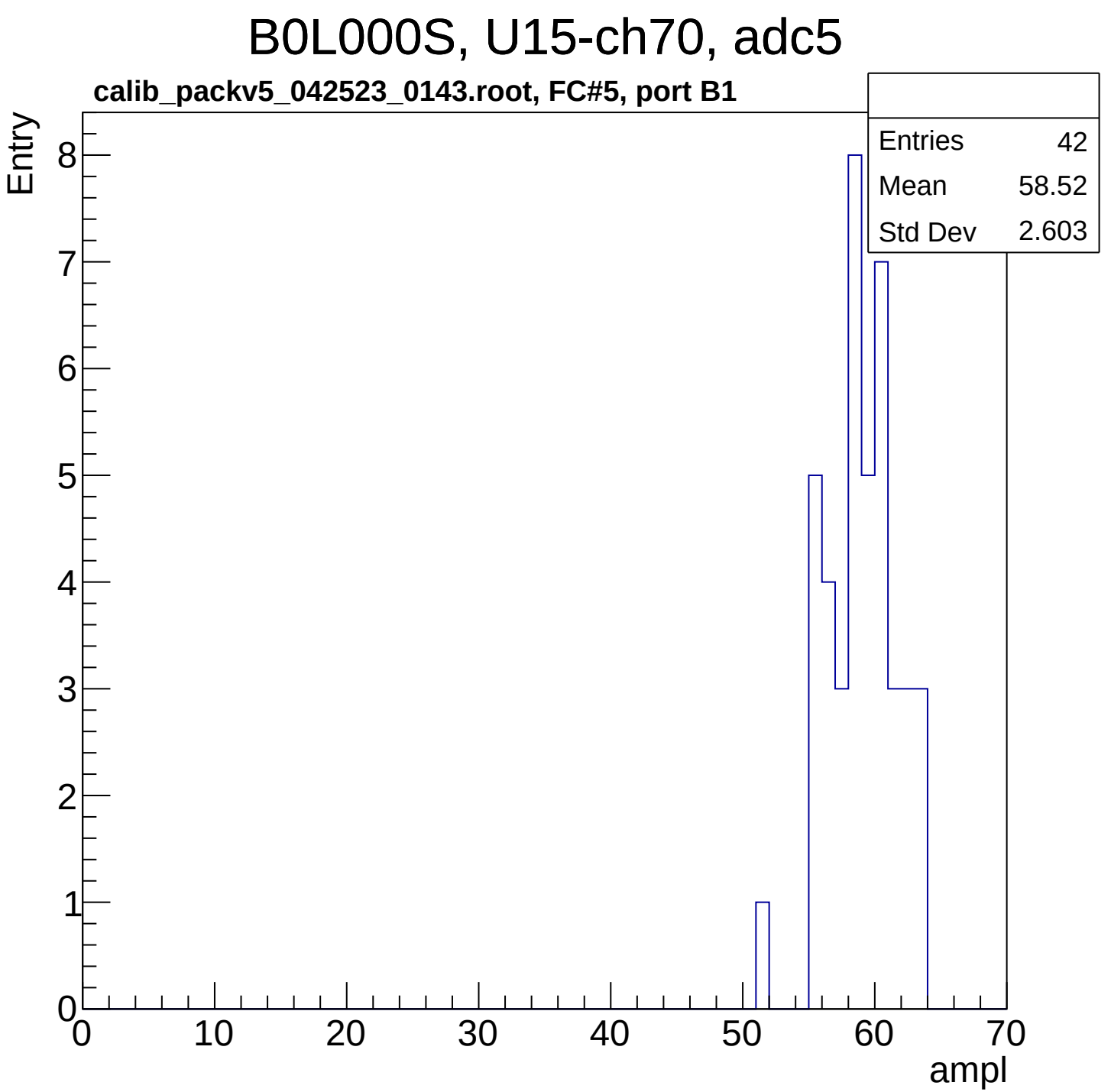
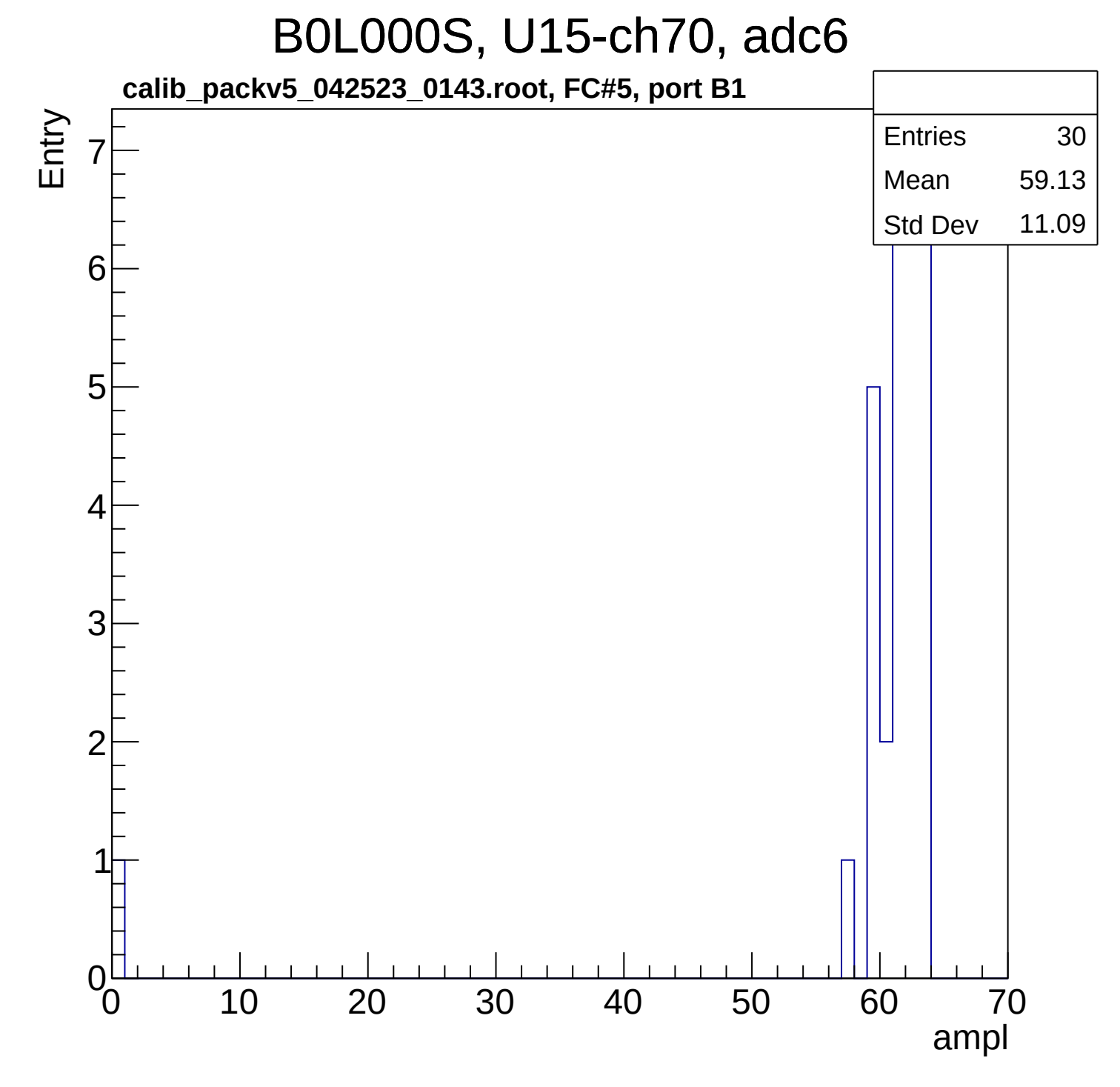
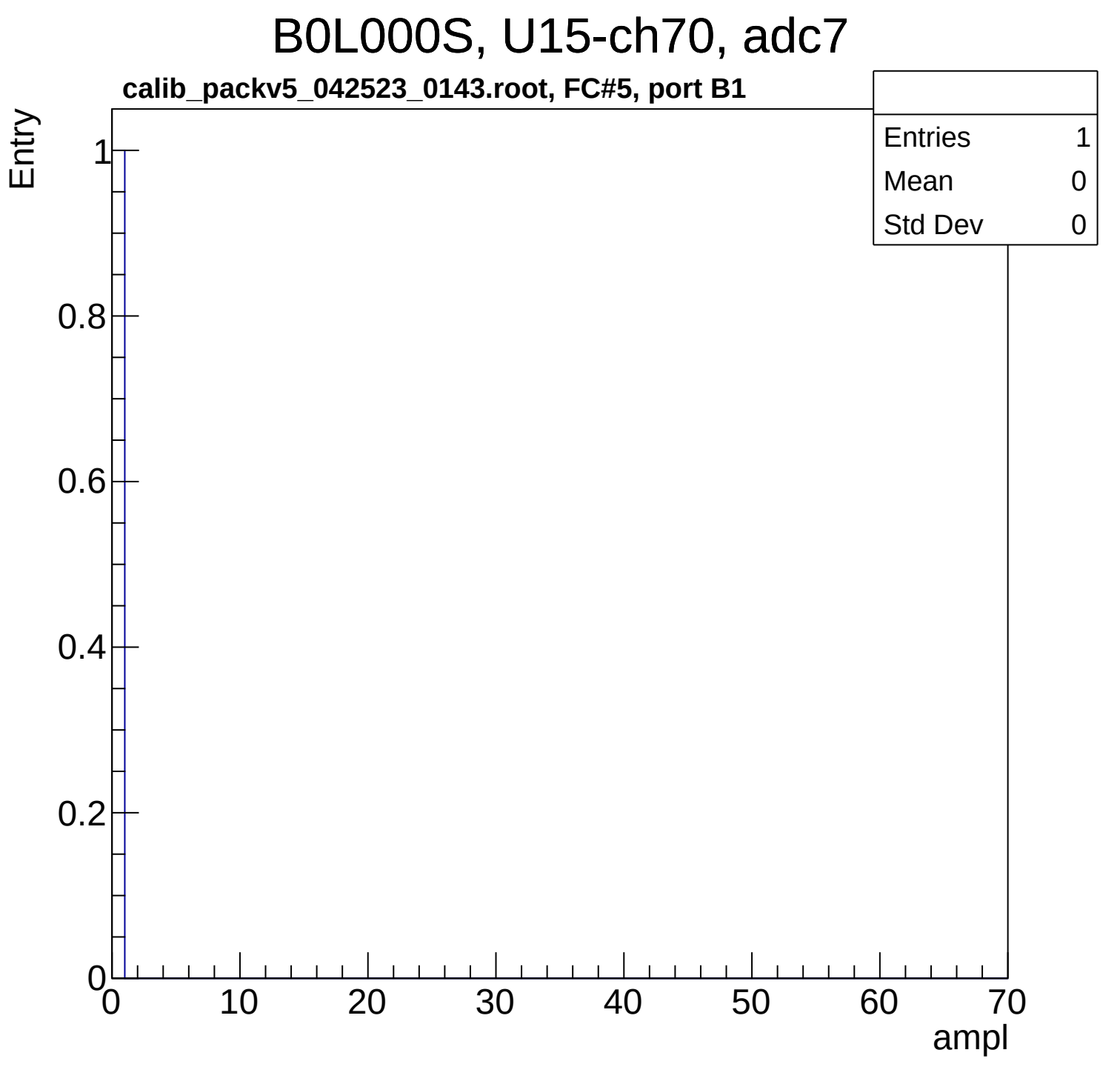

B0L000S, U15-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 52.56
Std Dev 3.215
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.52
Std Dev 2.603
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 59.13
Std Dev 11.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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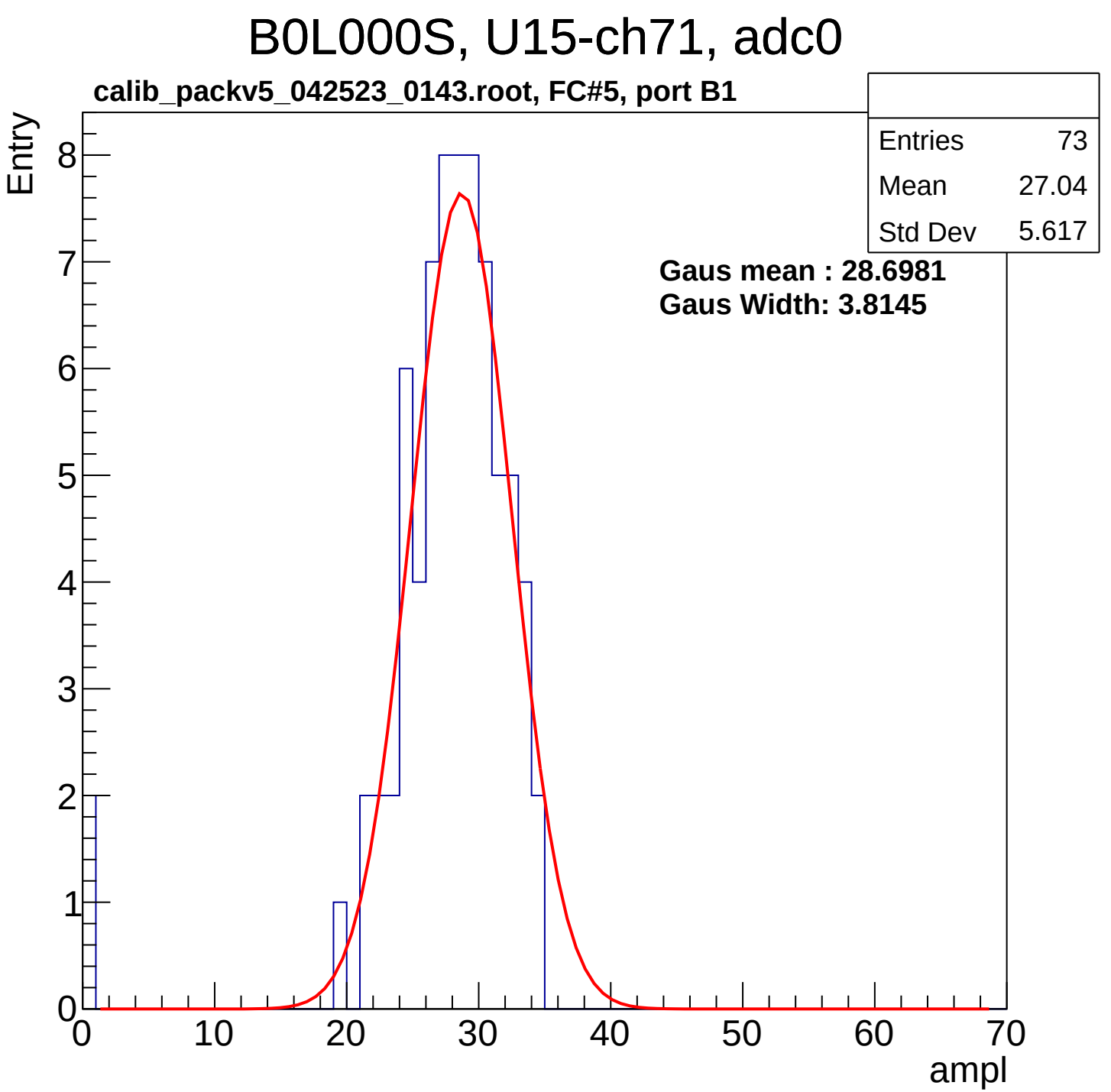
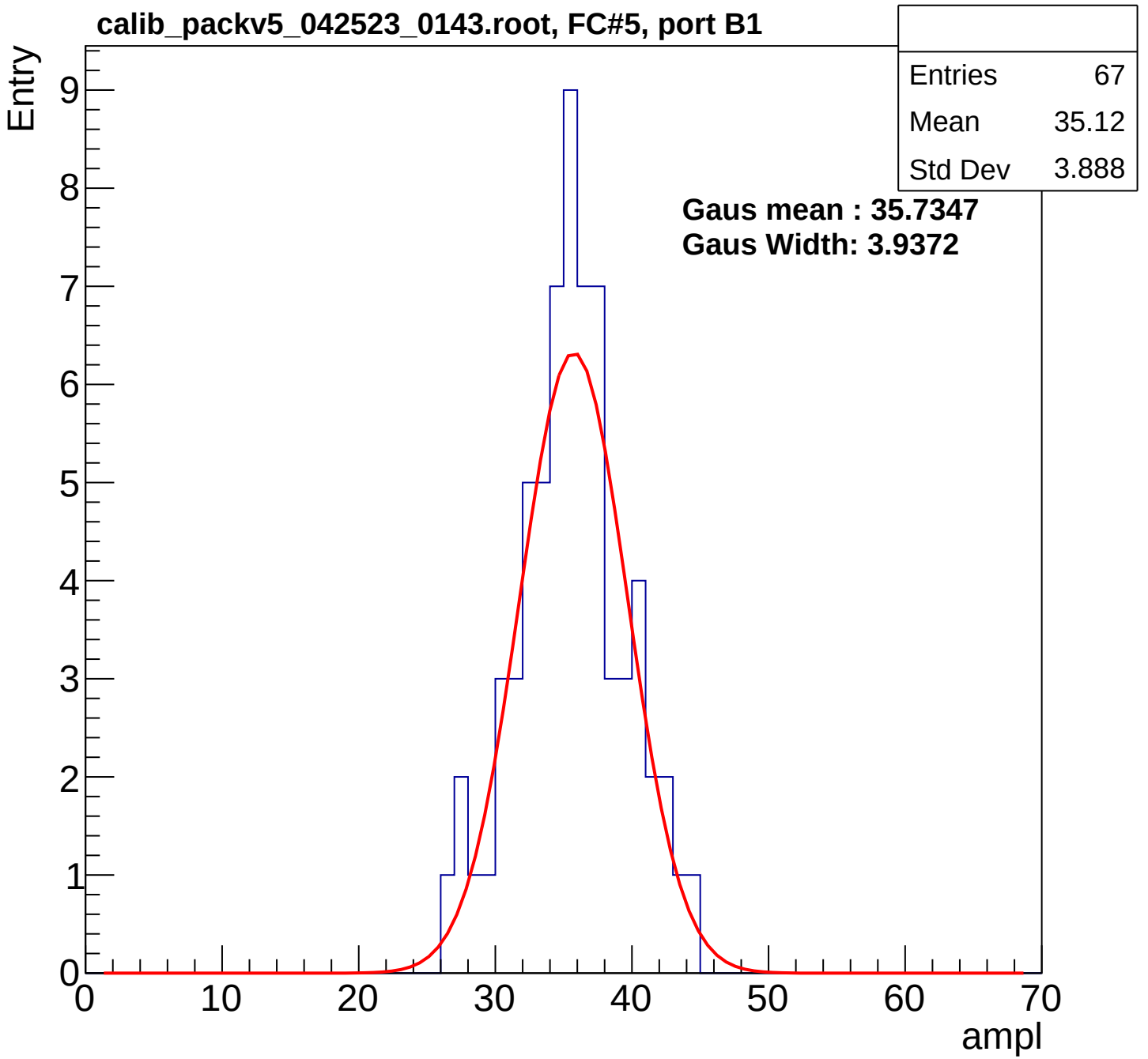
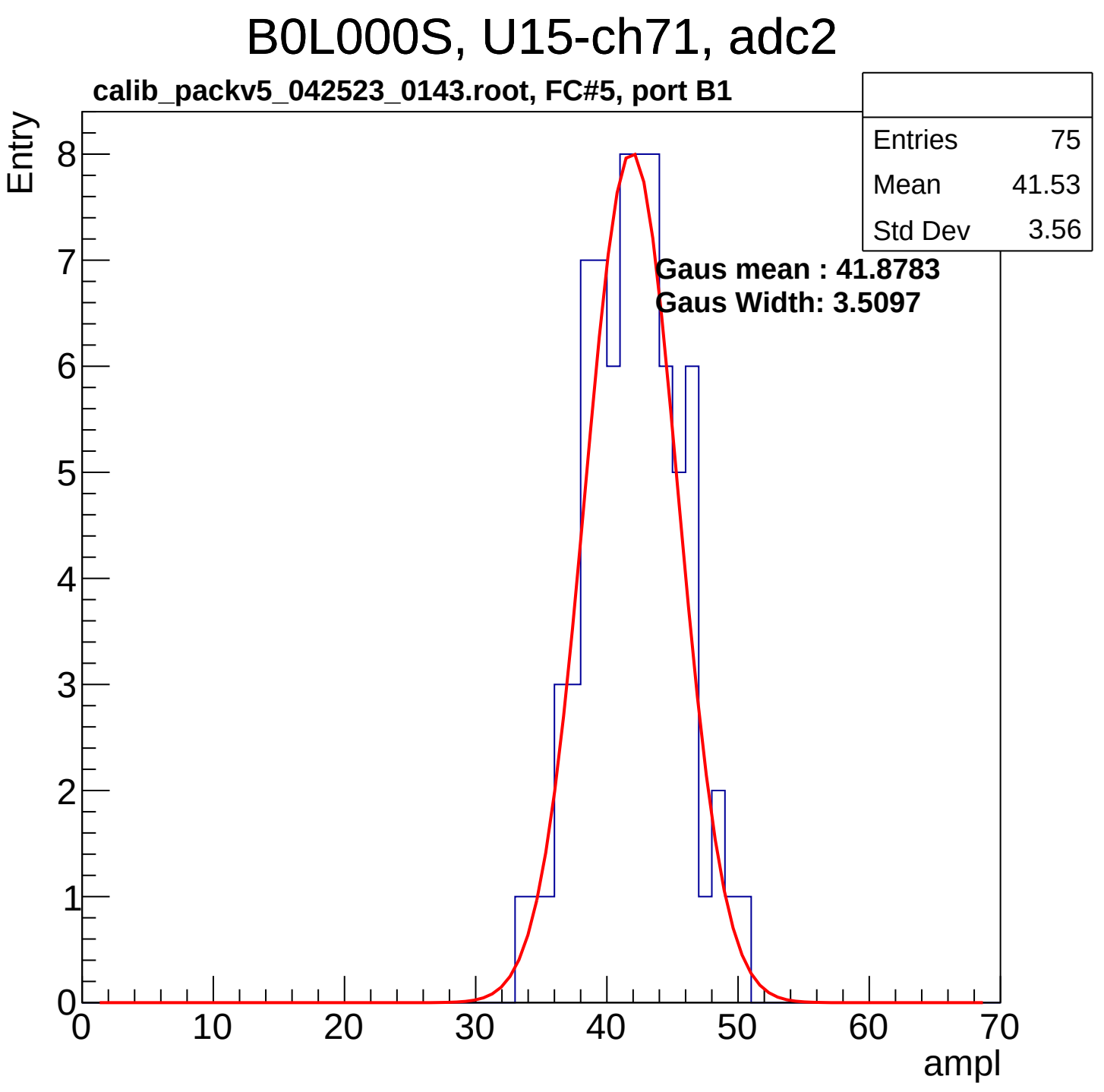
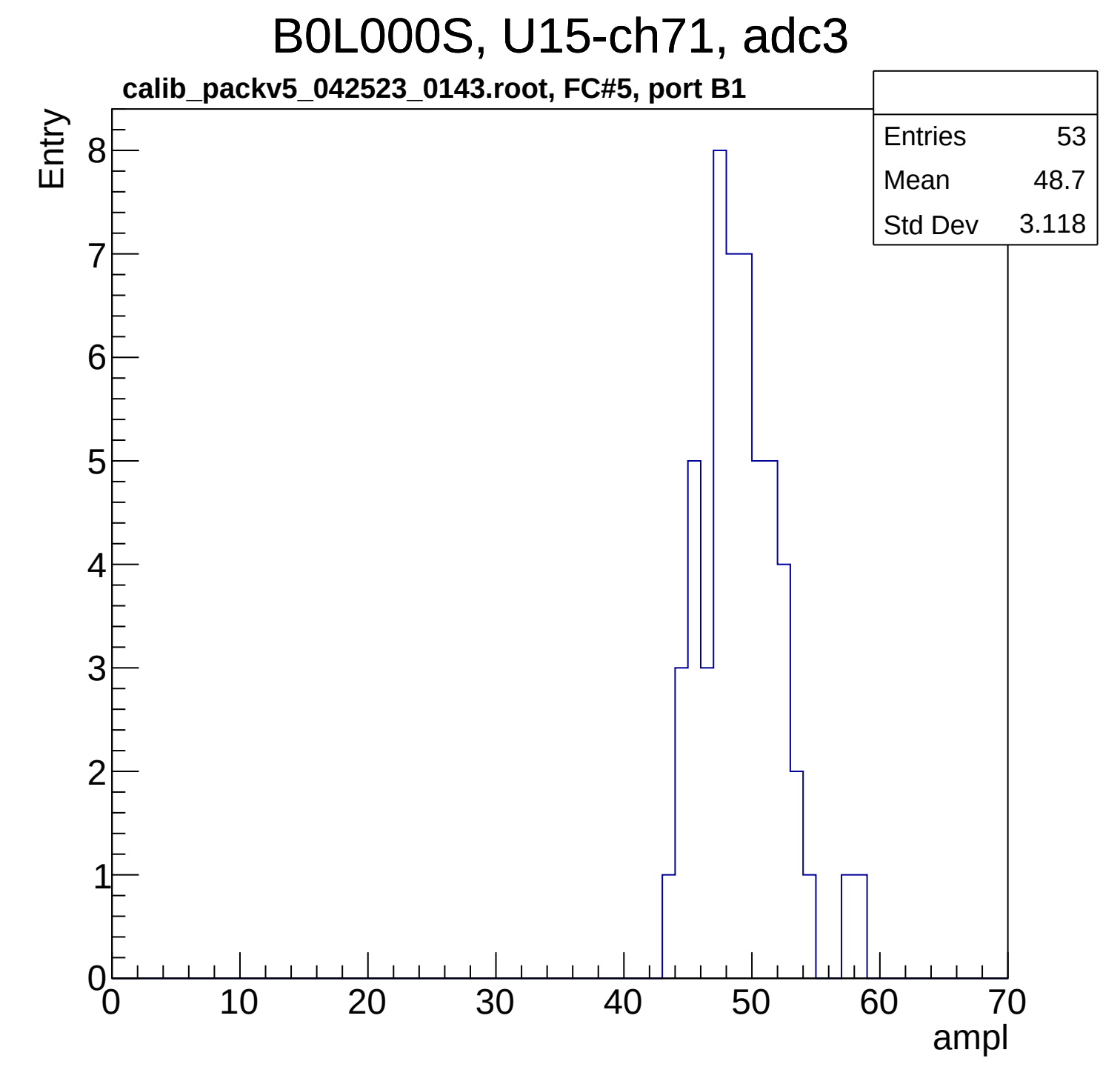
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 27.04
Std Dev 5.617
Gaus mean : 28.6981
Gaus Width: 3.8145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.12
Std Dev 3.888
Gaus mean : 35.7347
Gaus Width: 3.9372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 41.53
Std Dev 3.56
Gaus mean : 41.8783
Gaus Width: 3.5097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 48.7
Std Dev 3.118
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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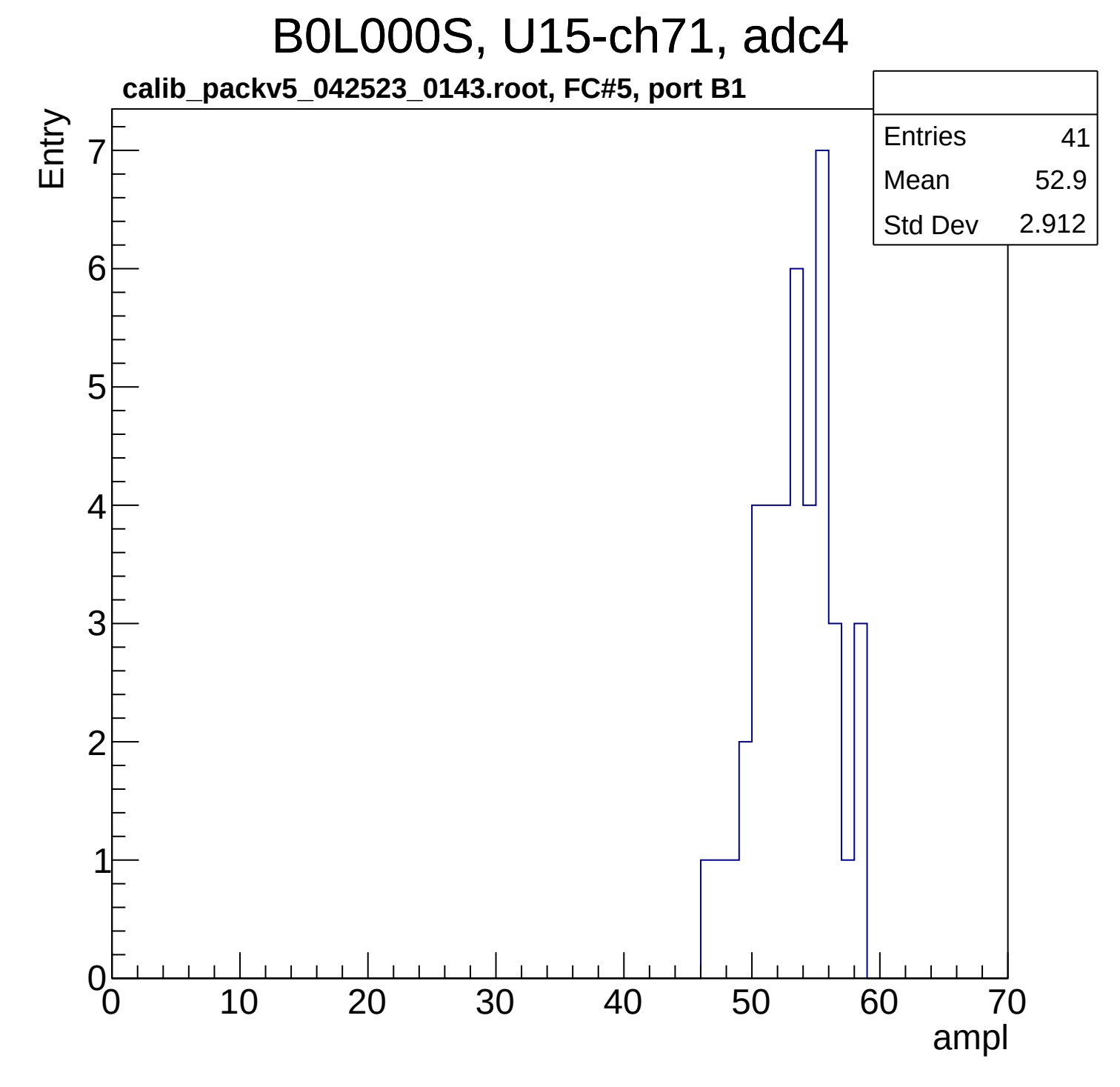
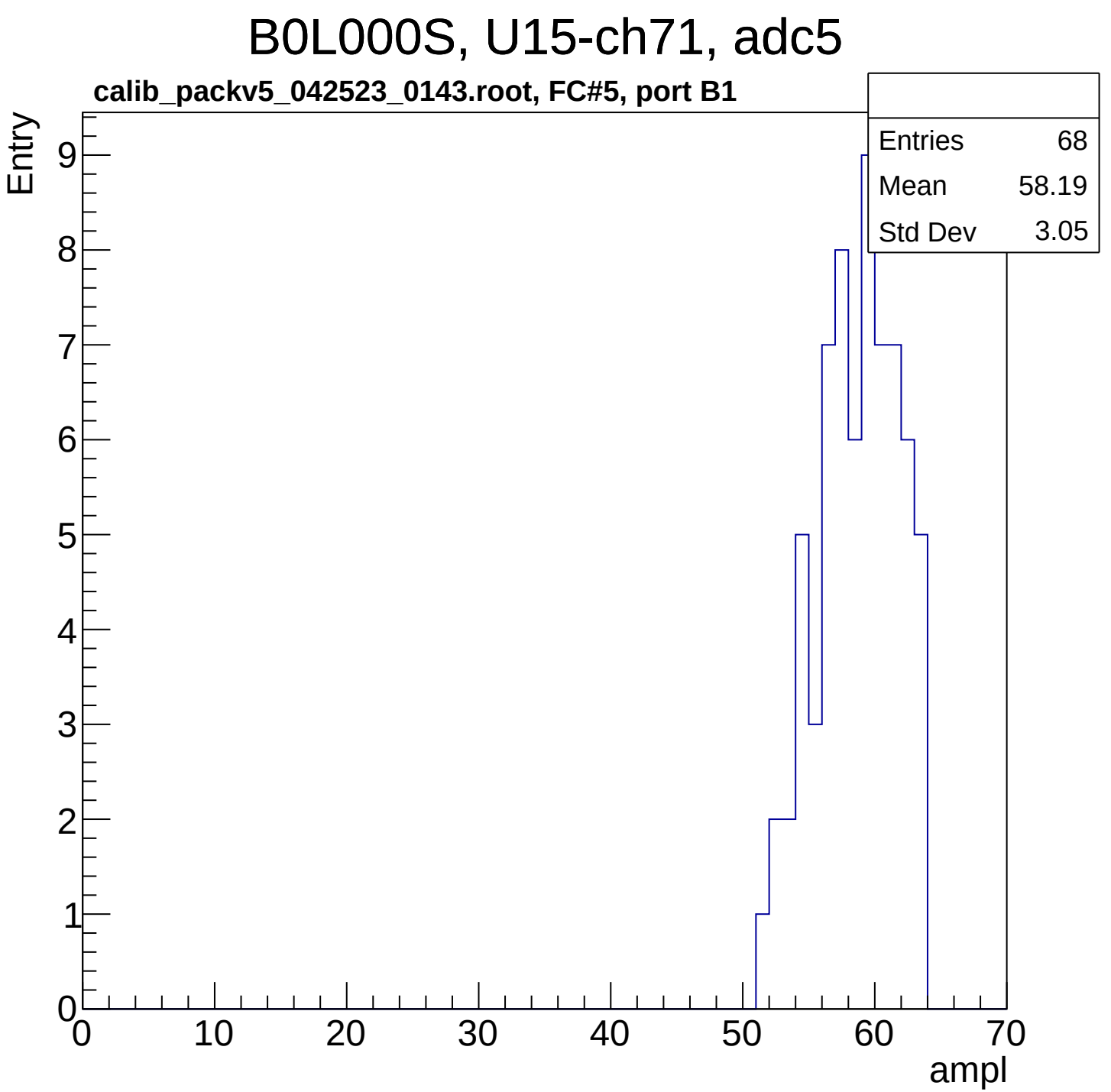

B0L000S, U15-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 52.9
Std Dev 2.912
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 58.19
Std Dev 3.05
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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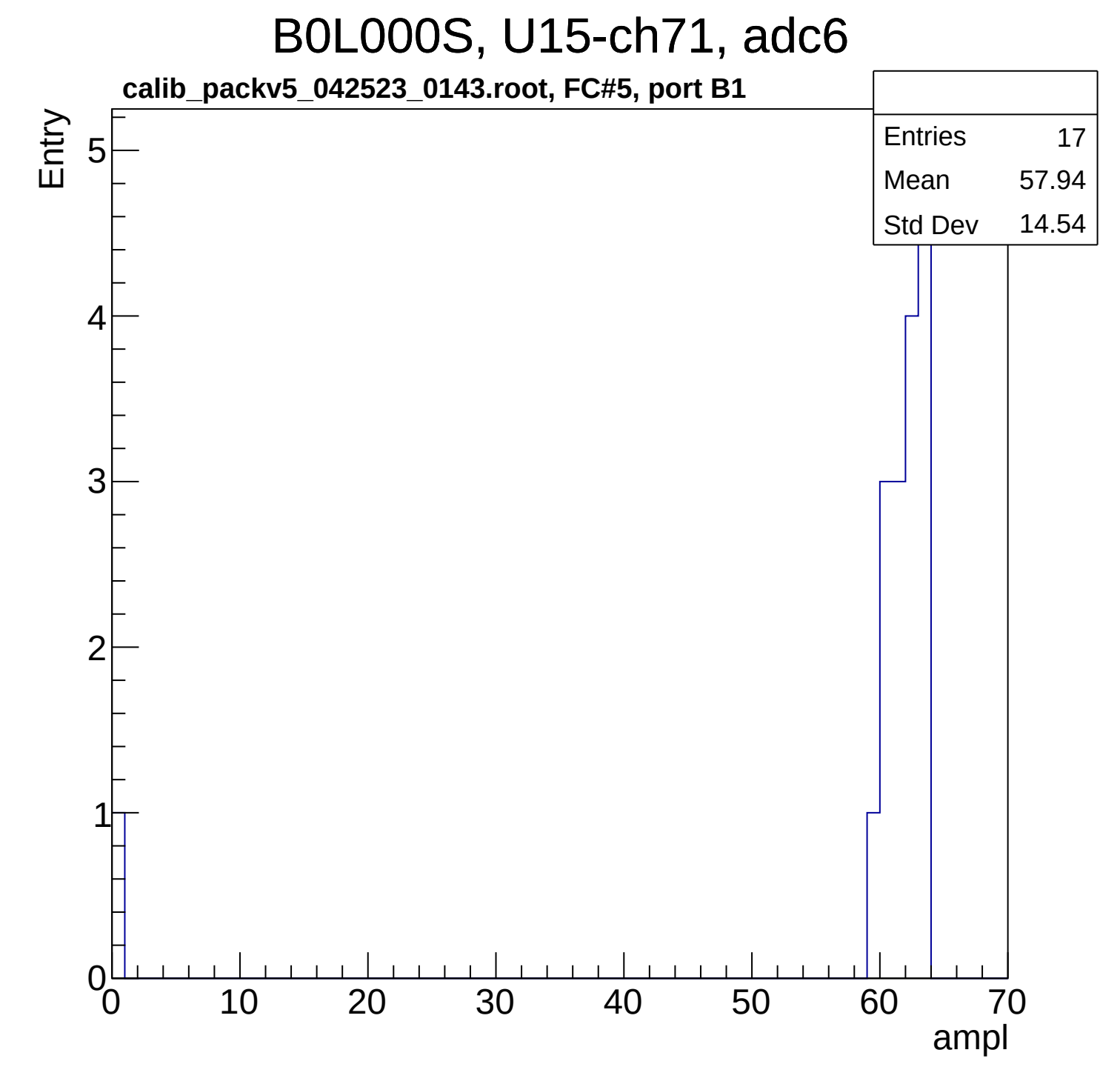
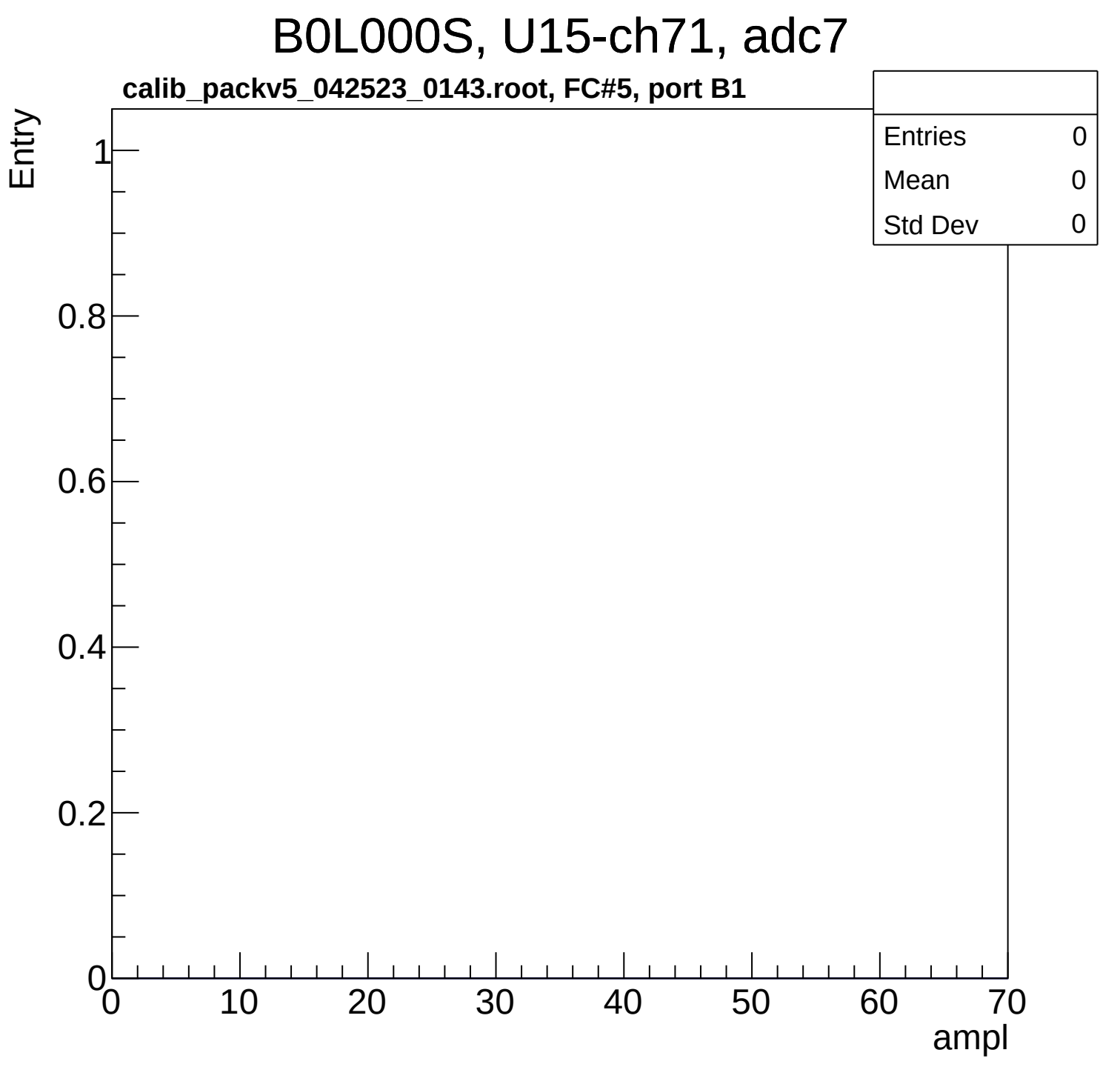

B0L000S, U15-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.94
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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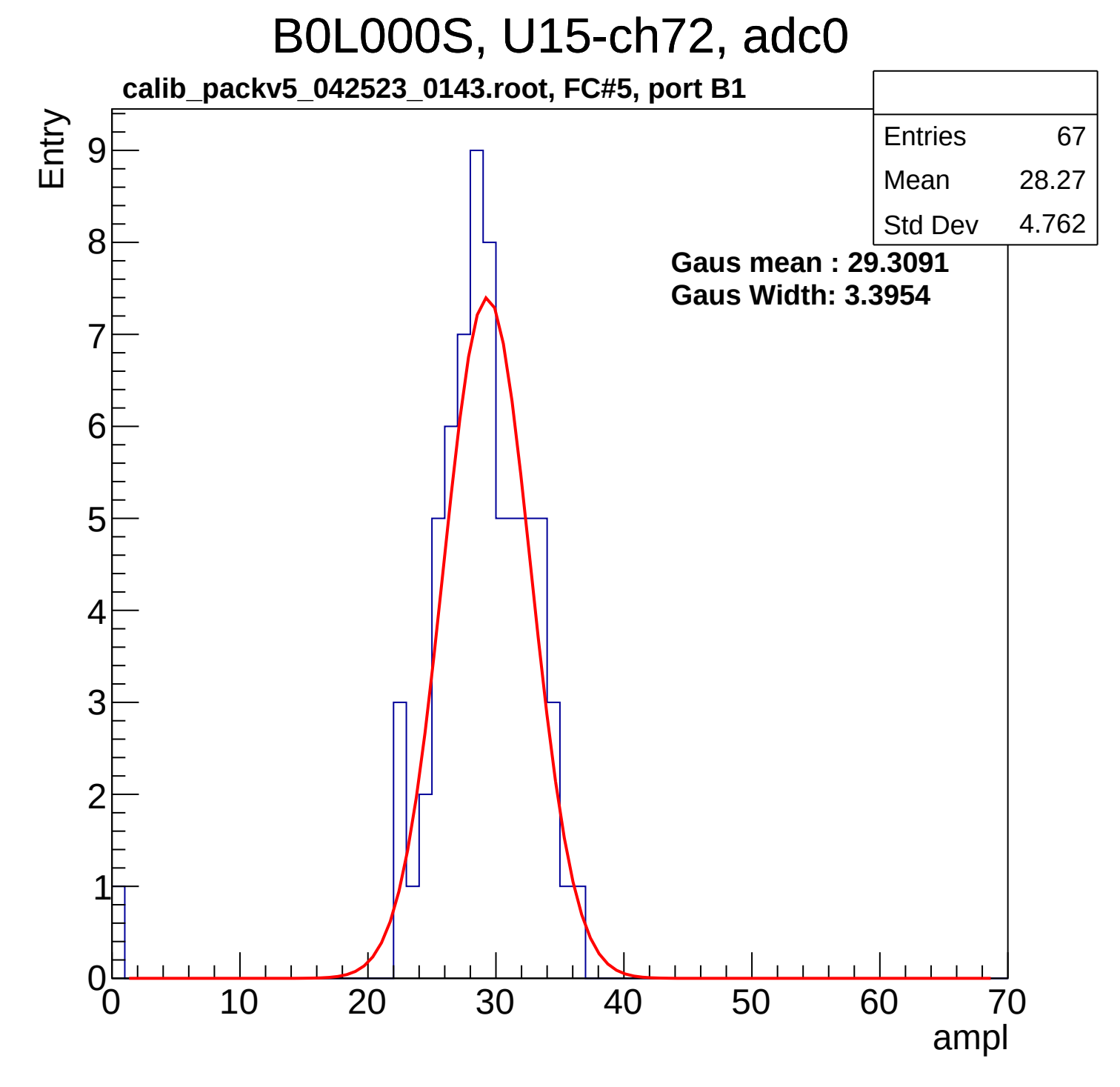
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.27
Std Dev 4.762
Gaus mean : 29.3091
Gaus Width: 3.3954
Entry
ampl
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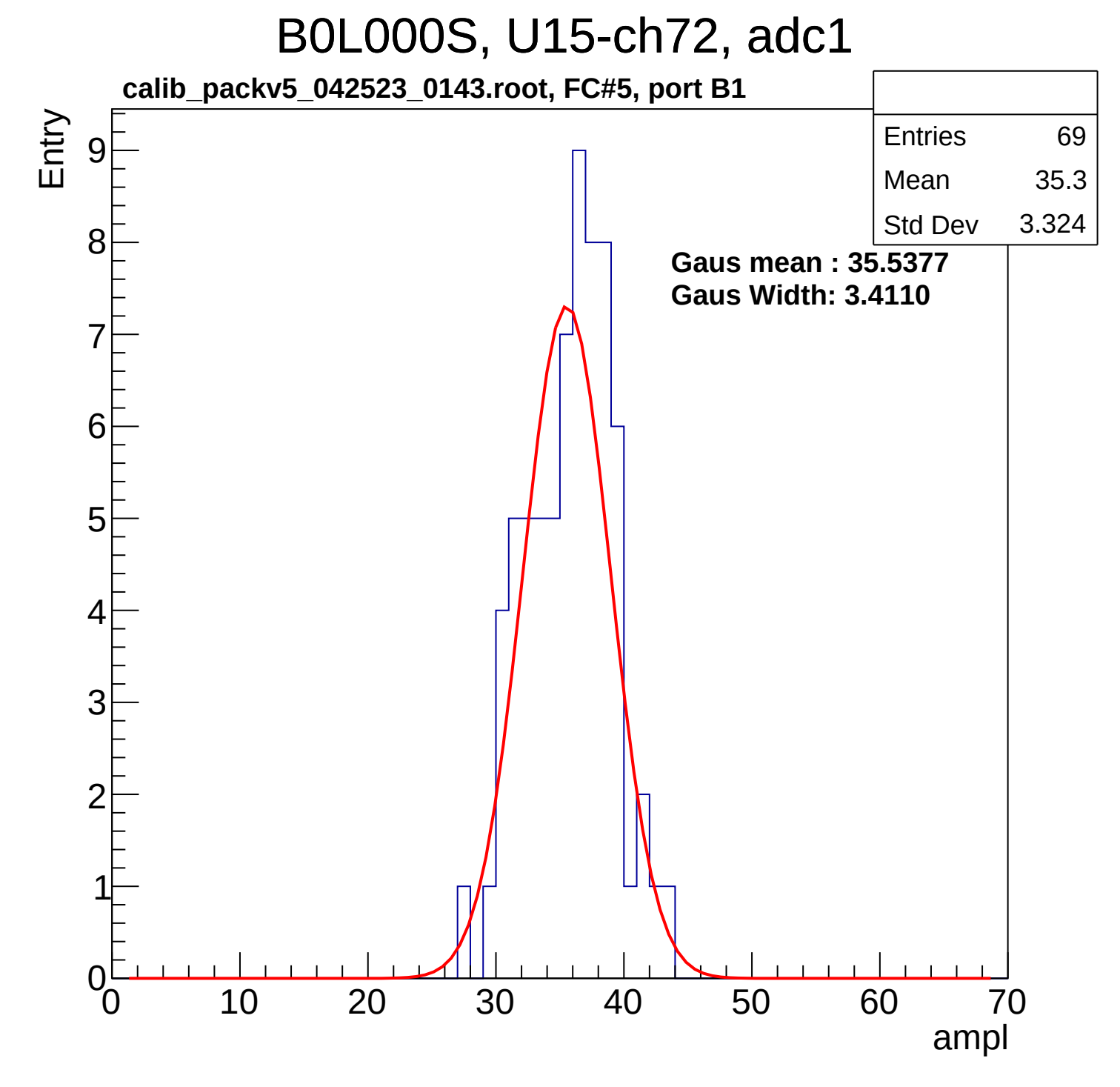
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.3
Std Dev 3.324
Gaus mean : 35.5377
Gaus Width: 3.4110
Entry
ampl
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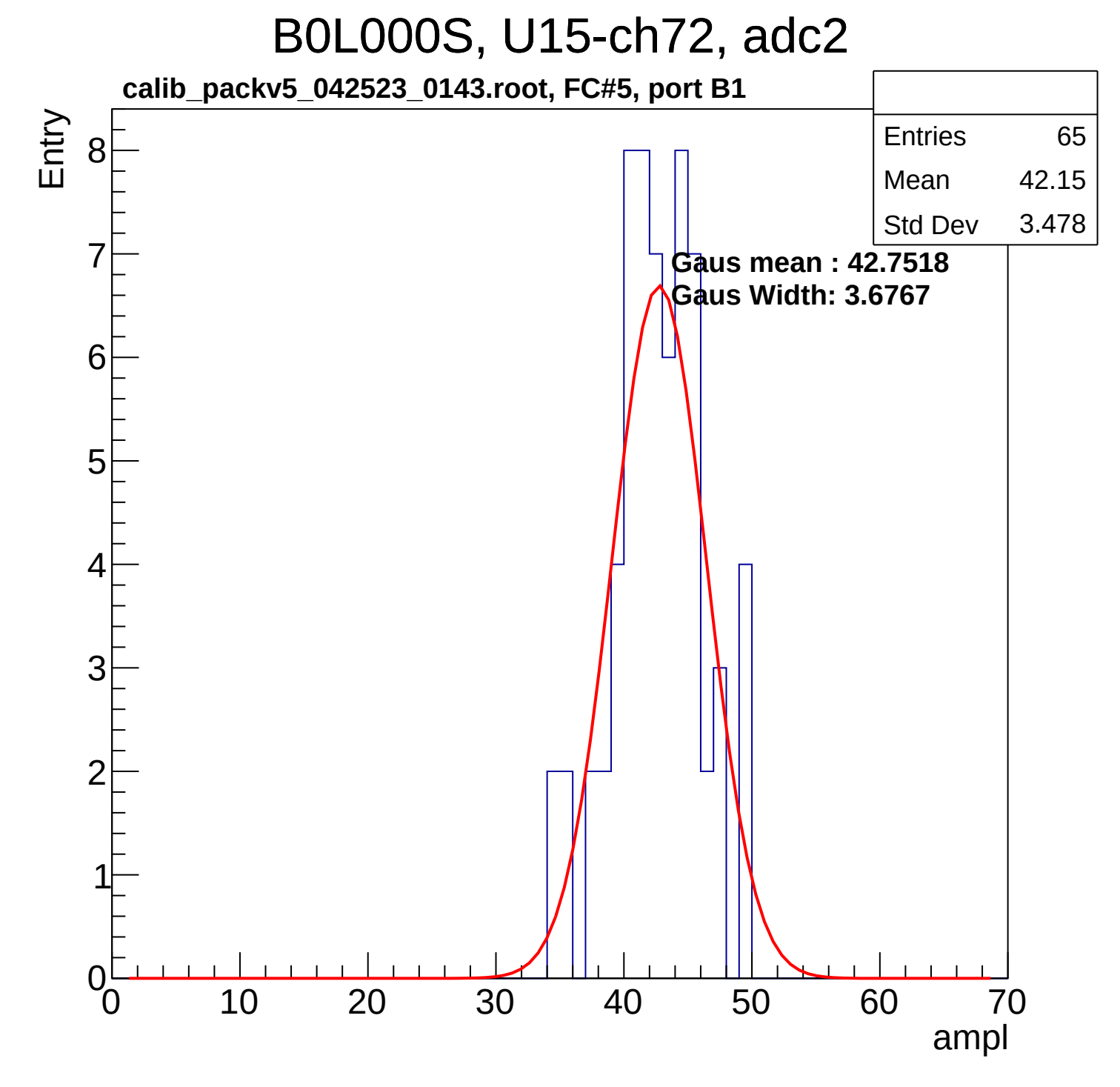
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.15
Std Dev 3.478
Gaus mean : 42.7518
Gaus Width: 3.6767
Entry
ampl
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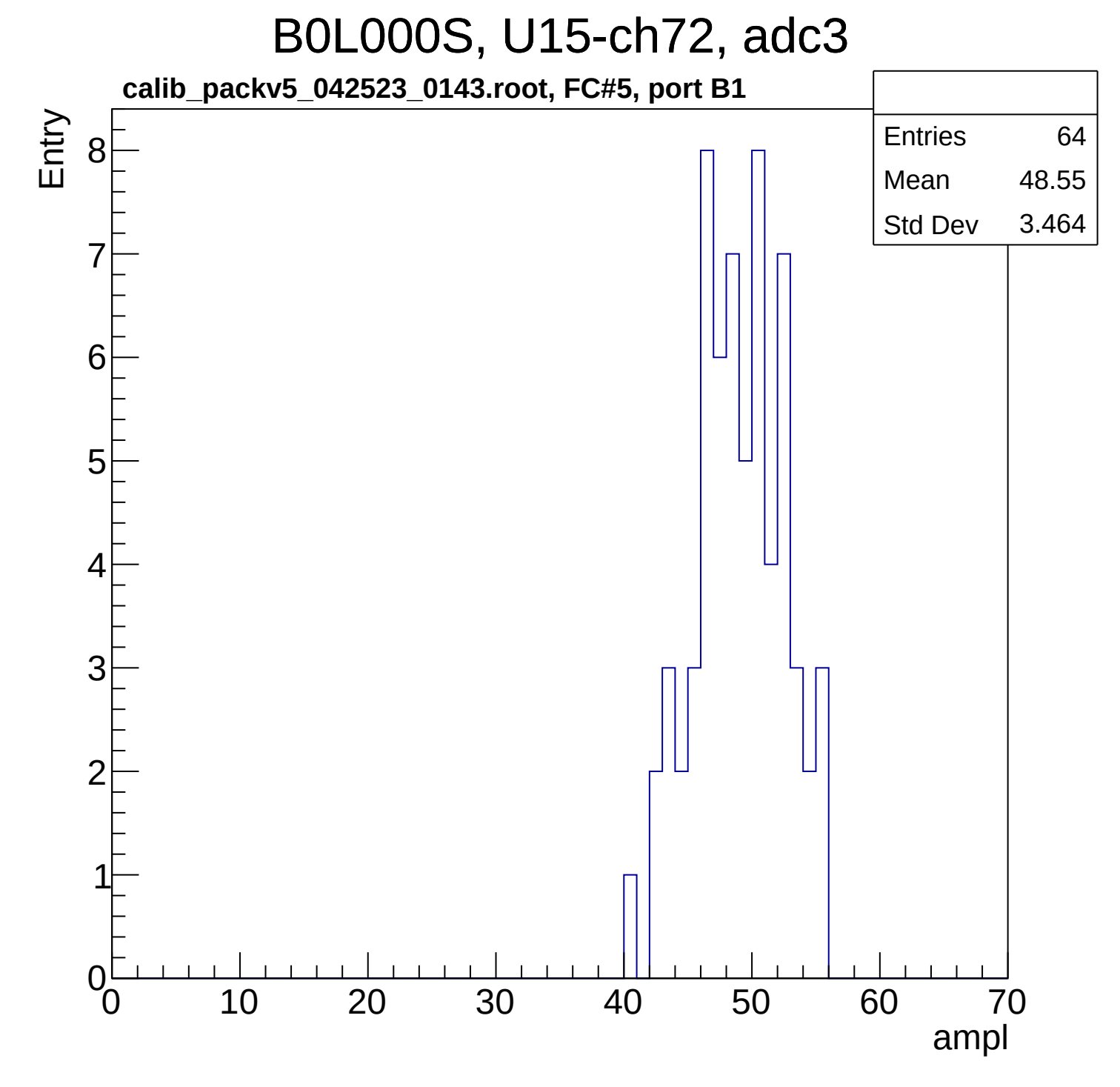
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 48.55
Std Dev 3.464
Entry
ampl
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4
5
6
7
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0
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20
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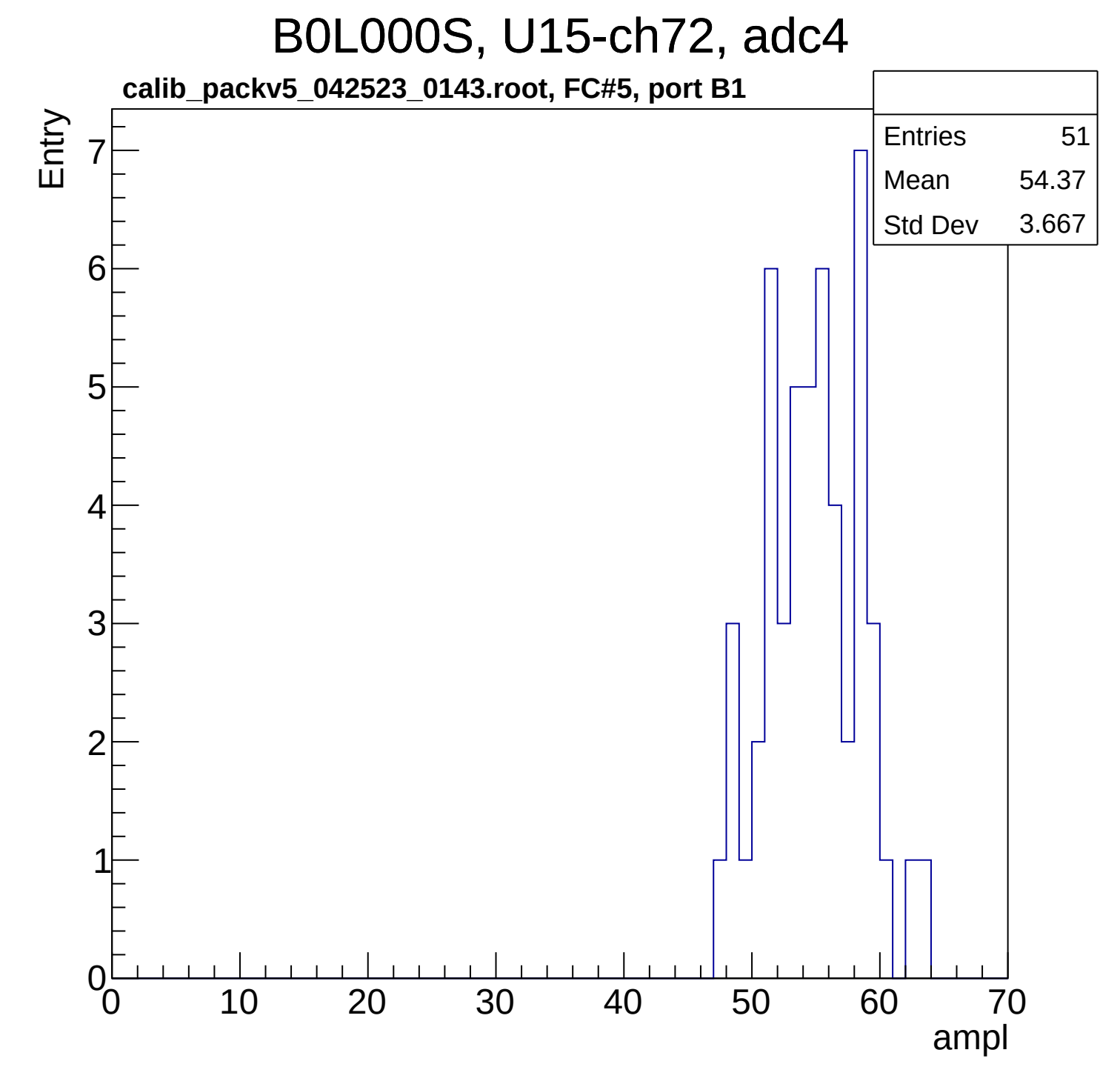
40
50
60
70


B0L000S, U15-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.37
Std Dev 3.667
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
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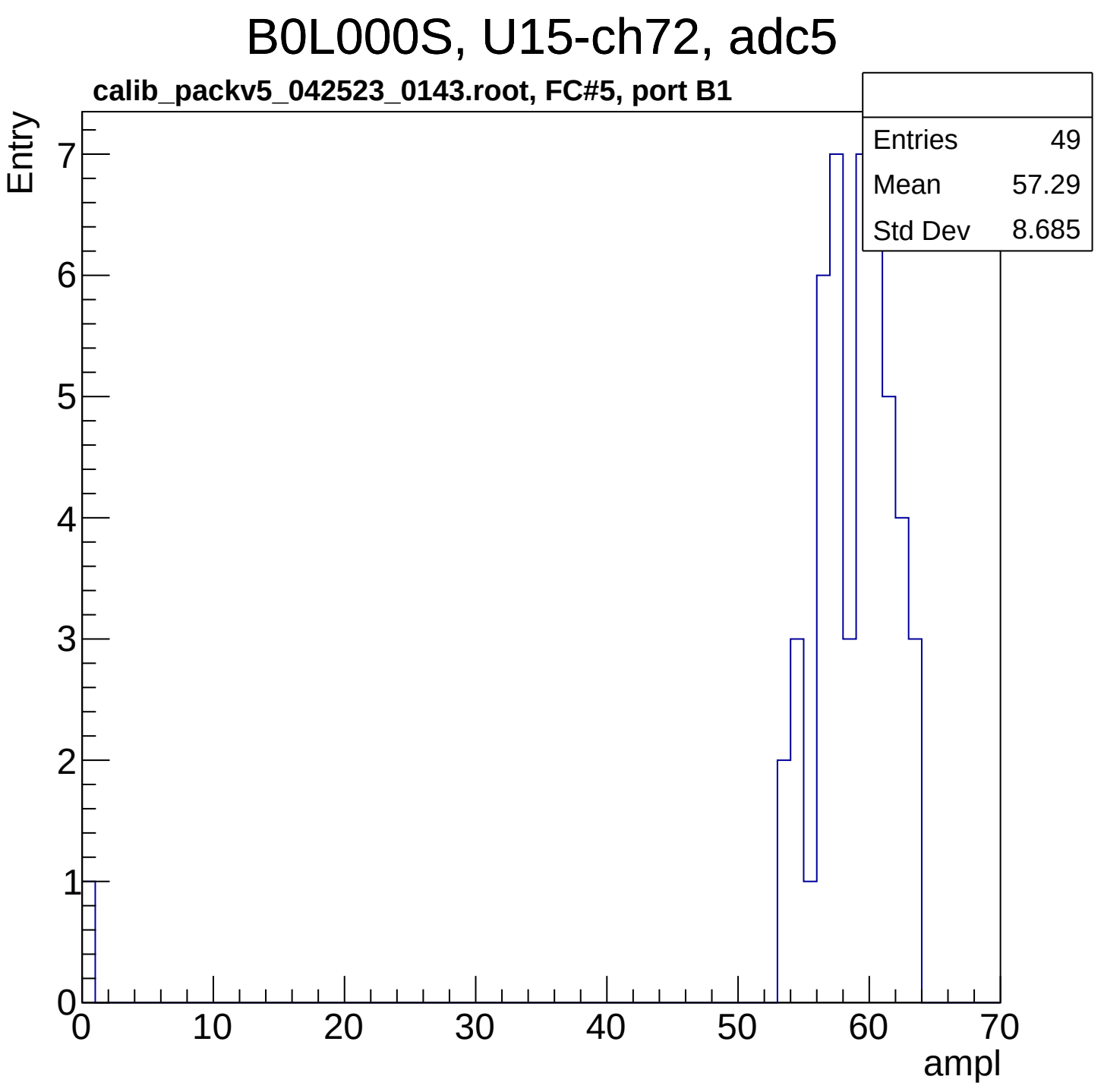
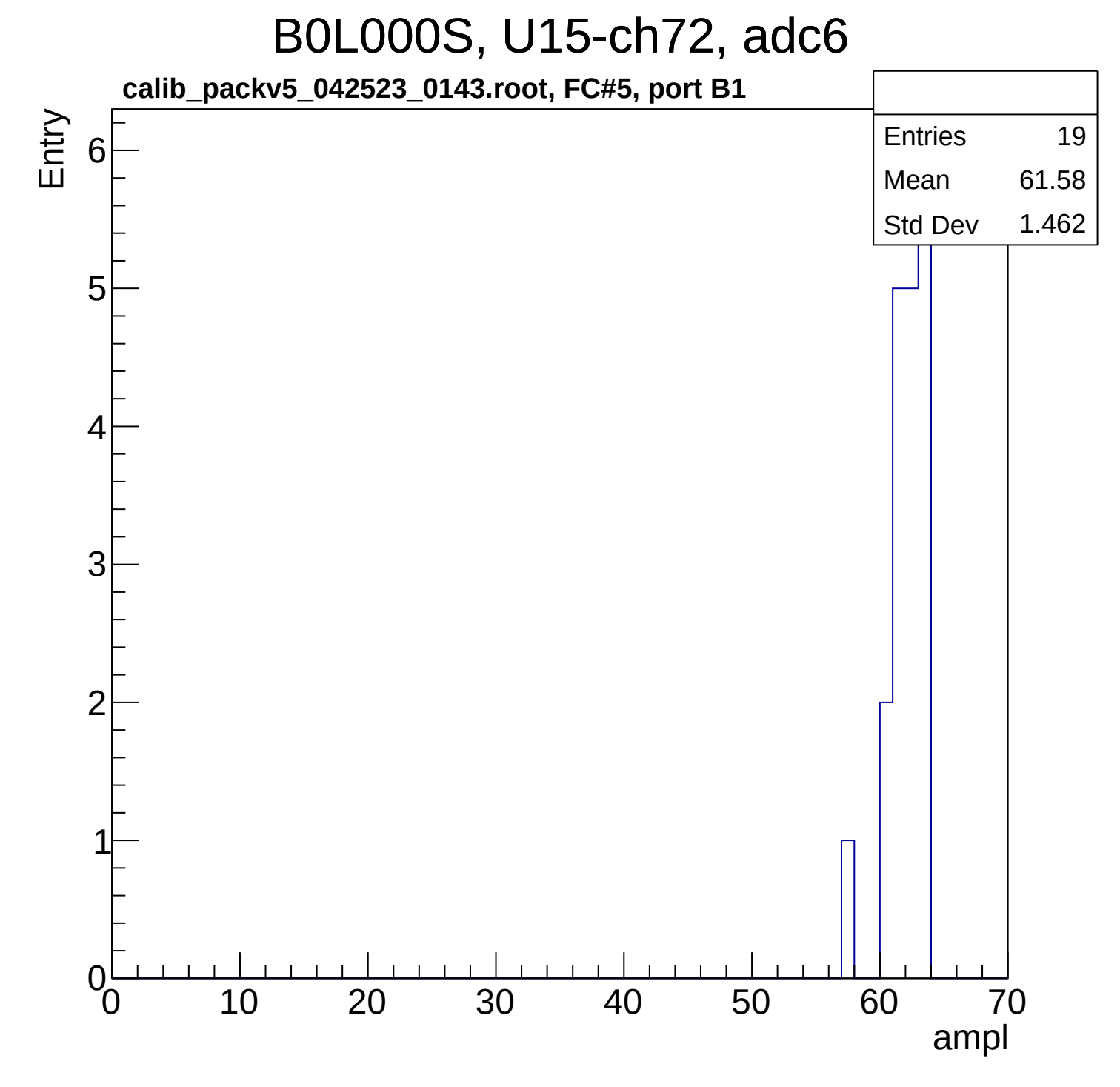
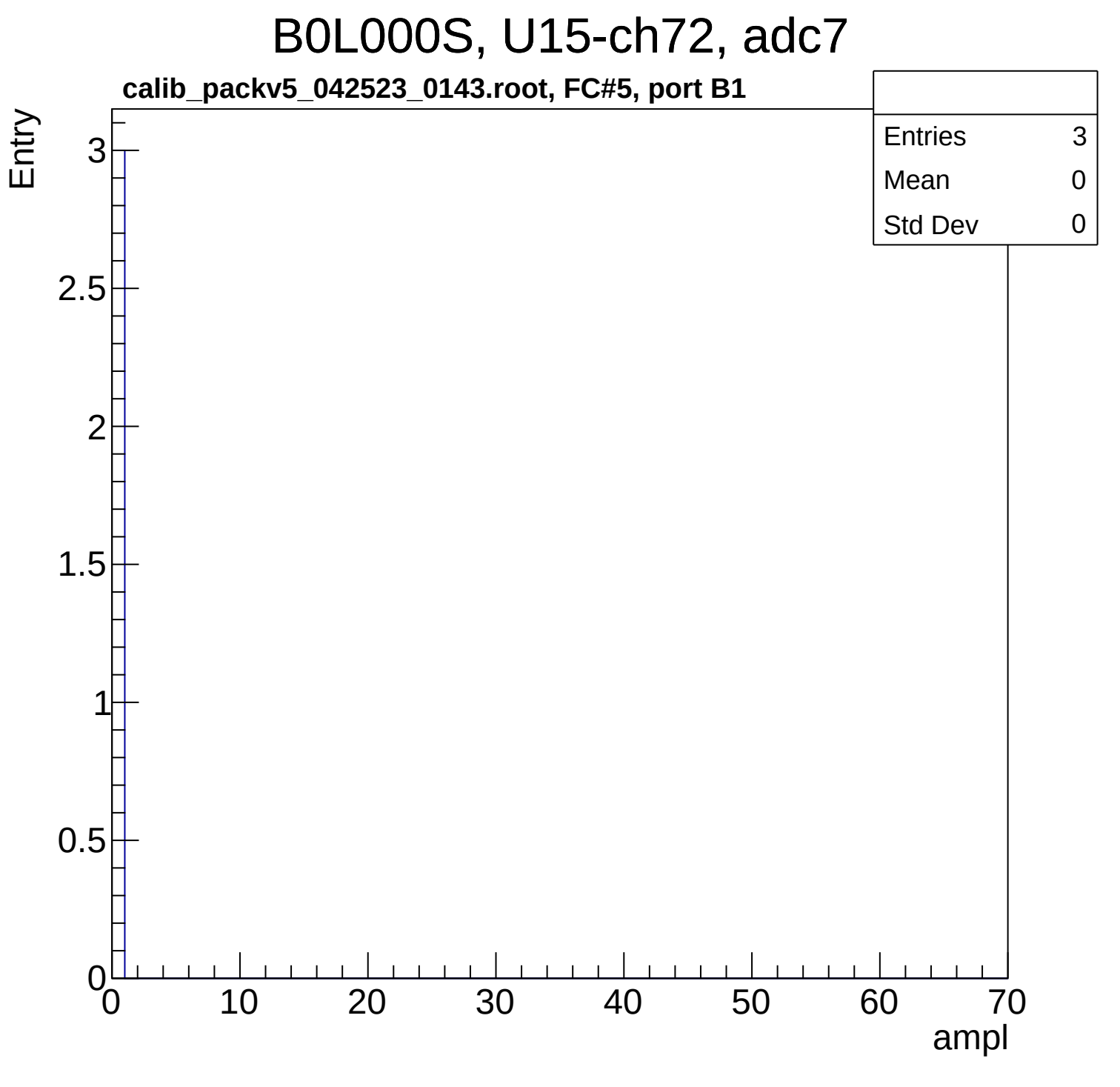
40
50
60
70


B0L000S, U15-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.29
Std Dev 8.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61.58
Std Dev 1.462
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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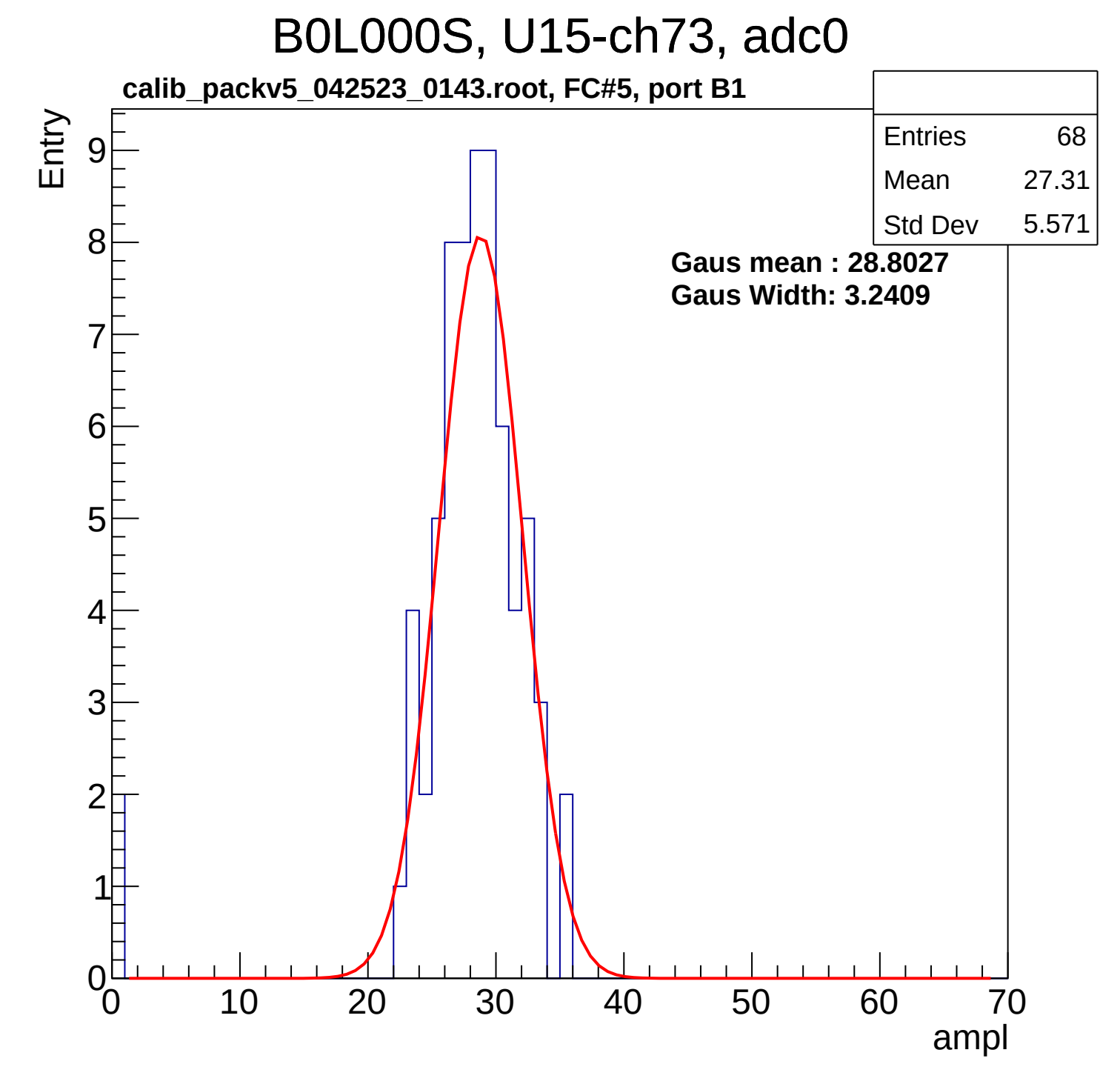
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 27.31
Std Dev 5.571
Gaus mean : 28.8027
Gaus Width: 3.2409
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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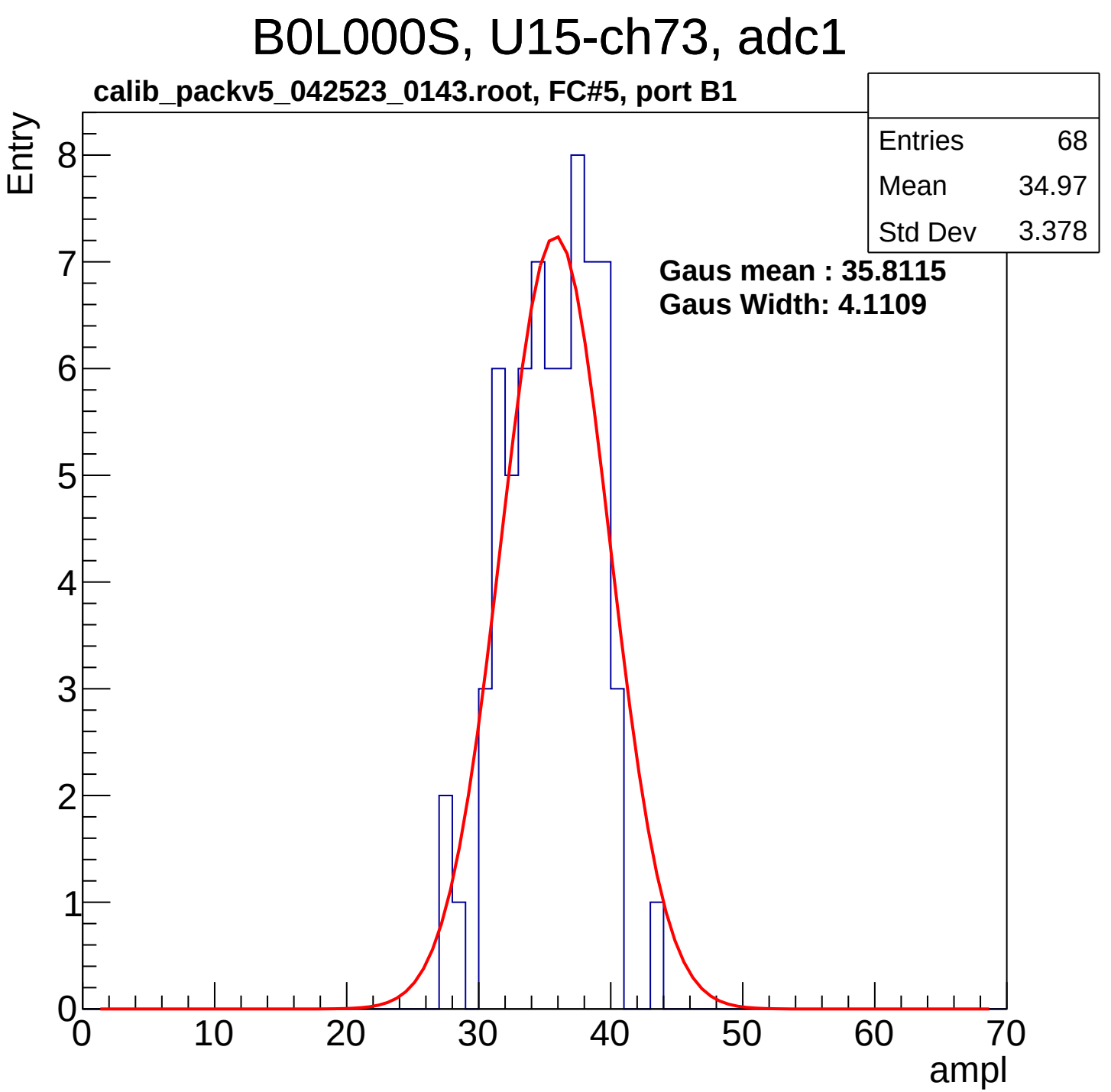
B0L000S, U15-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 34.97
Std Dev 3.378
Gaus mean : 35.8115
Gaus Width: 4.1109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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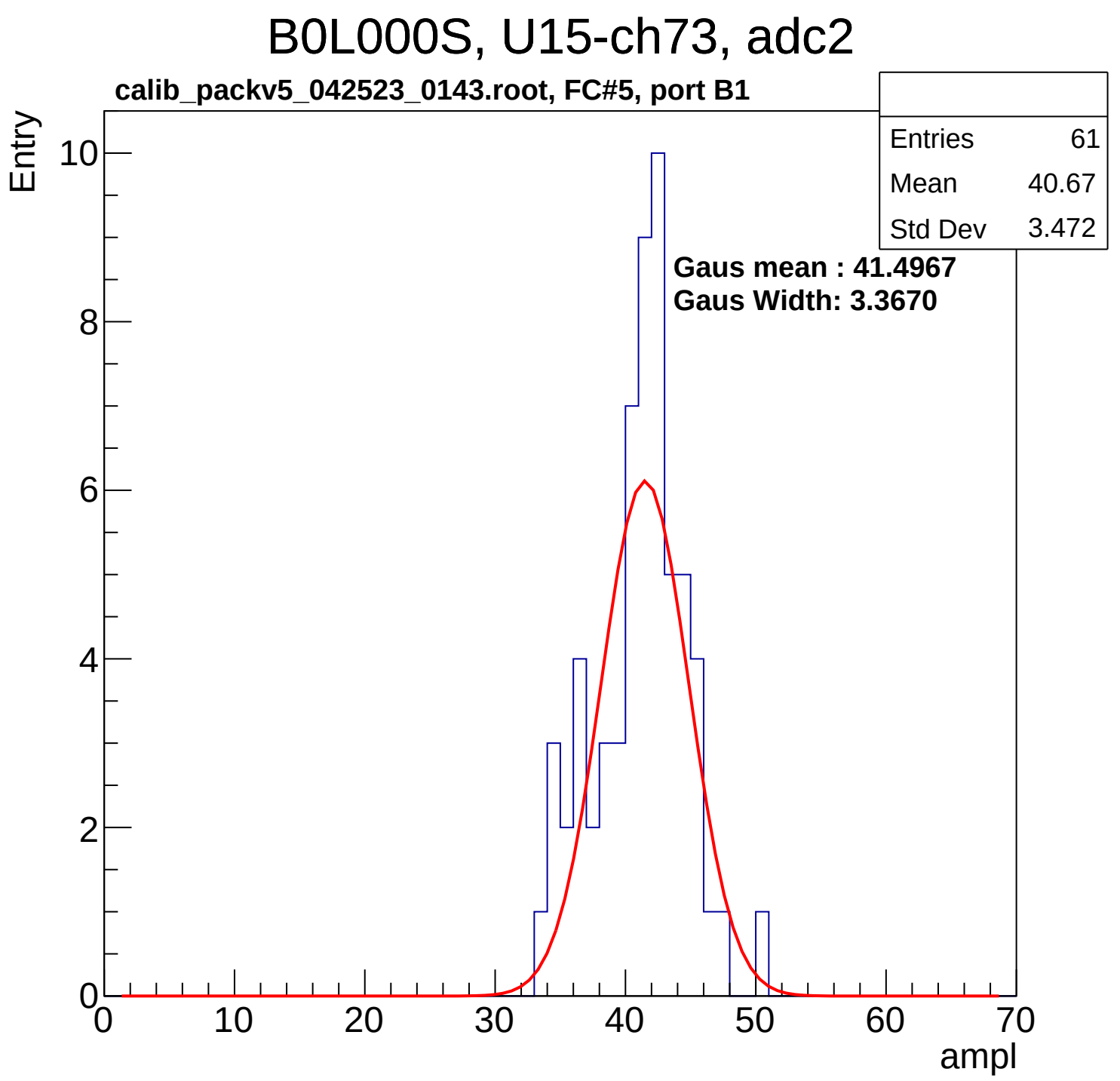
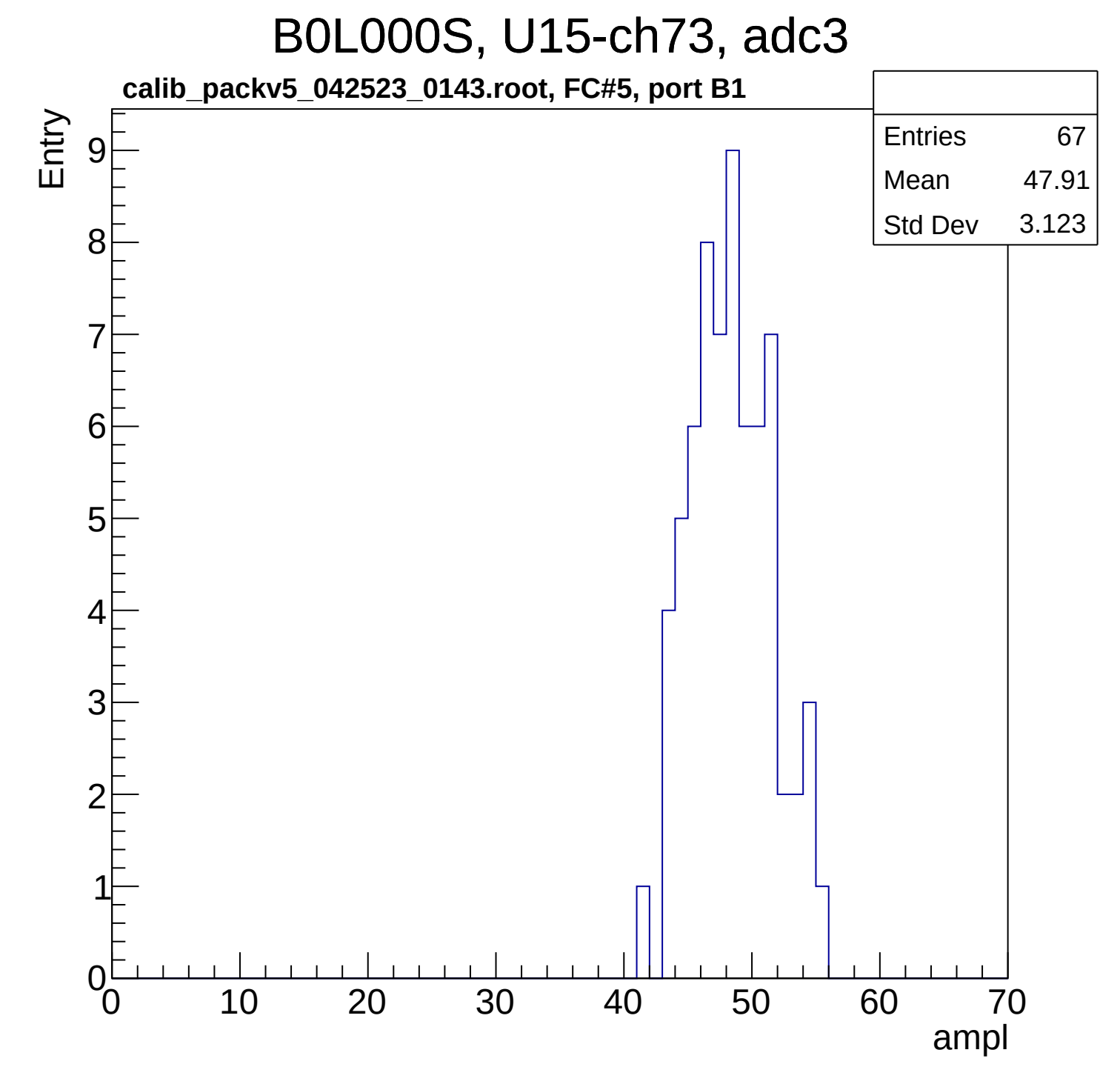

B0L000S, U15-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 40.67
Std Dev 3.472
Gaus mean : 41.4967
Gaus Width: 3.3670
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.91
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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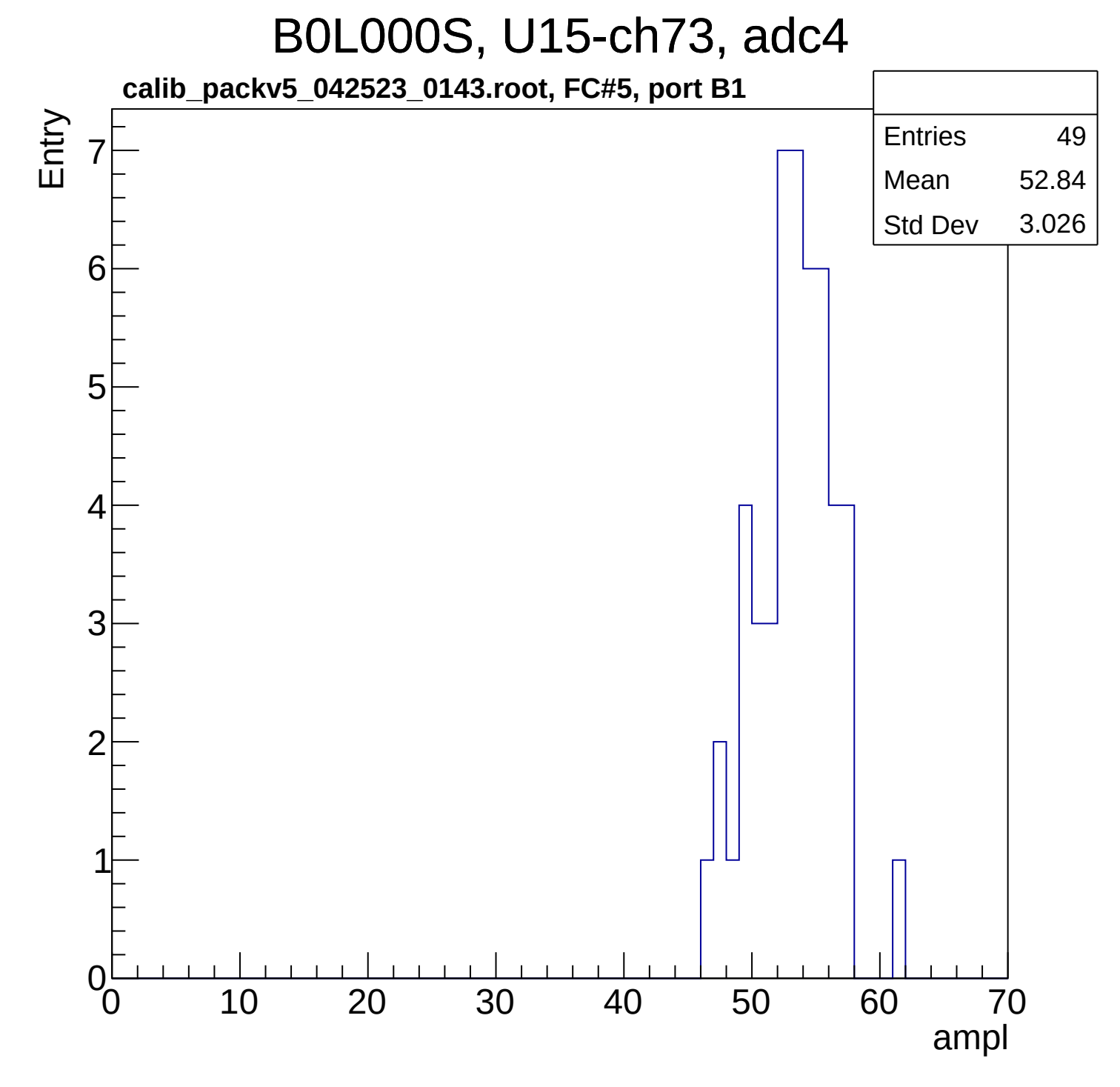
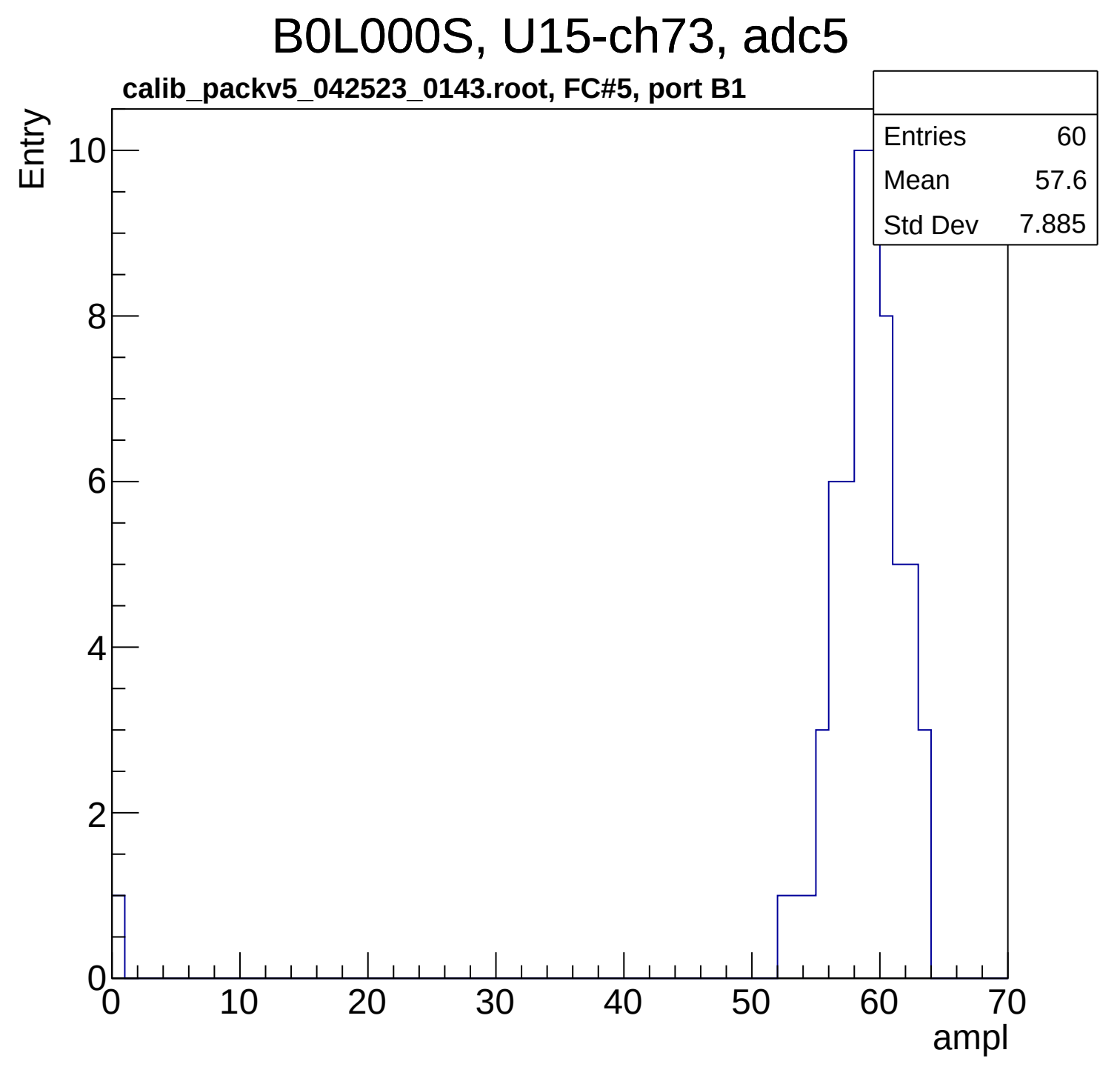
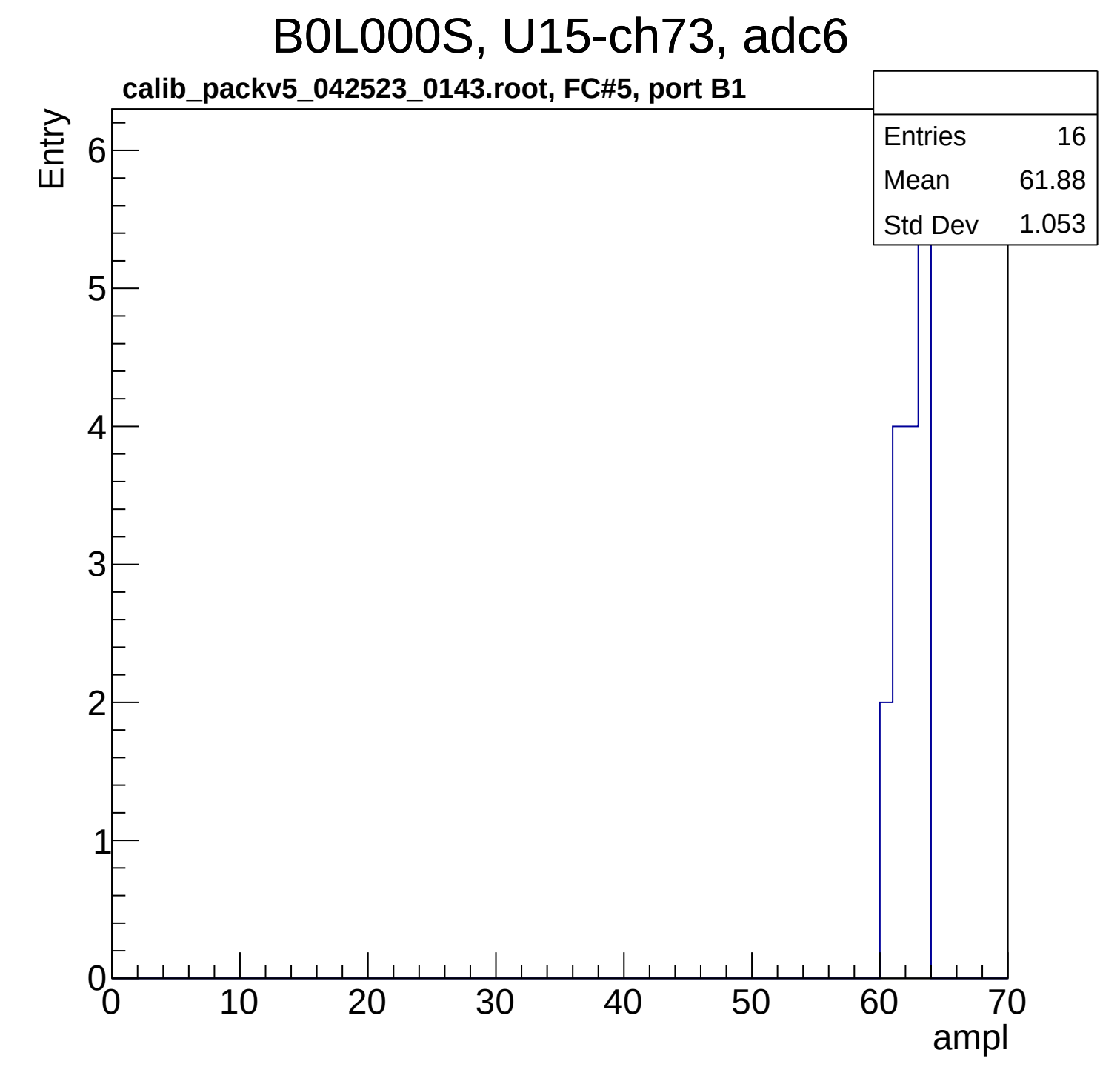
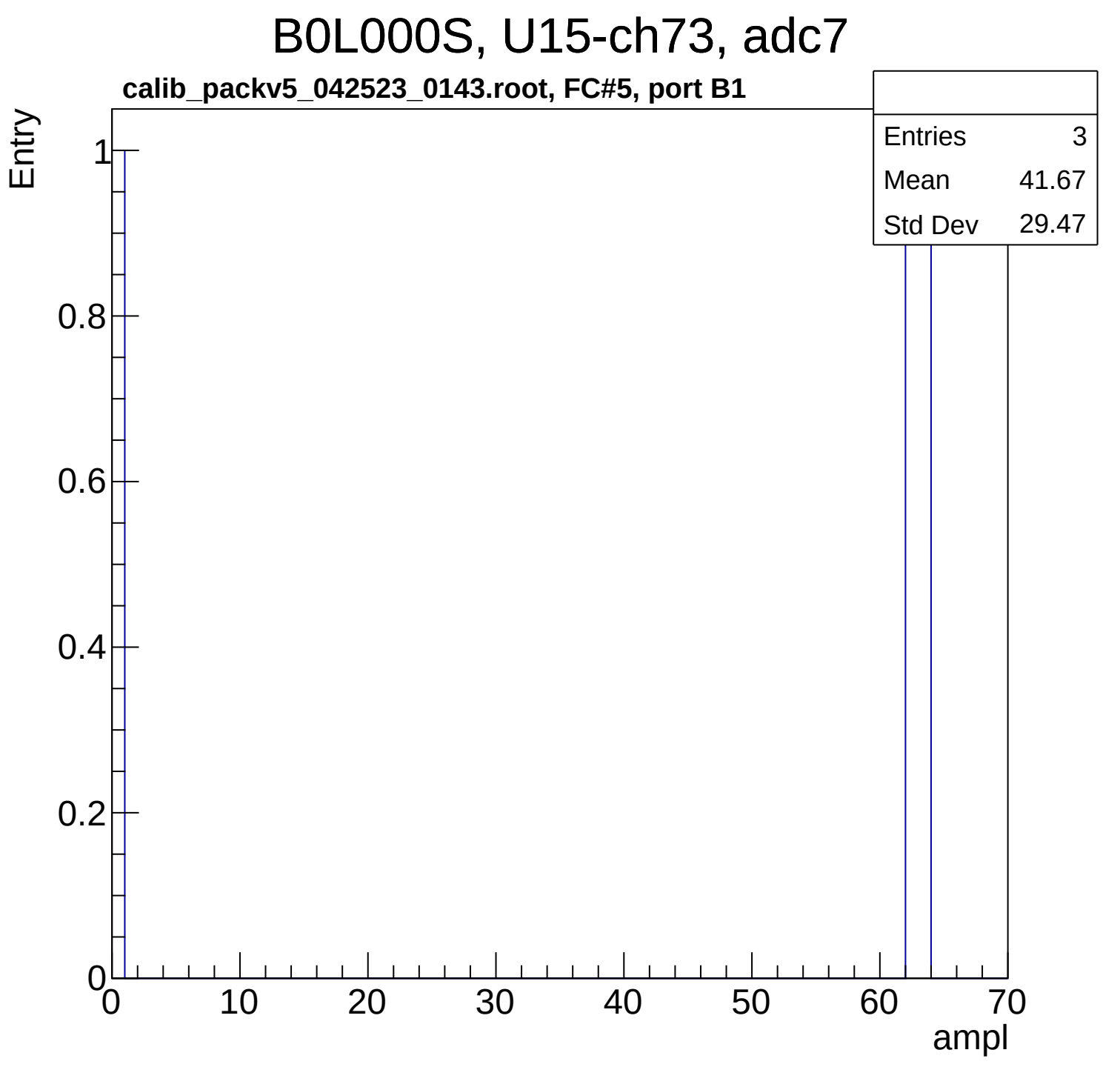

B0L000S, U15-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 52.84
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.6
Std Dev 7.885
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.88
Std Dev 1.053
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
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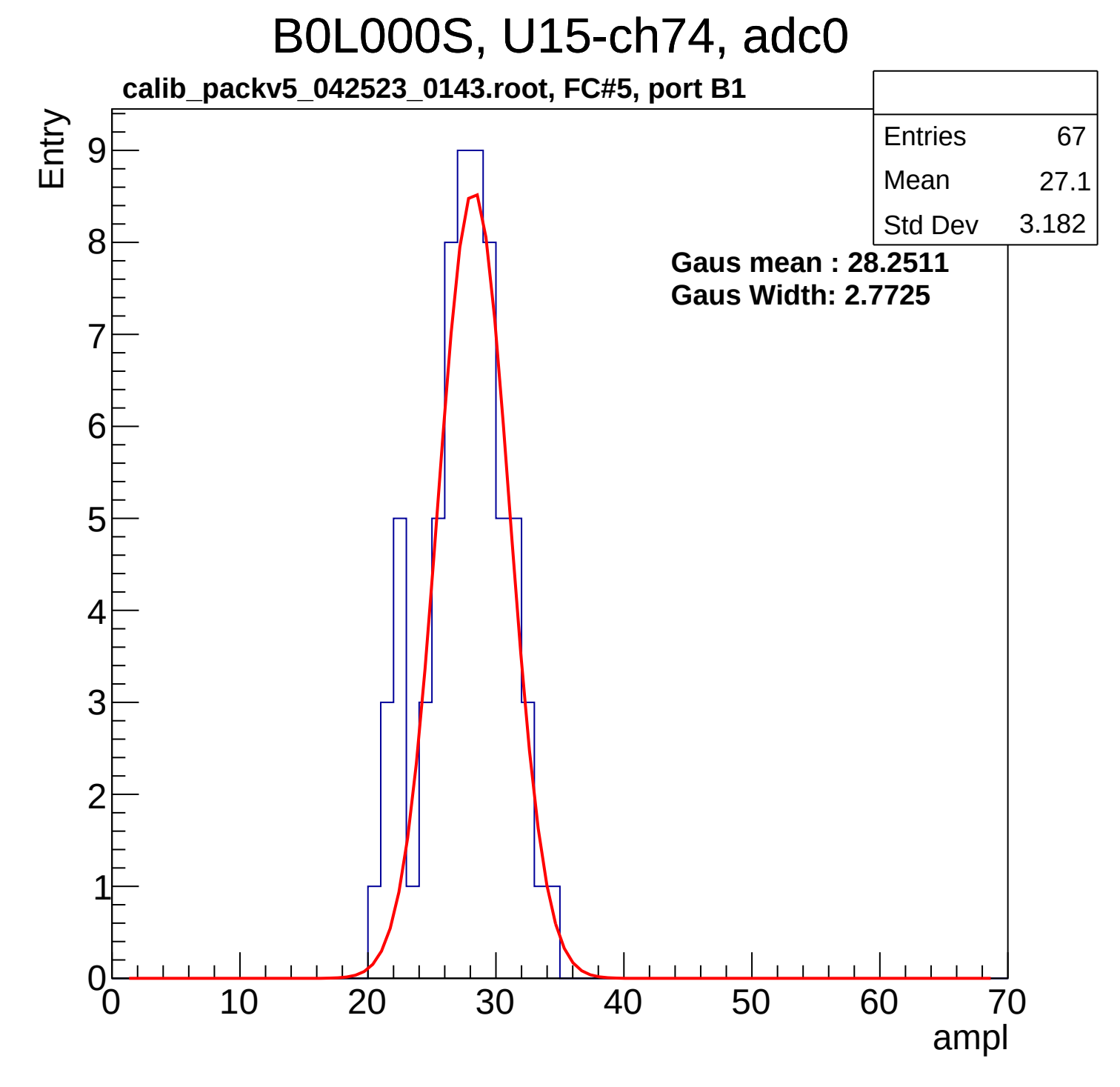
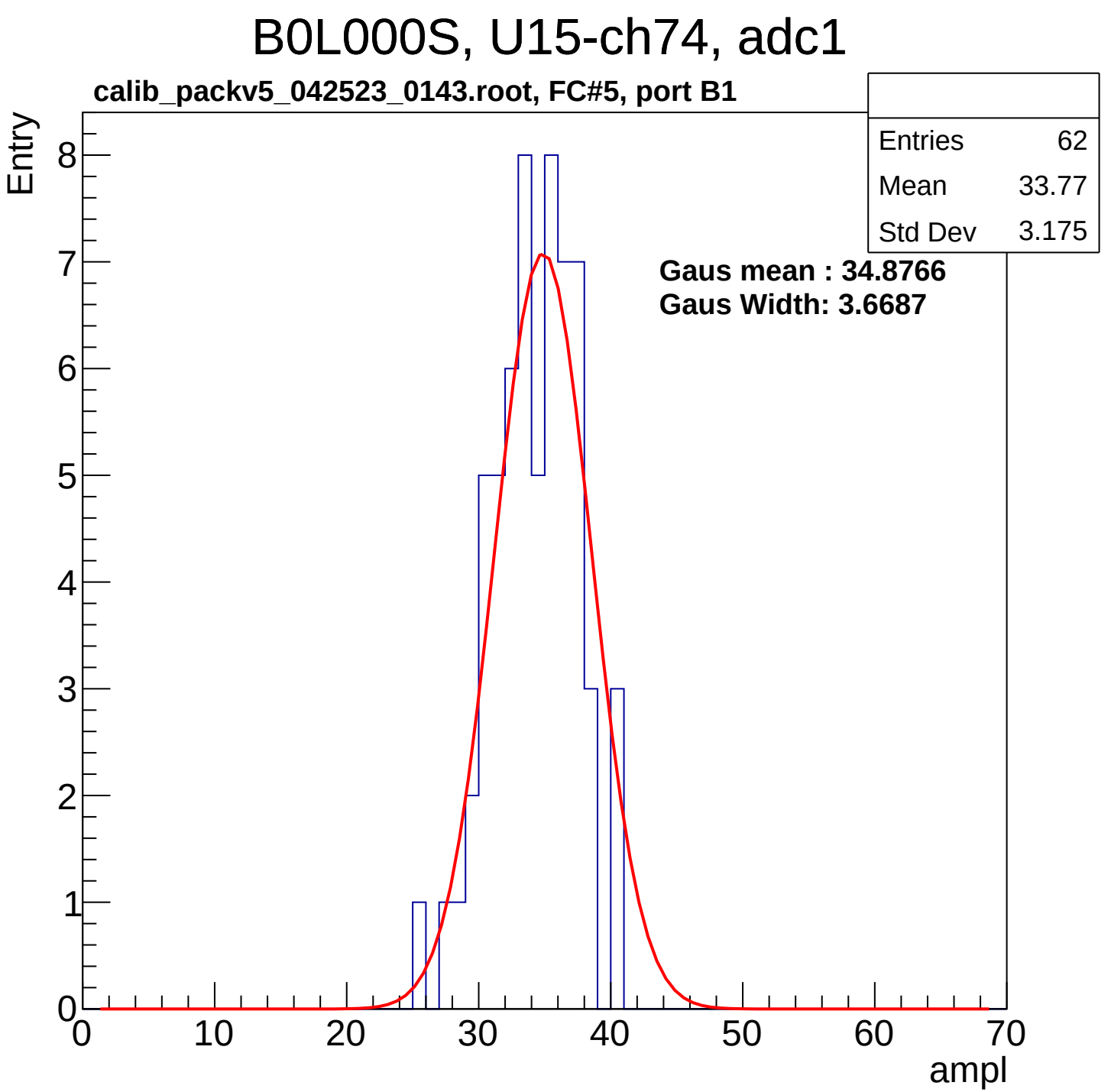
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 27.1
Std Dev 3.182
Gaus mean : 28.2511
Gaus Width: 2.7725
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 33.77
Std Dev 3.175
Gaus mean : 34.8766
Gaus Width: 3.6687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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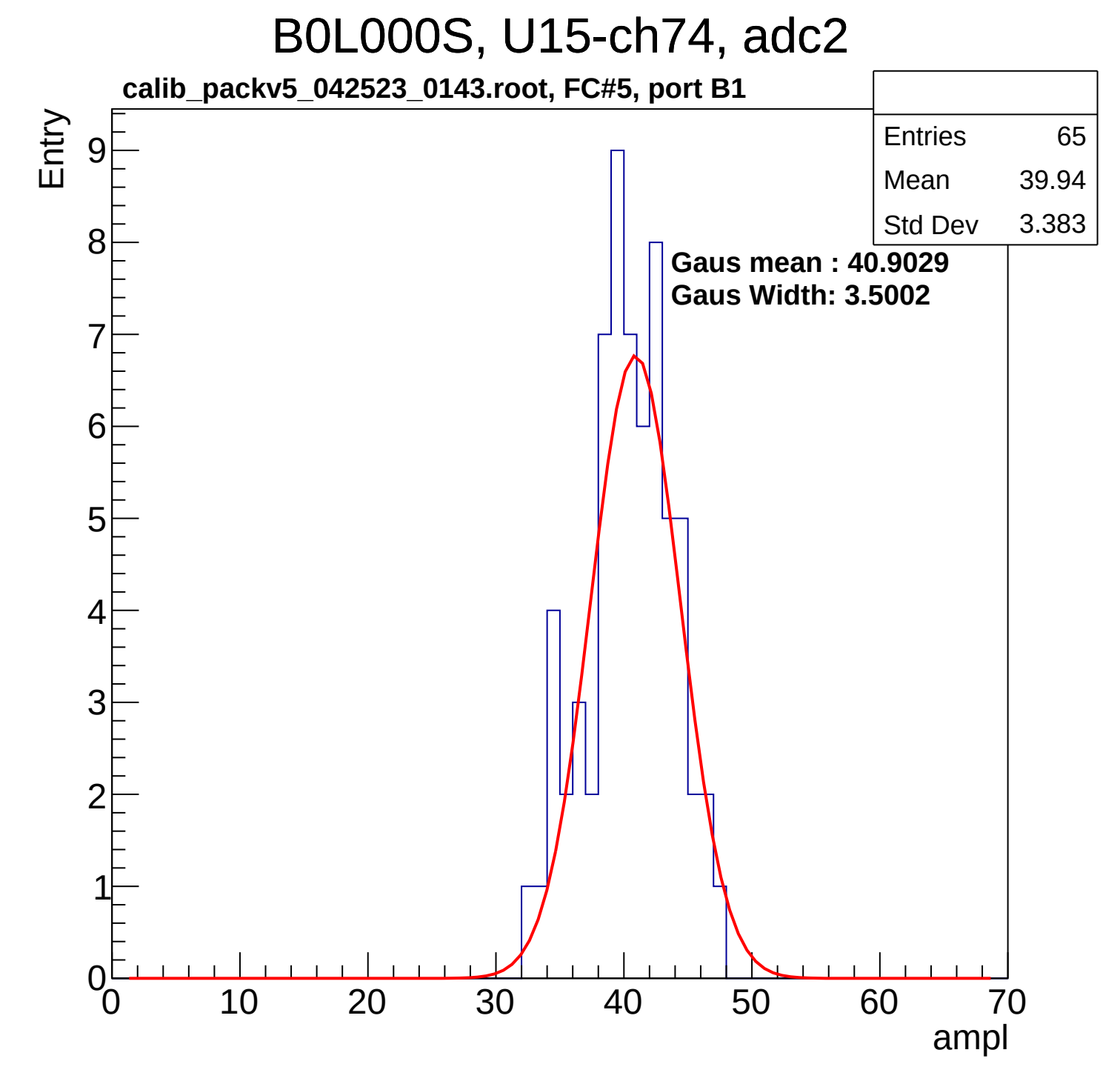
B0L000S, U15-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 39.94
Std Dev 3.383
Gaus mean : 40.9029
Gaus Width: 3.5002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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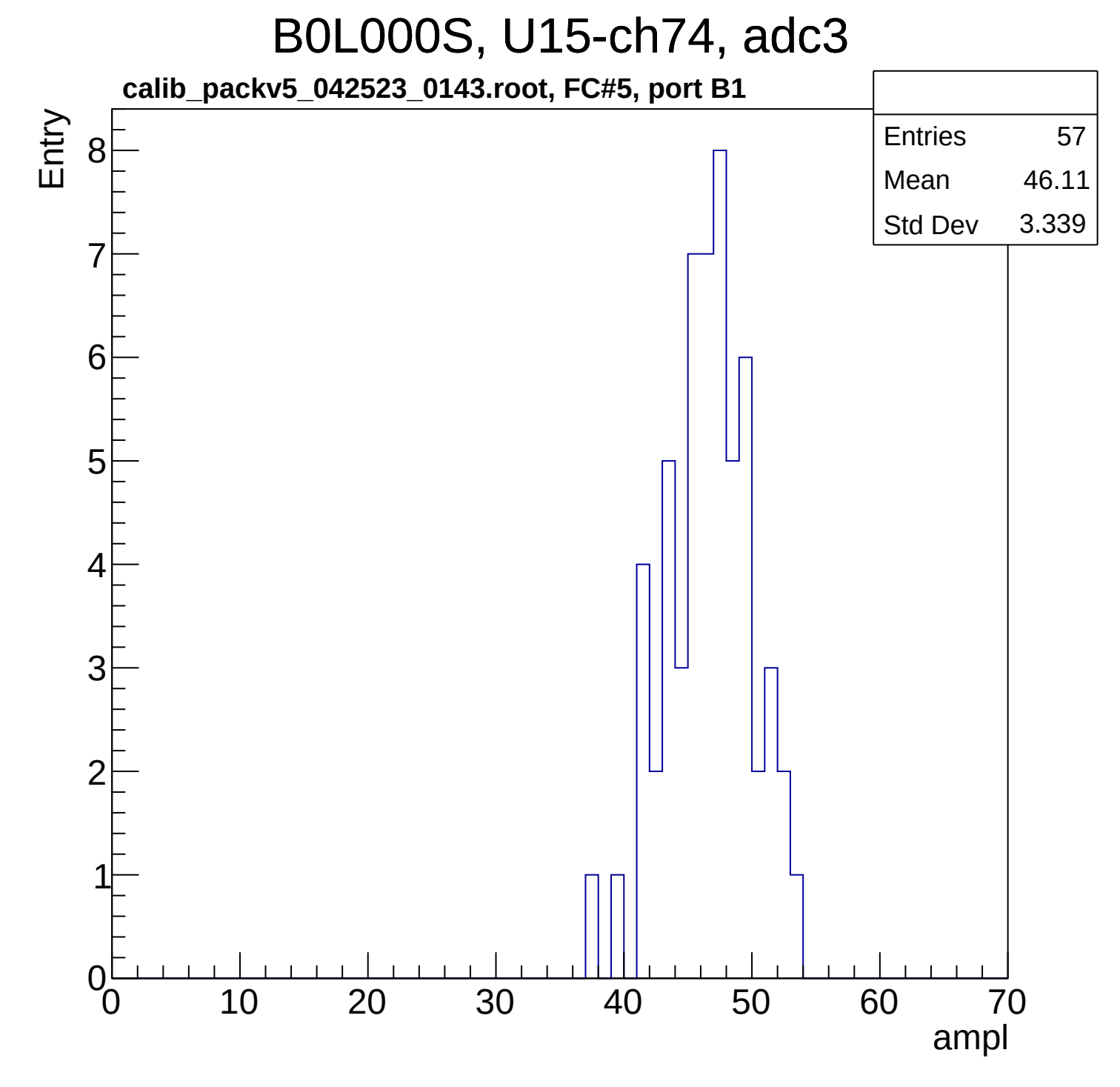
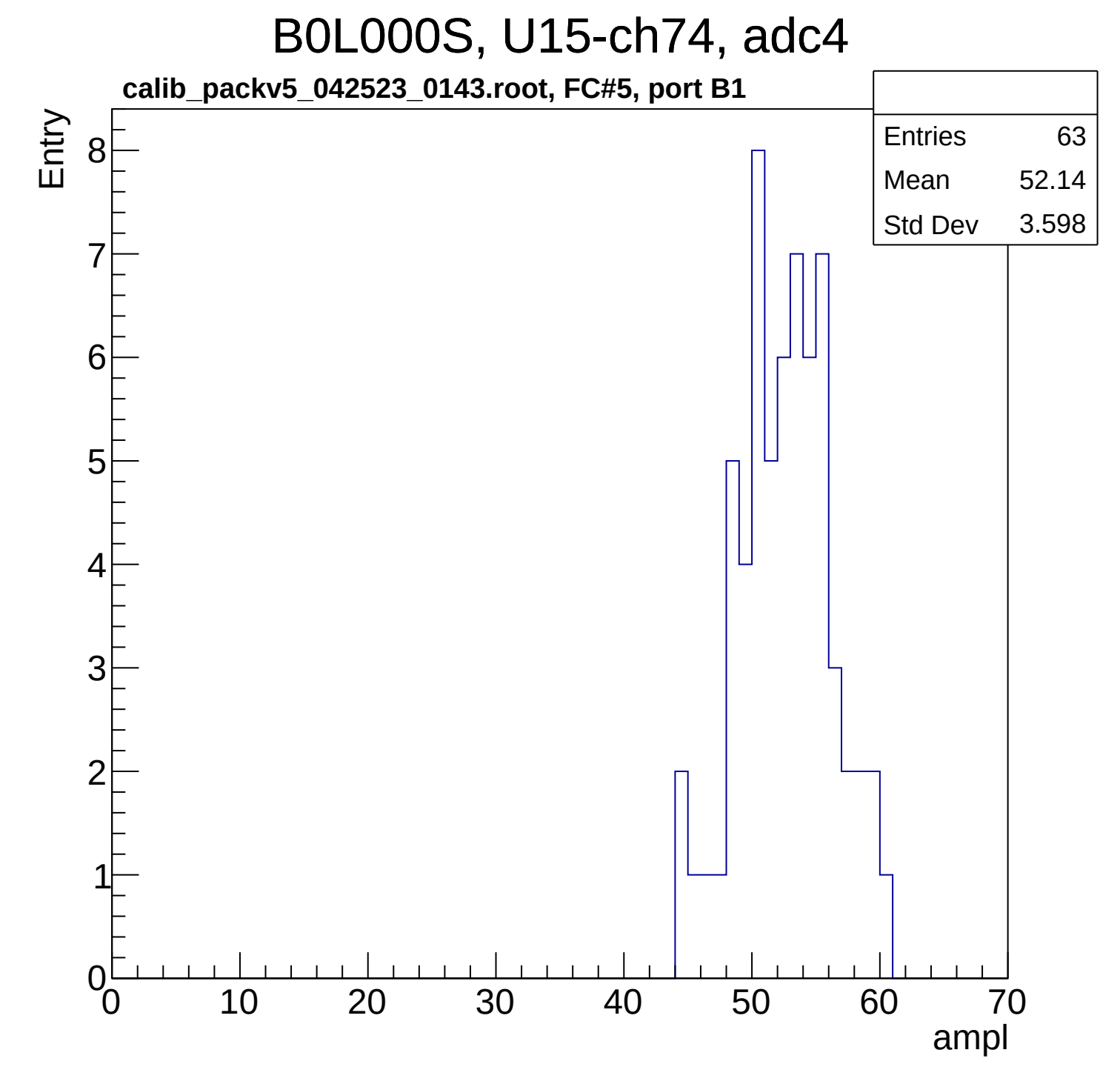
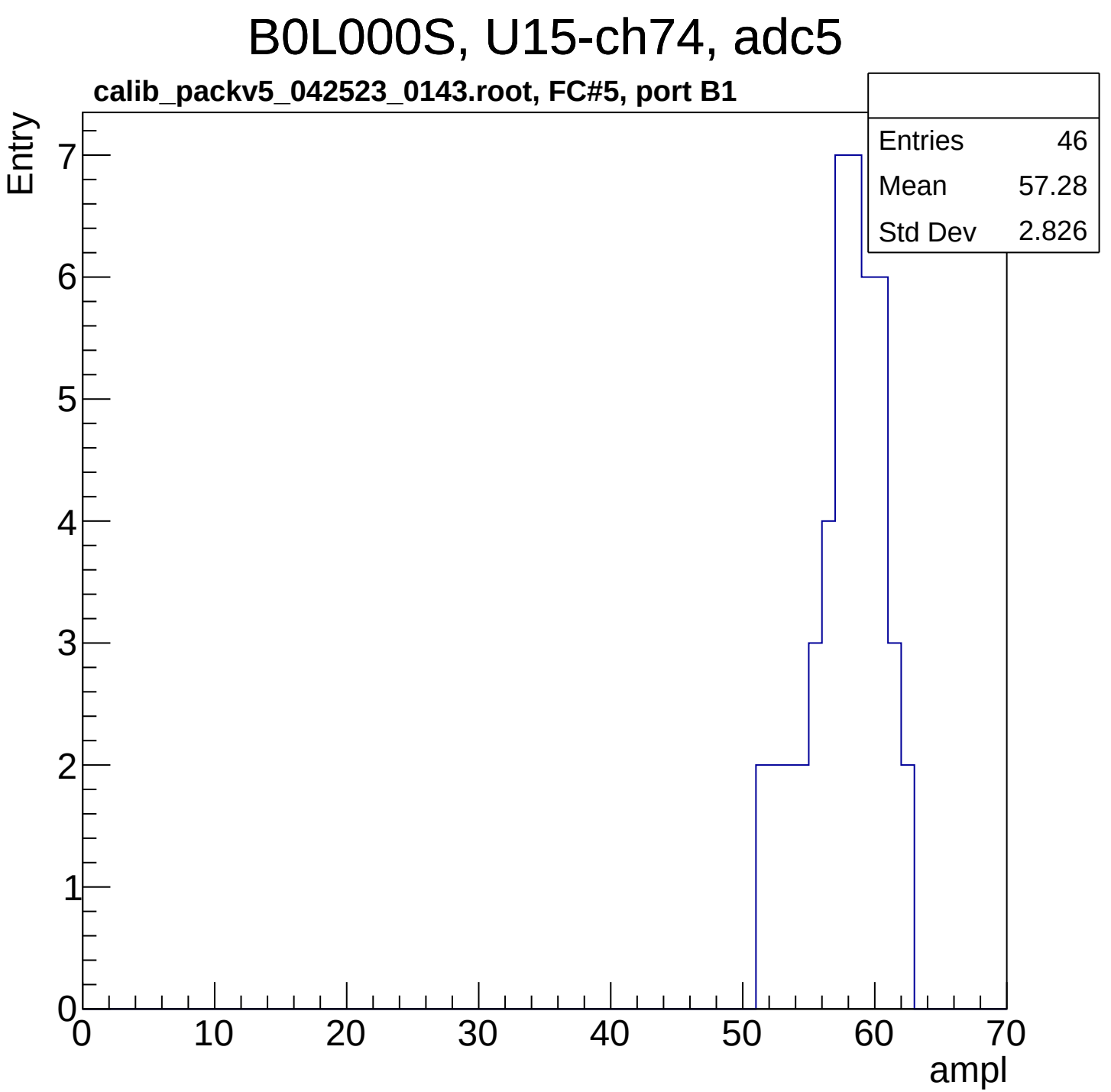

B0L000S, U15-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 46.11
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 52.14
Std Dev 3.598
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.28
Std Dev 2.826
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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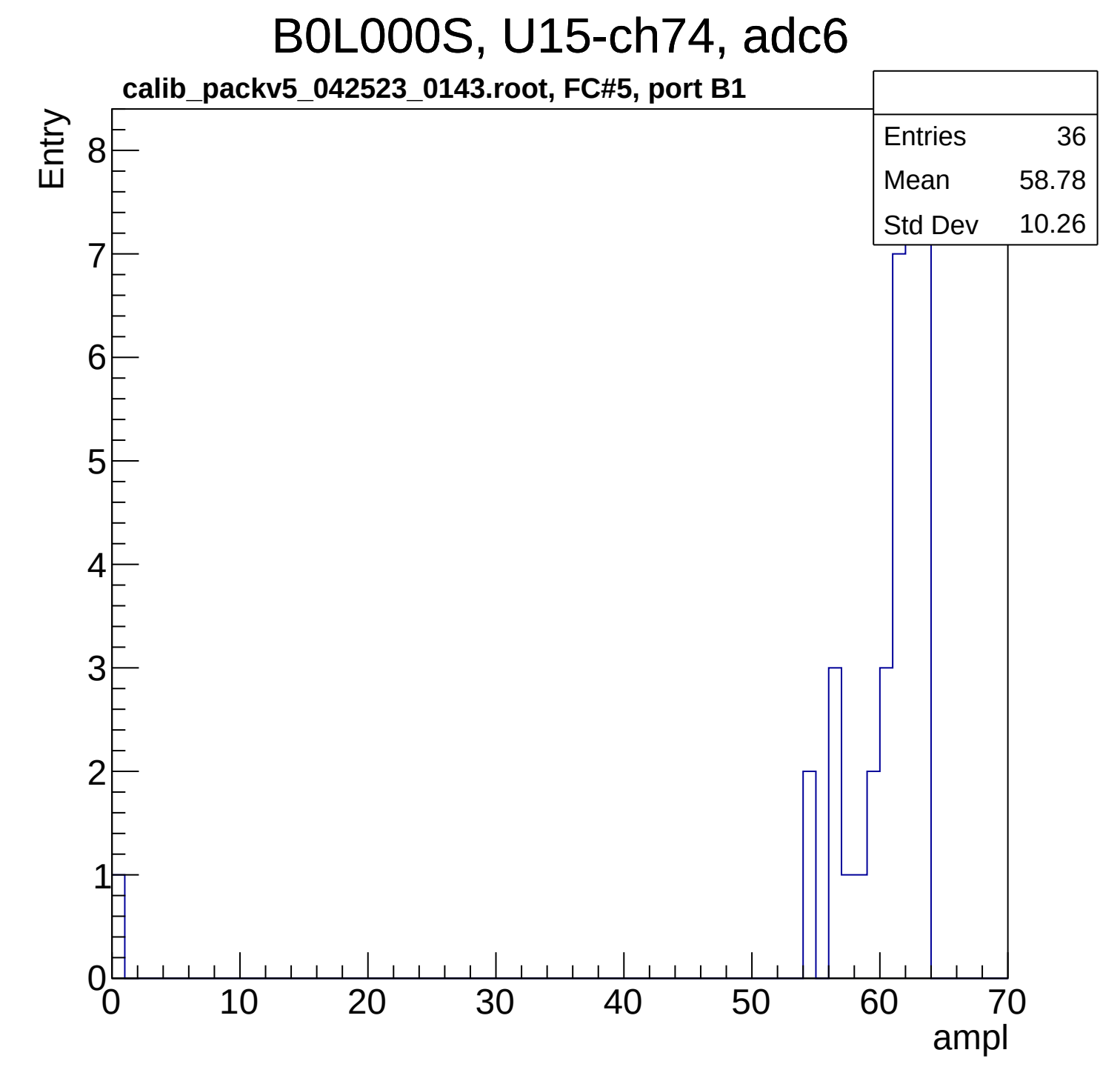
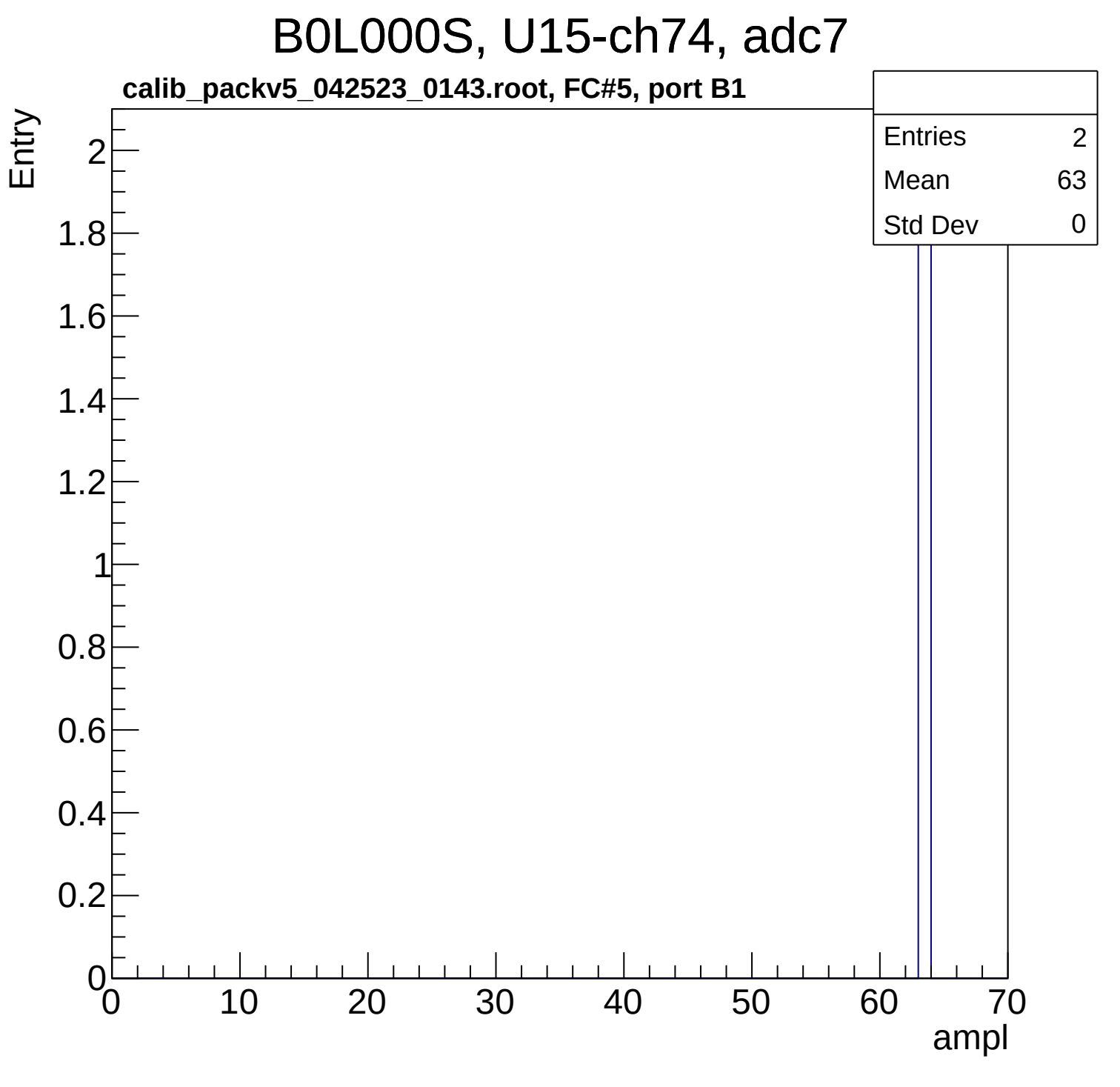
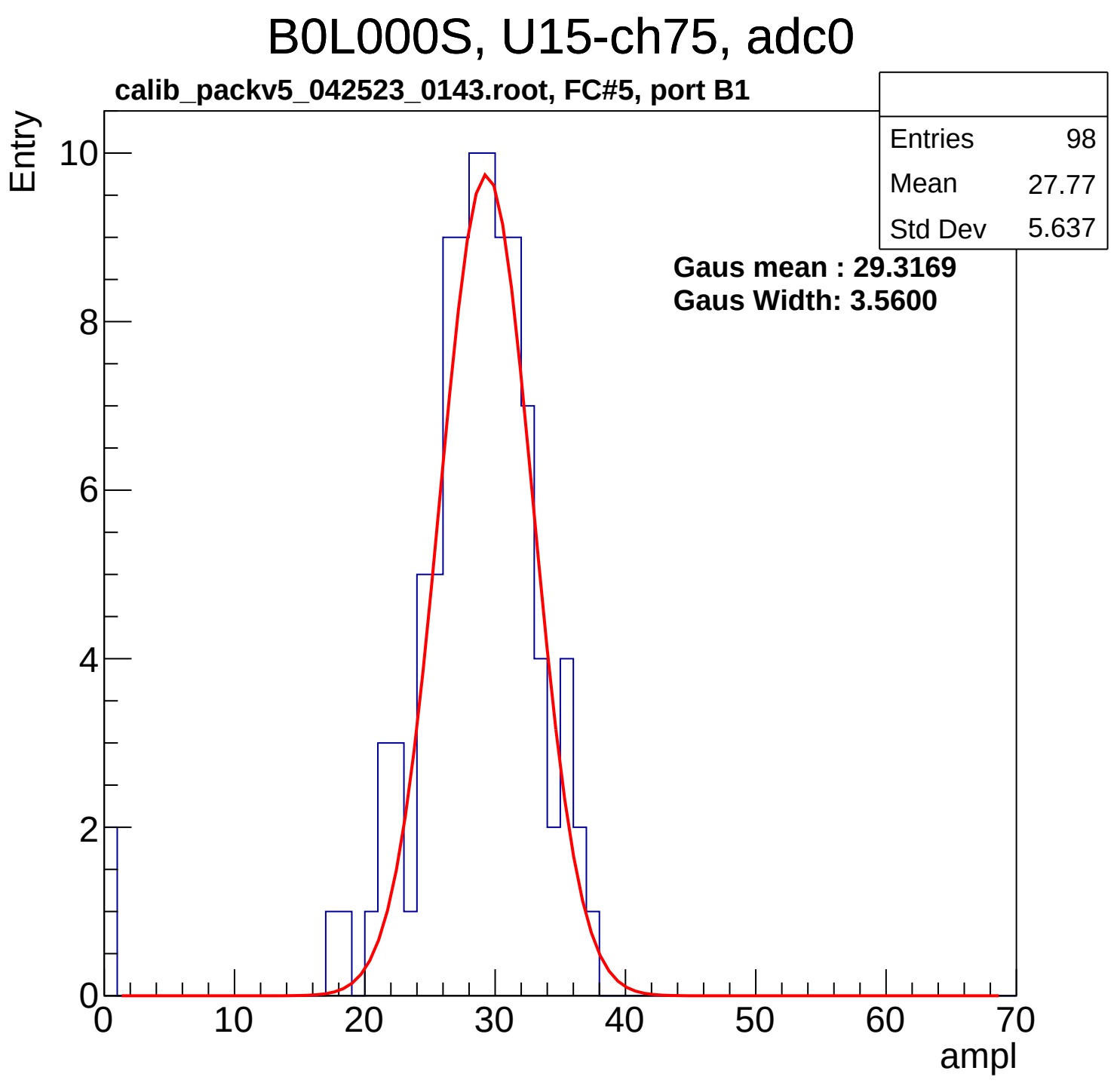
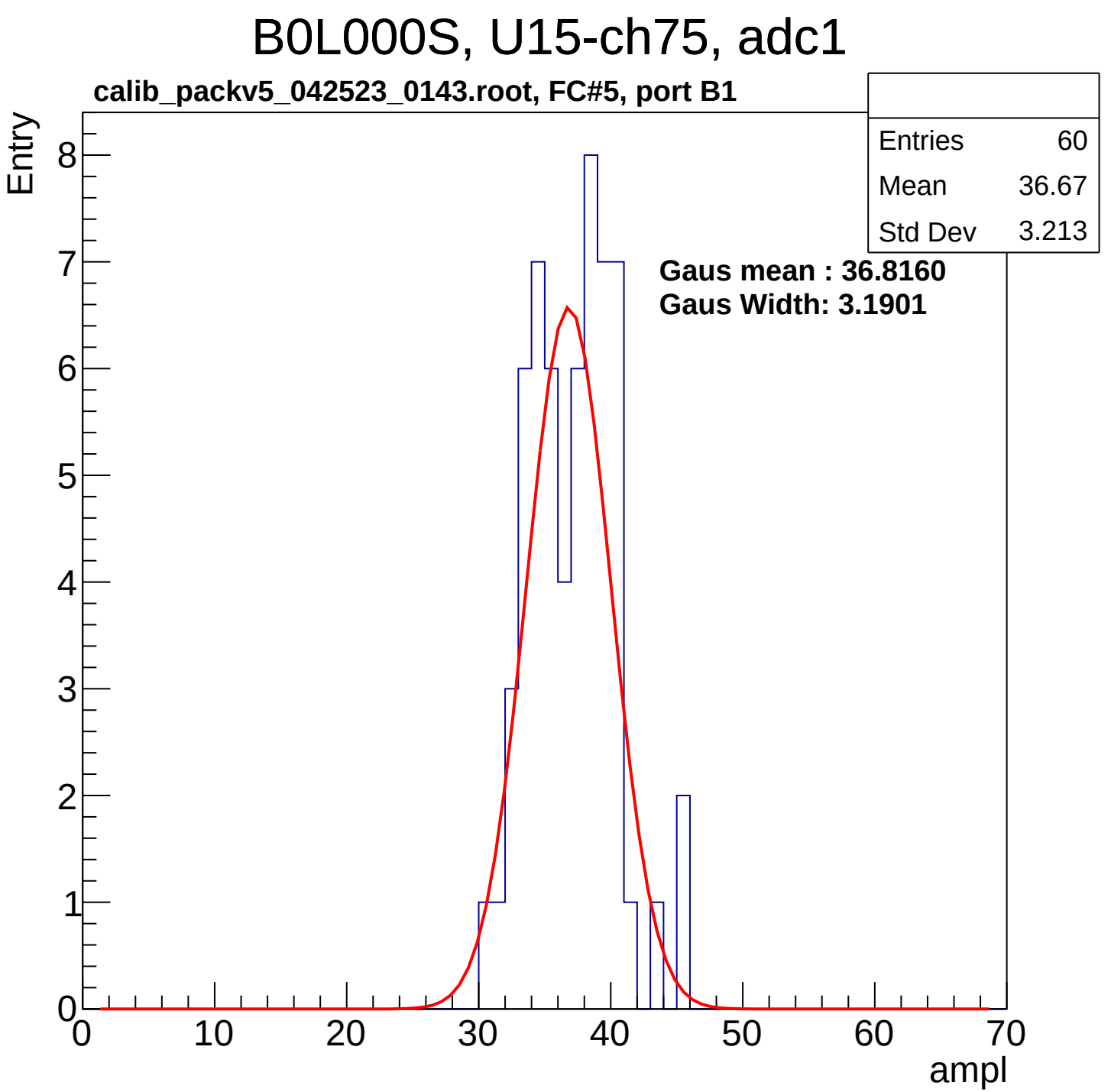

B0L000S, U15-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.78
Std Dev 10.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 98
Mean 27.77
Std Dev 5.637
Gaus mean : 29.3169
Gaus Width: 3.5600
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.67
Std Dev 3.213
Gaus mean : 36.8160
Gaus Width: 3.1901
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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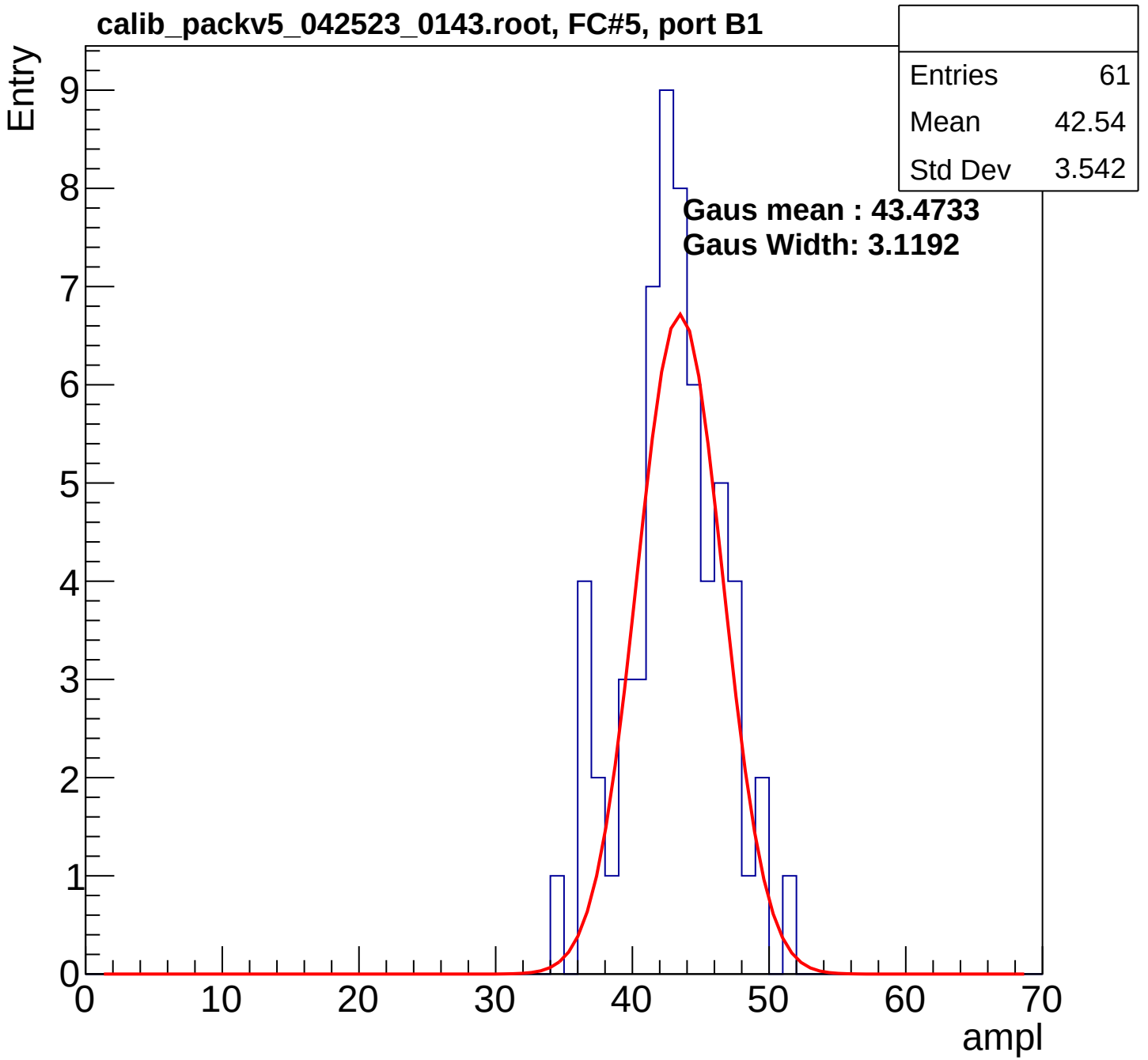
B0L000S, U15-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.54
Std Dev 3.542
Gaus mean : 43.4733
Gaus Width: 3.1192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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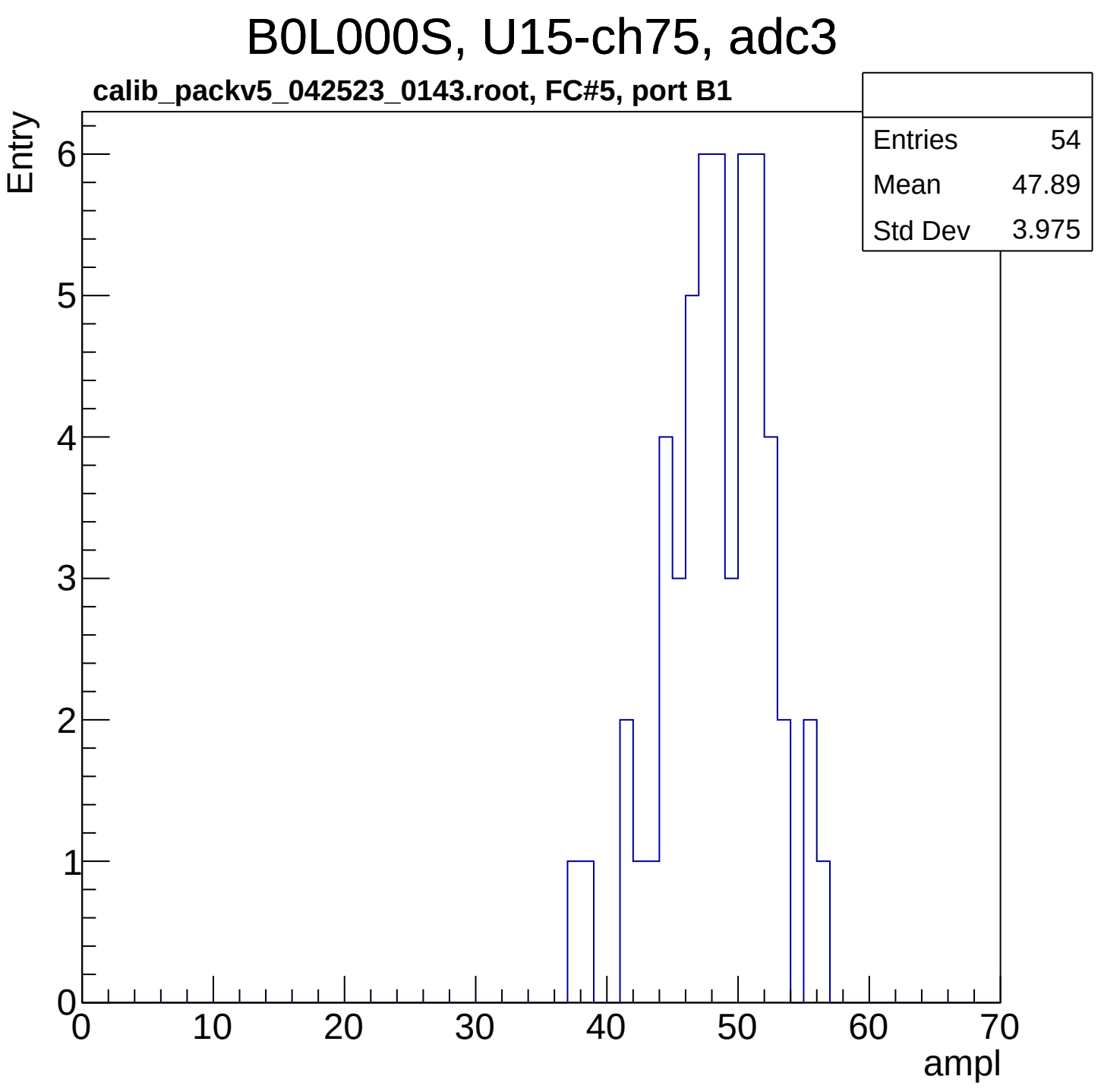
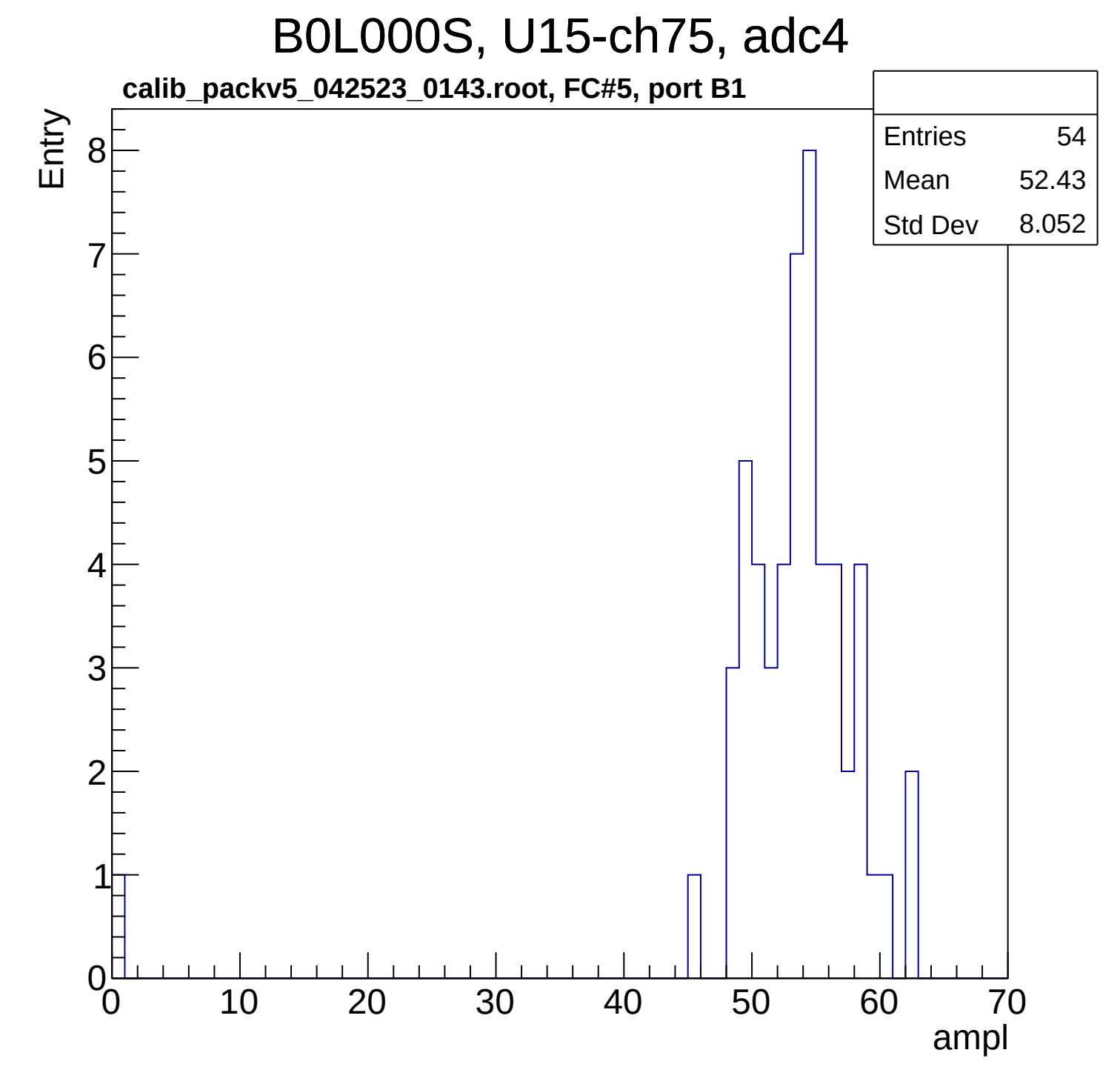

B0L000S, U15-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 47.89
Std Dev 3.975
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 52.43
Std Dev 8.052
Entry
ampl
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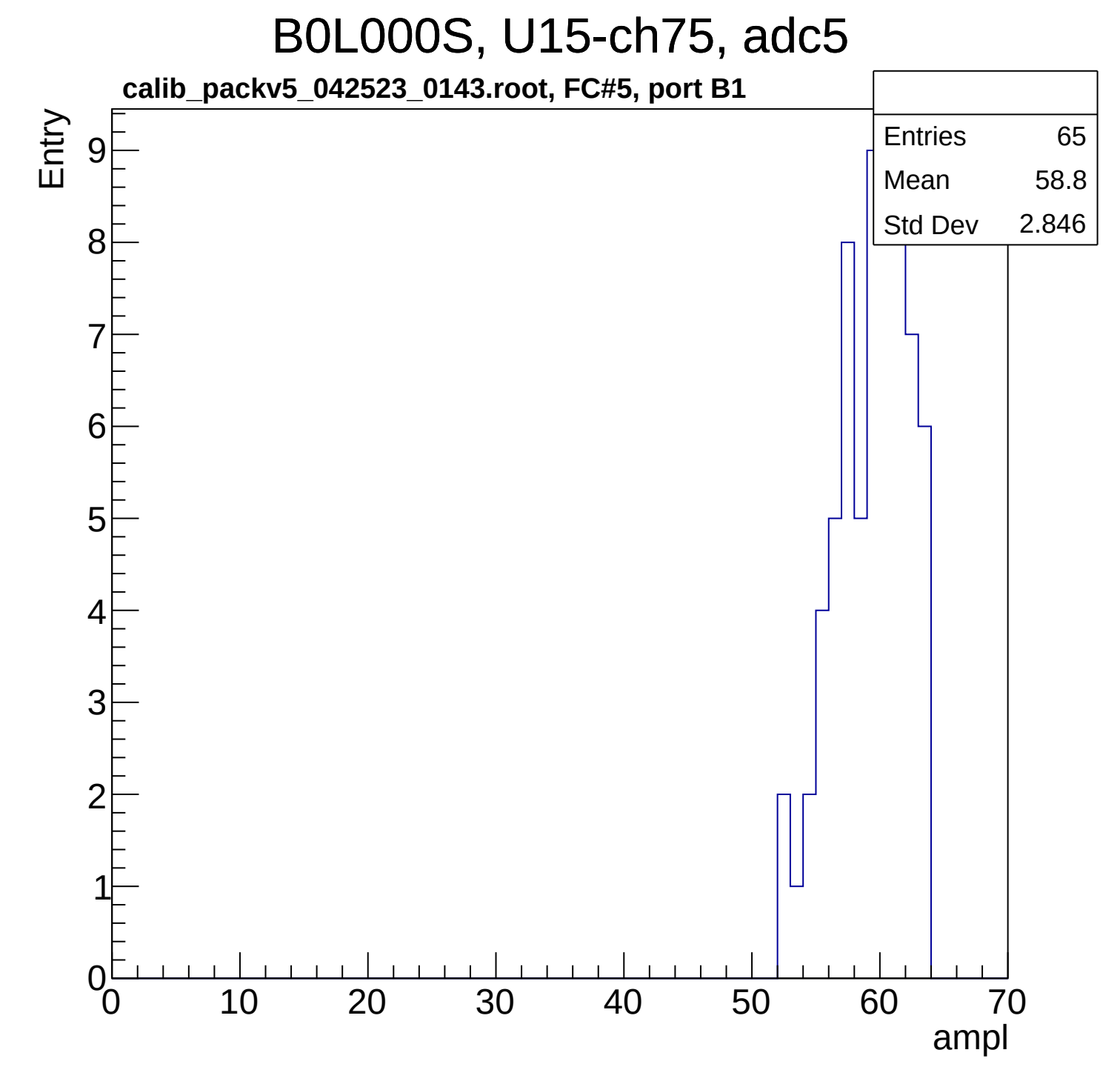
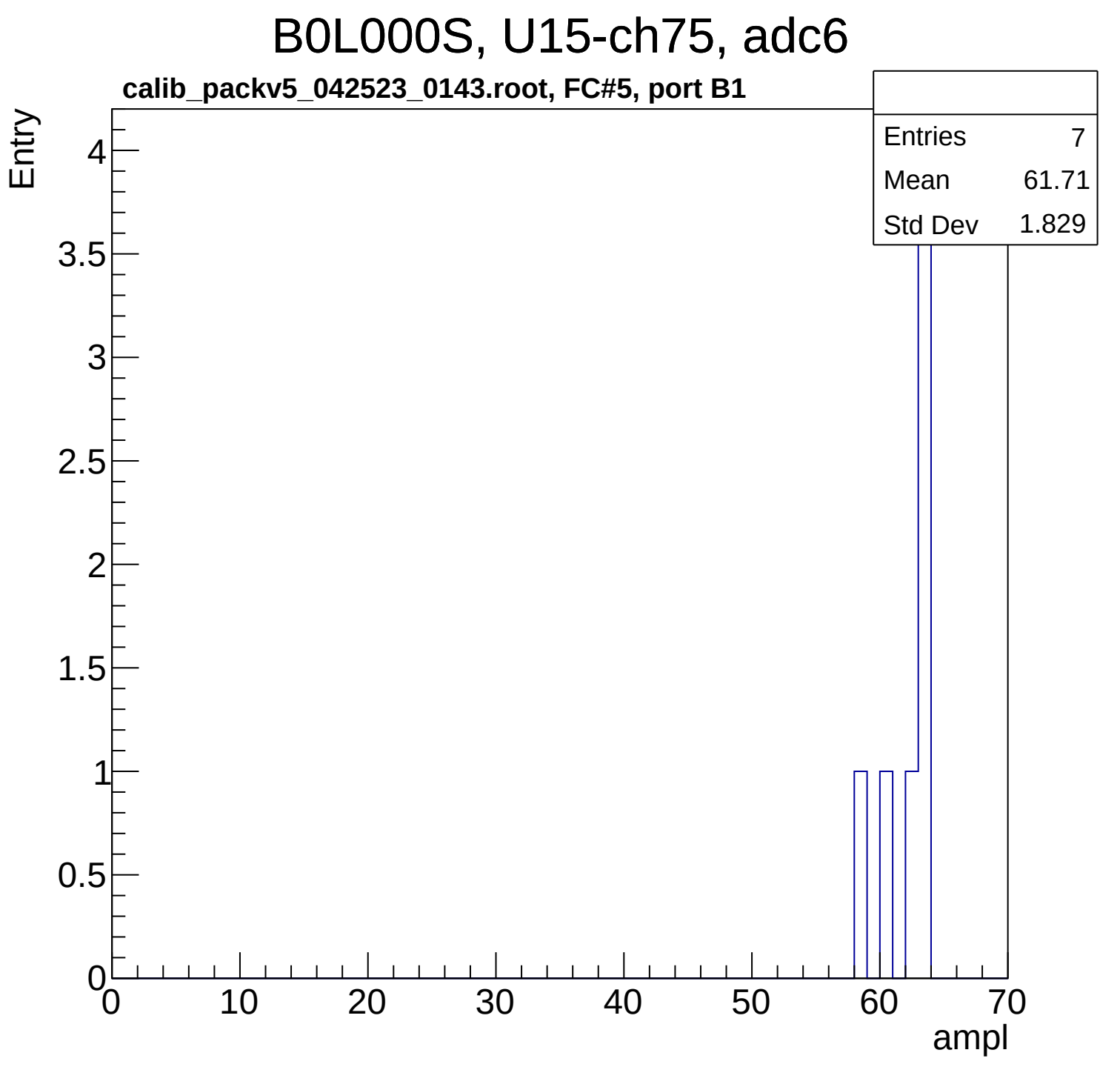
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 58.8
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 1.829
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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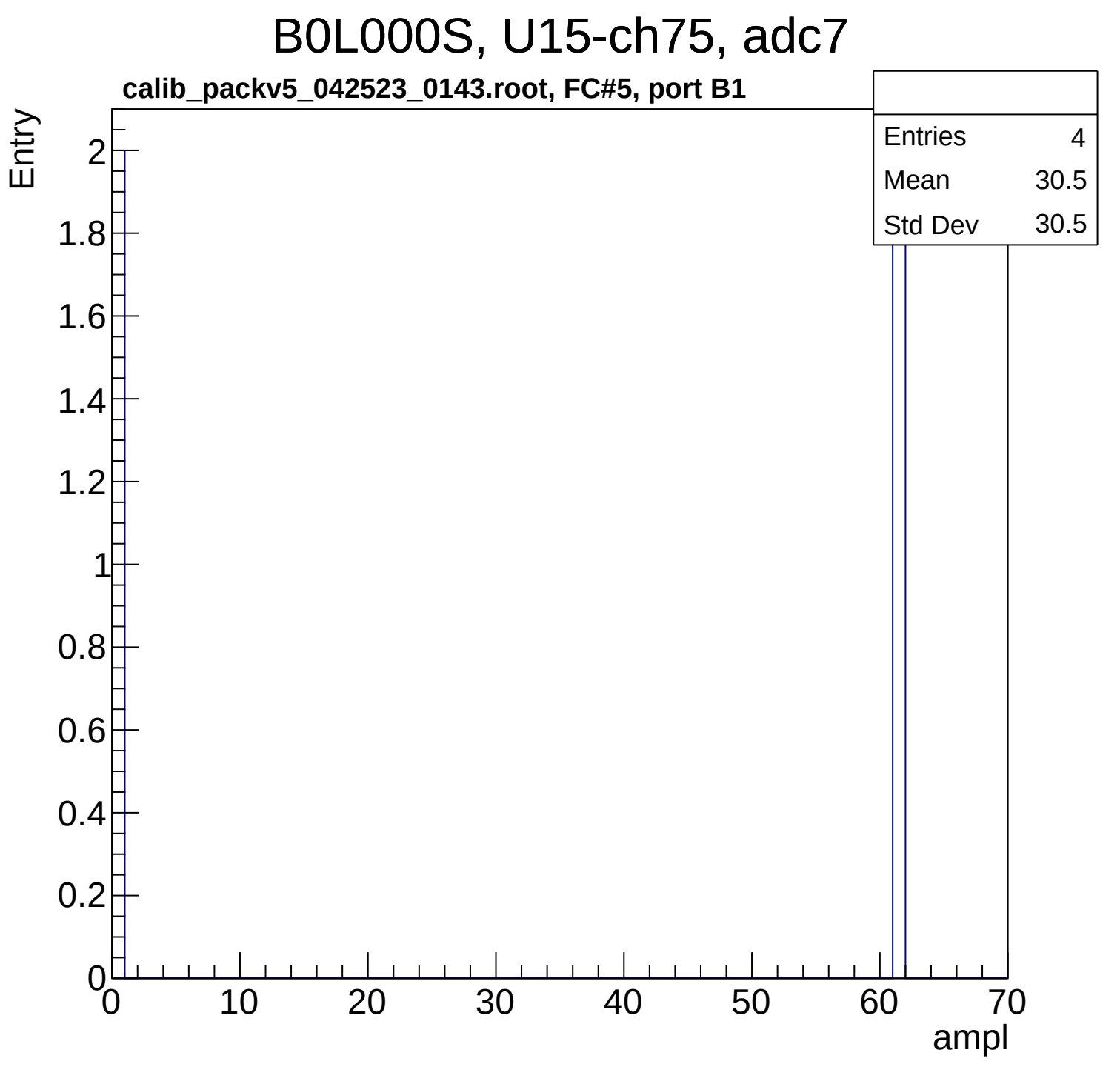
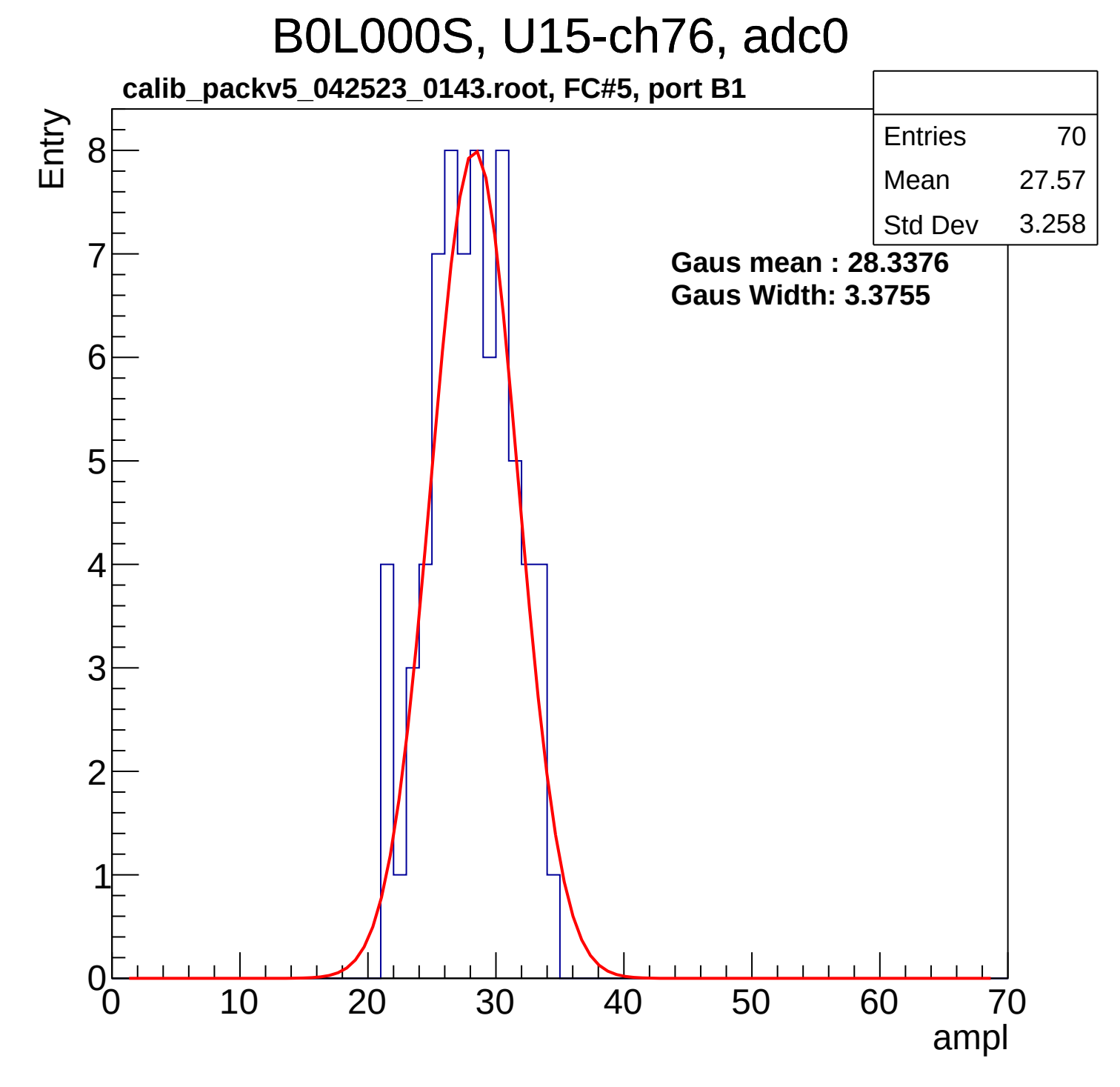
60
70


B0L000S, U15-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 30.5
Std Dev 30.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.57
Std Dev 3.258
Gaus mean : 28.3376
Gaus Width: 3.3755
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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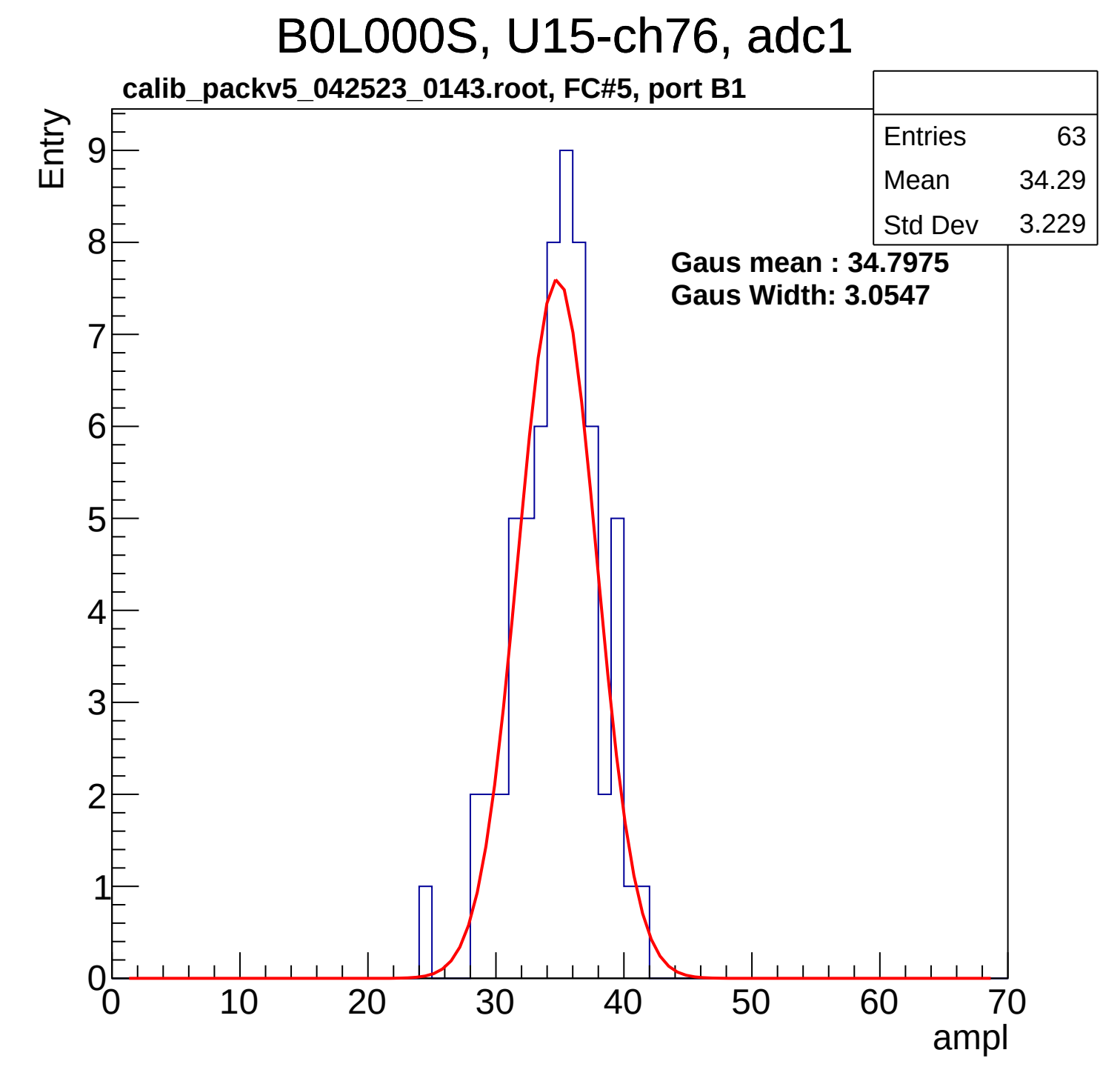
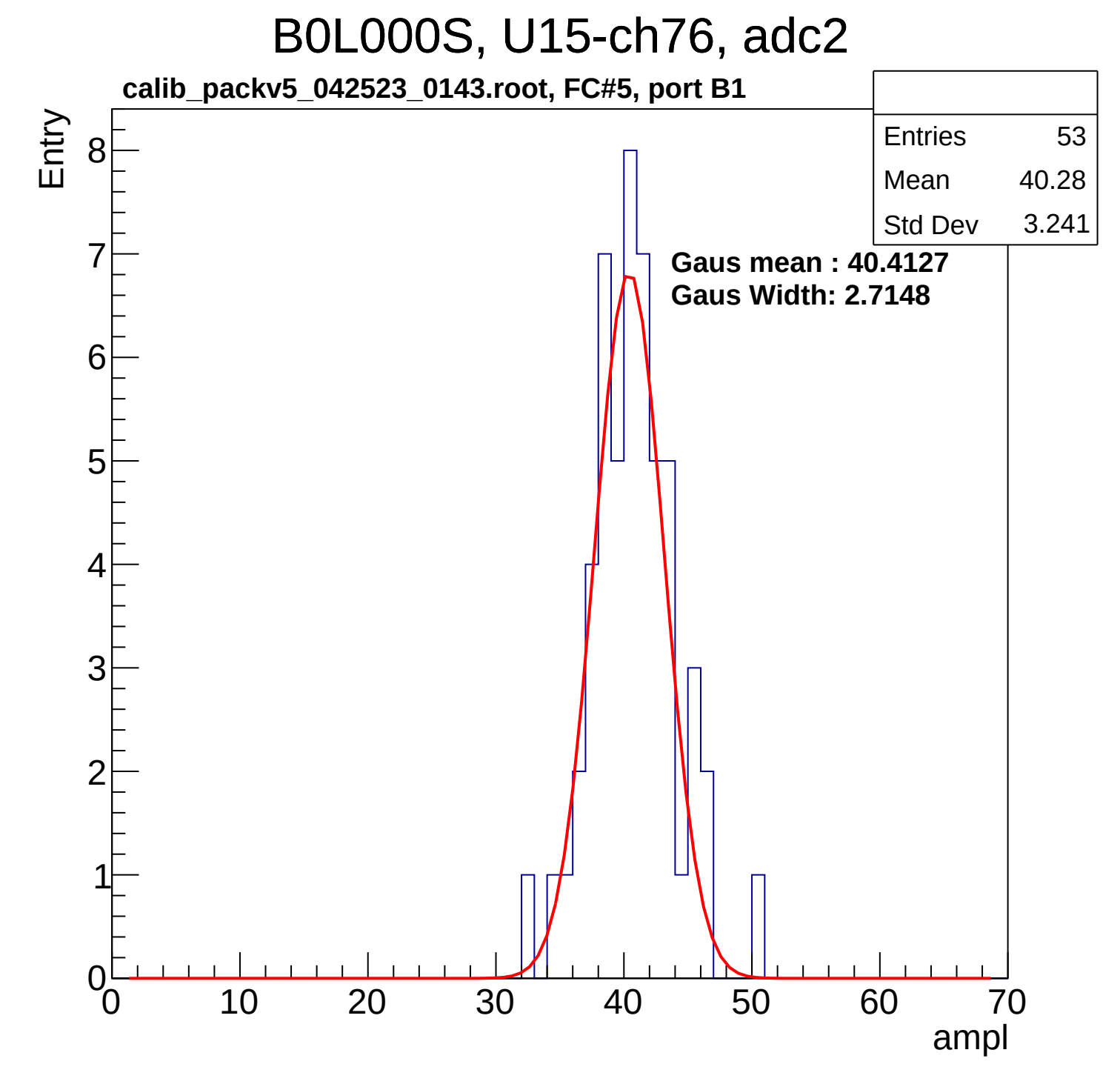

B0L000S, U15-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 34.29
Std Dev 3.229
Gaus mean : 34.7975
Gaus Width: 3.0547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 40.28
Std Dev 3.241
Gaus mean : 40.4127
Gaus Width: 2.7148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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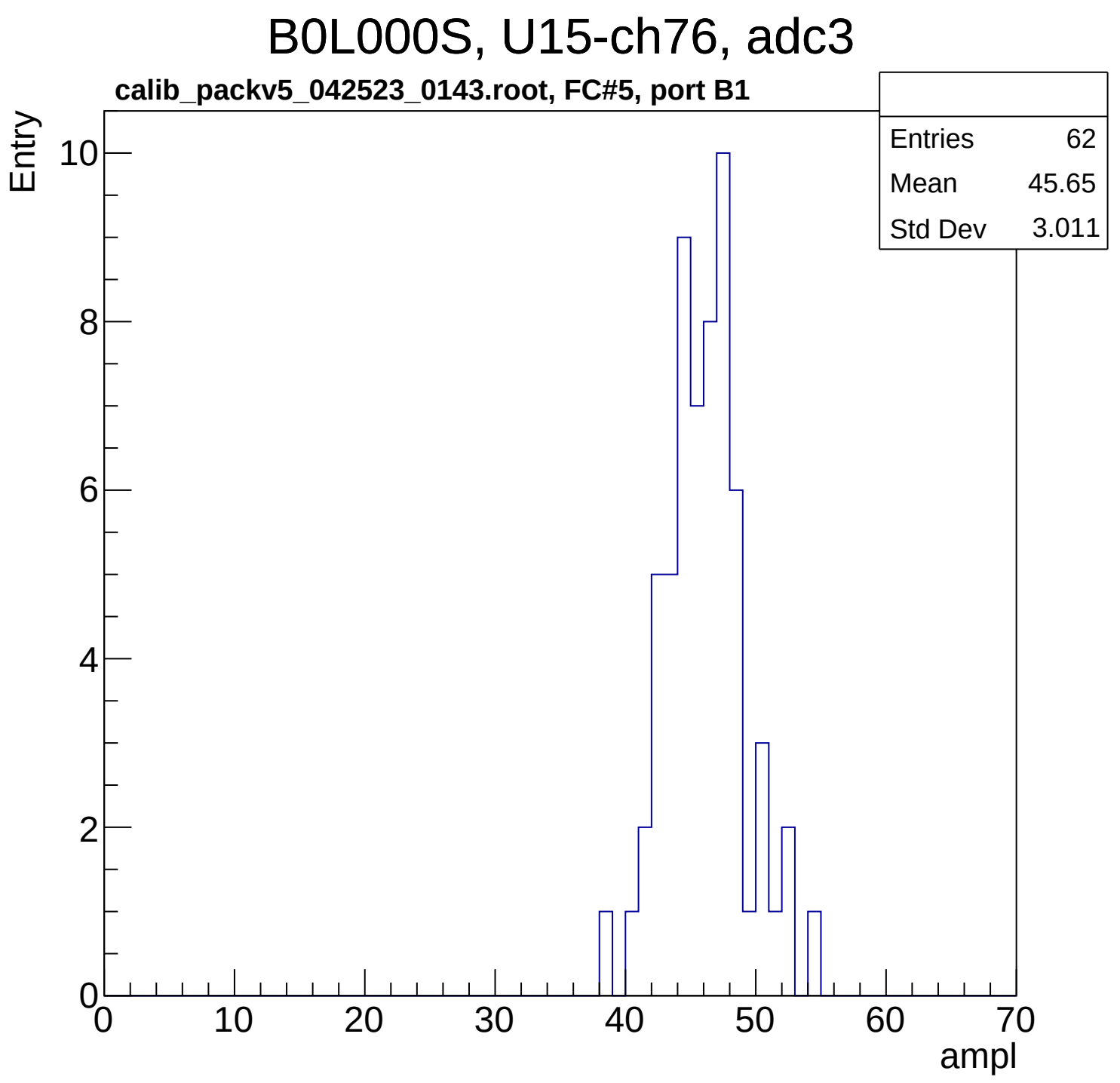
B0L000S, U15-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 45.65
Std Dev 3.011
Entry
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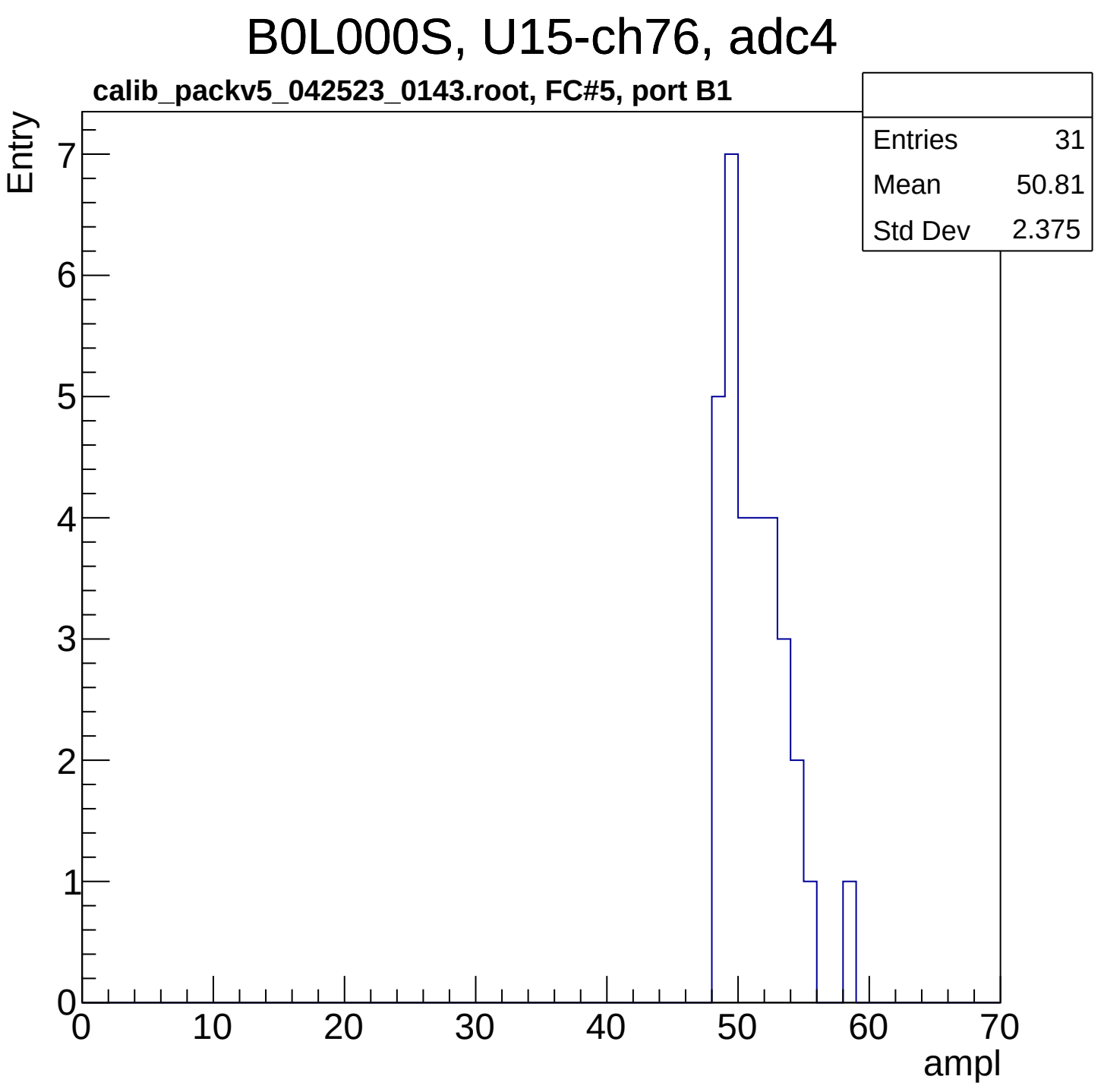
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 50.81
Std Dev 2.375
Entry
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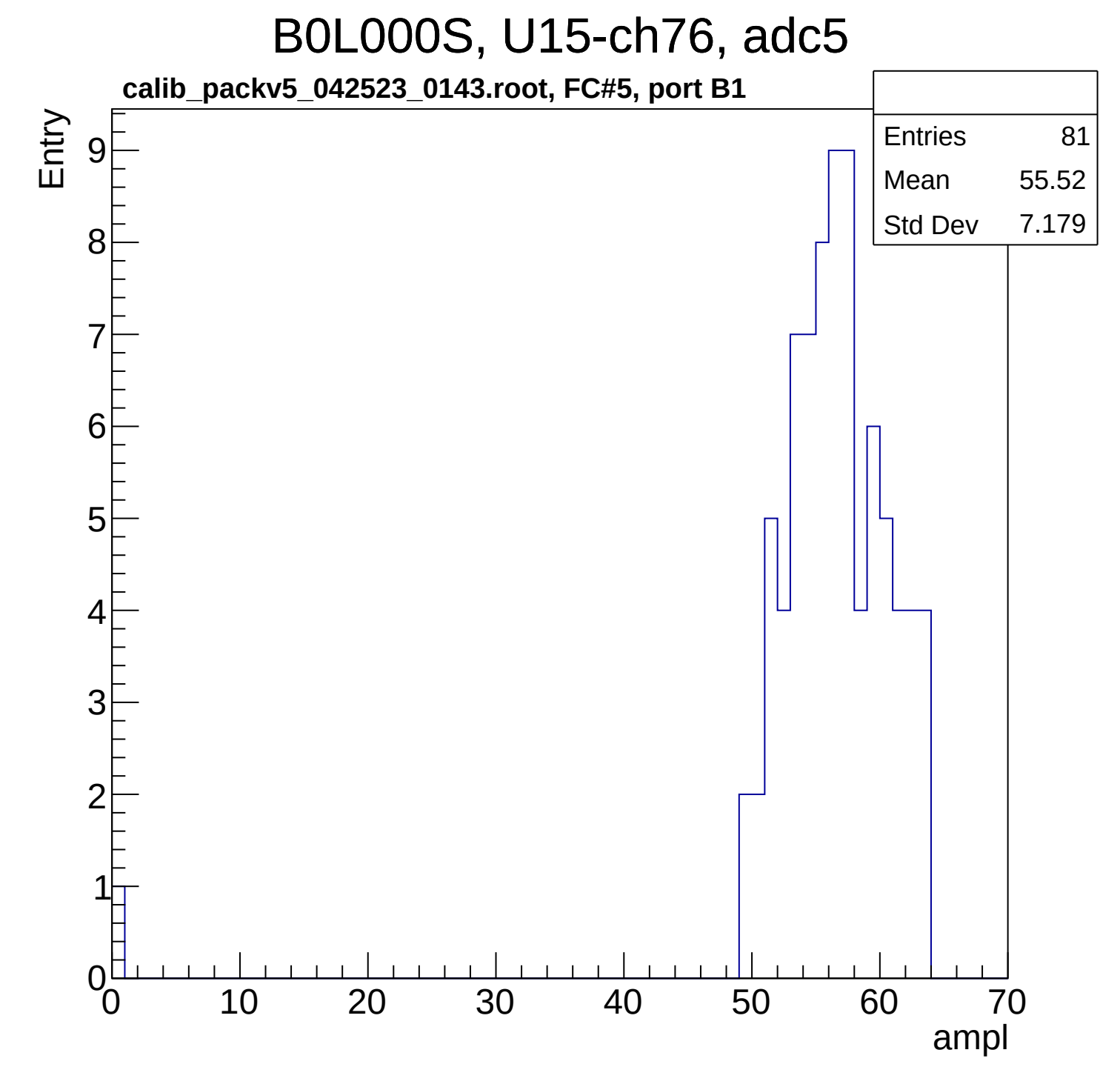
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 55.52
Std Dev 7.179
Entry
ampl
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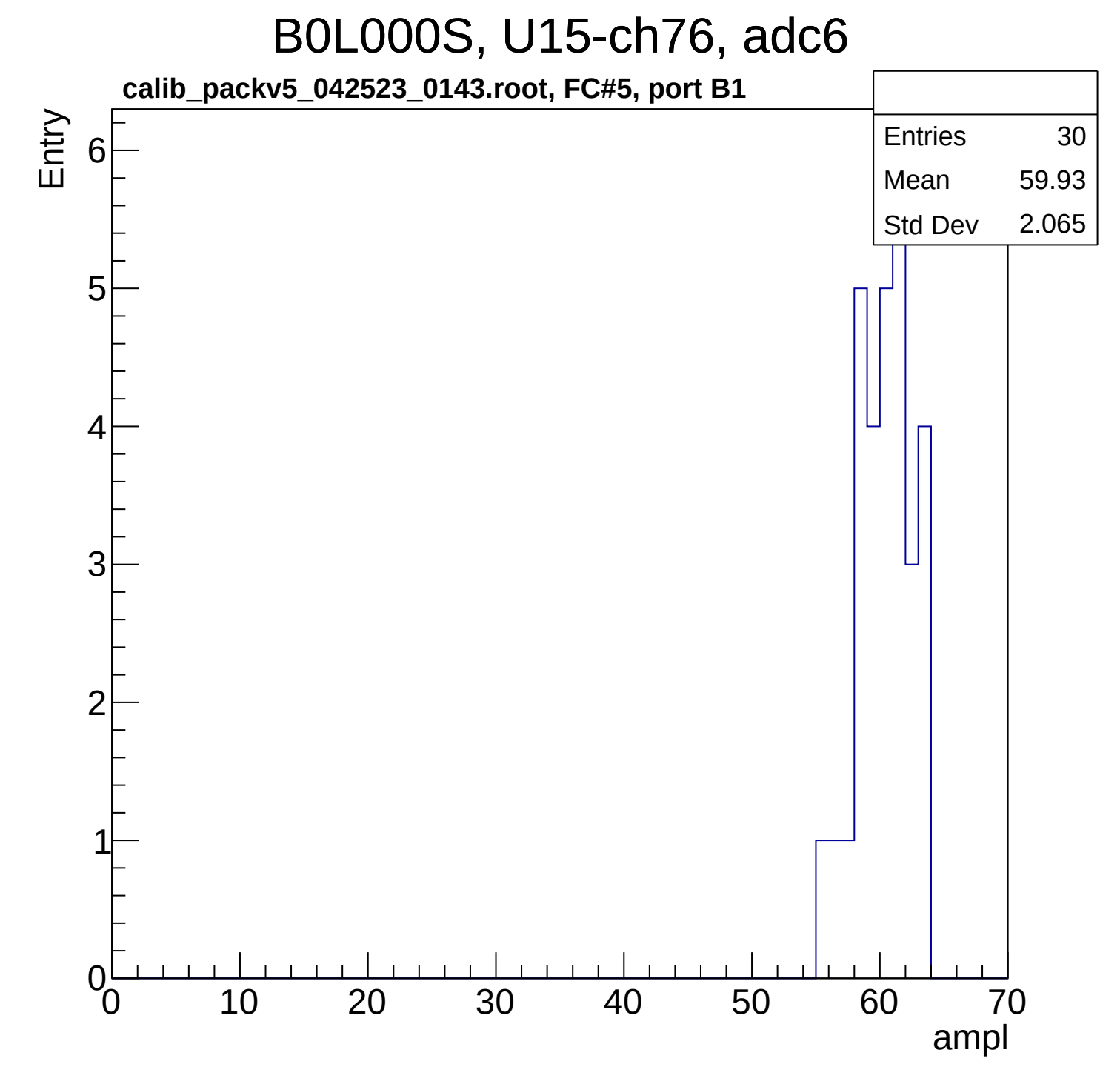
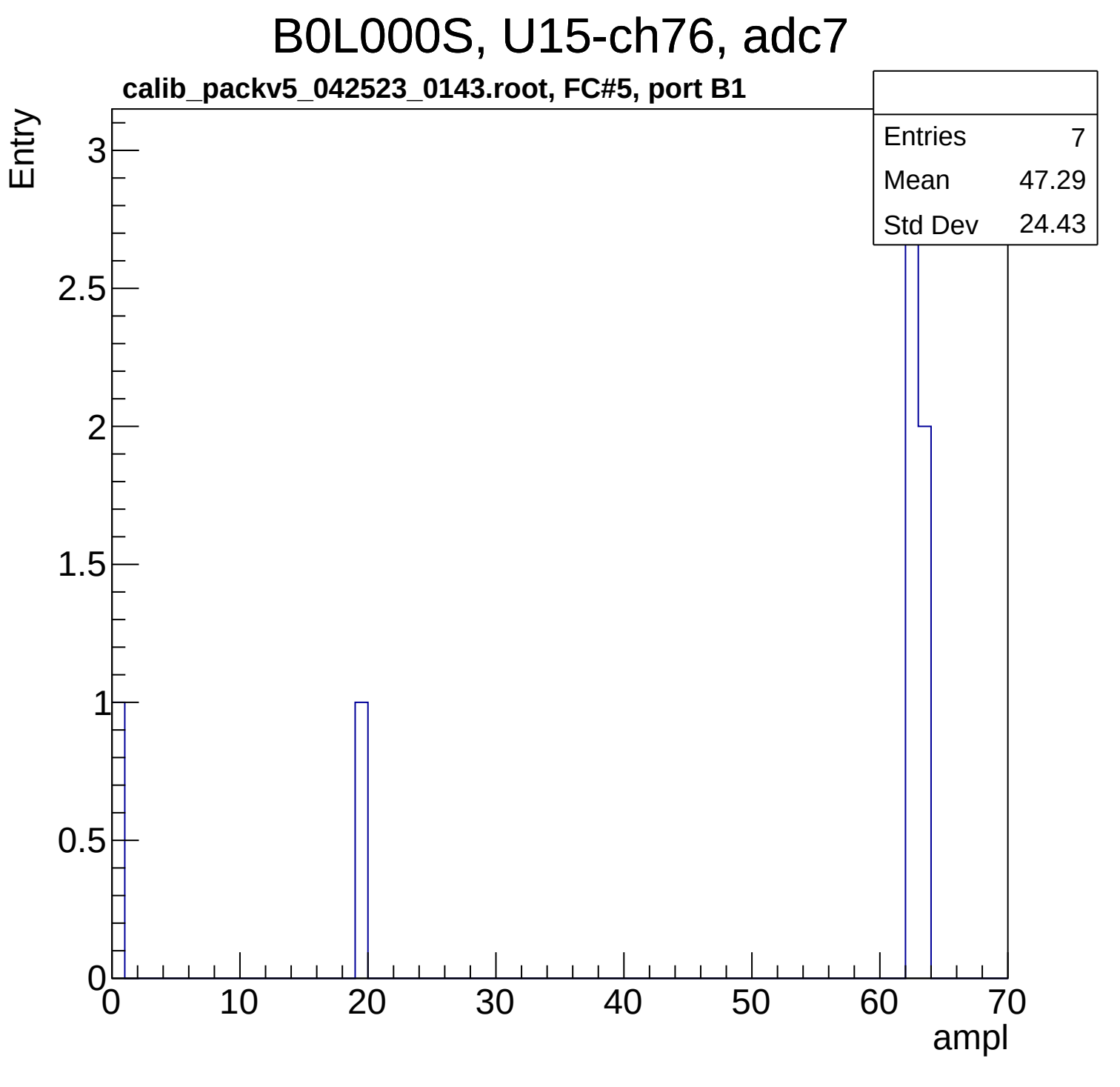
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 59.93
Std Dev 2.065
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 47.29
Std Dev 24.43
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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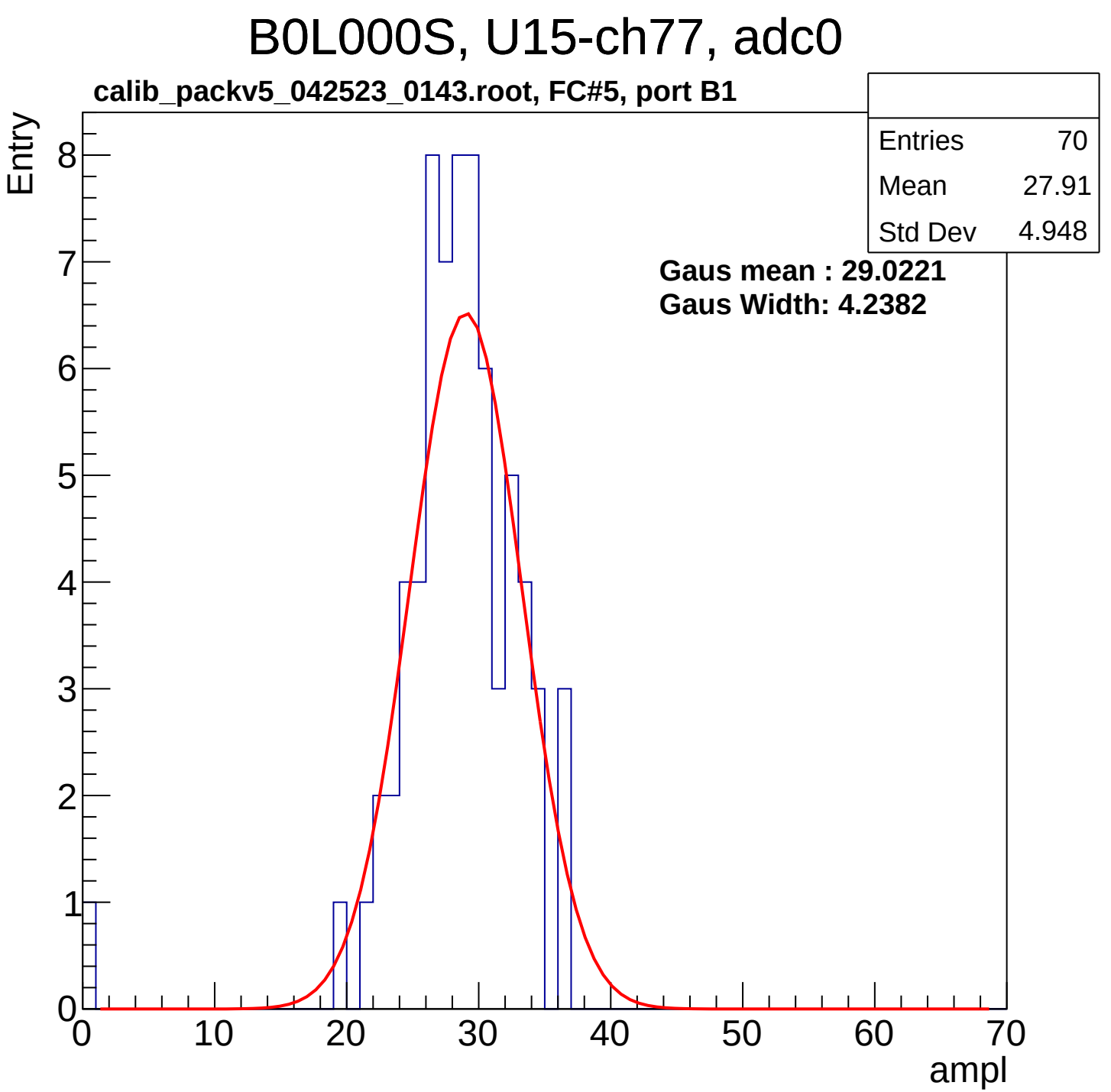
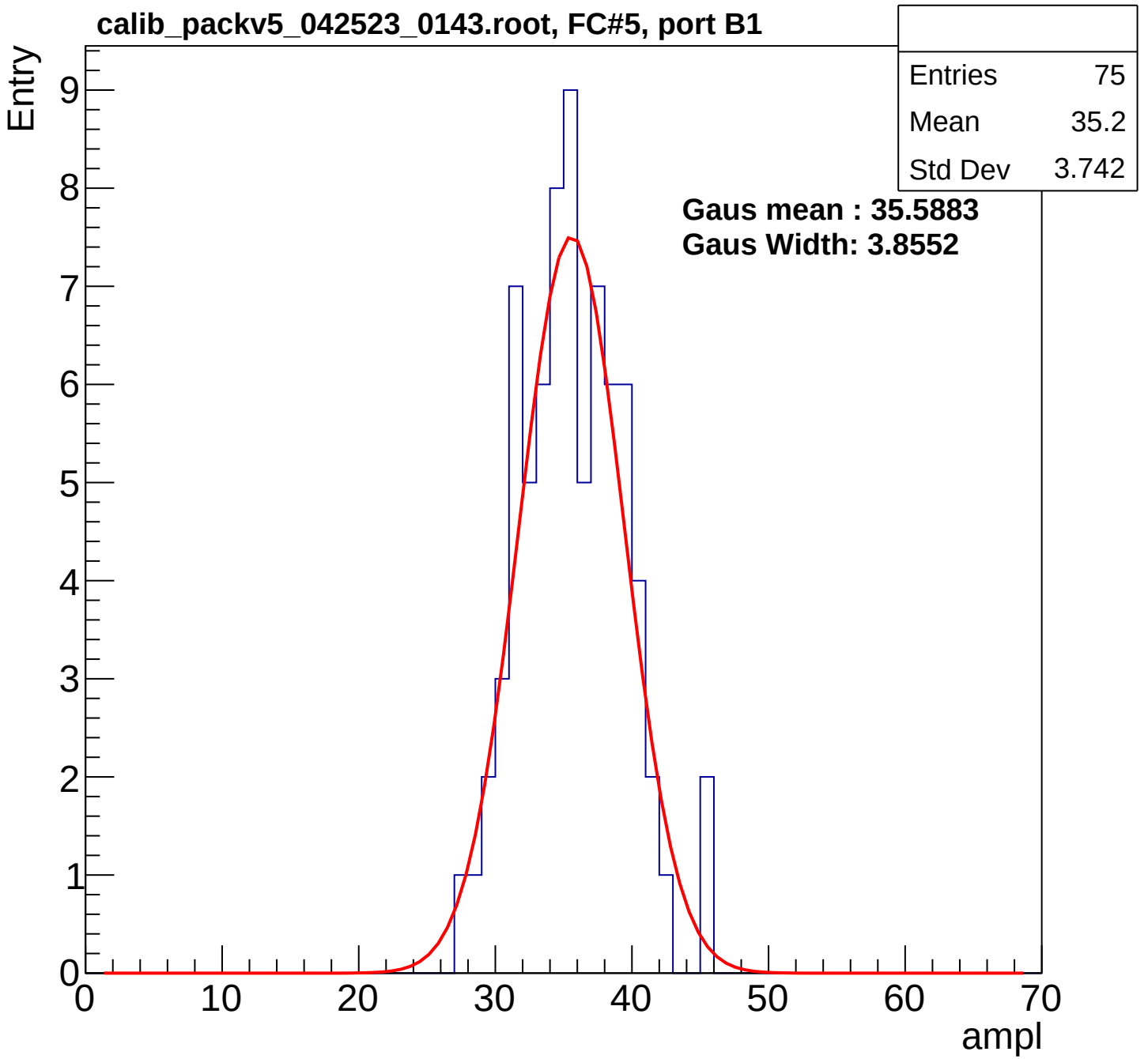

B0L000S, U15-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.91
Std Dev 4.948
Gaus mean : 29.0221
Gaus Width: 4.2382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.2
Std Dev 3.742
Gaus mean : 35.5883
Gaus Width: 3.8552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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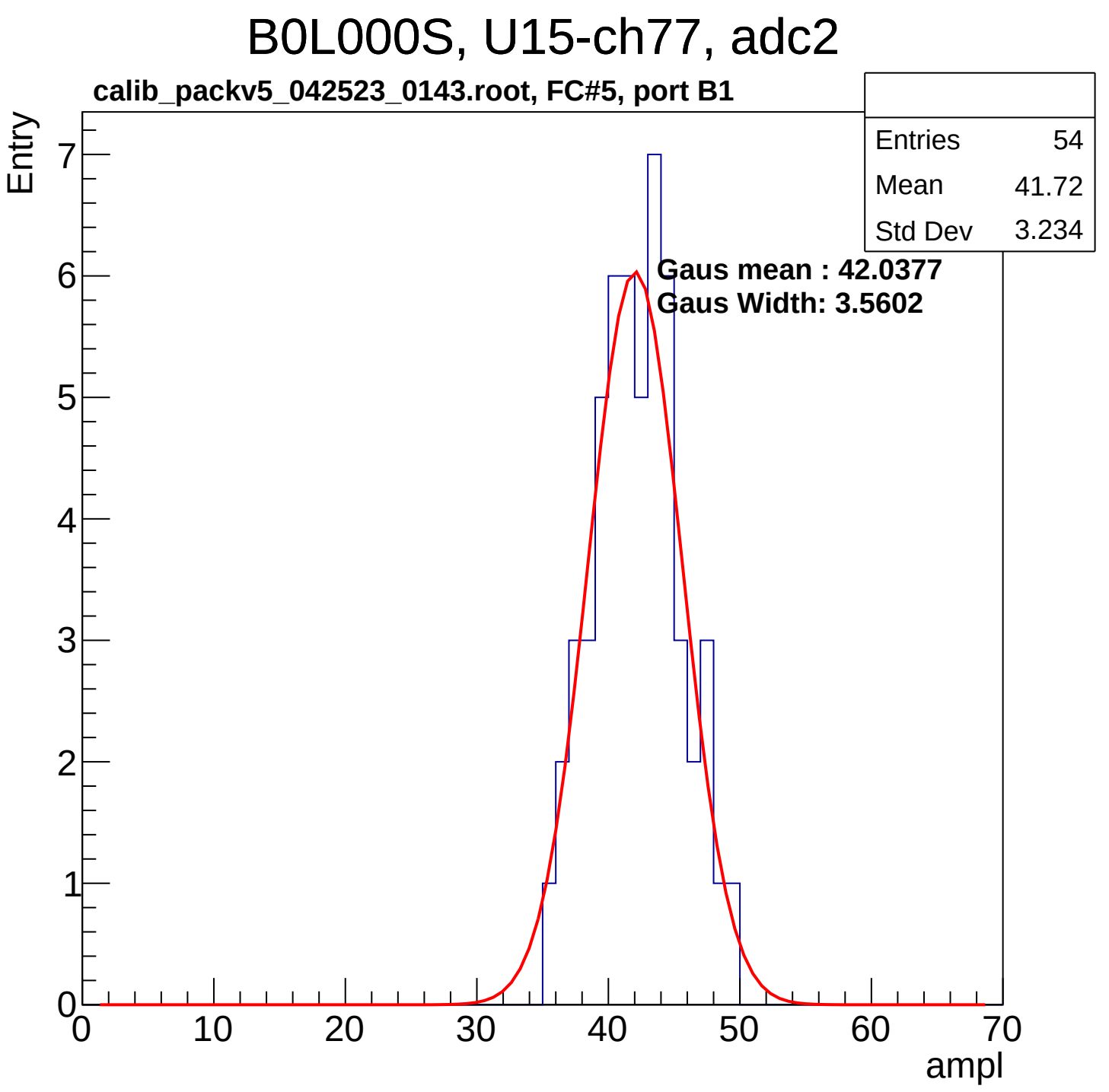
B0L000S, U15-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 41.72
Std Dev 3.234
Gaus mean : 42.0377
Gaus Width: 3.5602
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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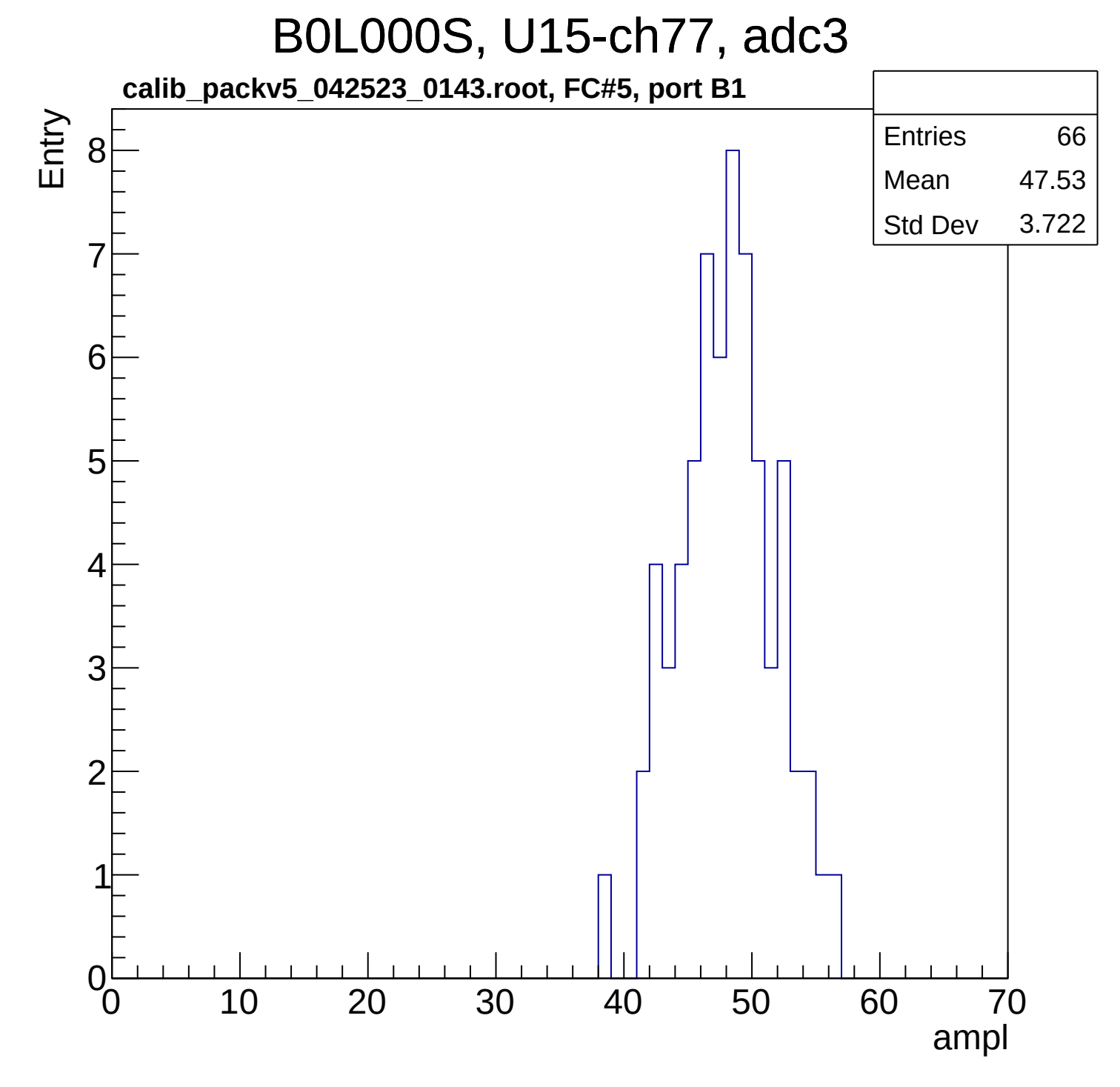
B0L000S, U15-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 47.53
Std Dev 3.722
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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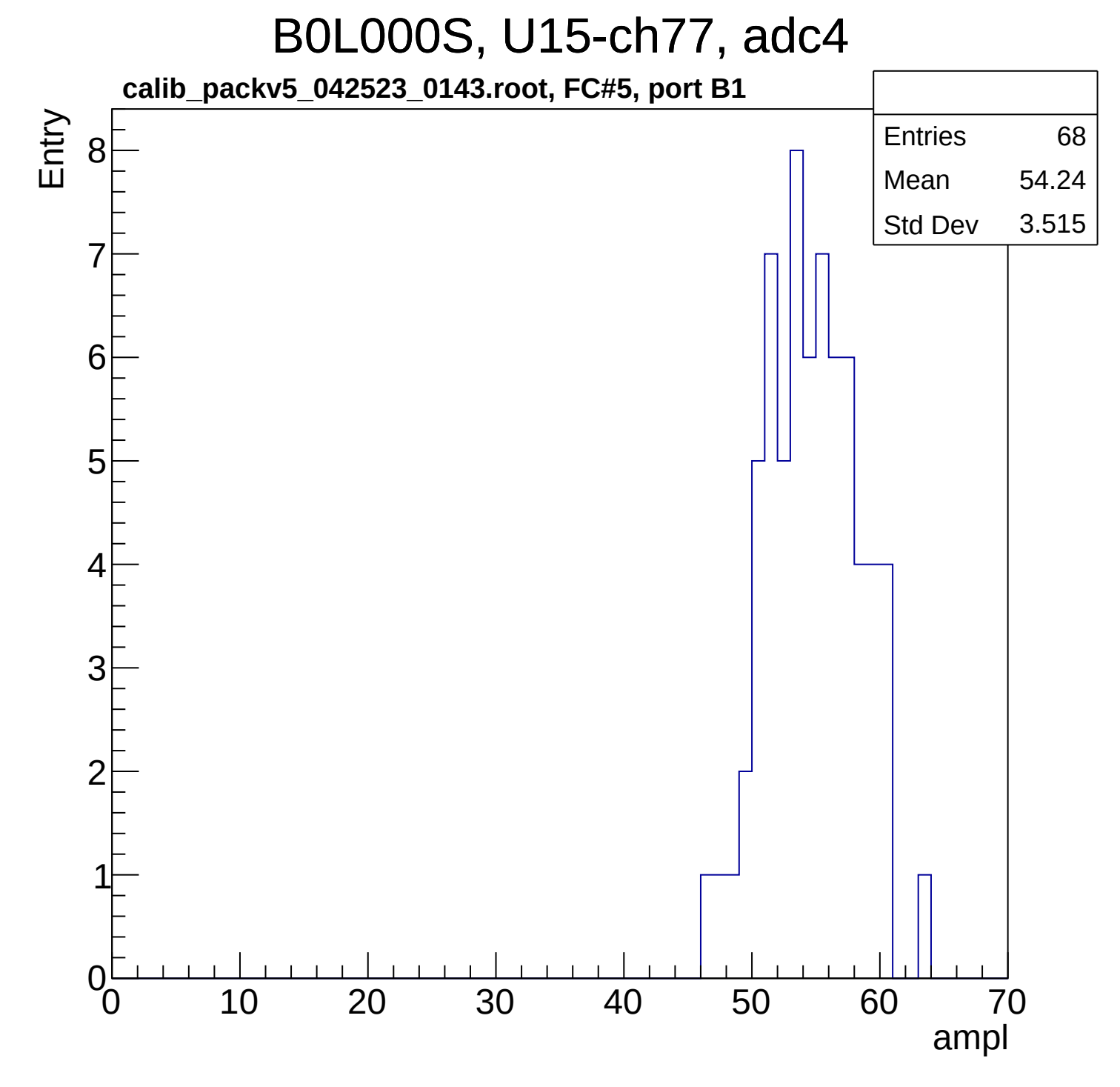
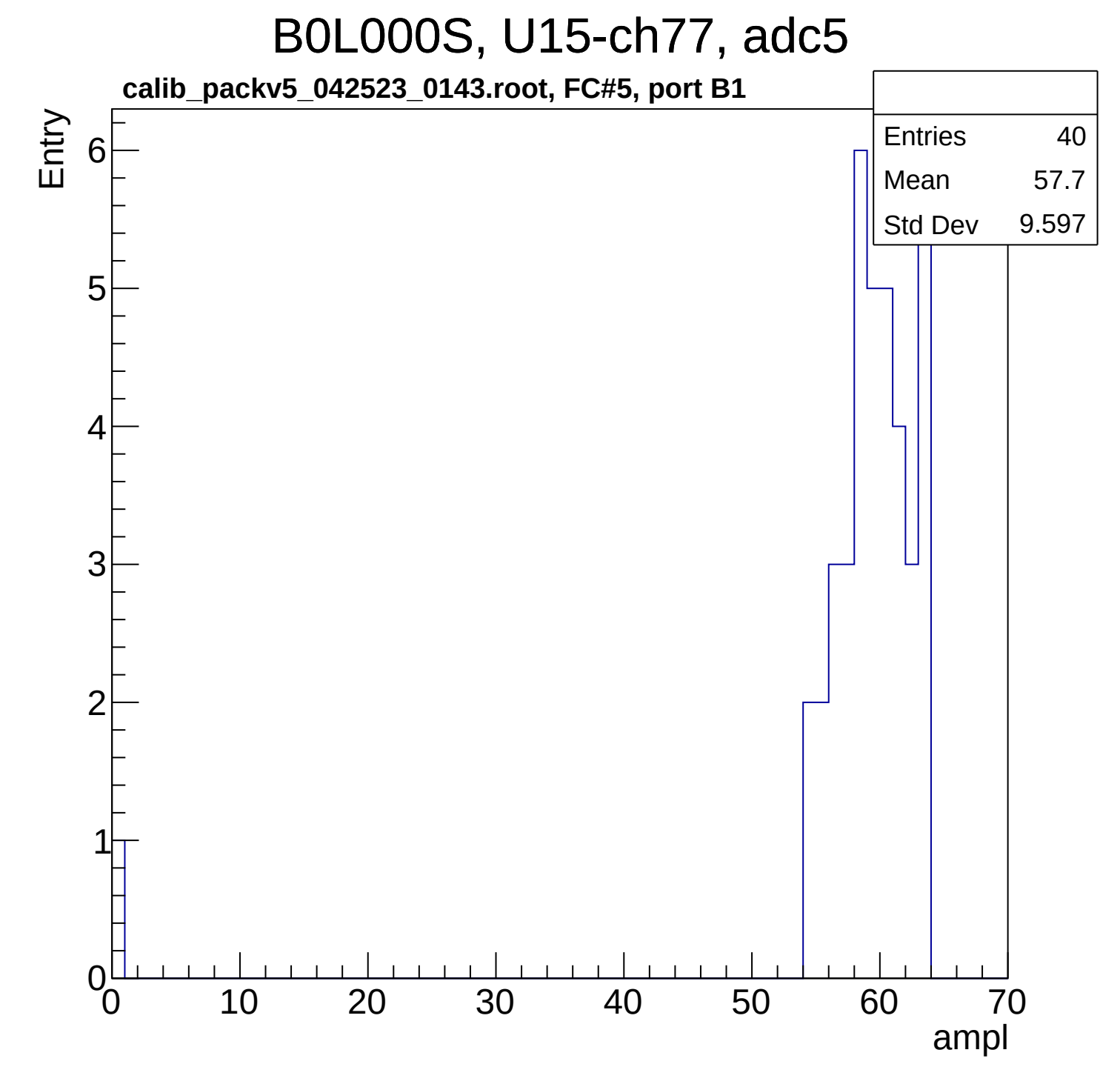

B0L000S, U15-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.24
Std Dev 3.515
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 57.7
Std Dev 9.597
Entry
ampl
0
1
2
3
4
5
6
0
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40
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60
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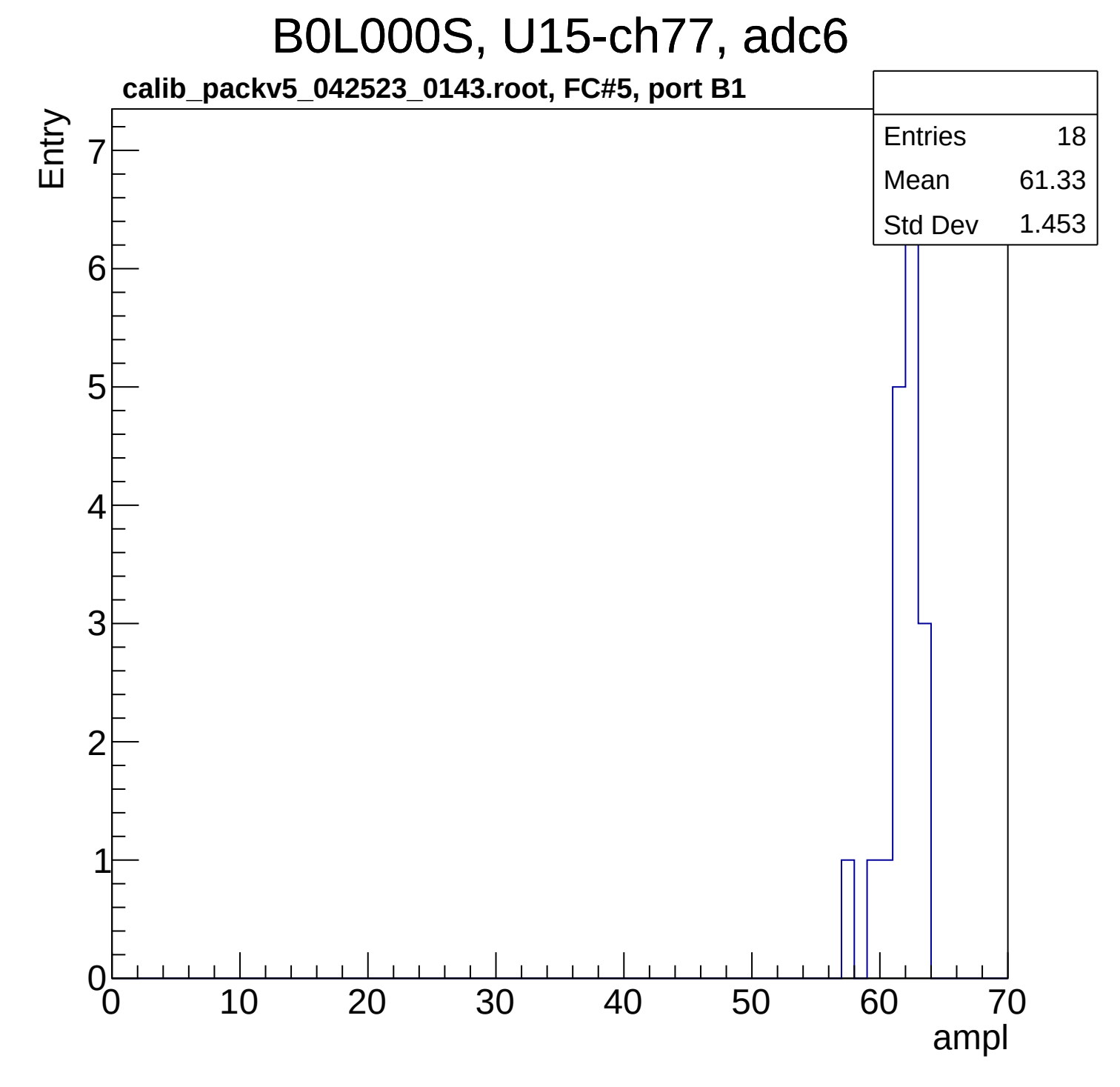
B0L000S, U15-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.33
Std Dev 1.453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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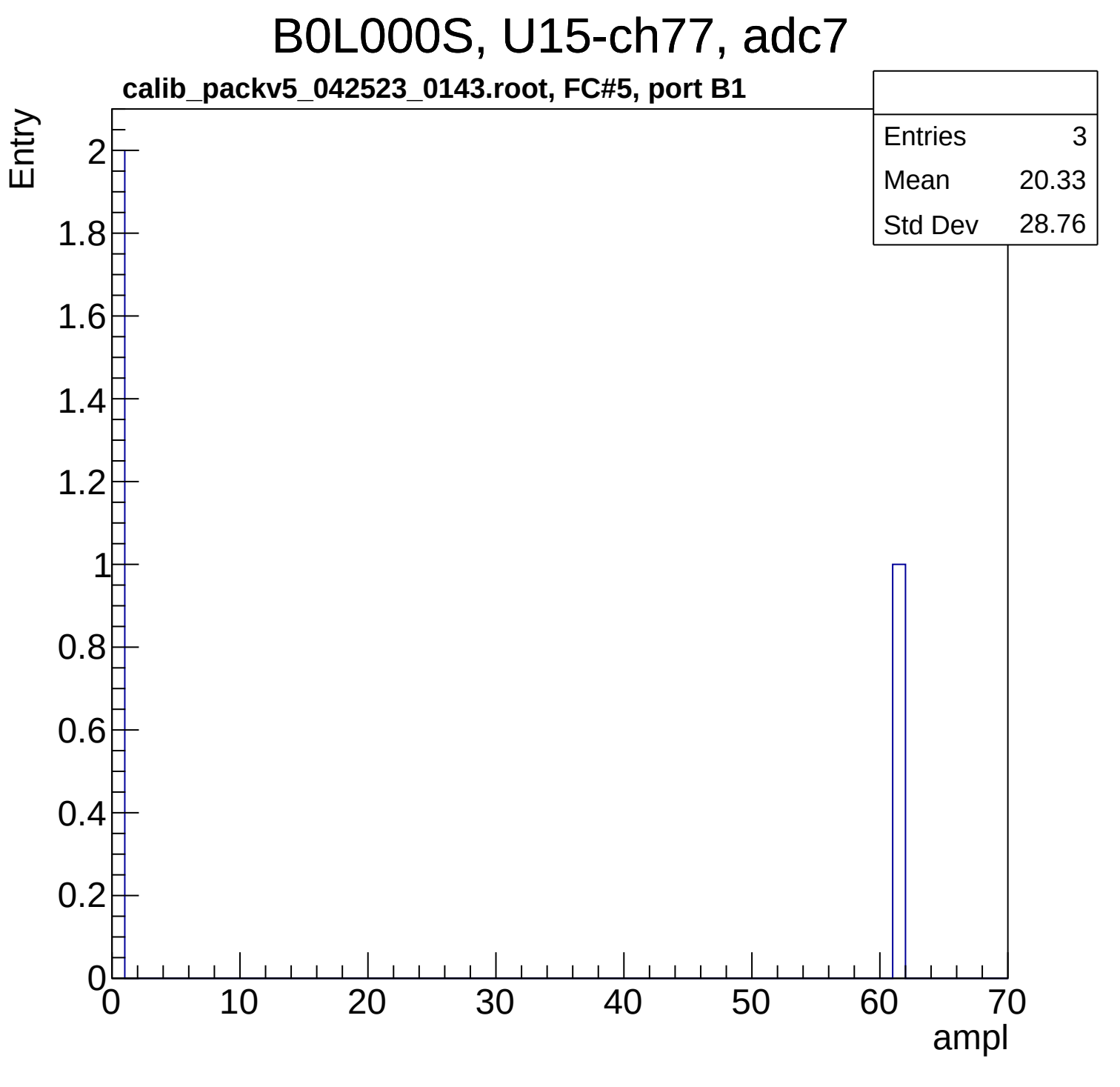
B0L000S, U15-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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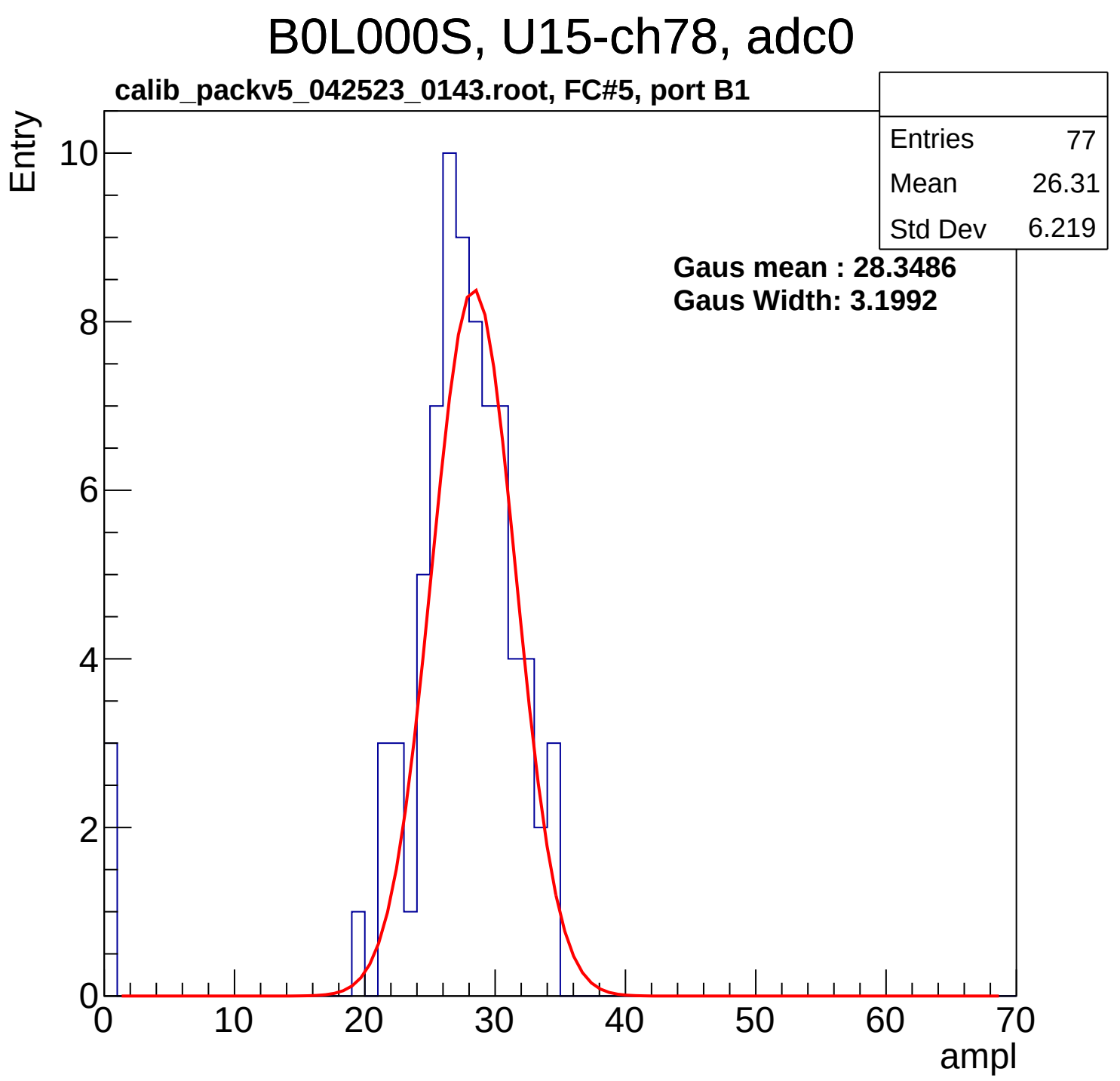
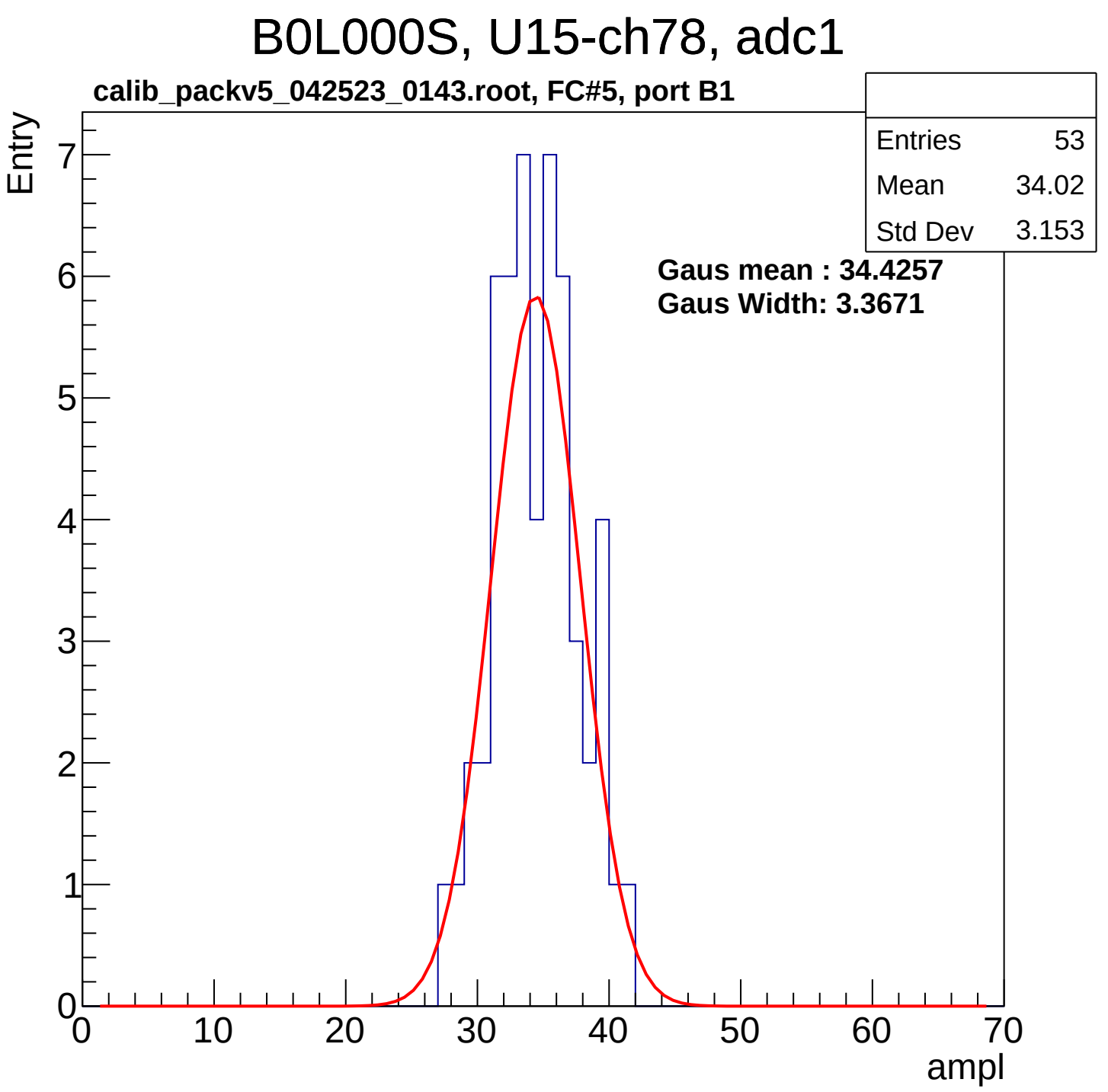

B0L000S, U15-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 26.31
Std Dev 6.219
Gaus mean : 28.3486
Gaus Width: 3.1992
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 34.02
Std Dev 3.153
Gaus mean : 34.4257
Gaus Width: 3.3671
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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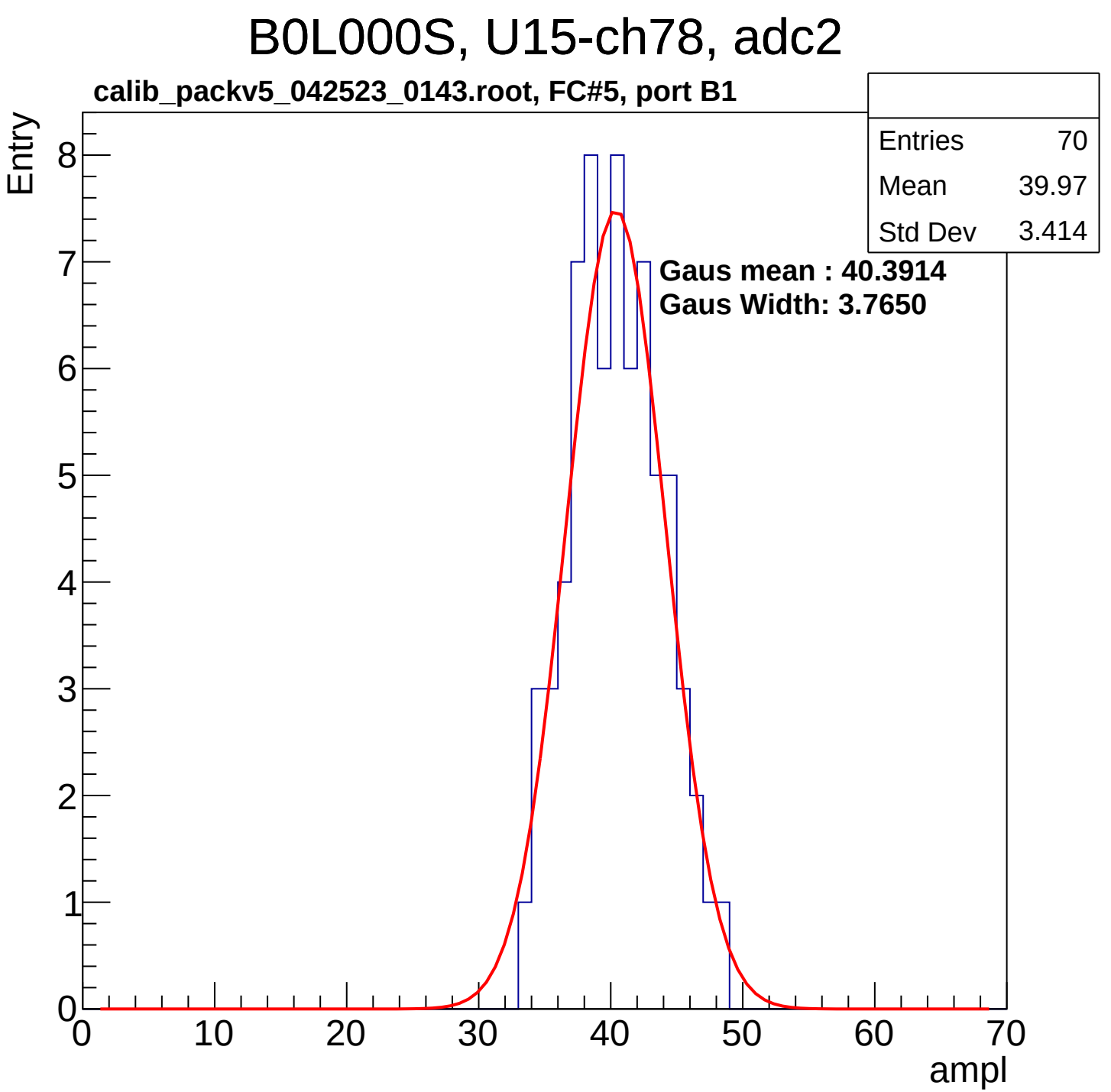
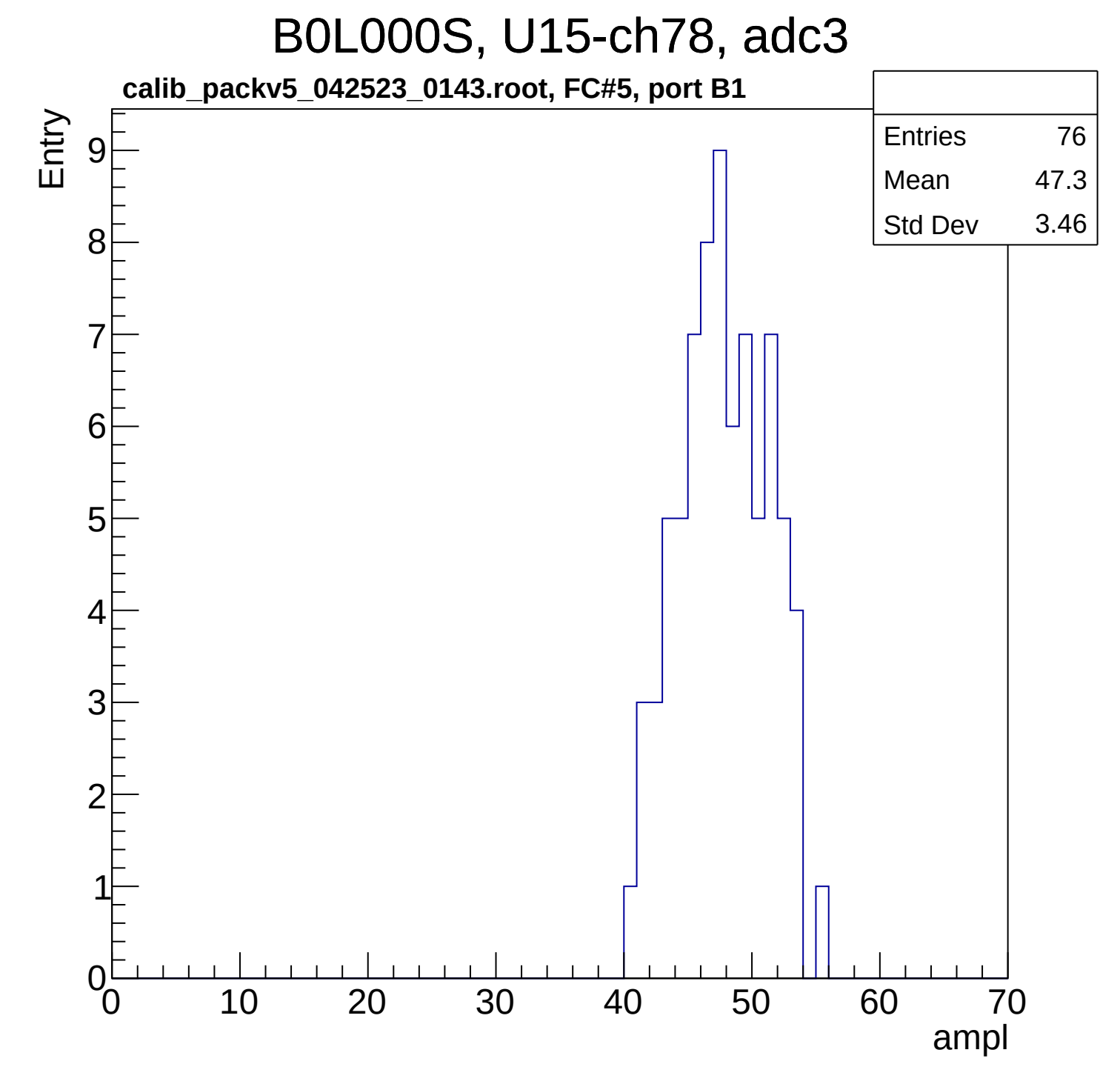
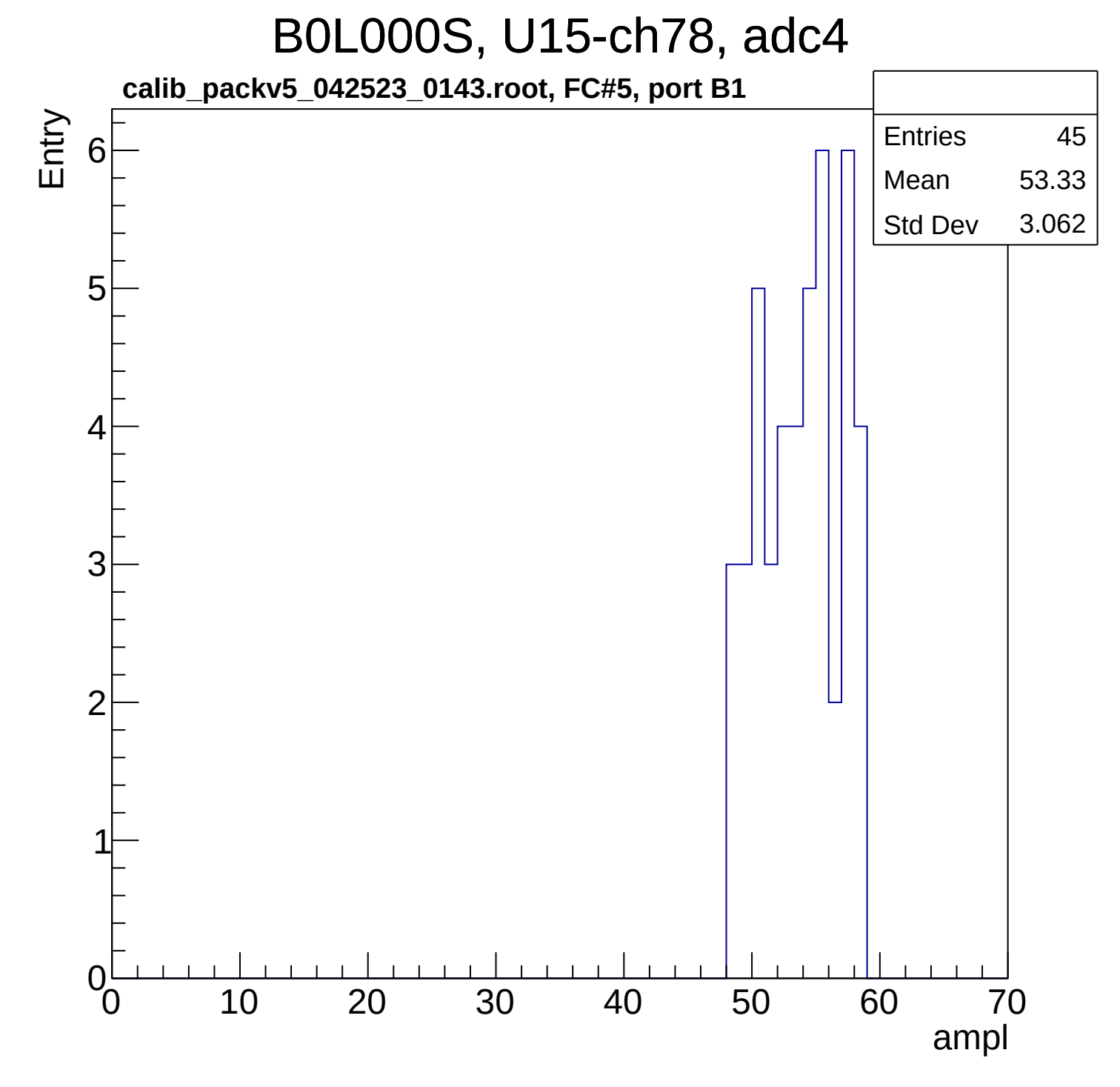
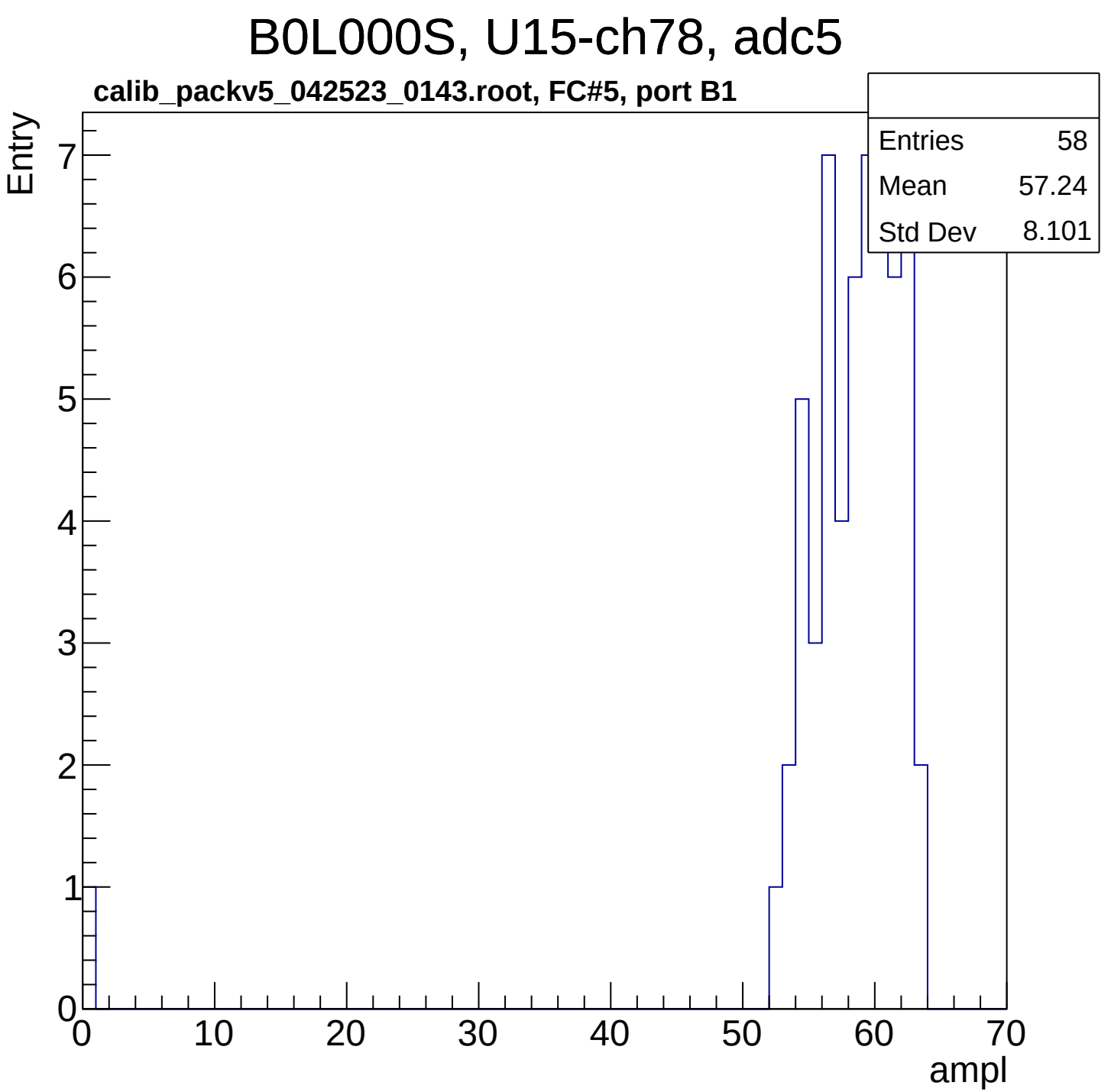
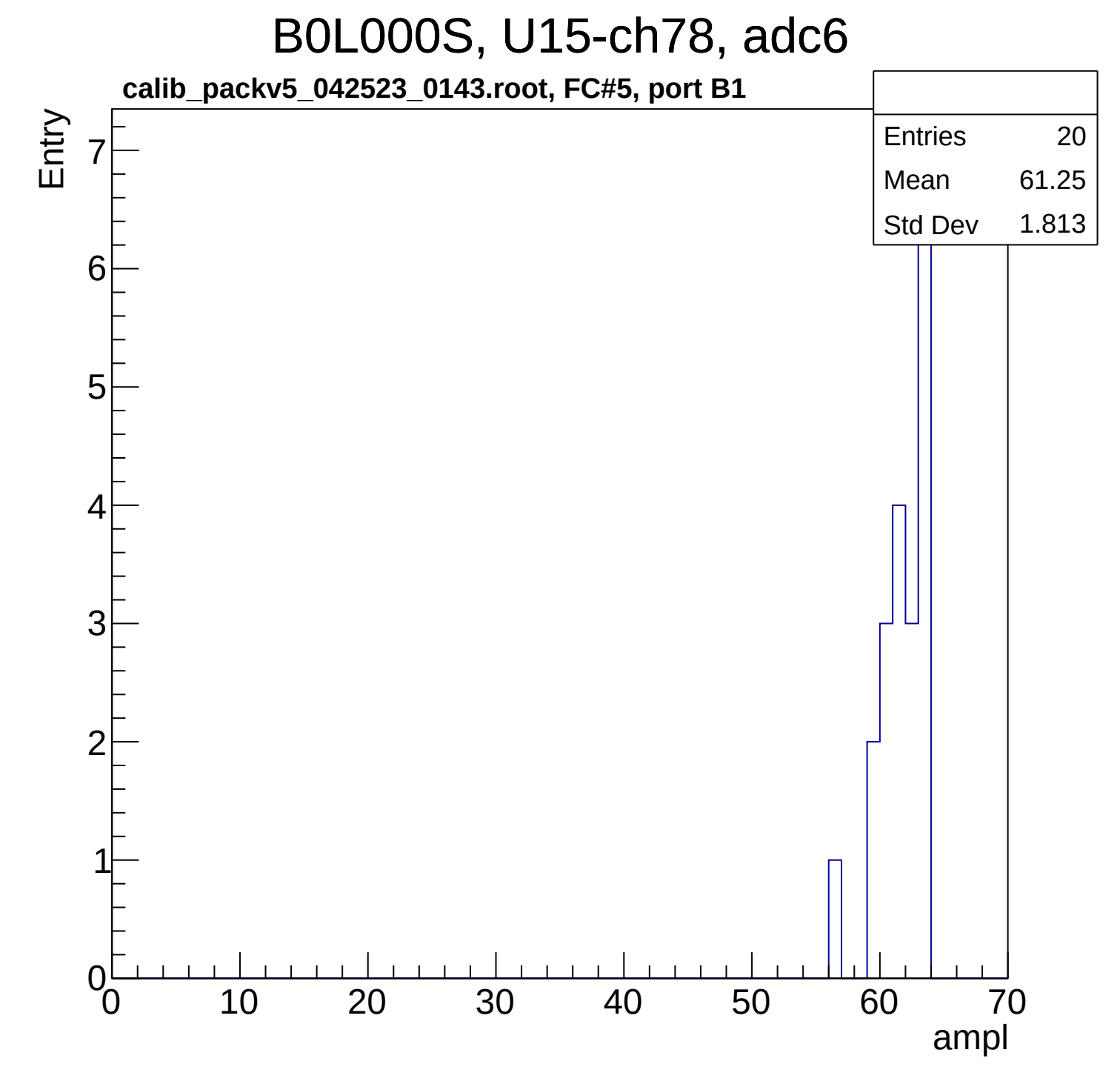
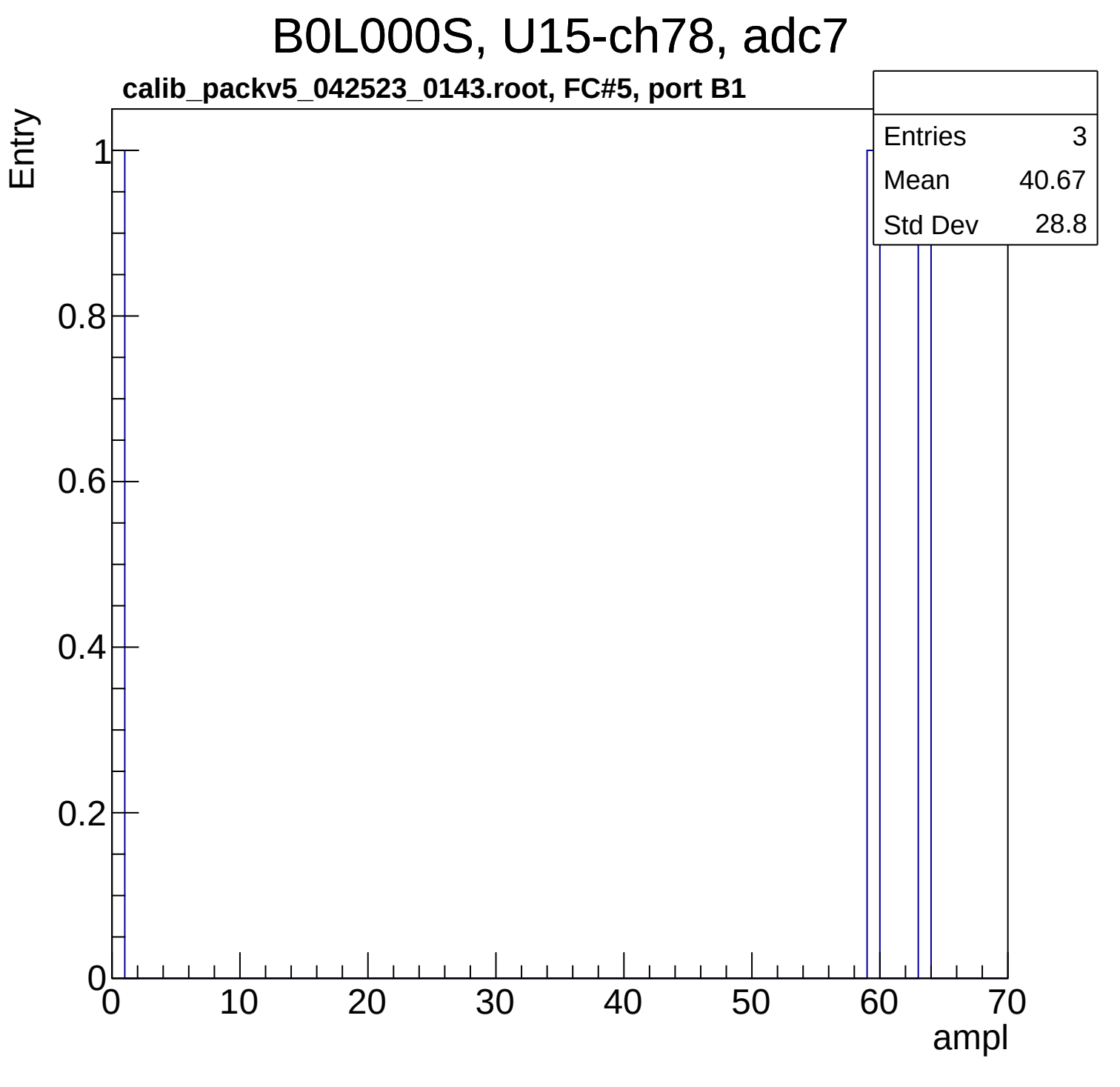

B0L000S, U15-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 39.97
Std Dev 3.414
Gaus mean : 40.3914
Gaus Width: 3.7650
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

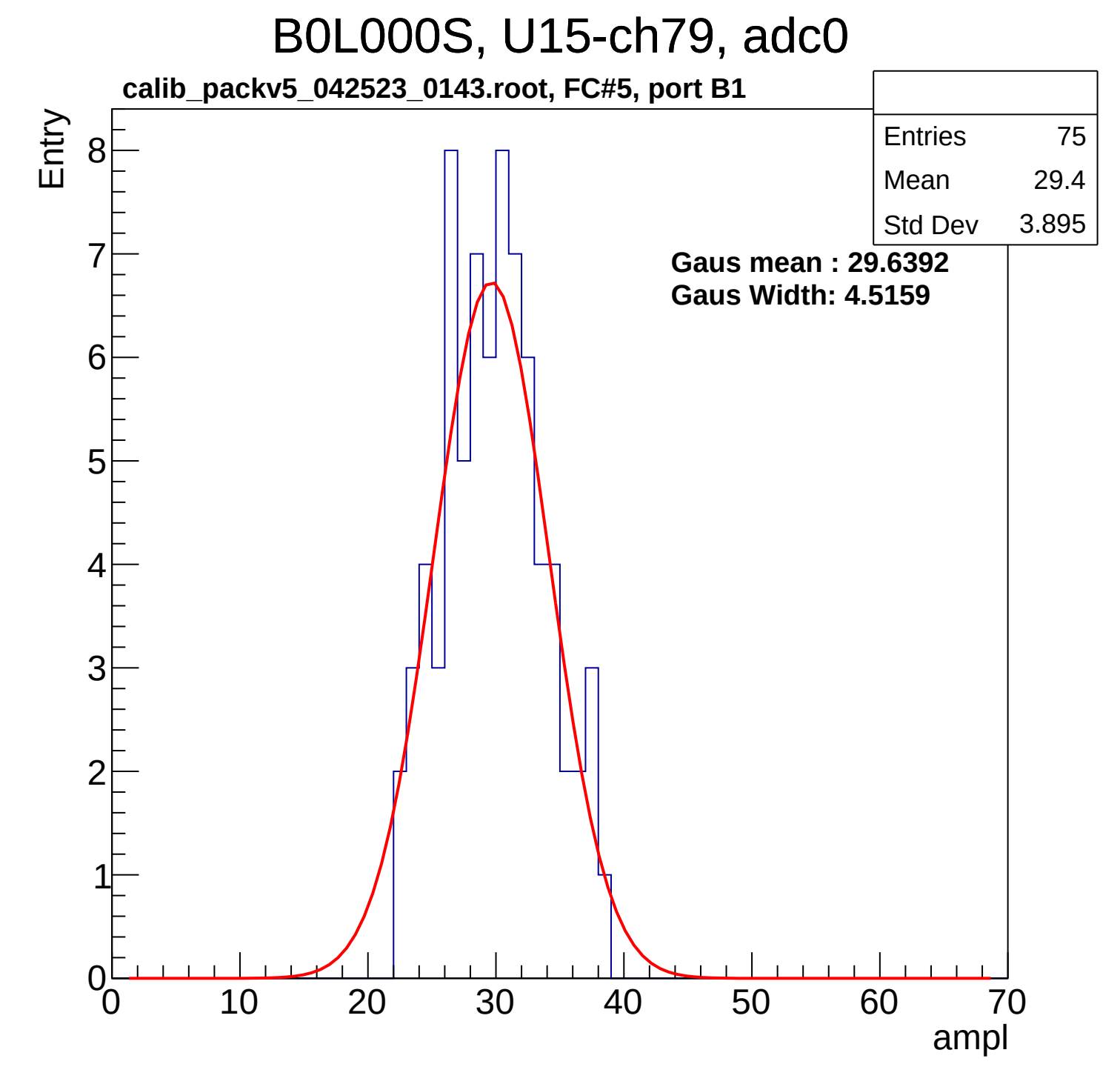

B0L000S, U15-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 47.3
Std Dev 3.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

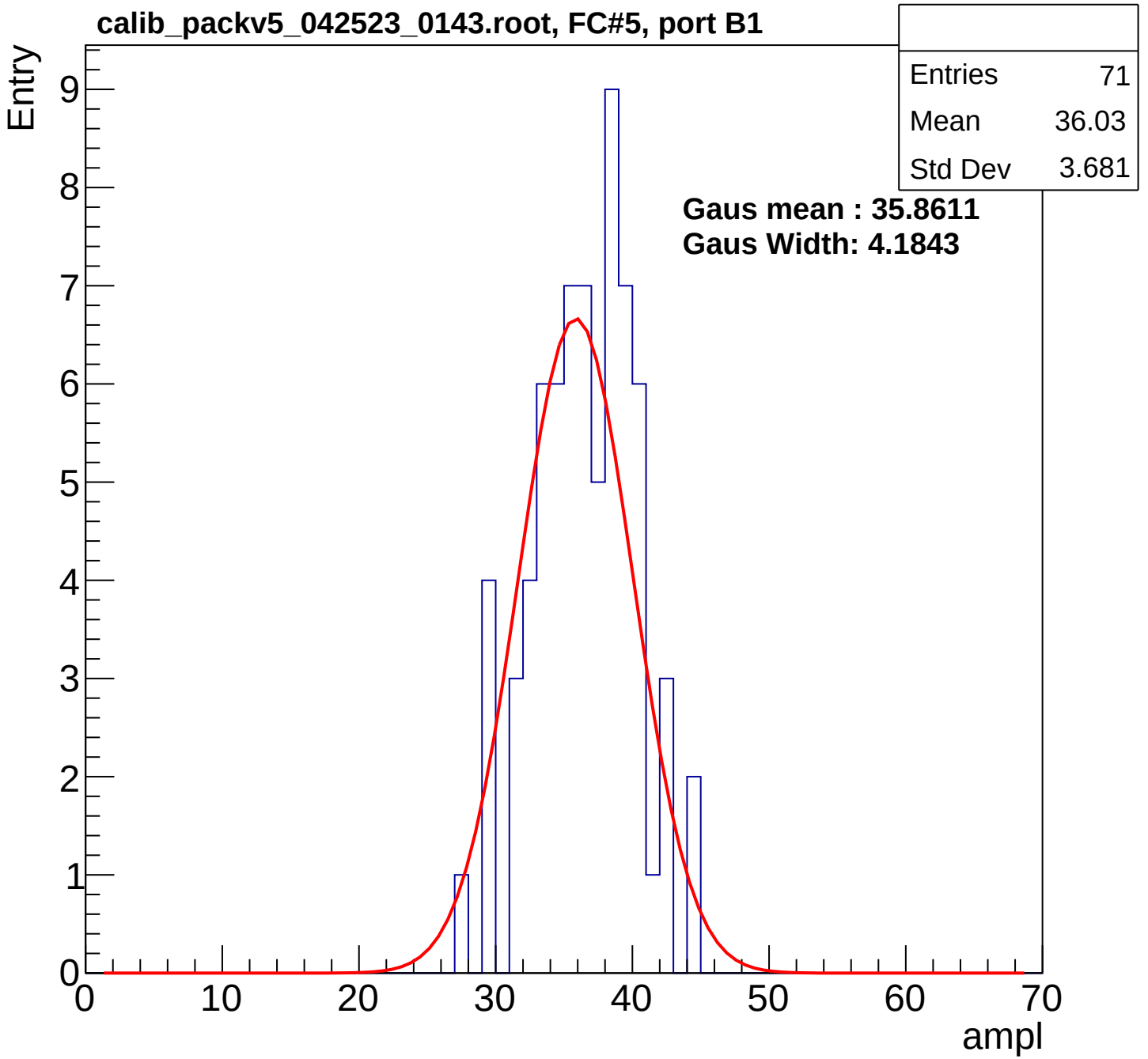

B0L000S, U15-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 53.33
Std Dev 3.062
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

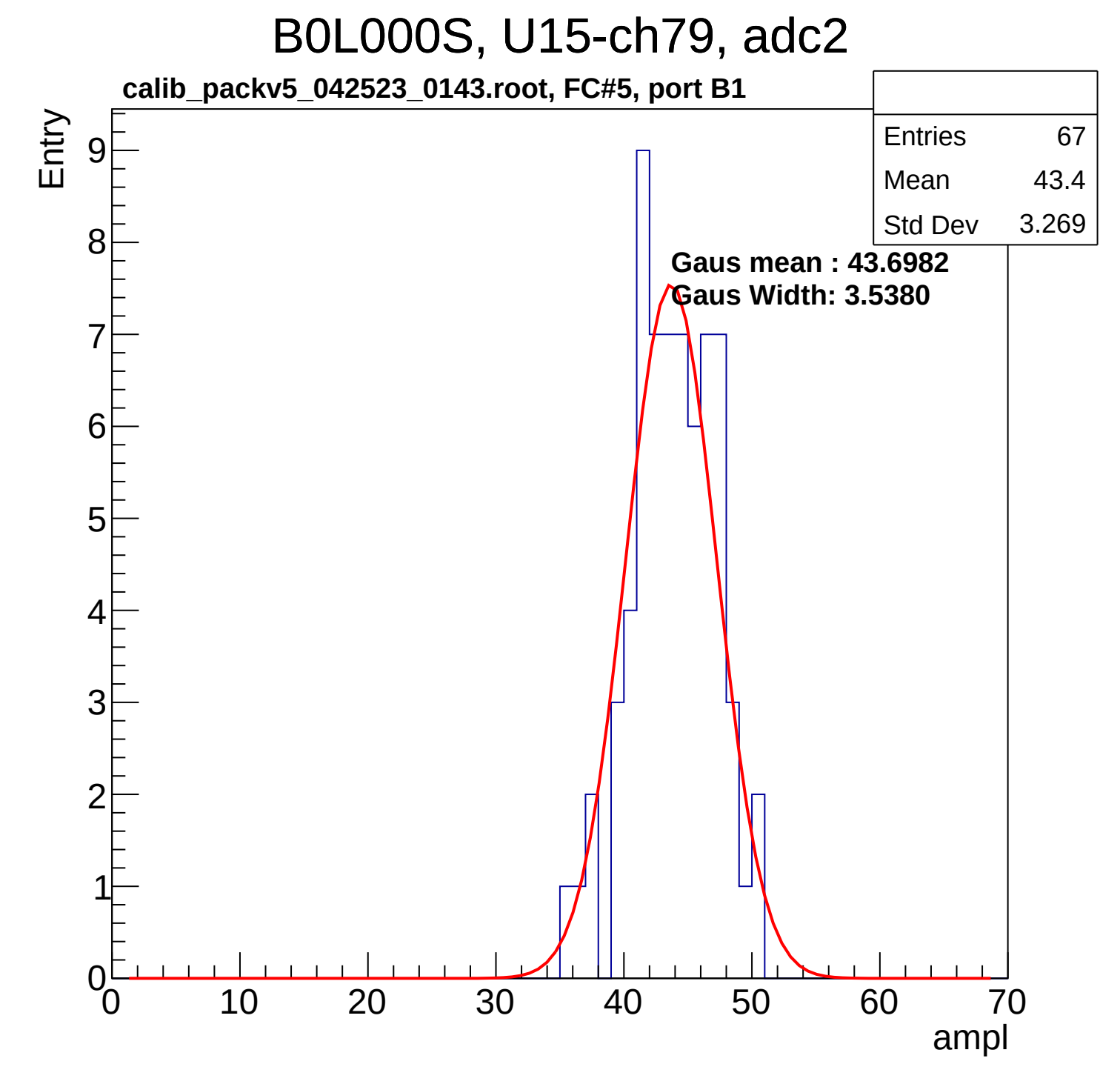

B0L000S, U15-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.24
Std Dev 8.101
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

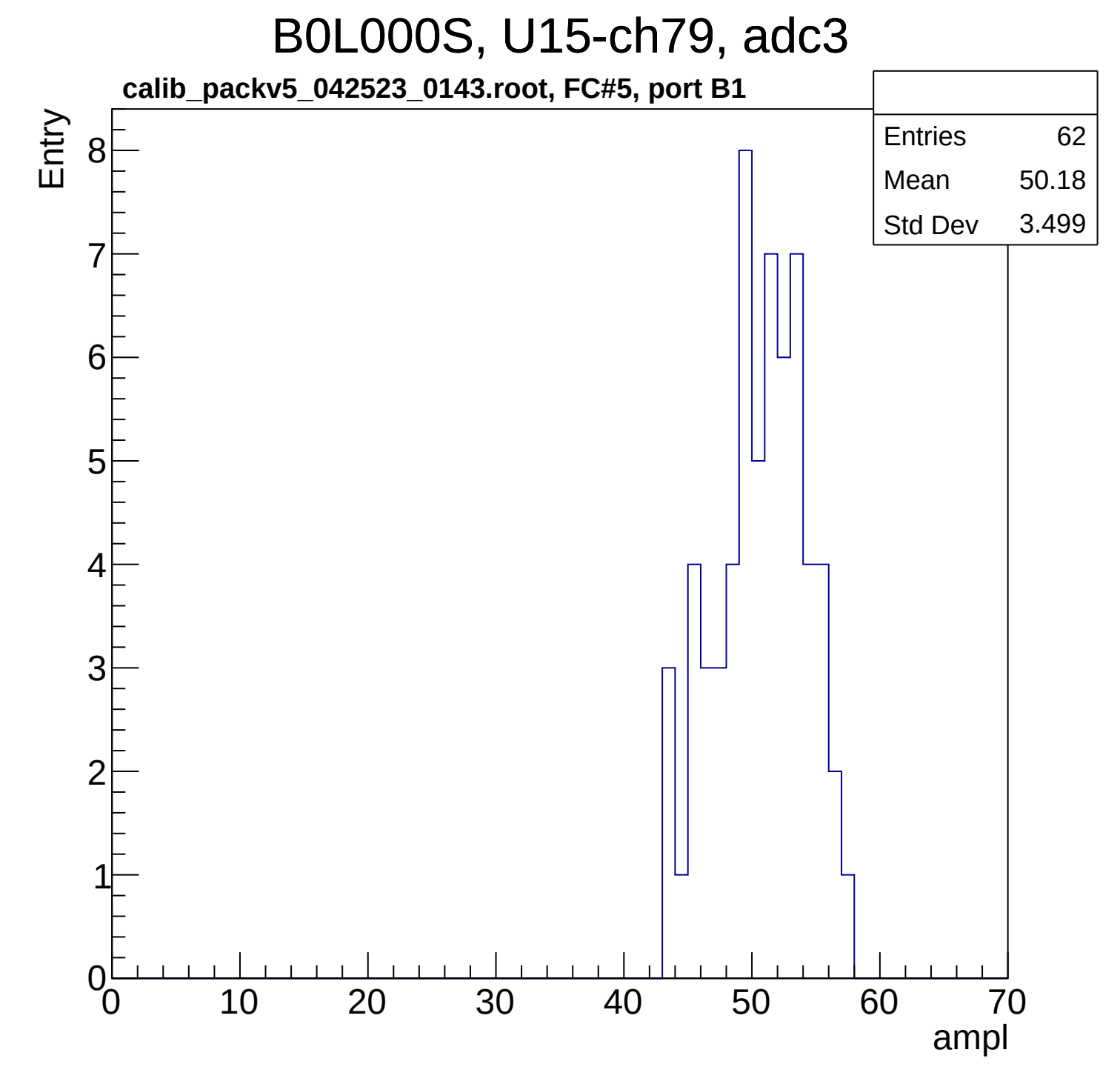

B0L000S, U15-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 61.25
Std Dev 1.813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

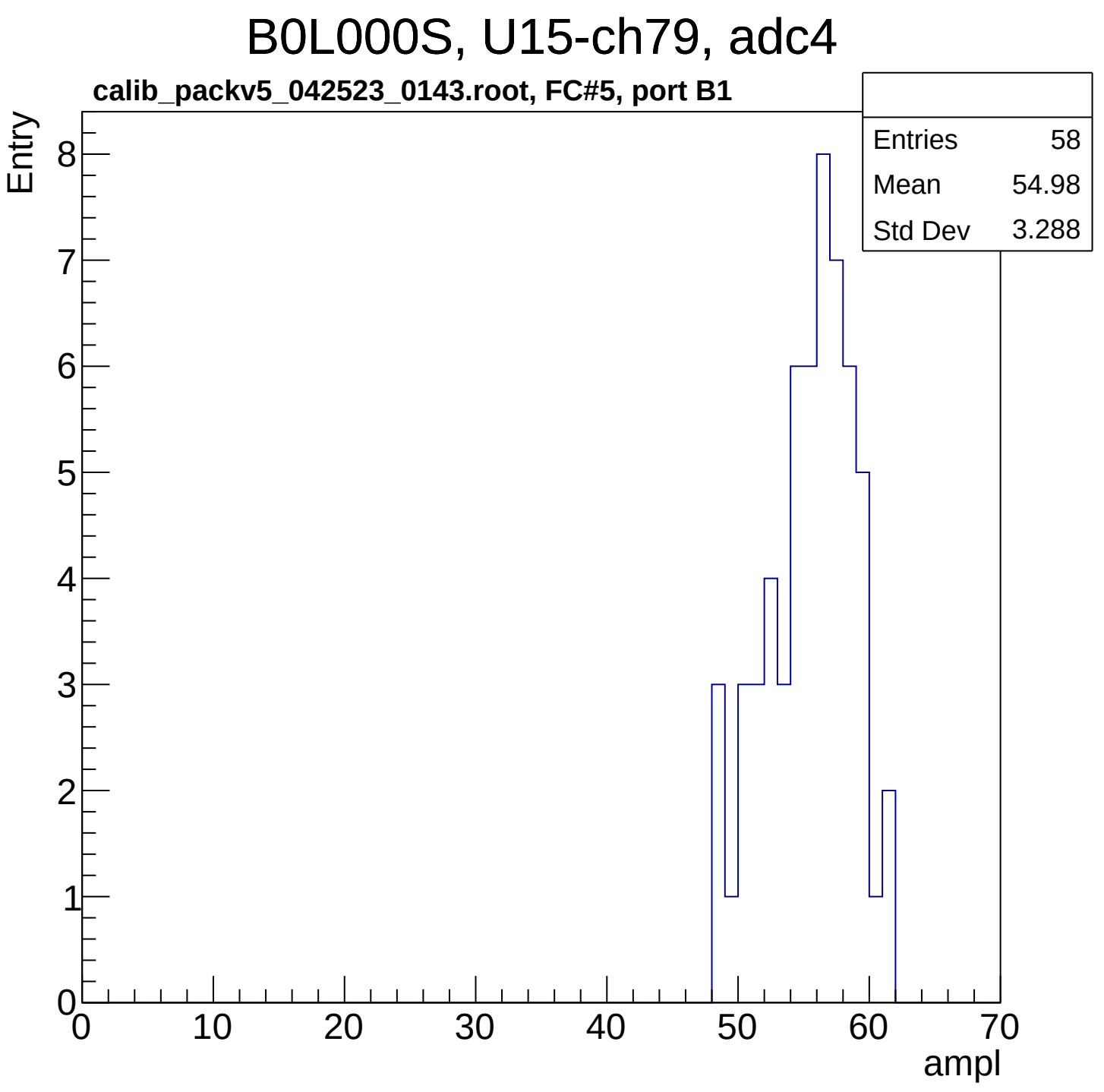

B0L000S, U15-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 40.67
Std Dev 28.8
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

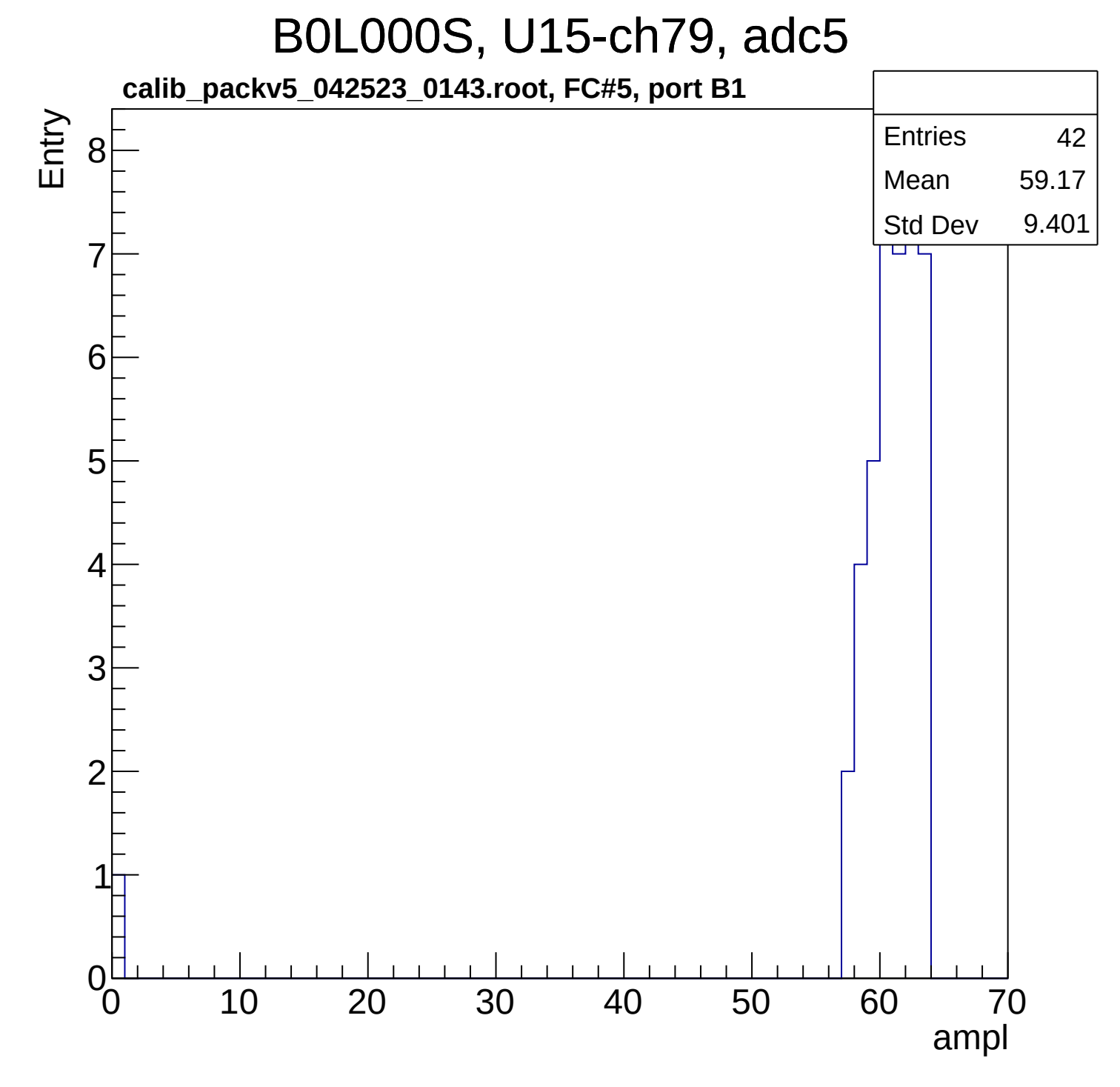

B0L000S, U15-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.4
Std Dev 3.895
Gaus mean : 29.6392
Gaus Width: 4.5159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.03
Std Dev 3.681
Gaus mean : 35.8611
Gaus Width: 4.1843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.4
Std Dev 3.269
Gaus mean : 43.6982
Gaus Width: 3.5380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.18
Std Dev 3.499
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.98
Std Dev 3.288
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.17
Std Dev 9.401
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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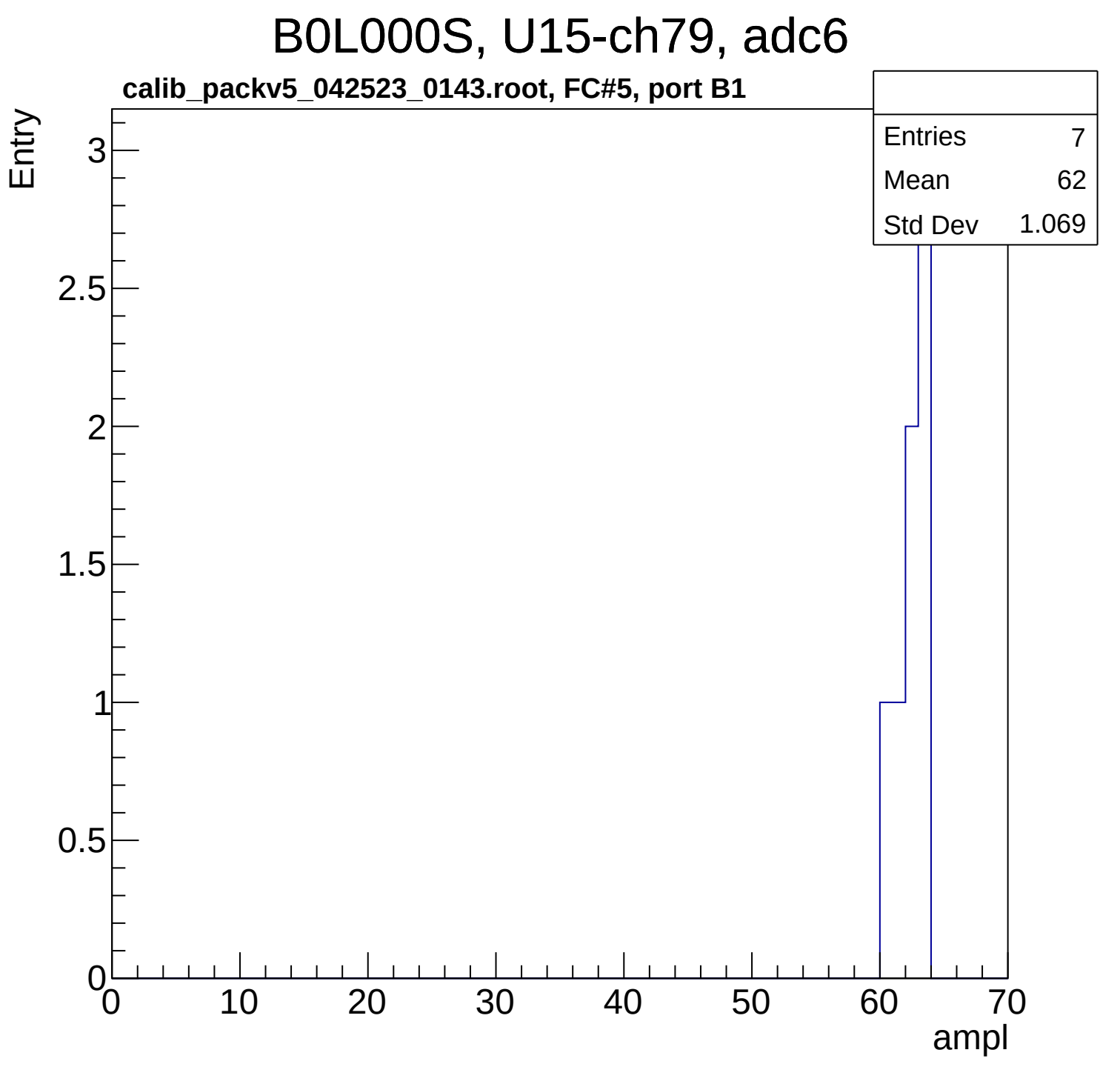
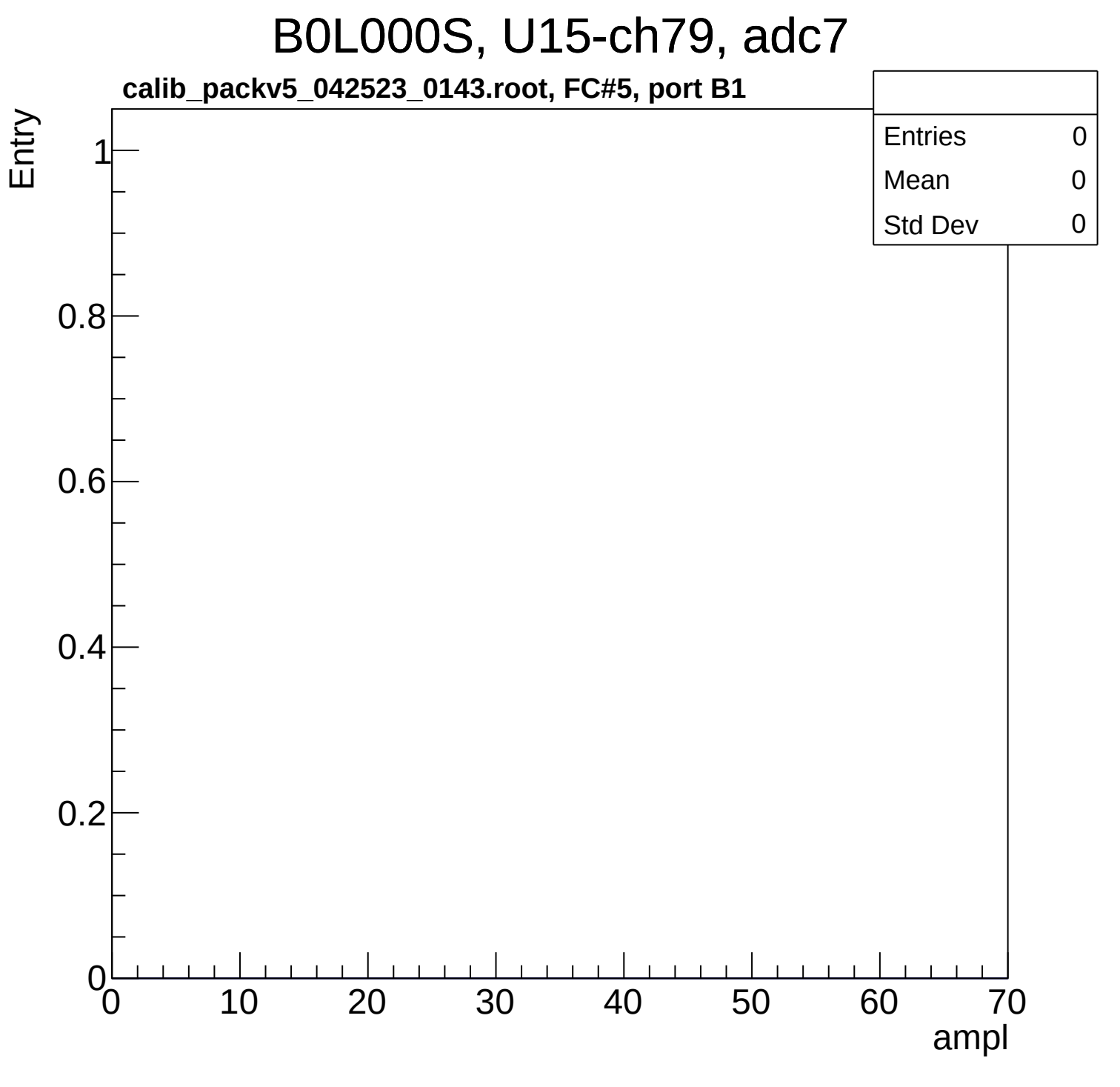
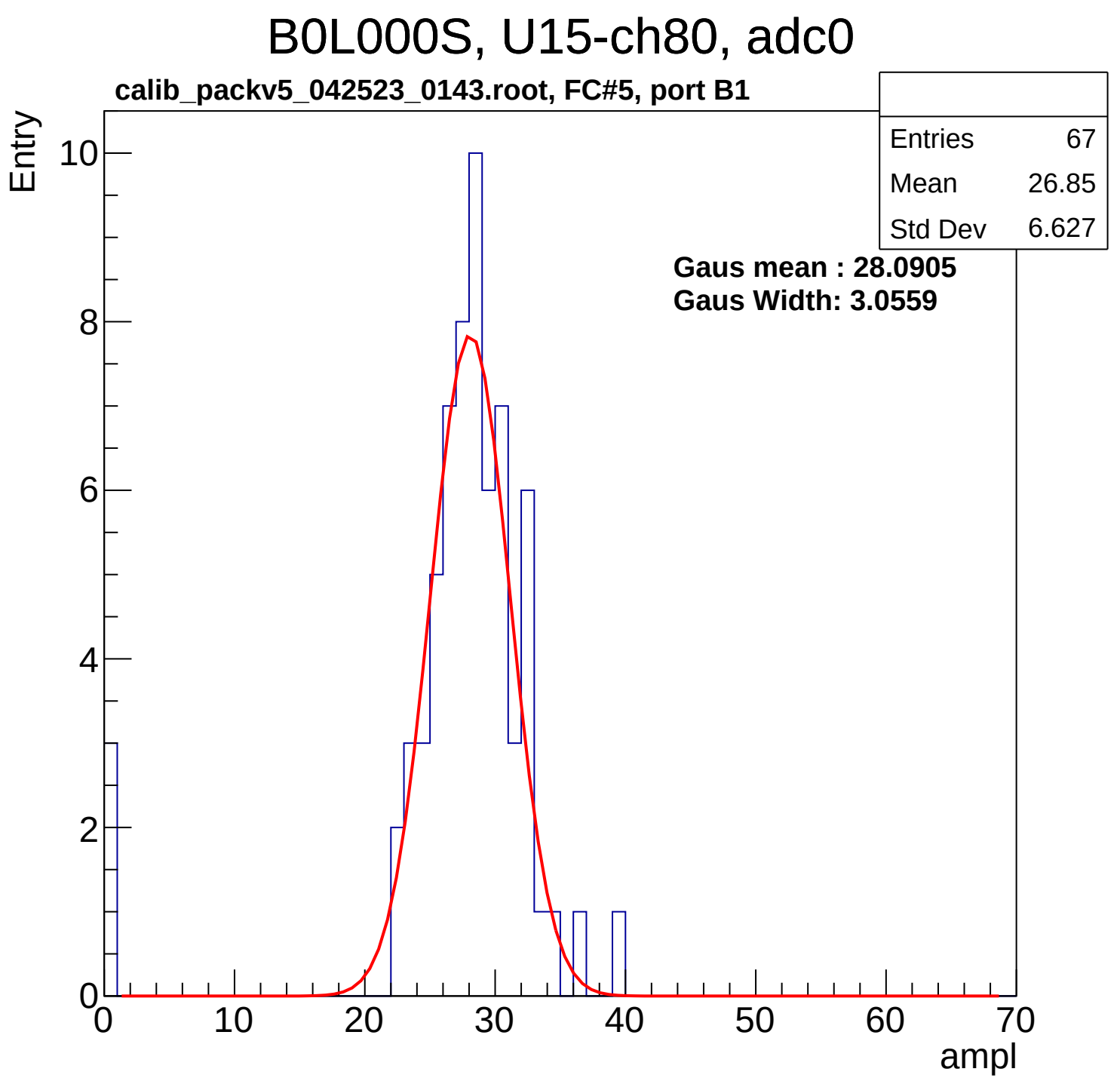
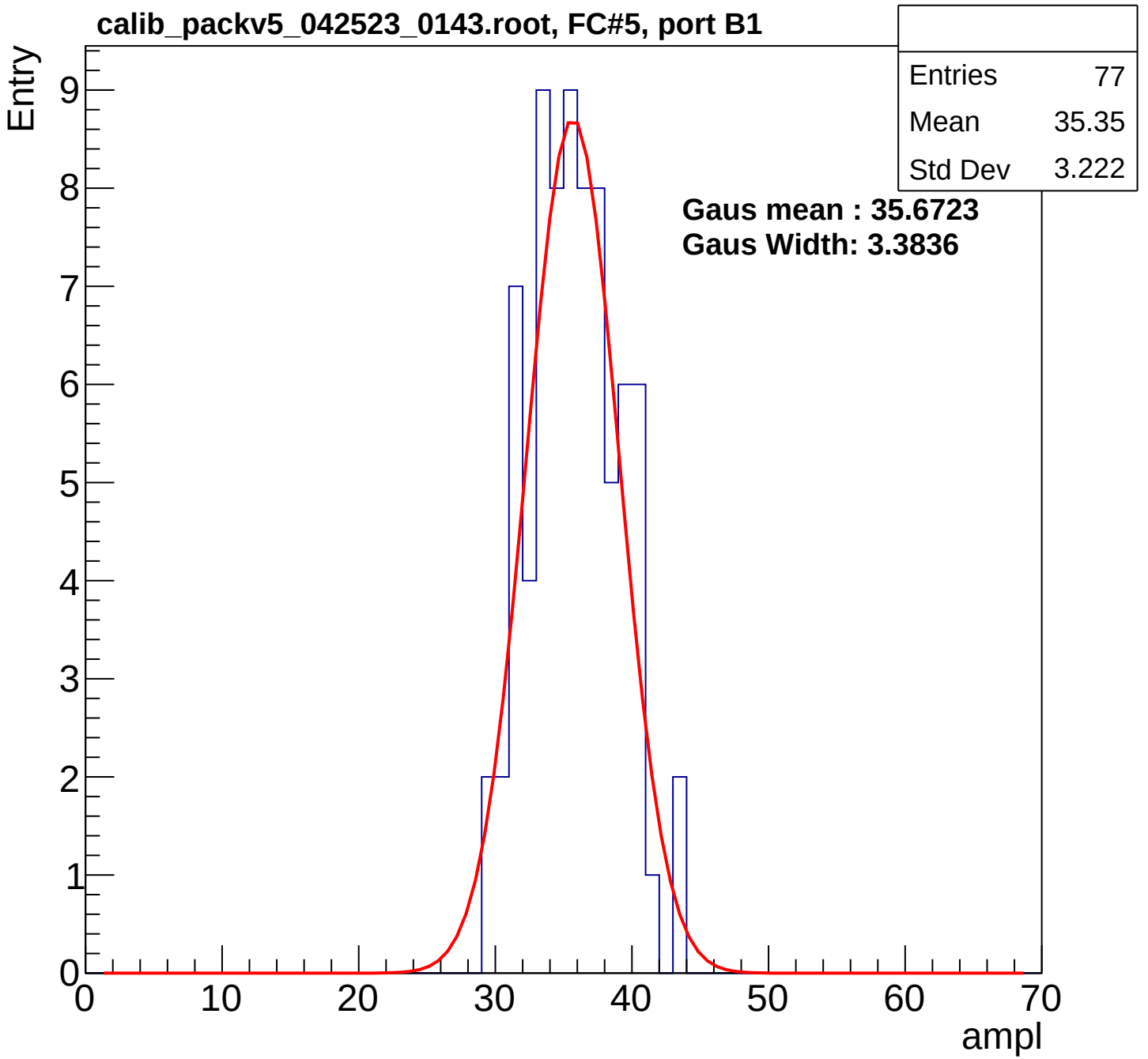
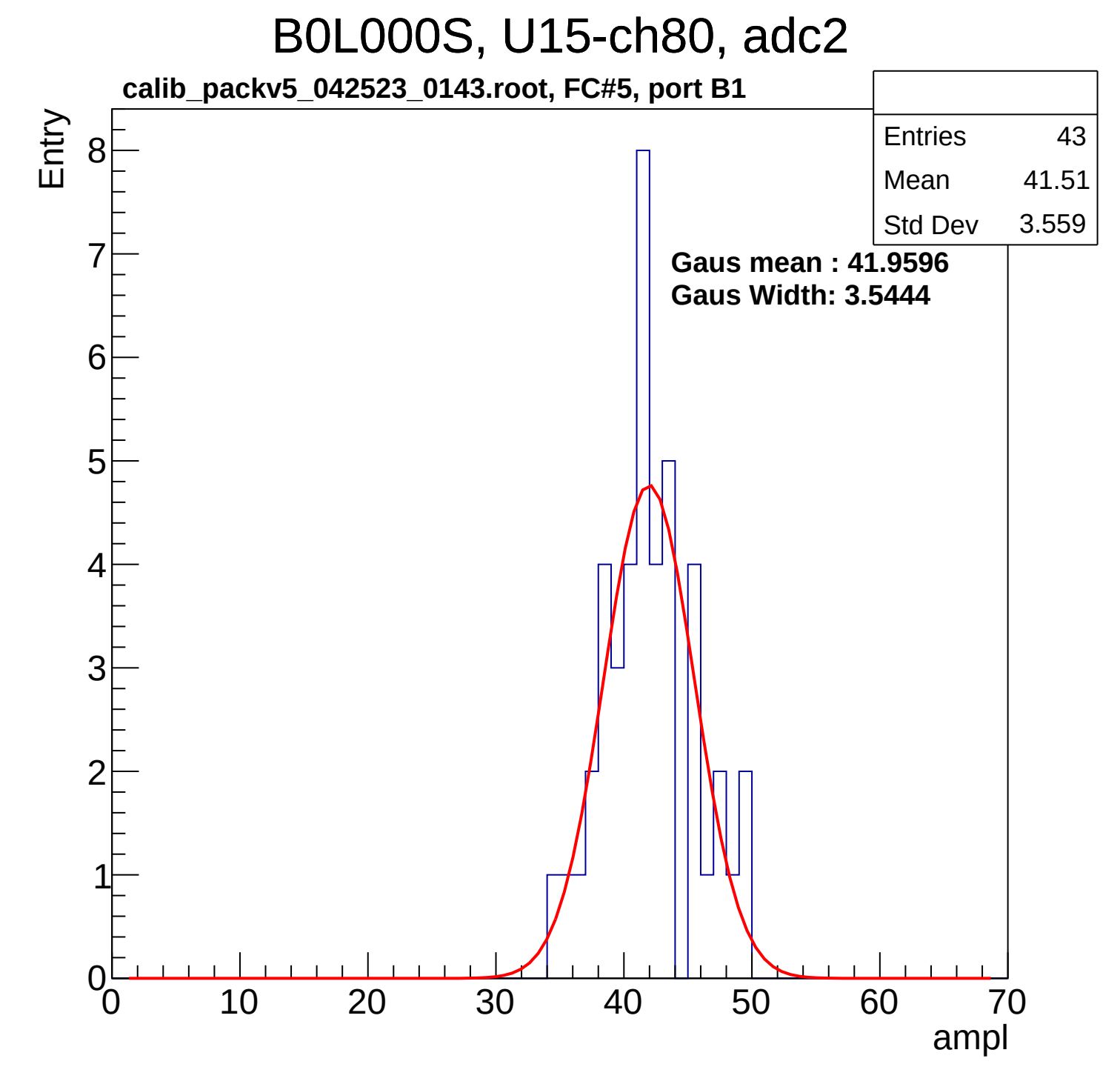
40
50
60
70


B0L000S, U15-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 1.069
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 26.85
Std Dev 6.627
Gaus mean : 28.0905
Gaus Width: 3.0559
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 35.35
Std Dev 3.222
Gaus mean : 35.6723
Gaus Width: 3.3836
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 41.51
Std Dev 3.559
Gaus mean : 41.9596
Gaus Width: 3.5444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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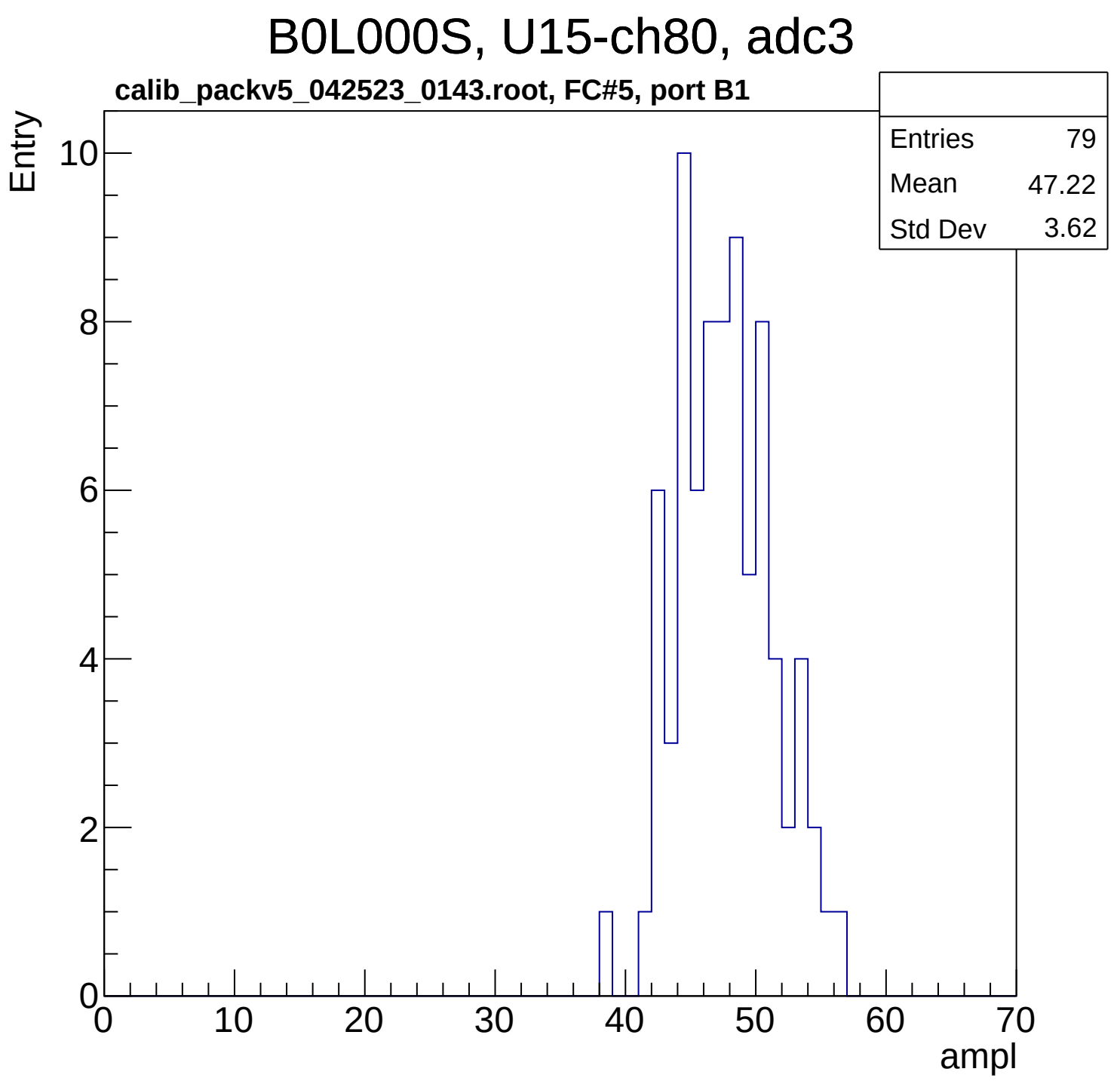
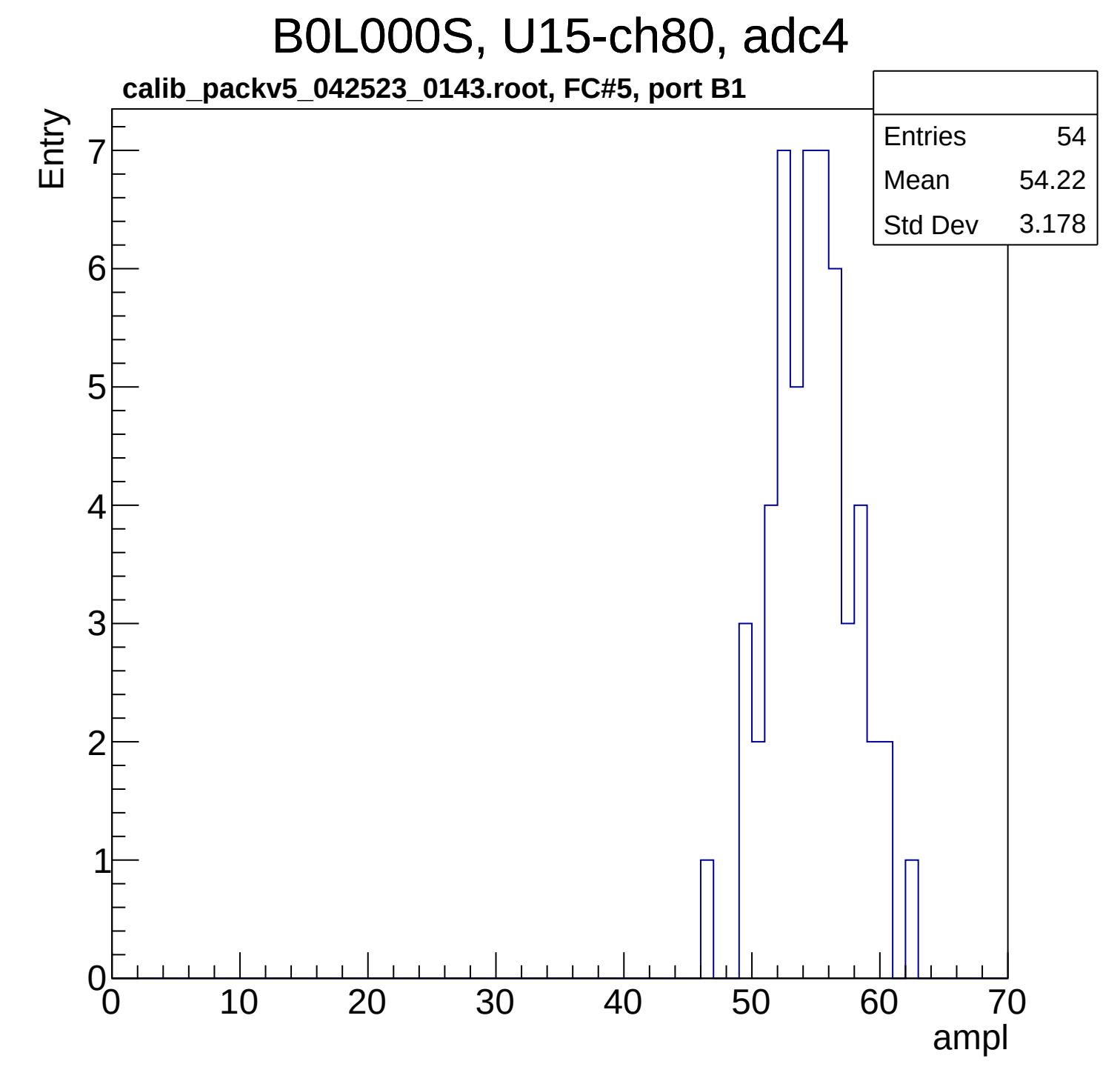
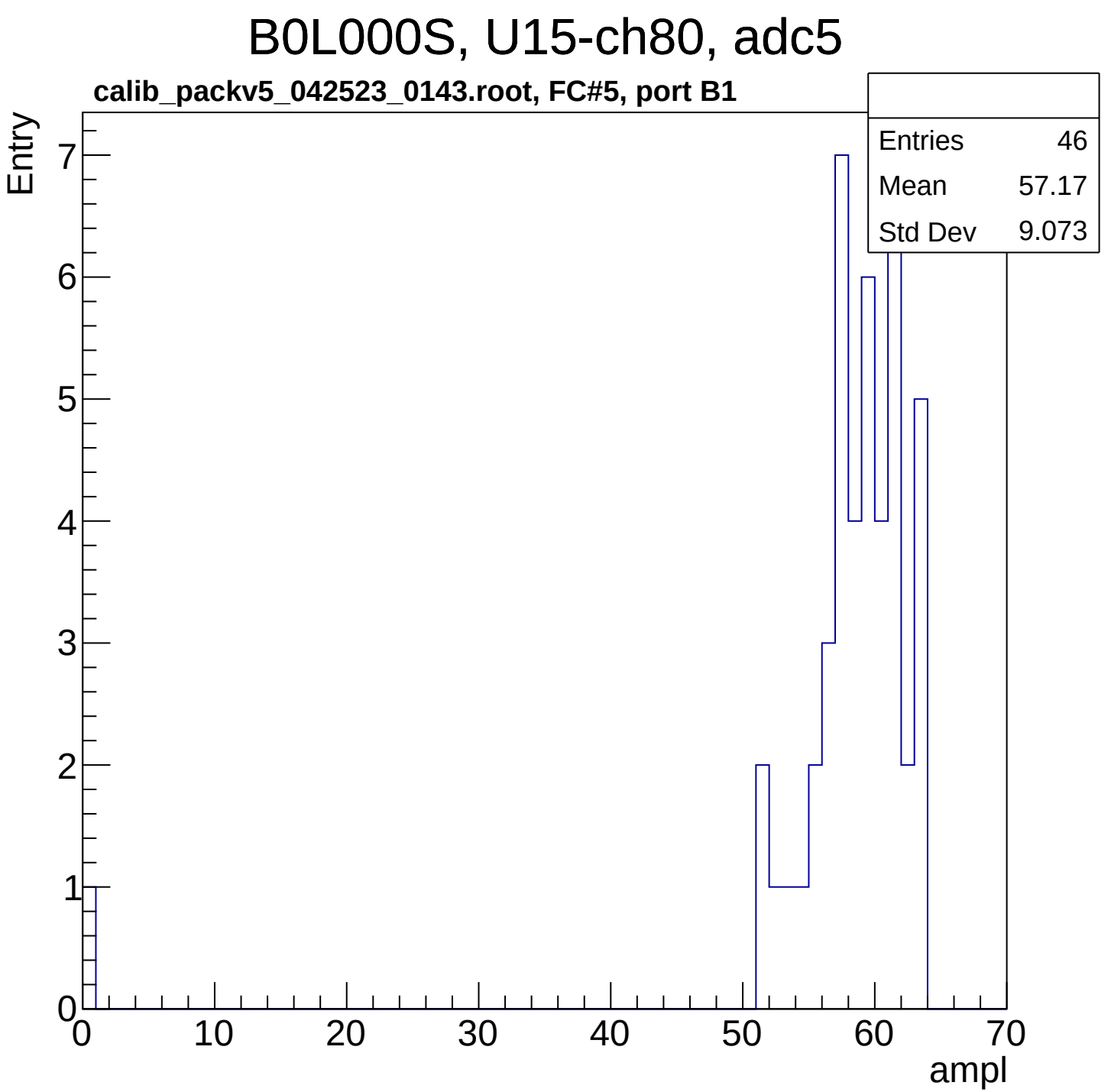
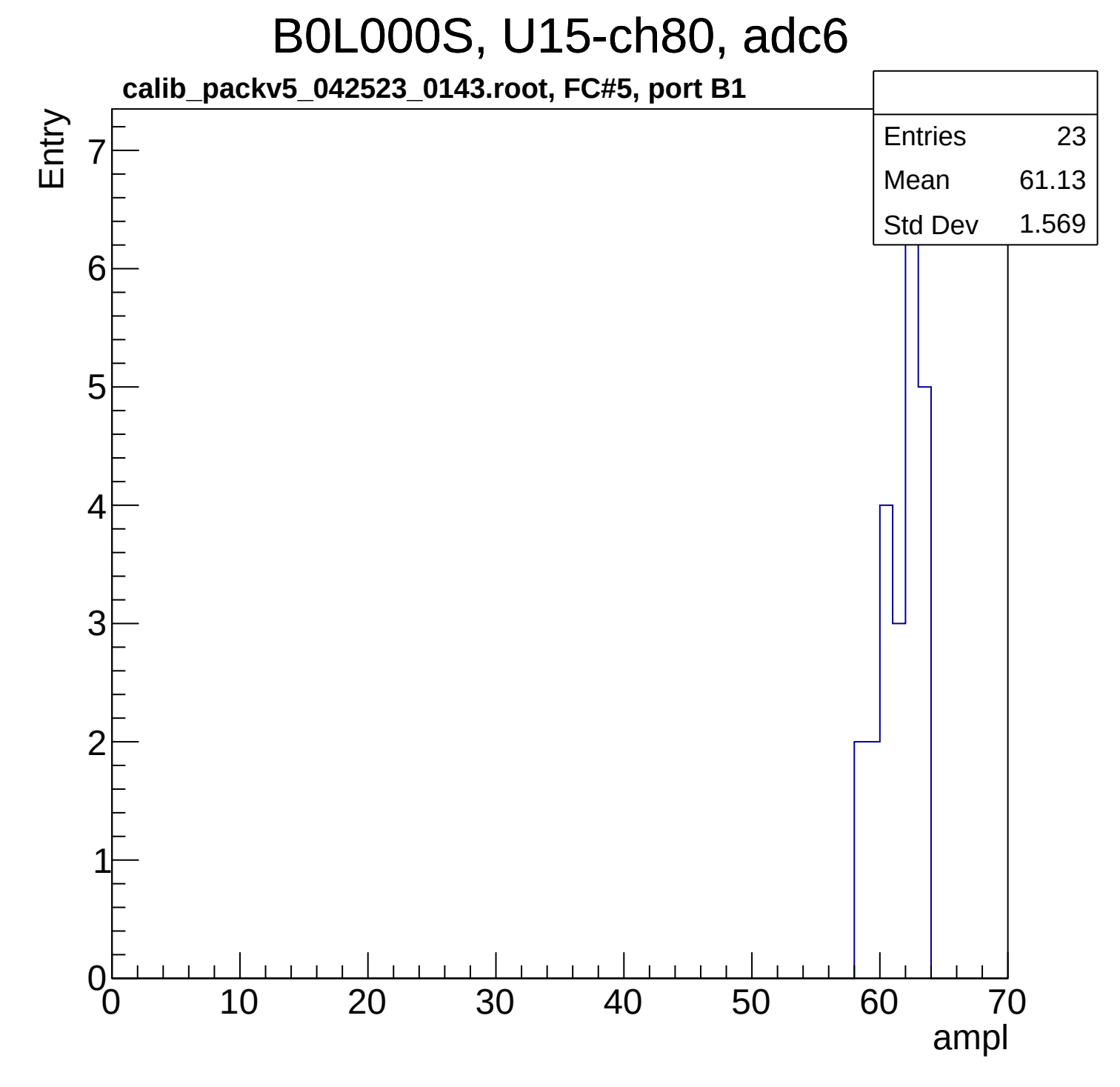
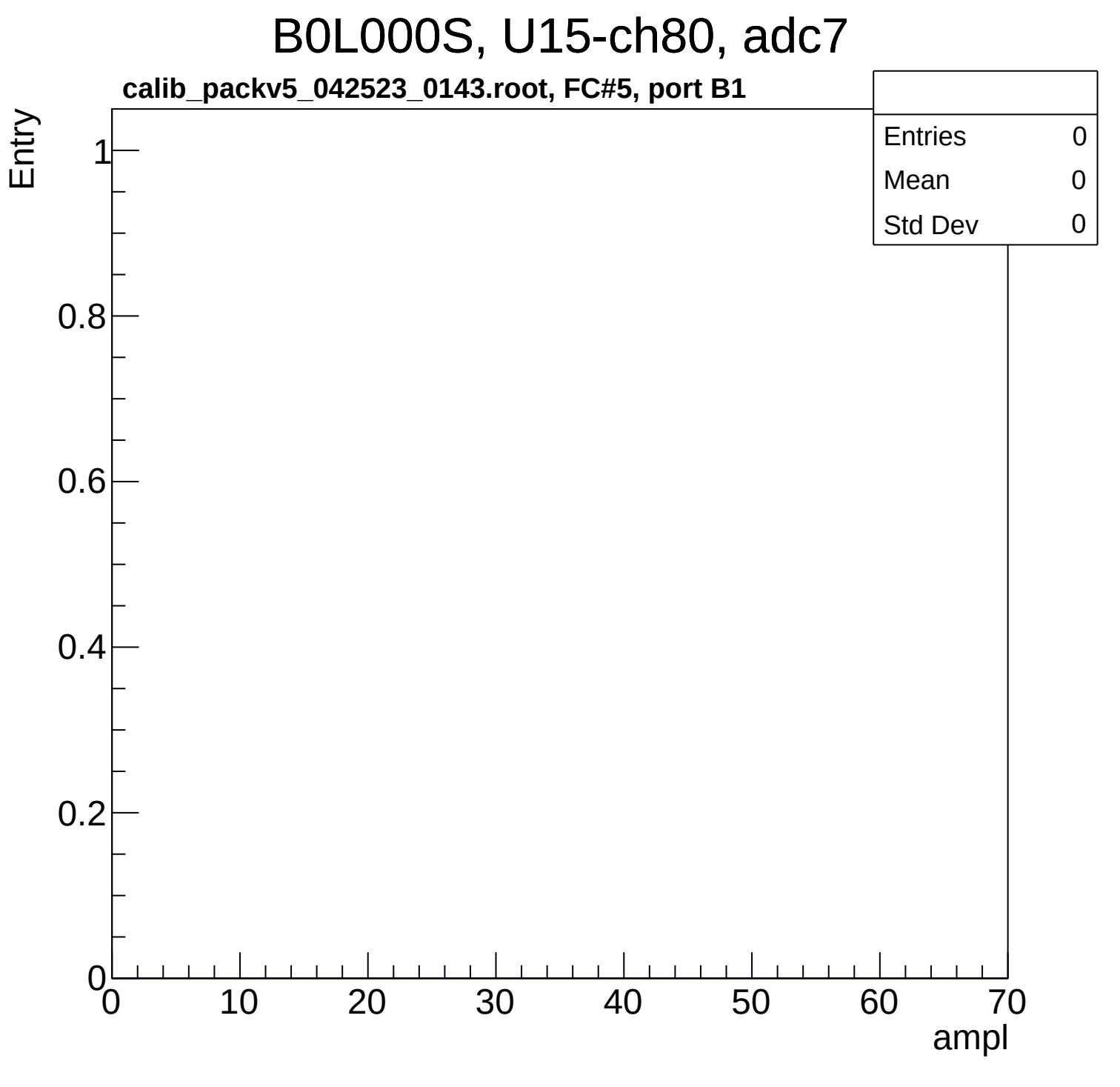

B0L000S, U15-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 47.22
Std Dev 3.62
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.22
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.17
Std Dev 9.073
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 61.13
Std Dev 1.569
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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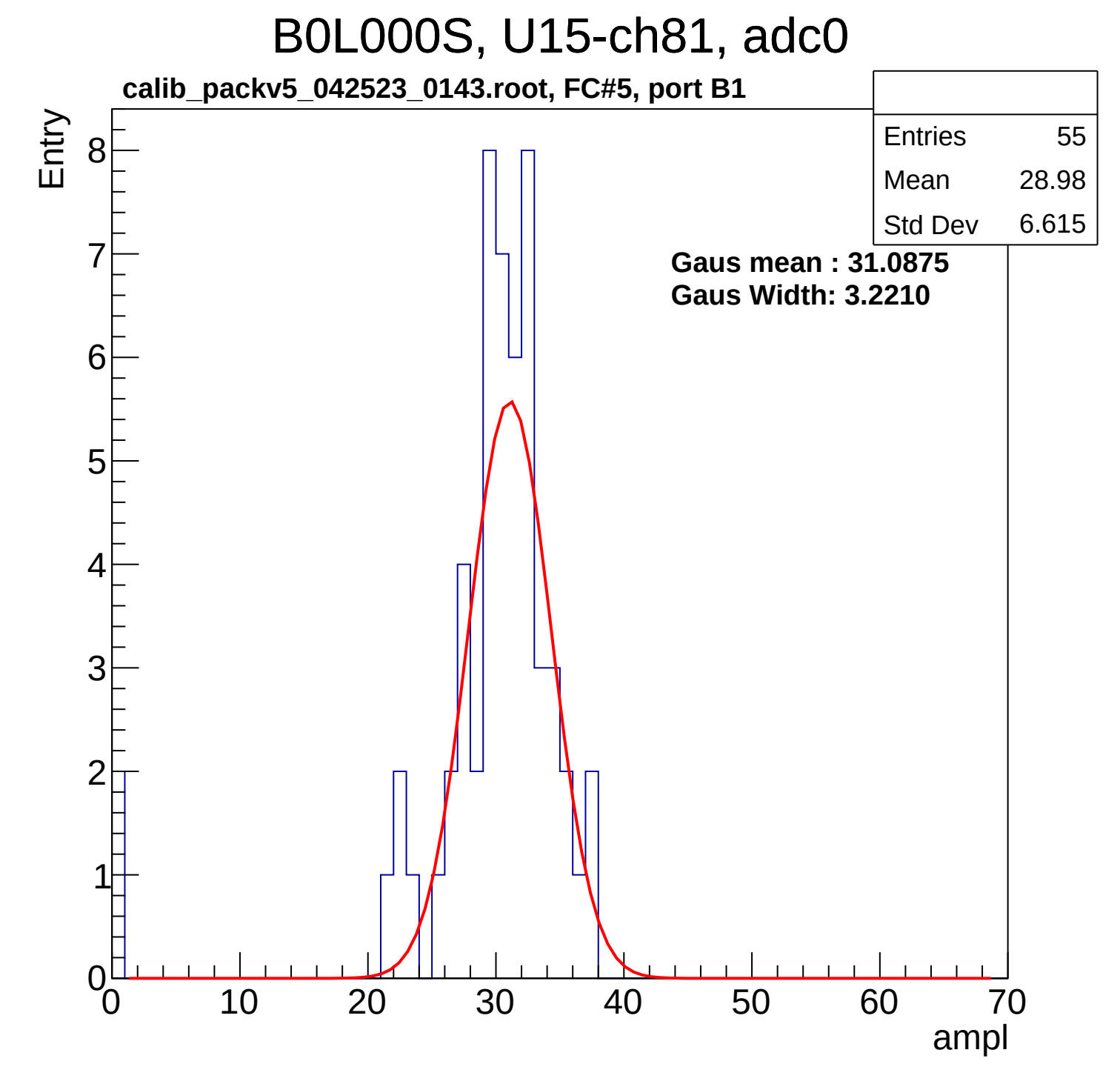
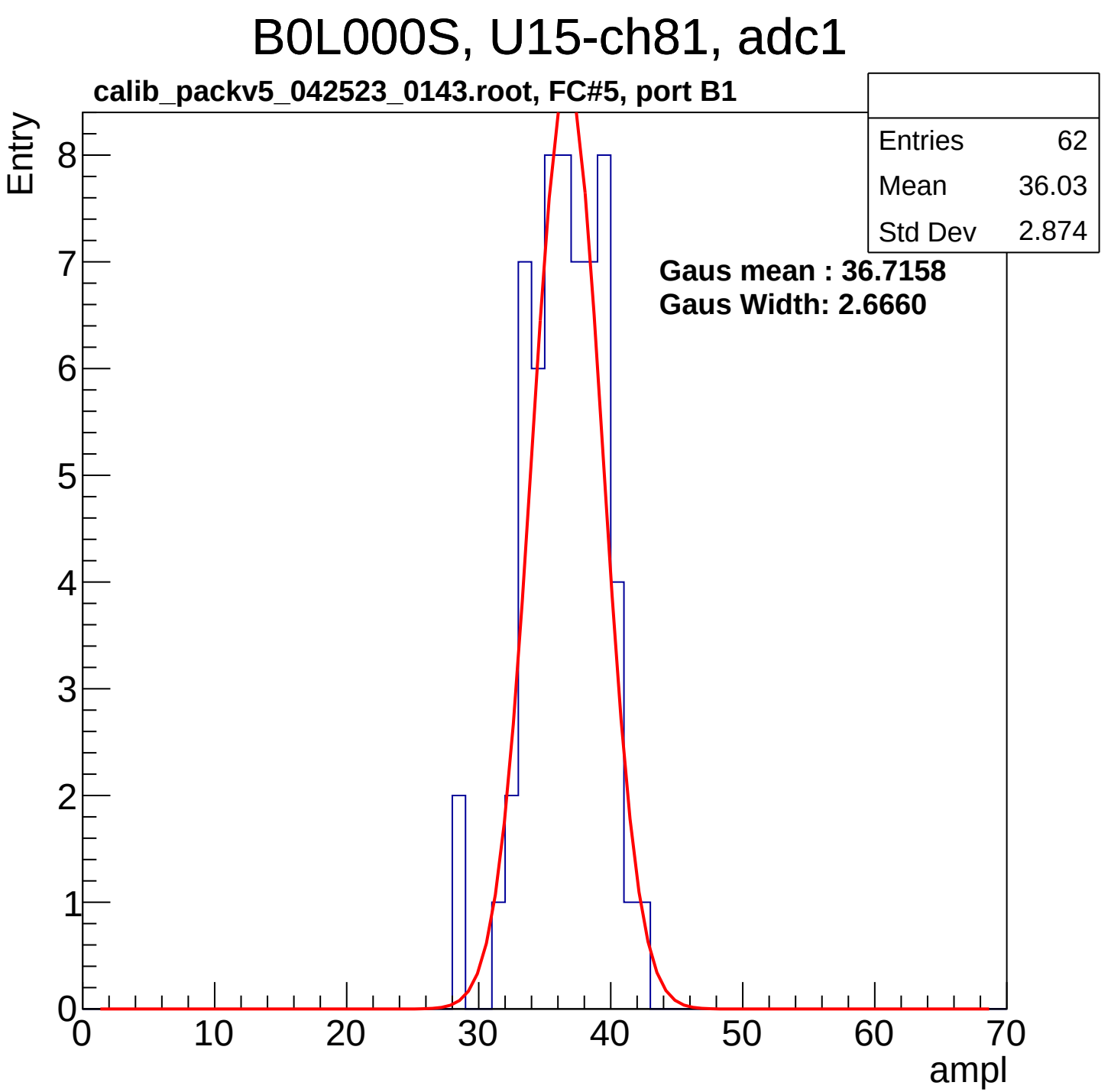
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 28.98
Std Dev 6.615
Gaus mean : 31.0875
Gaus Width: 3.2210
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.03
Std Dev 2.874
Gaus mean : 36.7158
Gaus Width: 2.6660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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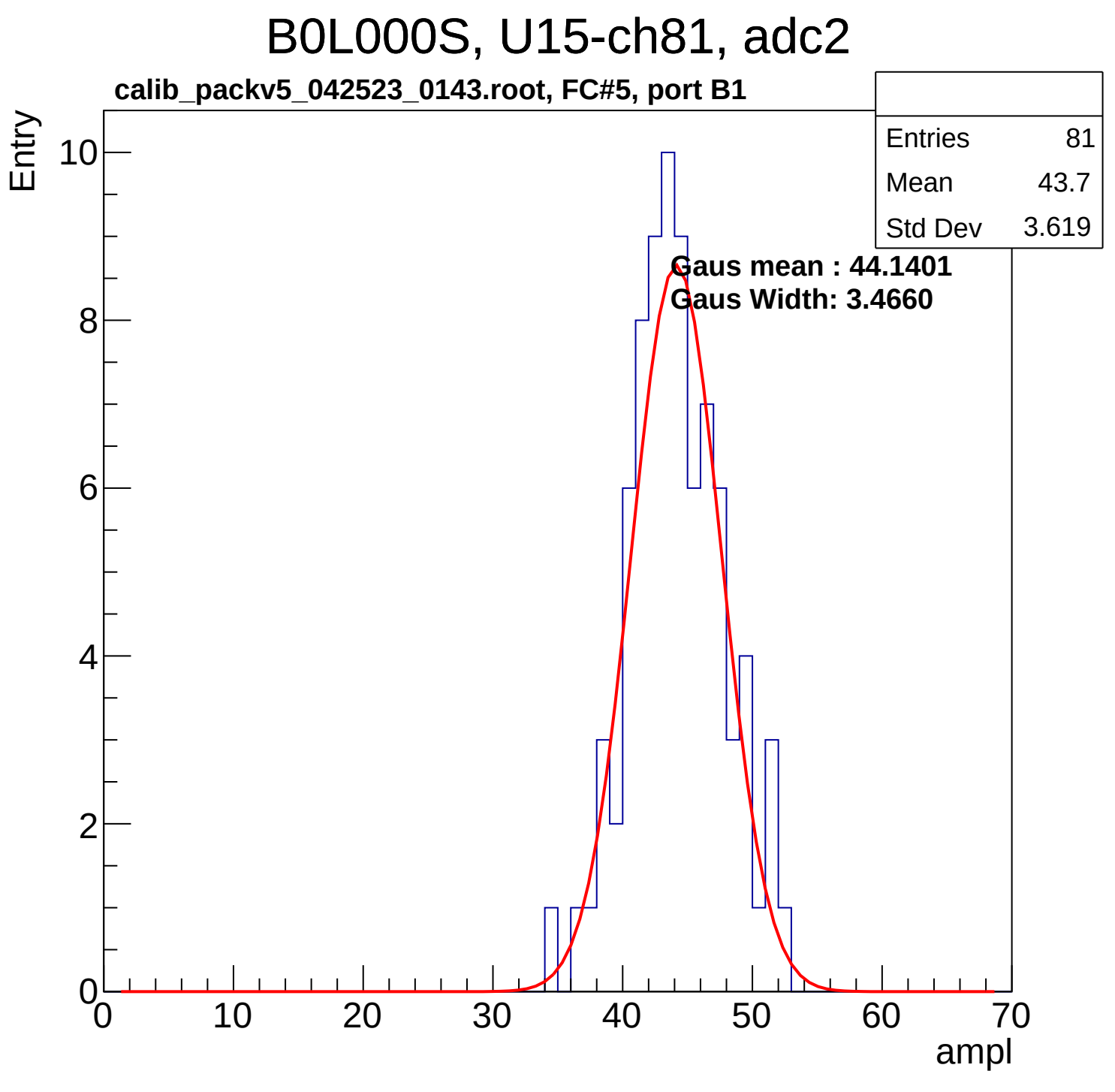
B0L000S, U15-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 43.7
Std Dev 3.619
Gaus mean : 44.1401
Gaus Width: 3.4660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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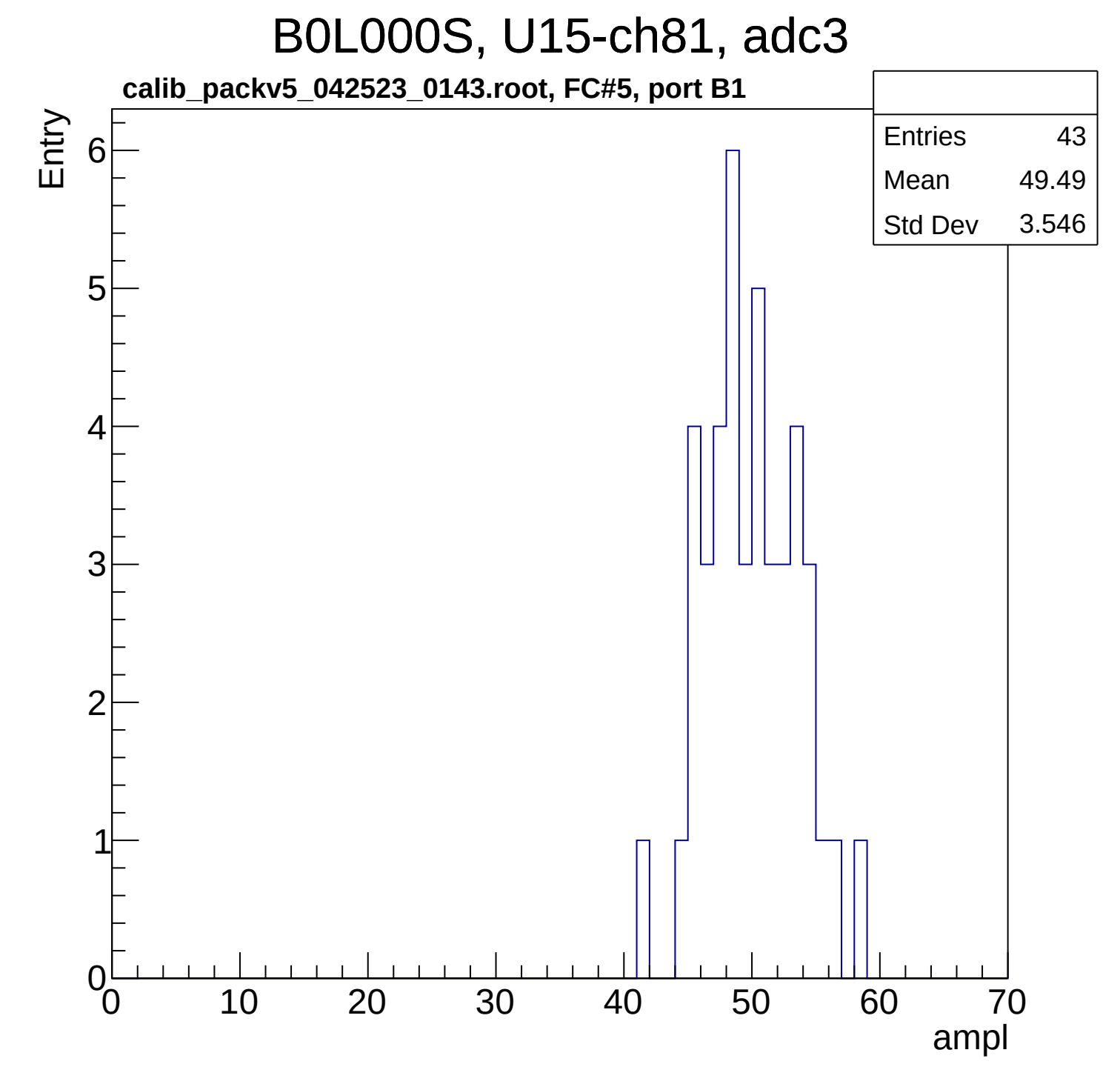
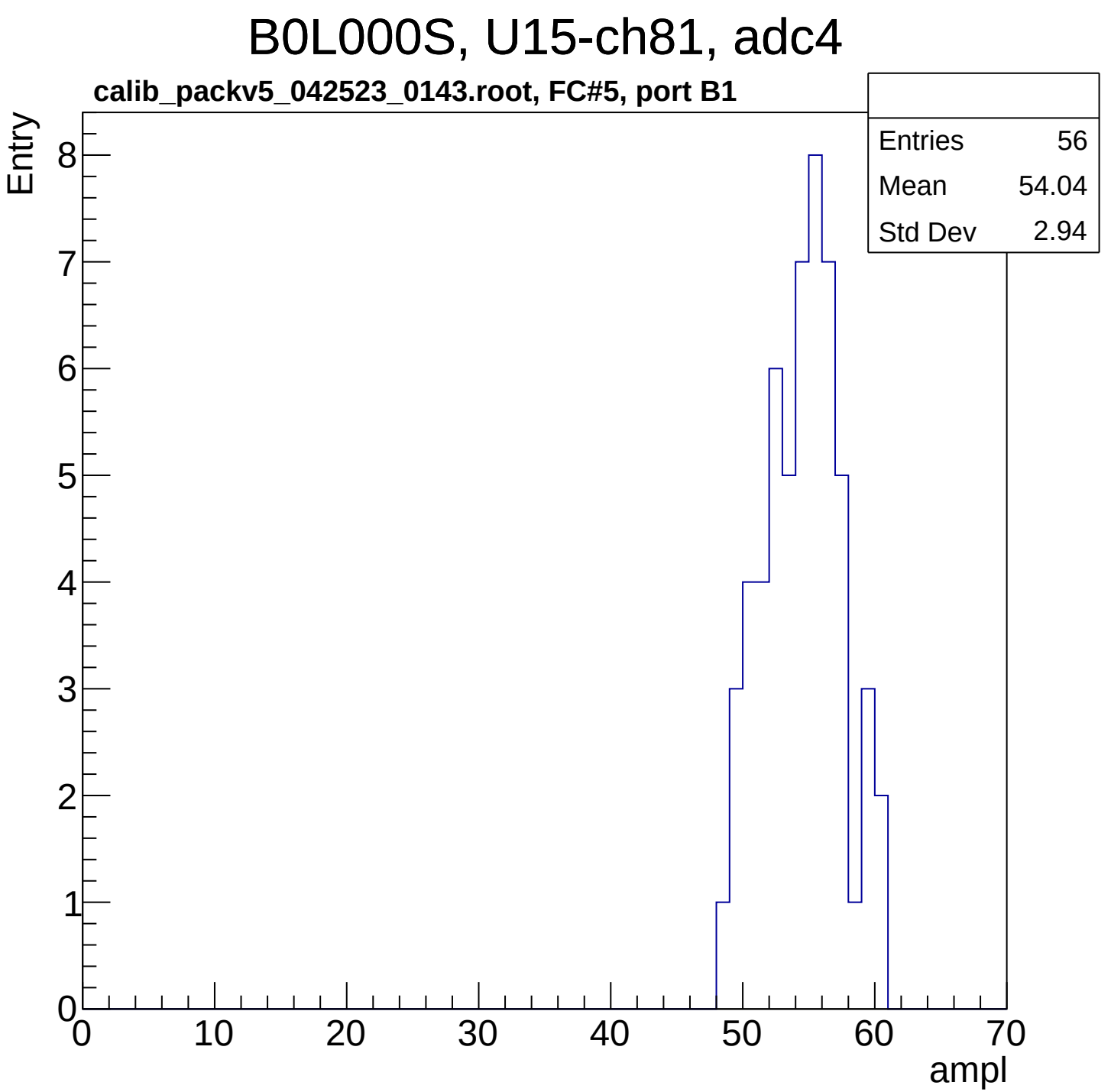
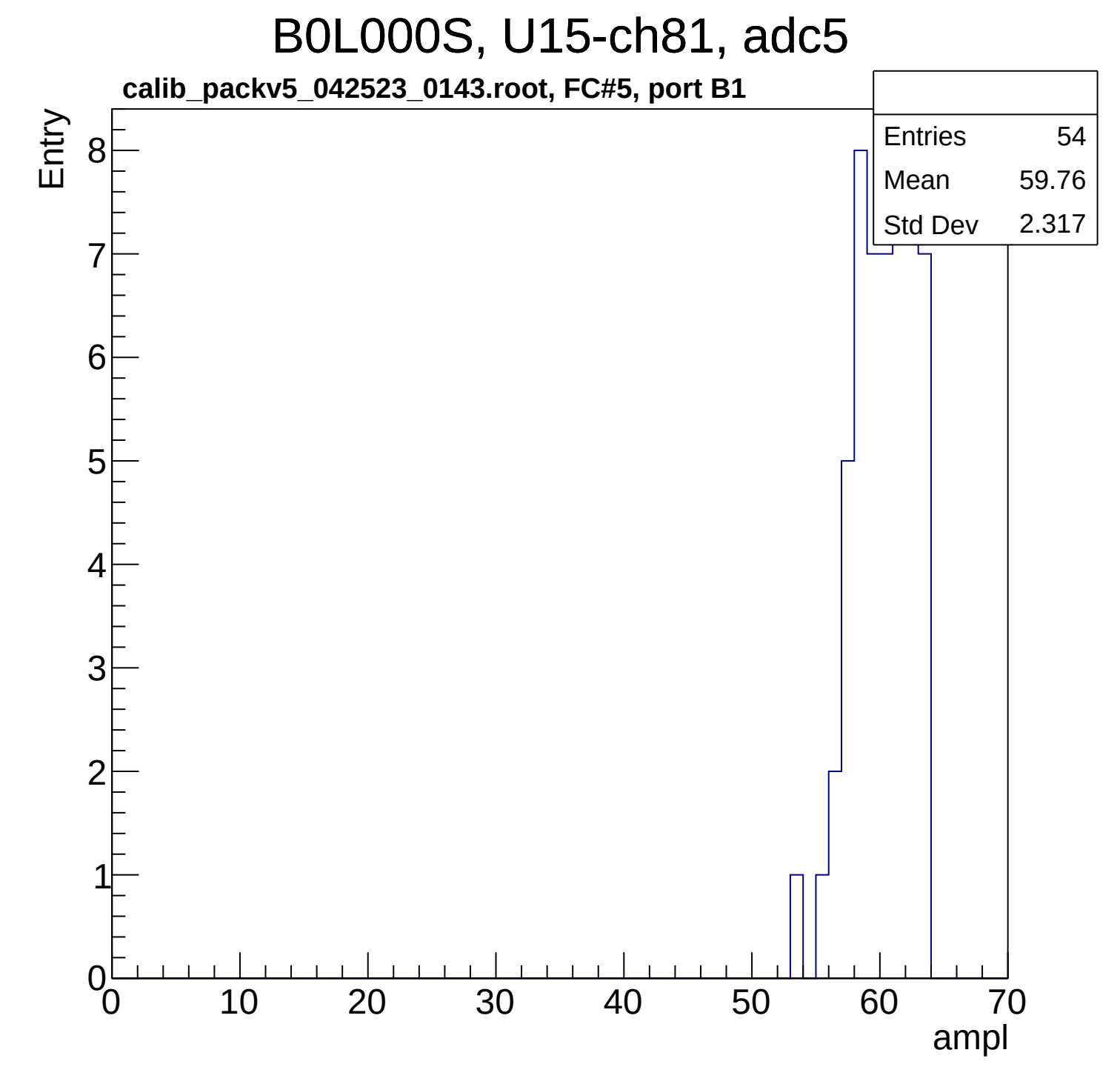

B0L000S, U15-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 49.49
Std Dev 3.546
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.04
Std Dev 2.94
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.76
Std Dev 2.317
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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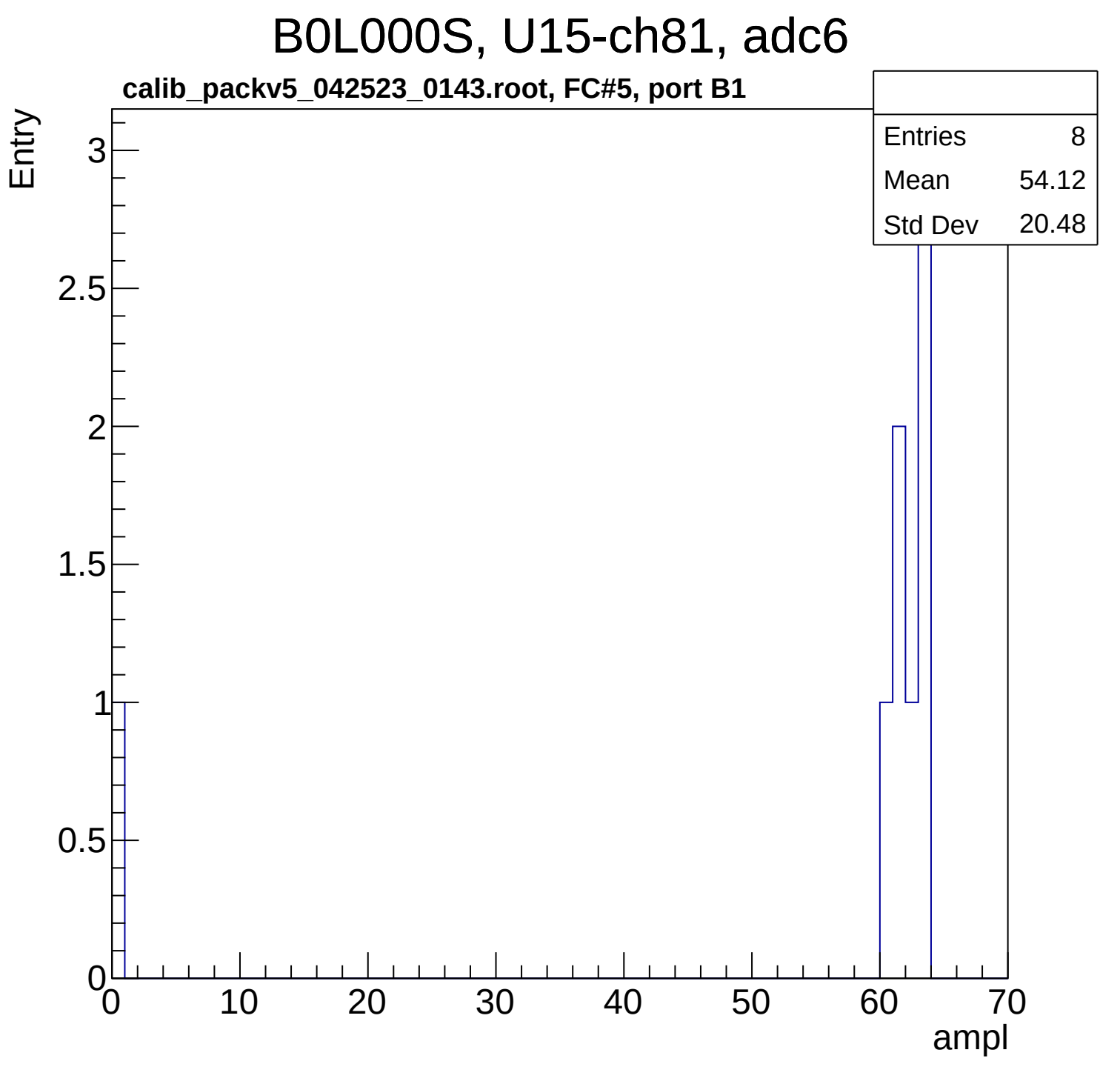
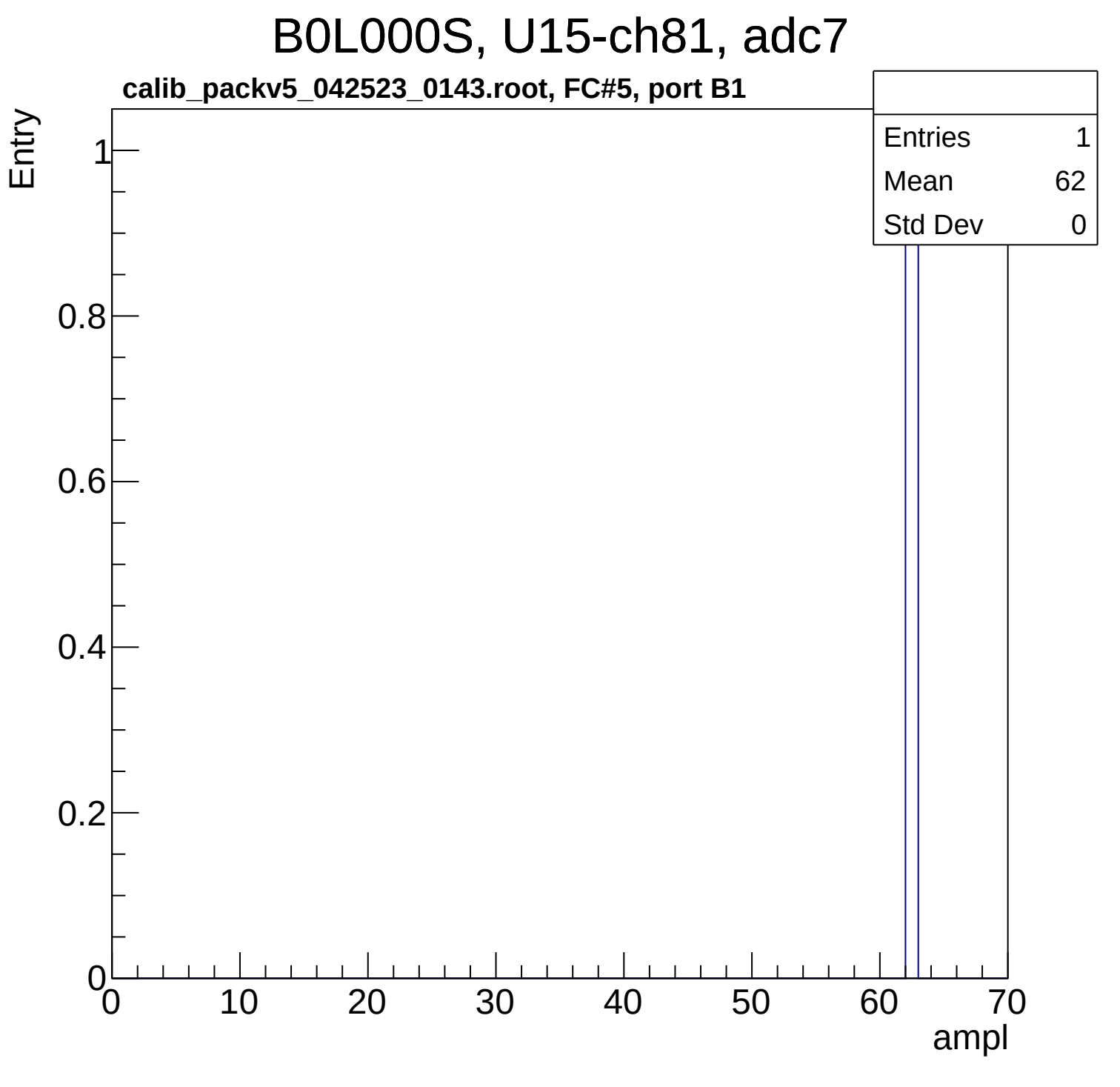

B0L000S, U15-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.12
Std Dev 20.48
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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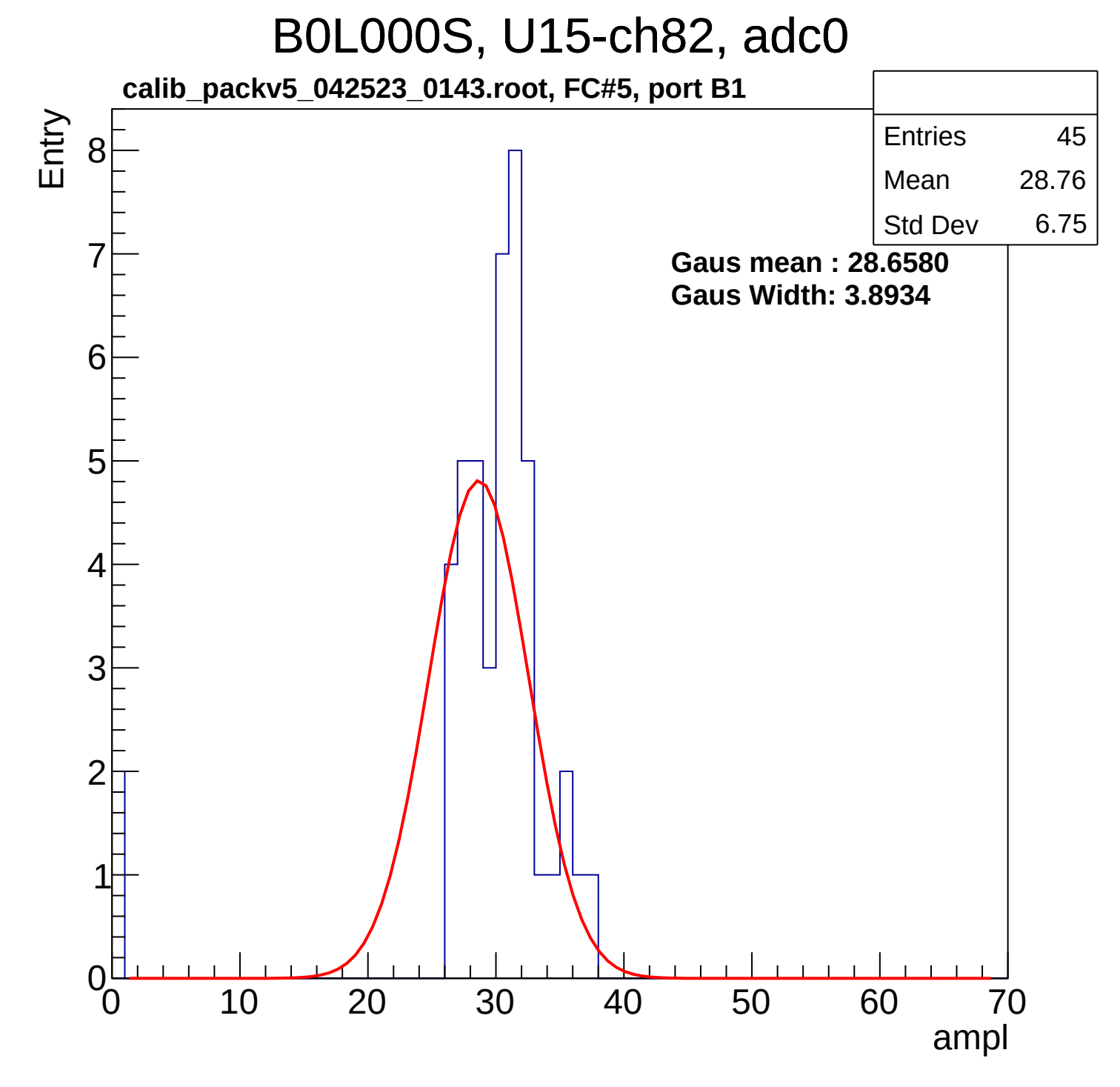
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 28.76
Std Dev 6.75
Gaus mean : 28.6580
Gaus Width: 3.8934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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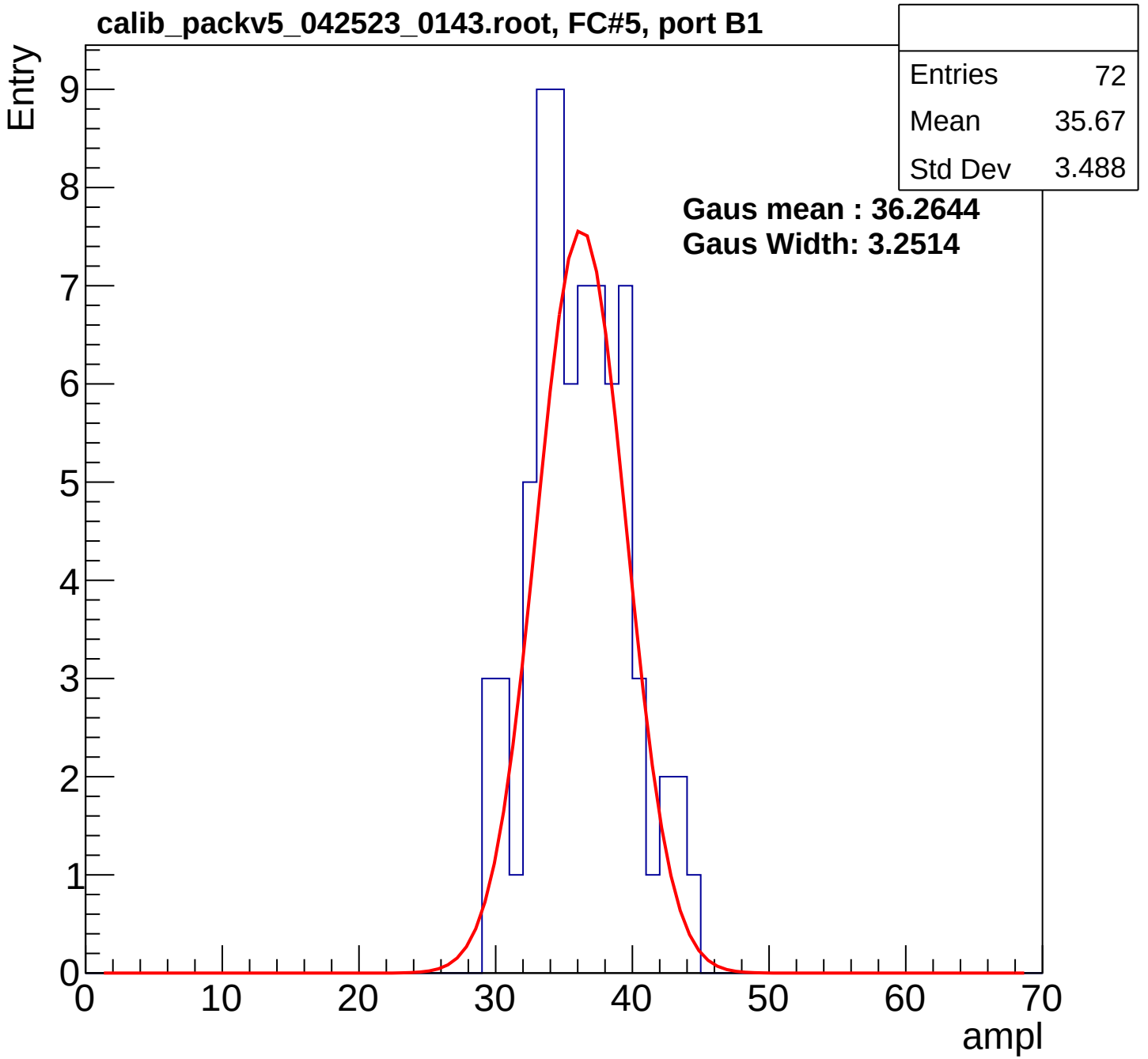
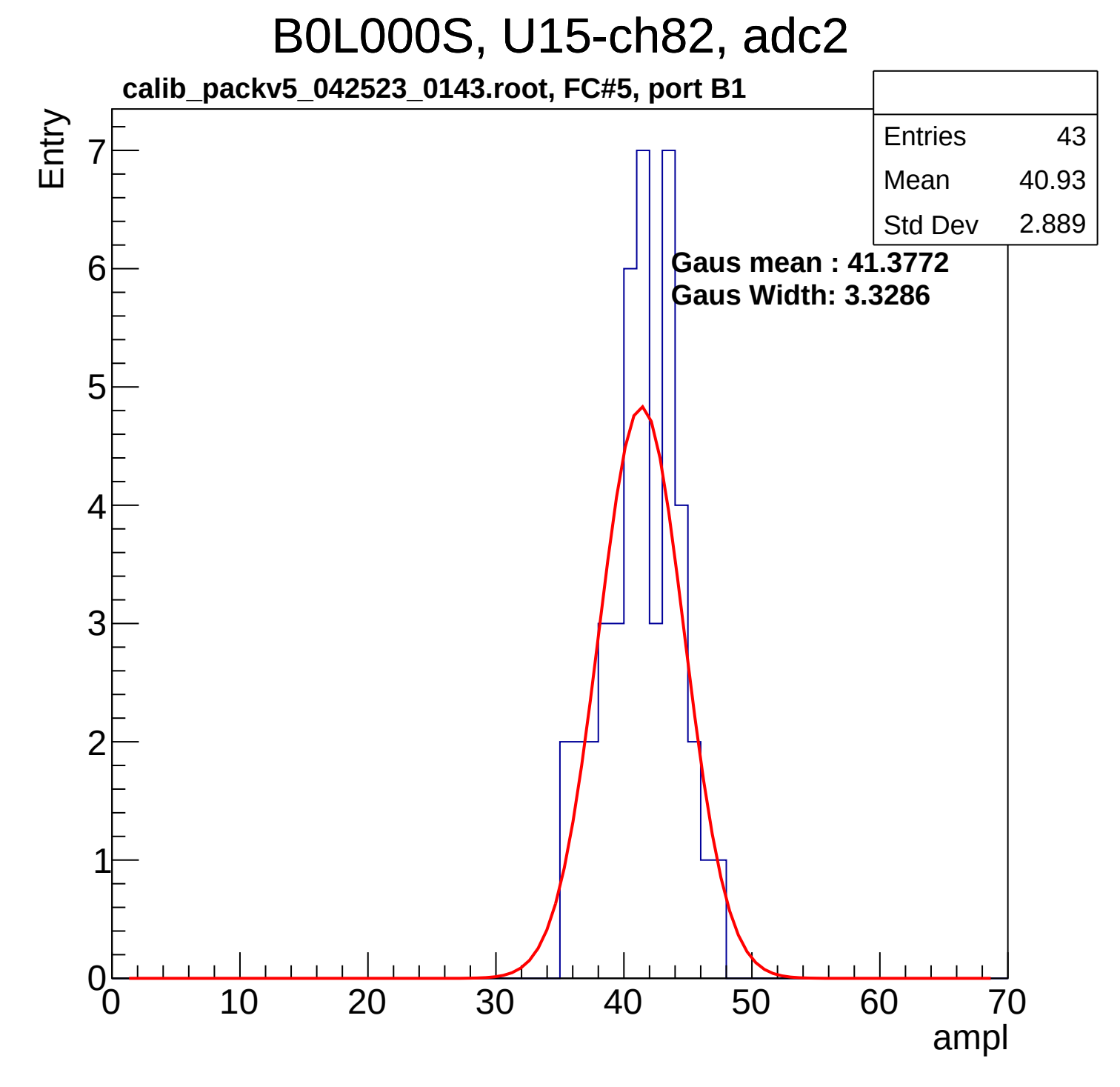
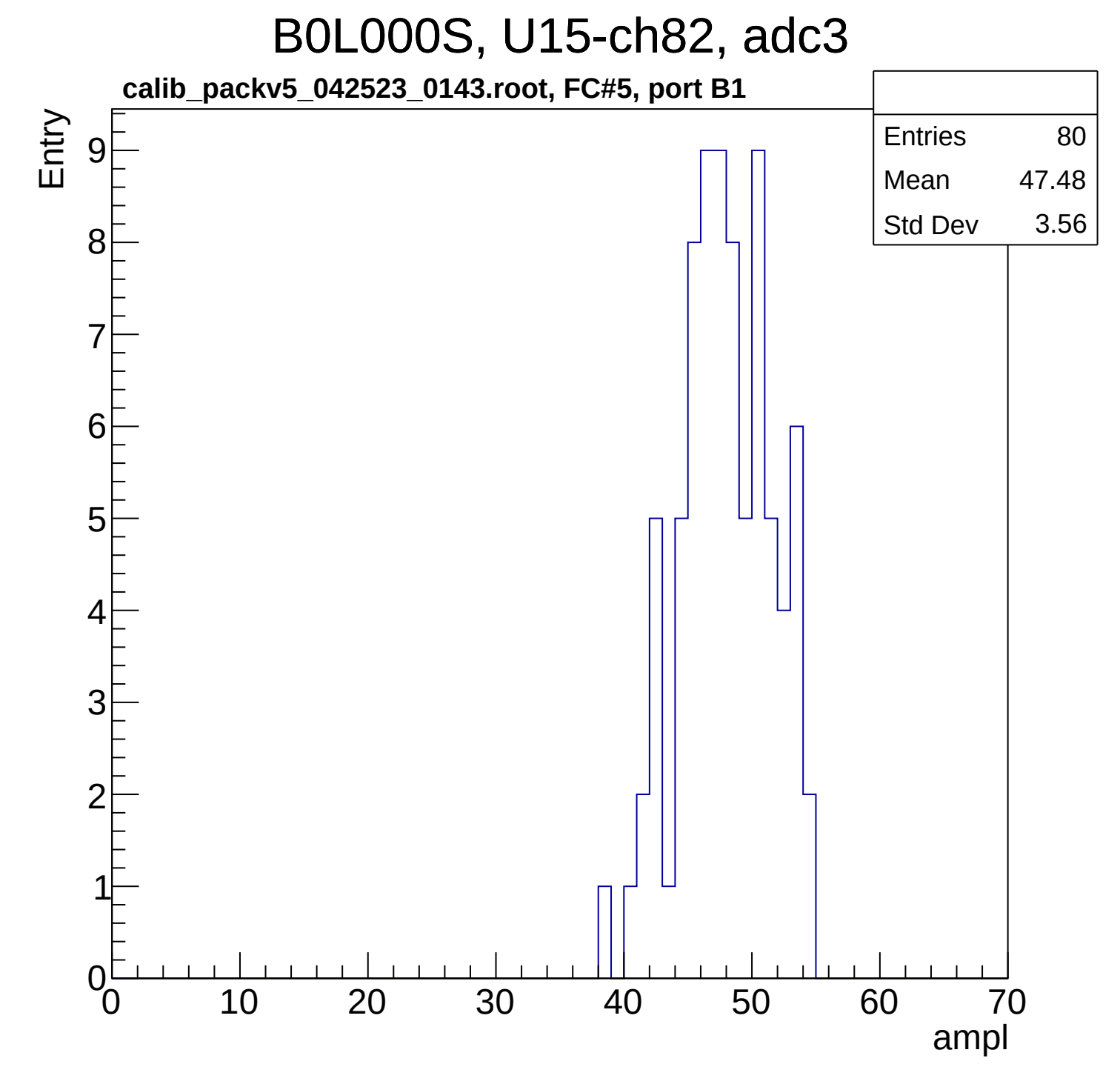

B0L000S, U15-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.67
Std Dev 3.488
Gaus mean : 36.2644
Gaus Width: 3.2514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 40.93
Std Dev 2.889
Gaus mean : 41.3772
Gaus Width: 3.3286
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 47.48
Std Dev 3.56
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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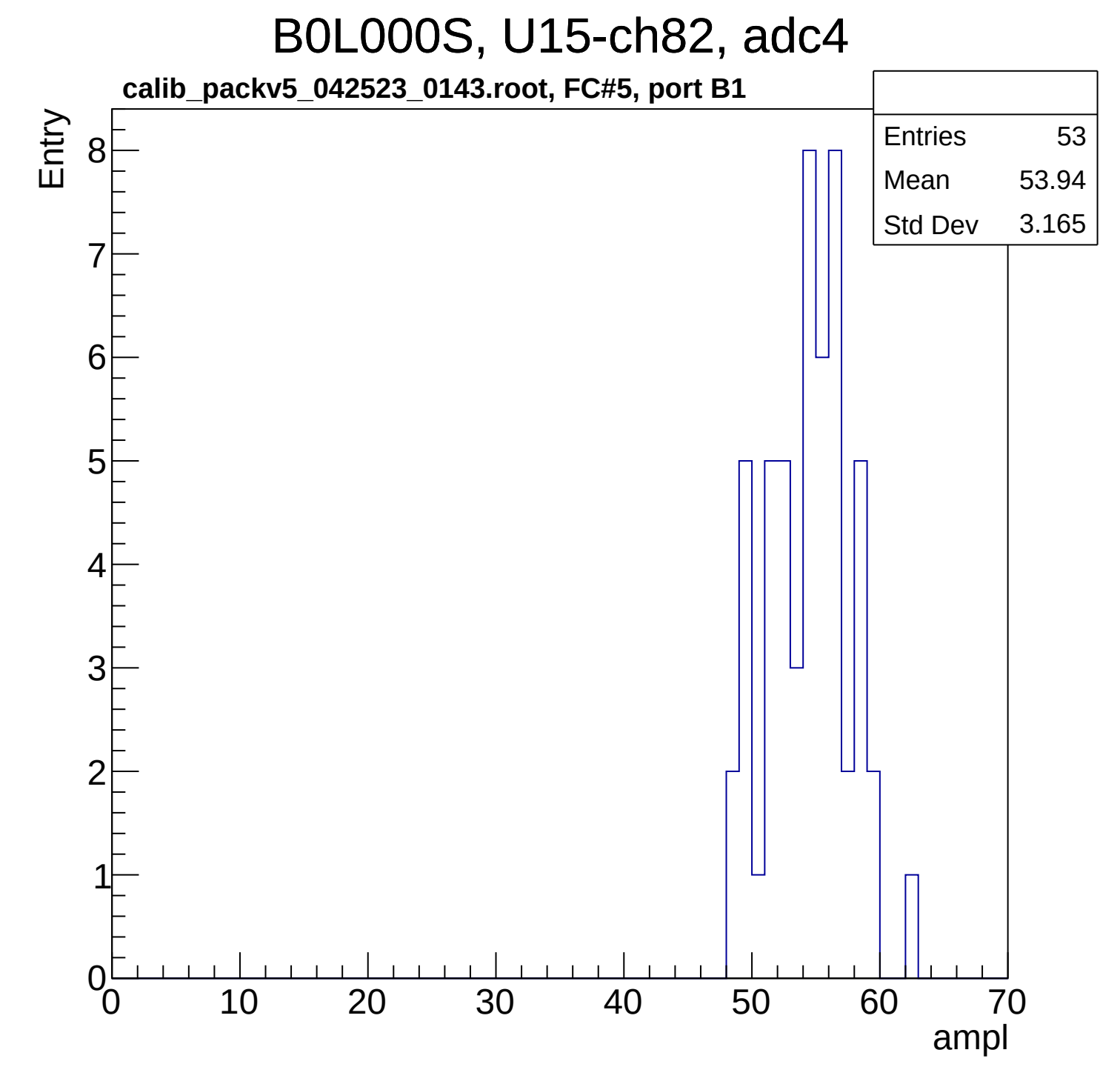
B0L000S, U15-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.94
Std Dev 3.165
Entry
ampl
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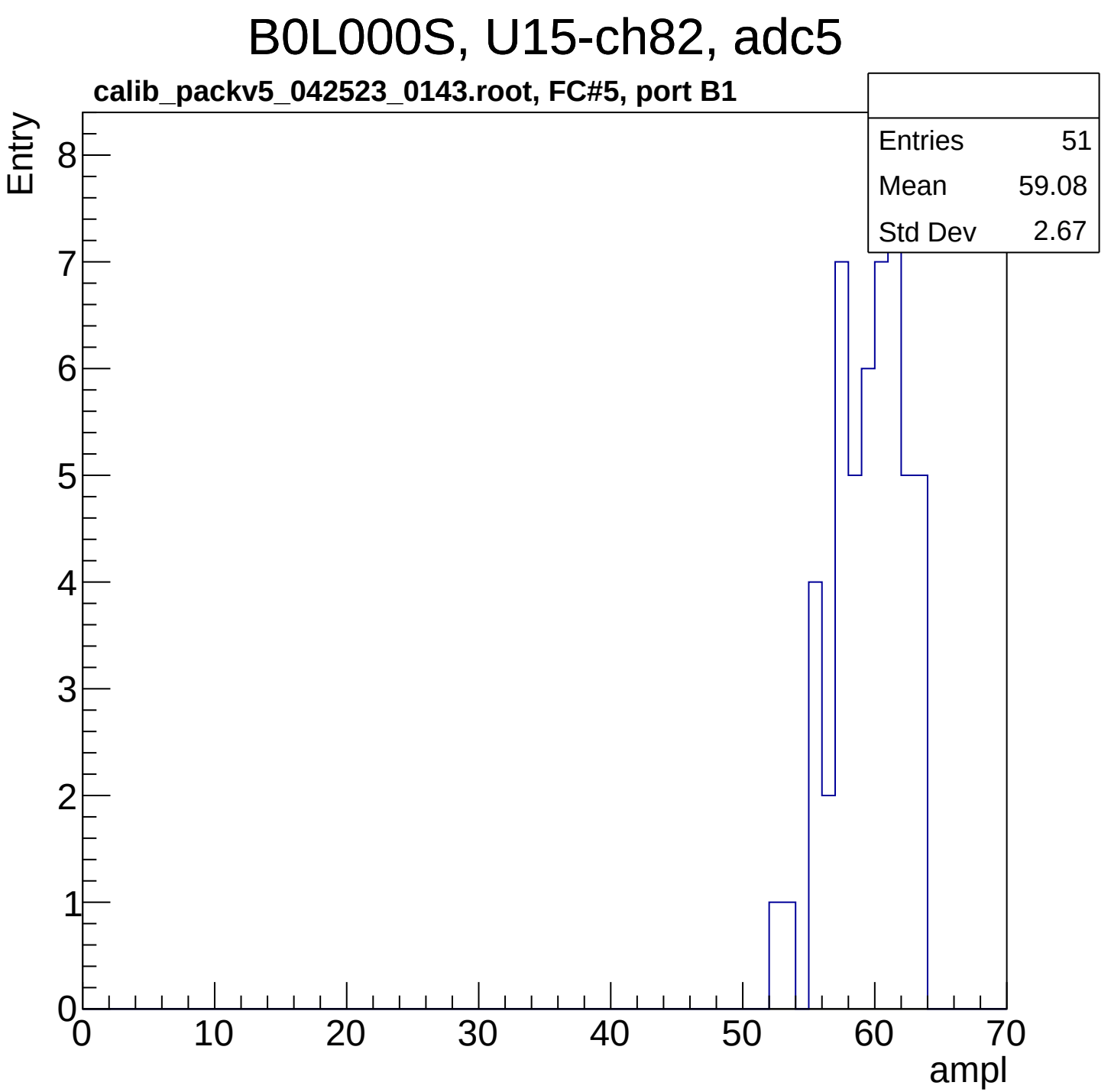
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.08
Std Dev 2.67
Entry
ampl
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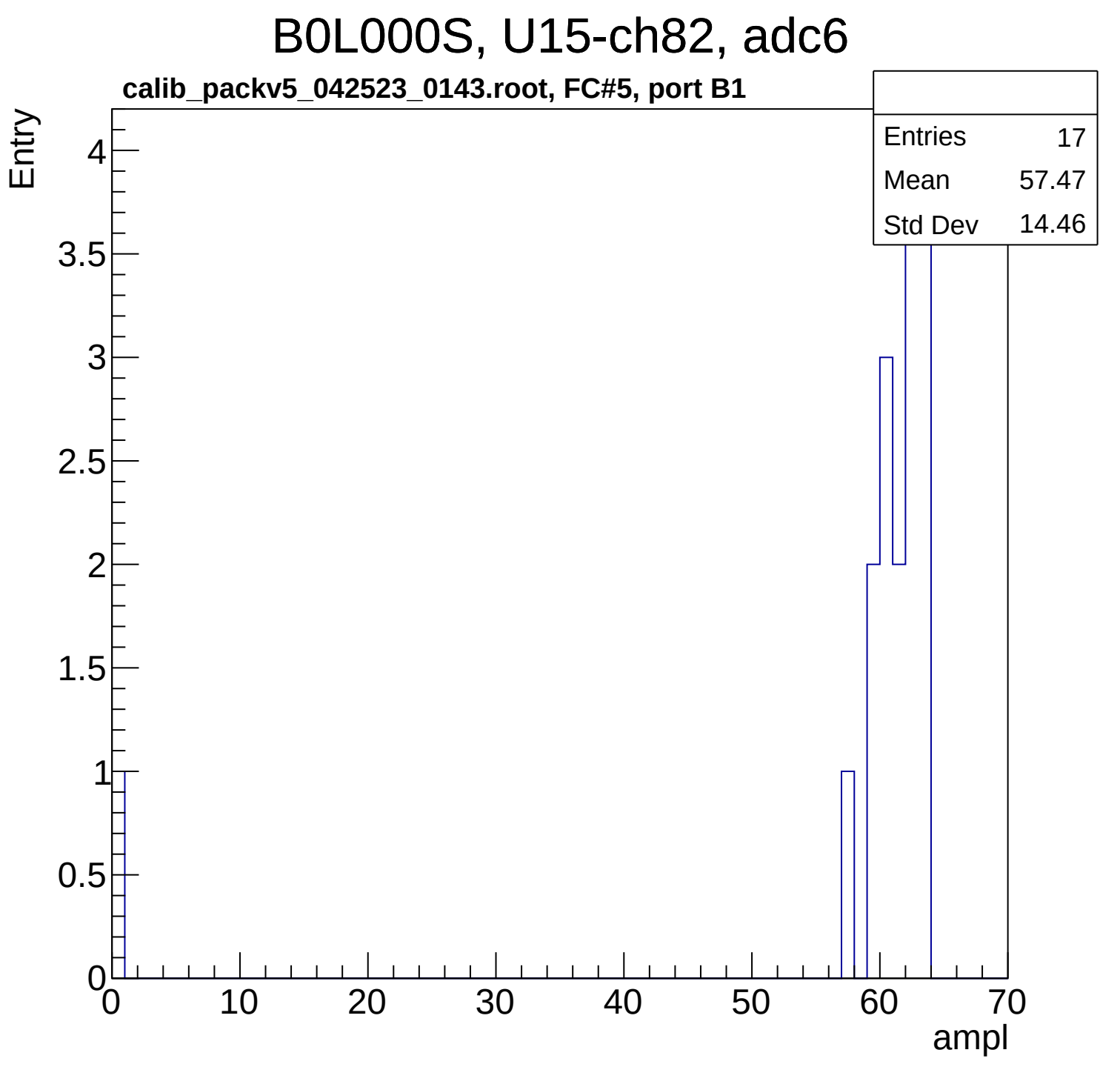
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.47
Std Dev 14.46
Entry
ampl
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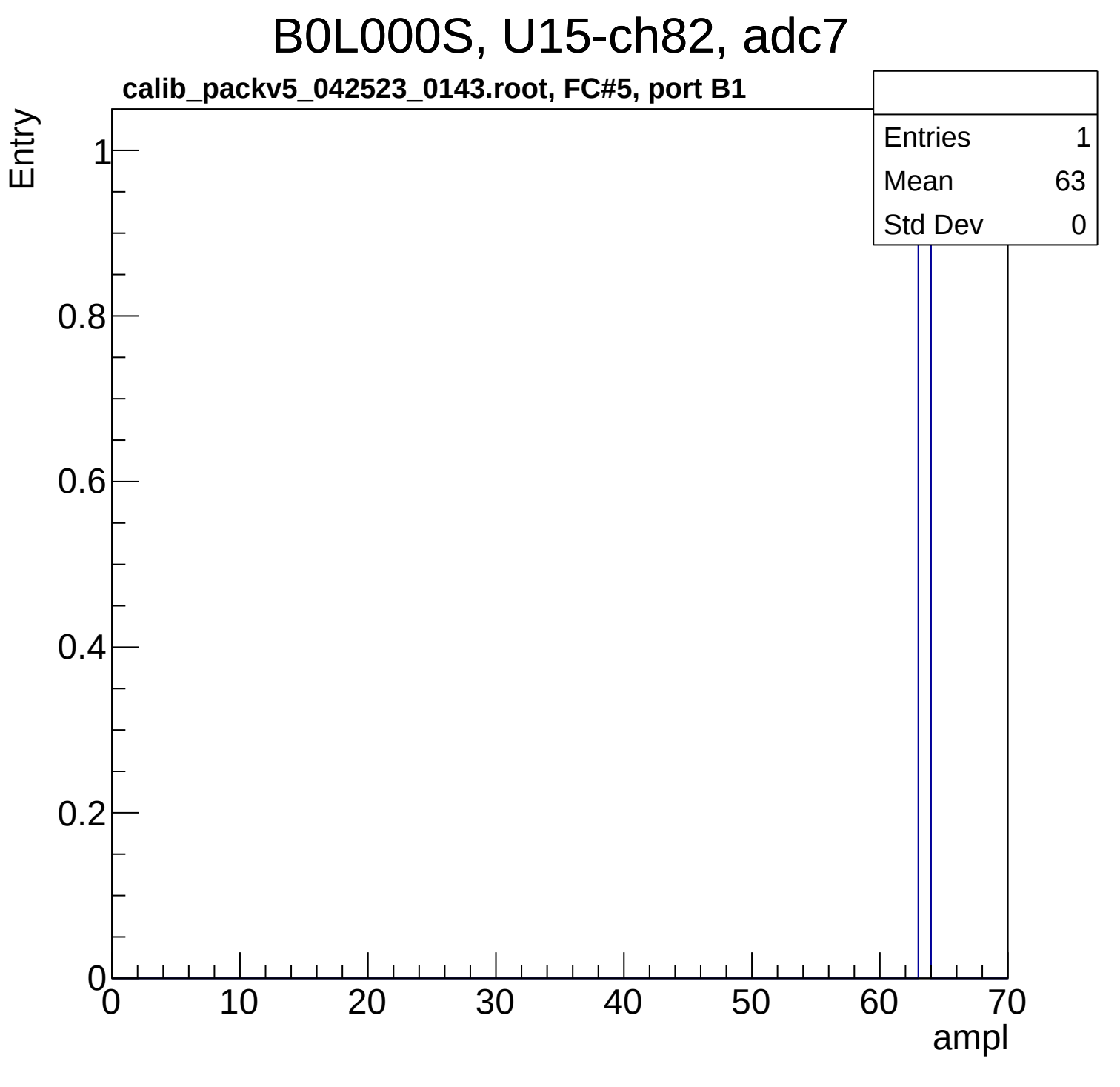
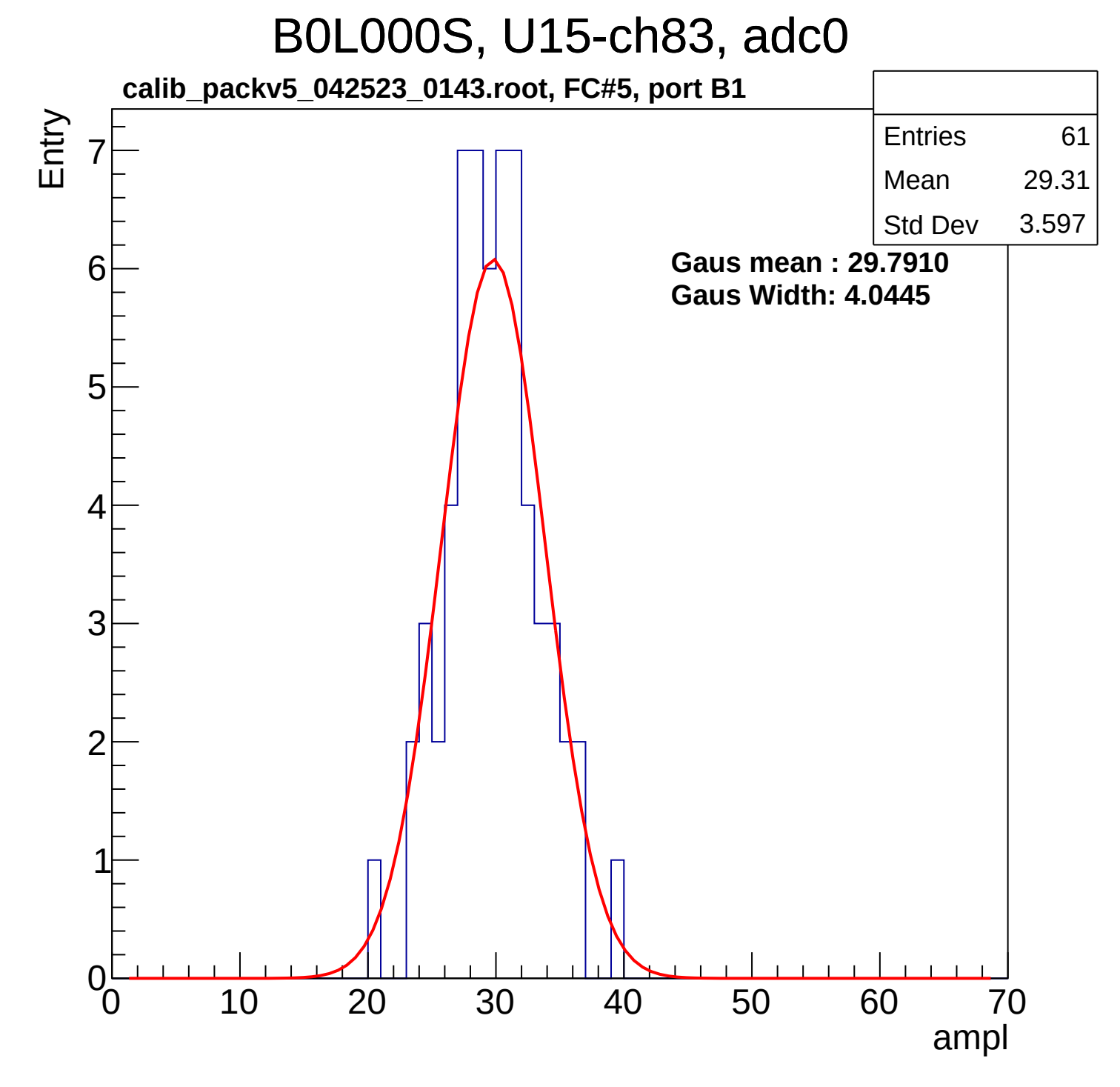
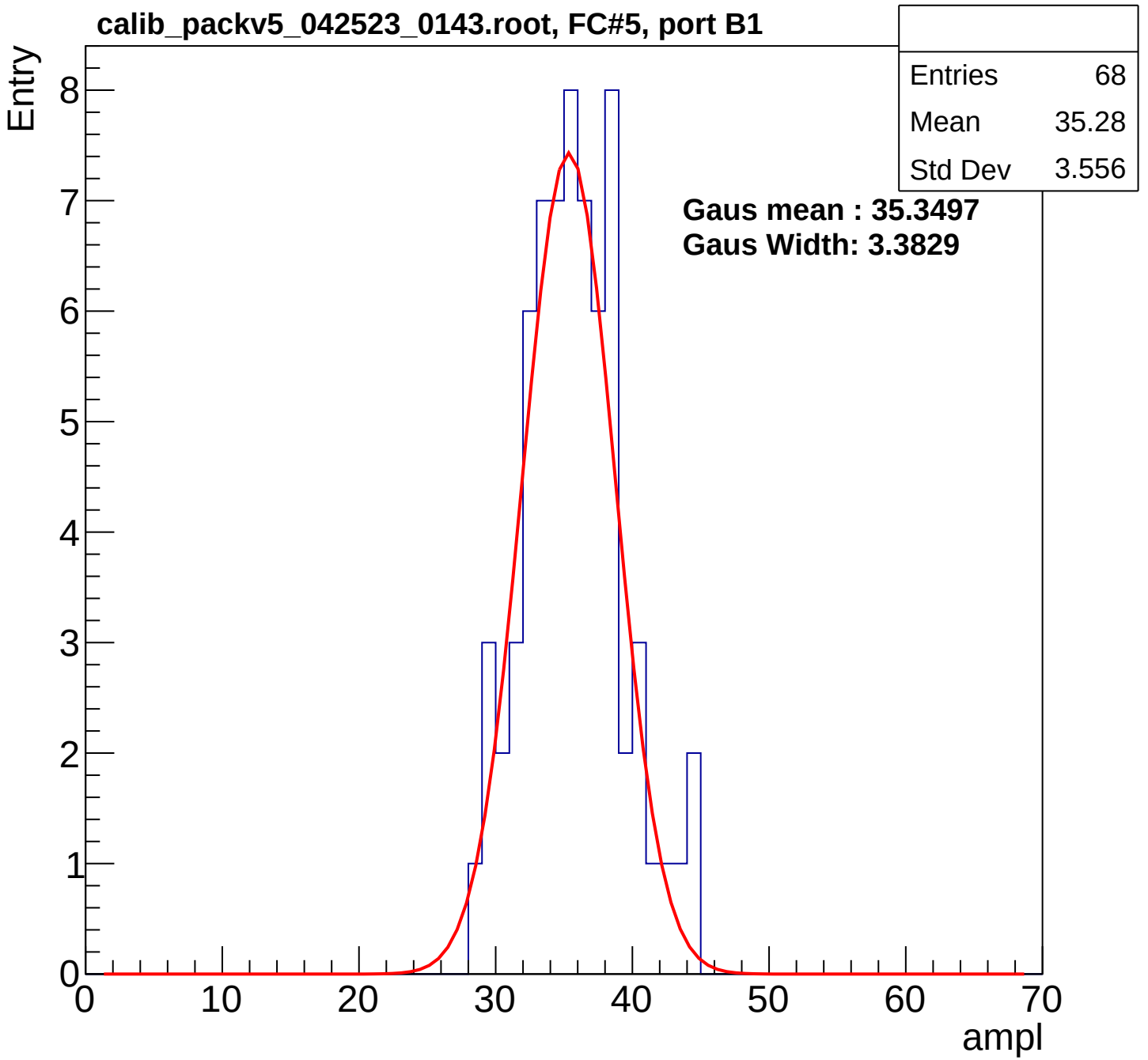
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.31
Std Dev 3.597
Gaus mean : 29.7910
Gaus Width: 4.0445
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.28
Std Dev 3.556
Gaus mean : 35.3497
Gaus Width: 3.3829
Entry
ampl
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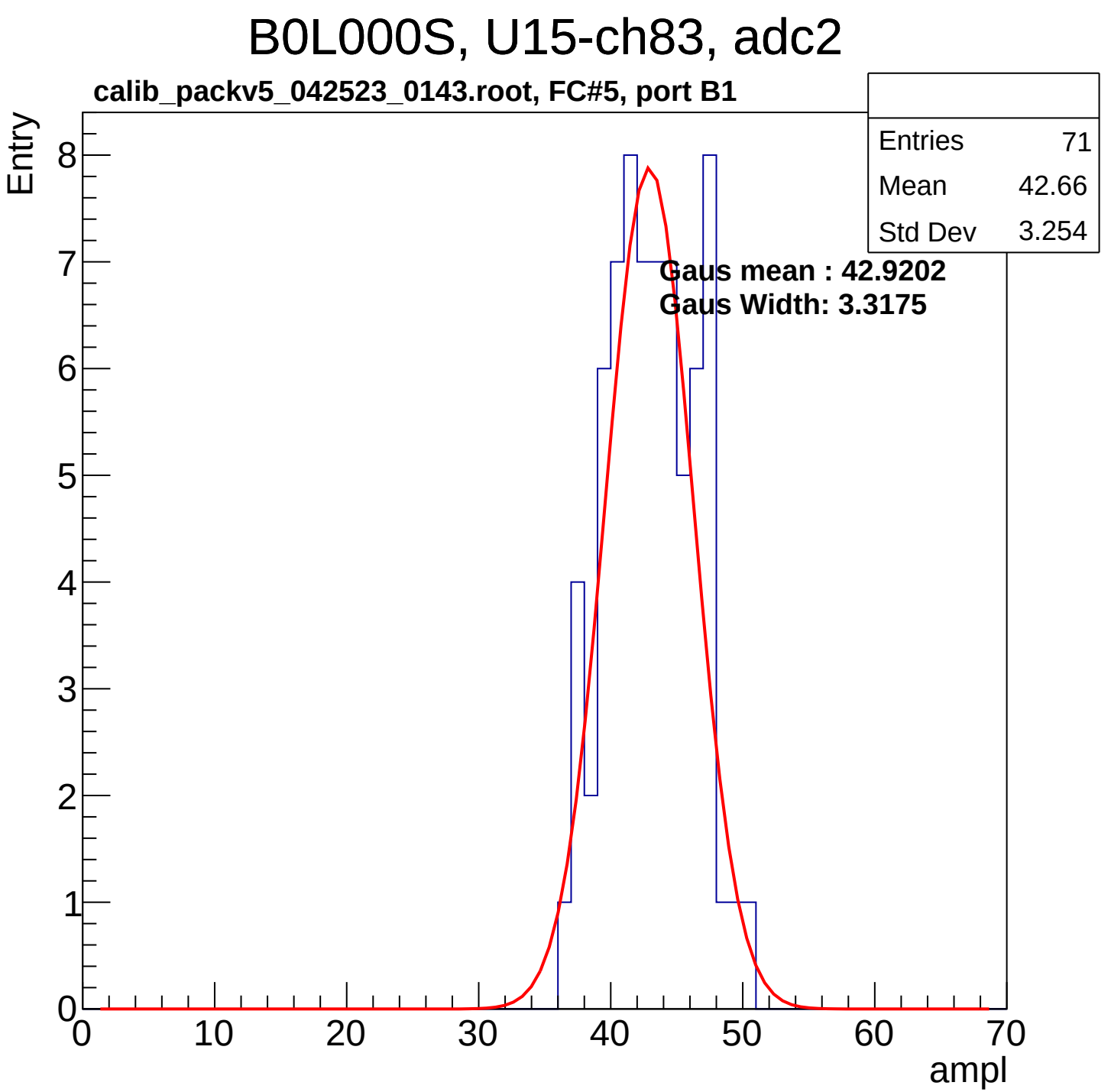
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.66
Std Dev 3.254
Gaus mean : 42.9202
Gaus Width: 3.3175
Entry
ampl
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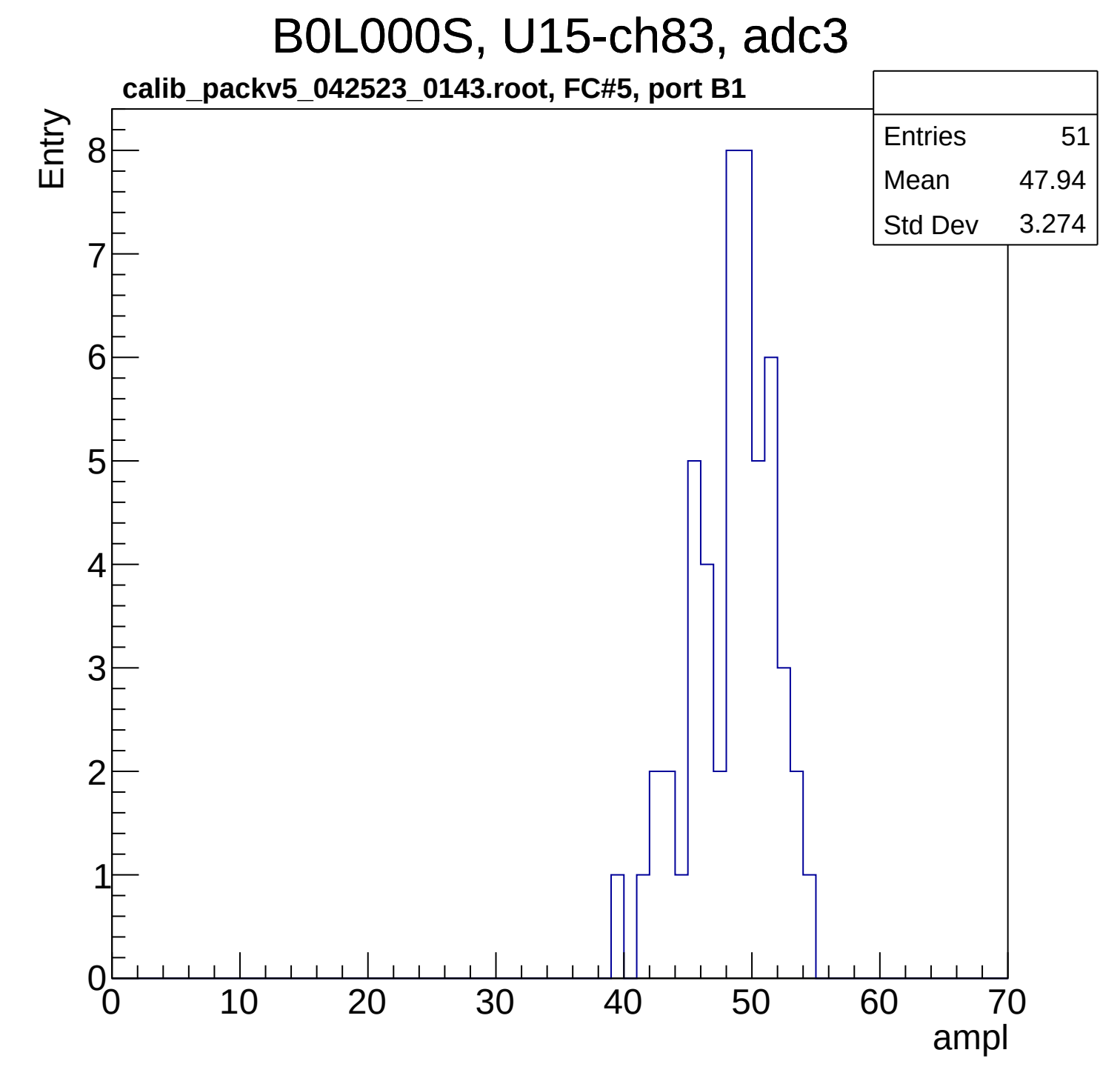
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 47.94
Std Dev 3.274
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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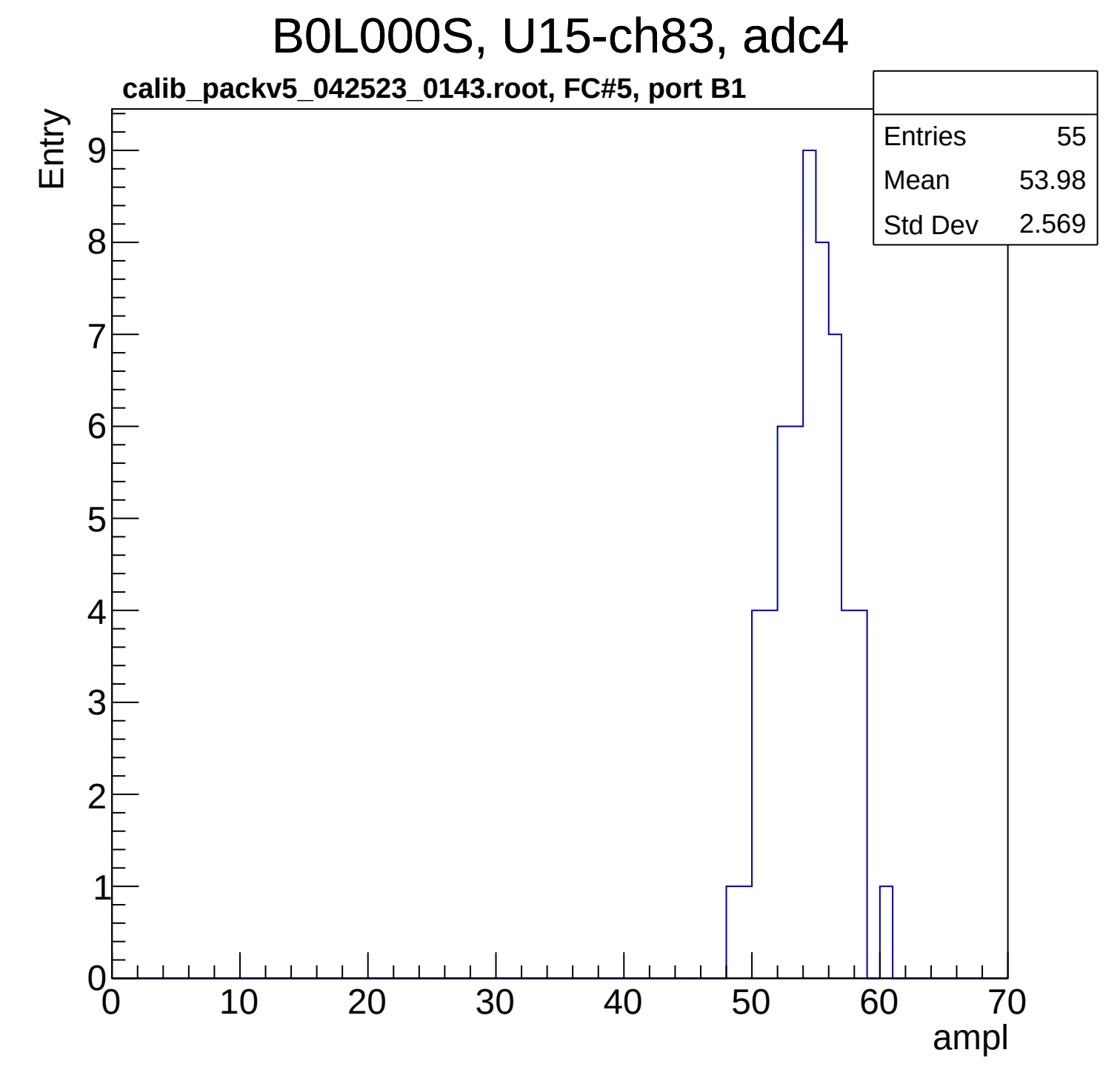
40
50
60
70


B0L000S, U15-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.98
Std Dev 2.569
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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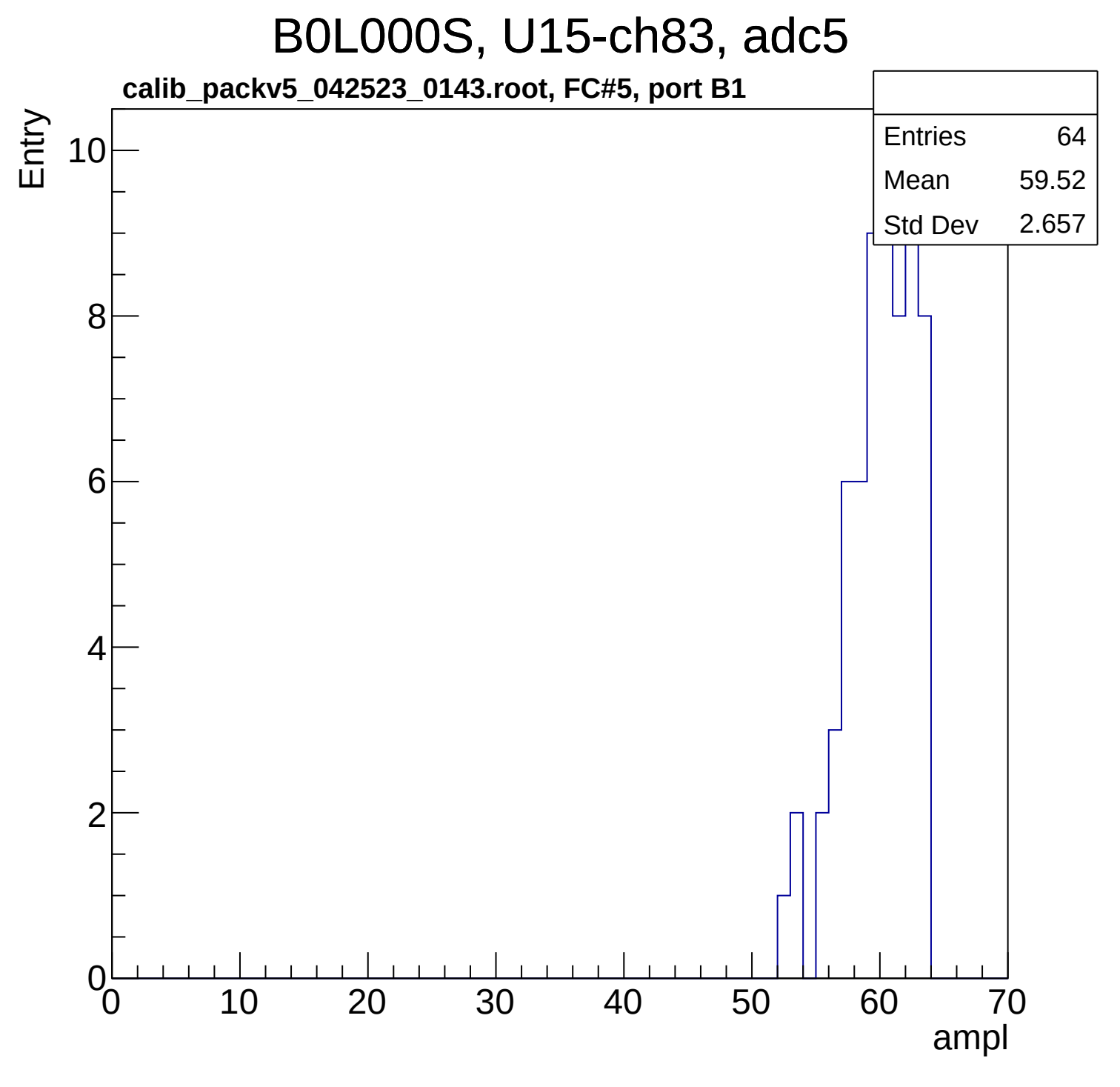
B0L000S, U15-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 59.52
Std Dev 2.657
Entry
ampl
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2
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6
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10
0
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20
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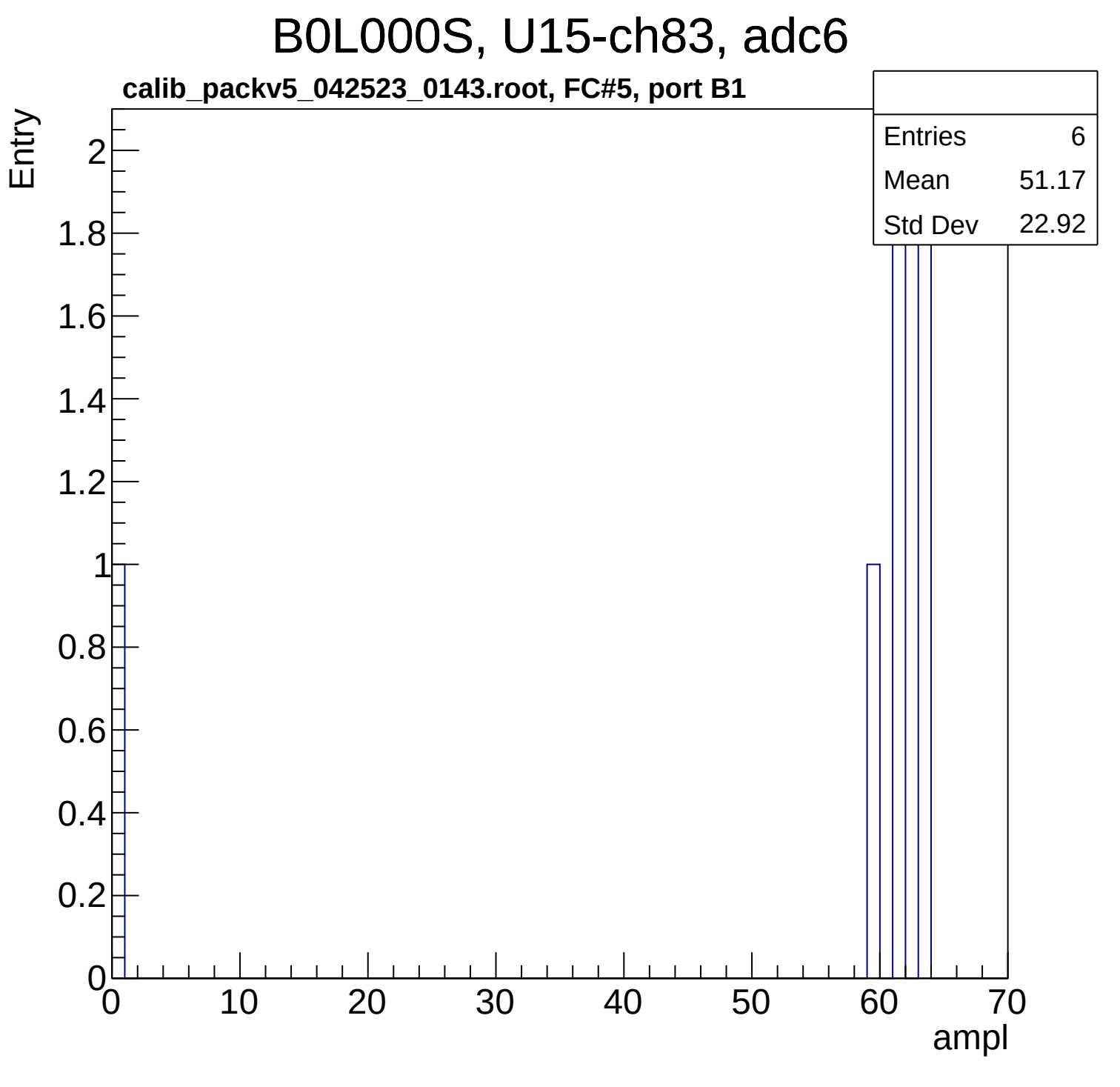
40
50
60
70


B0L000S, U15-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.17
Std Dev 22.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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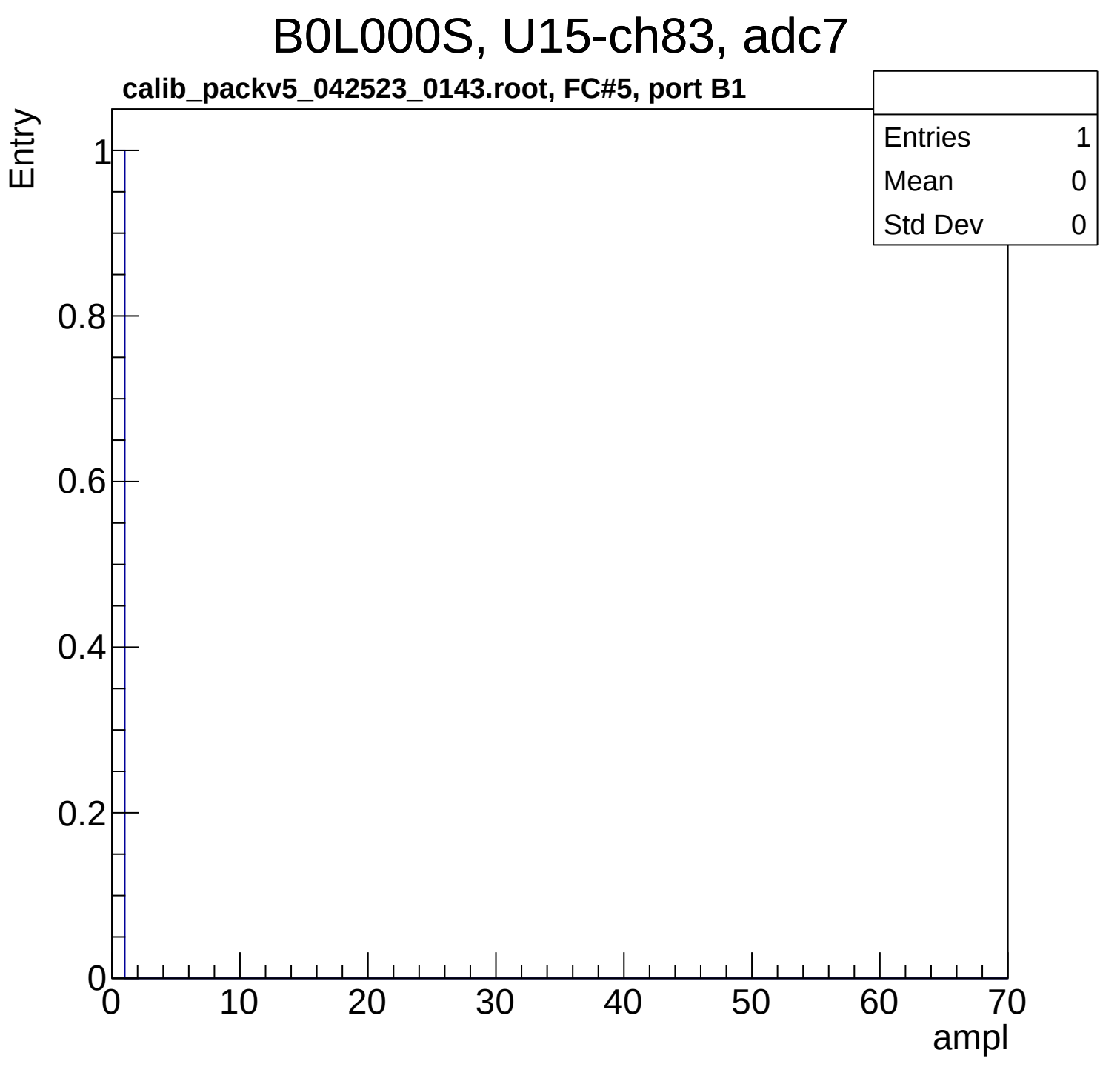
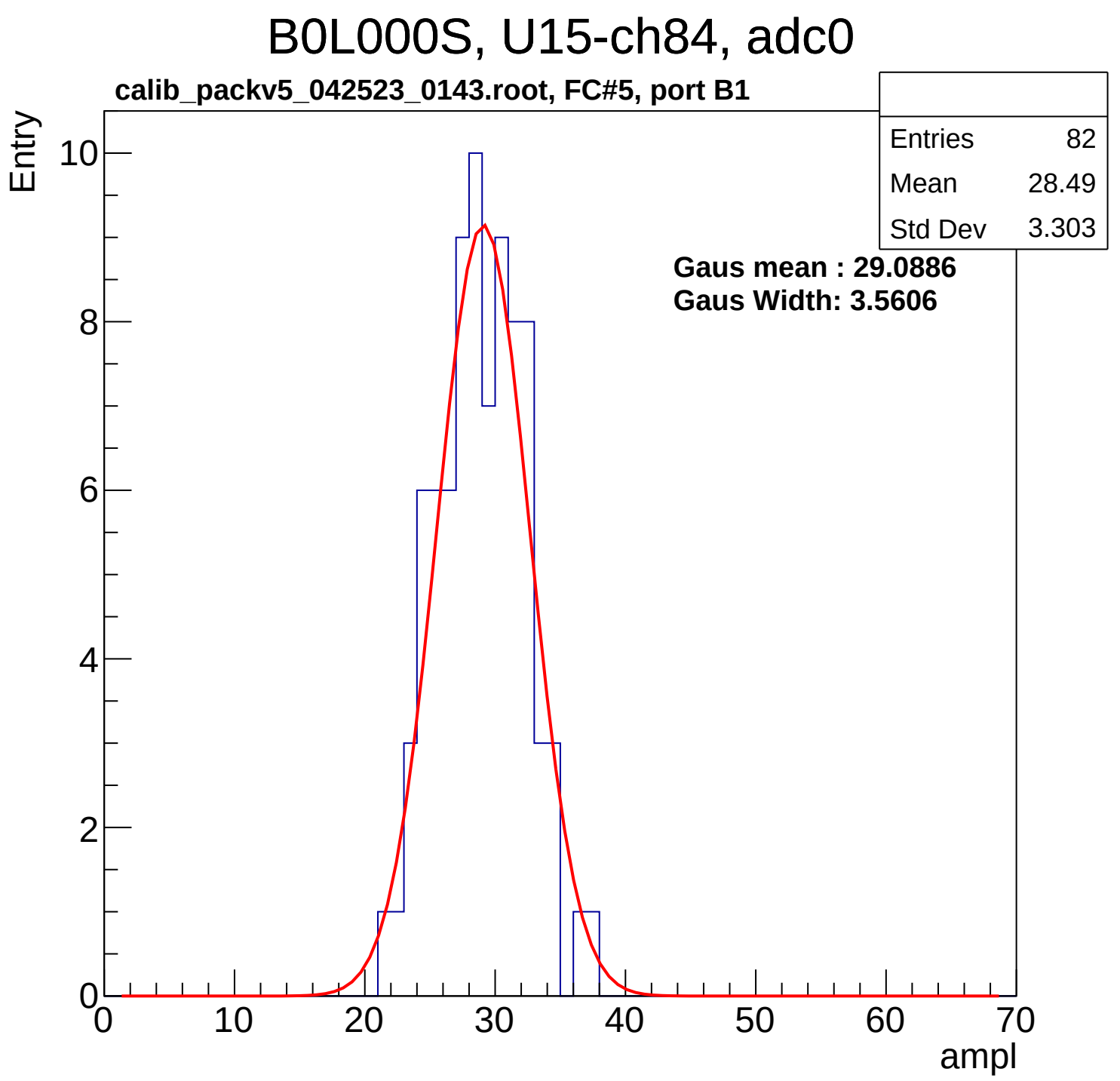
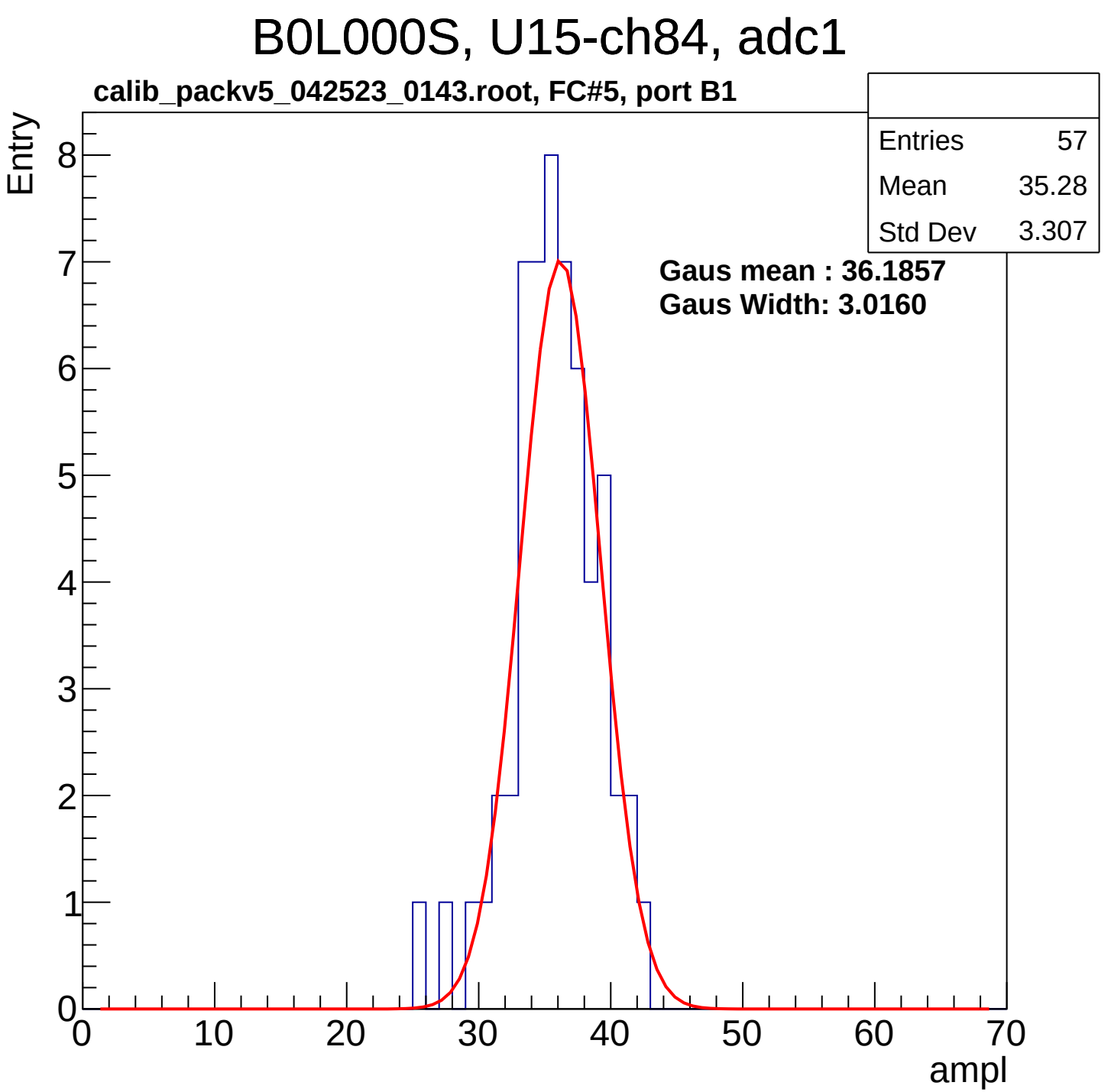
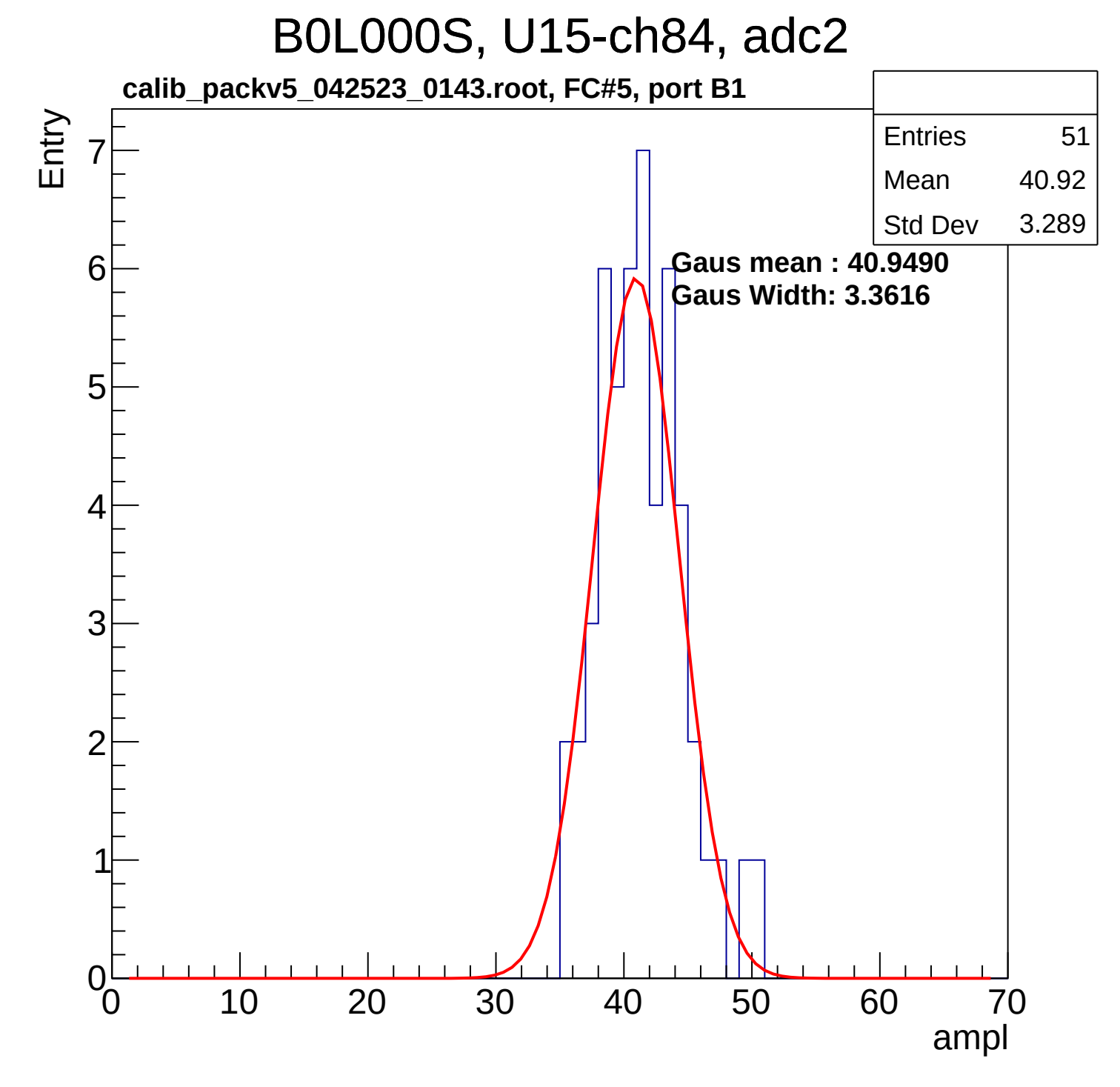
40
50
60
70


B0L000S, U15-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 28.49
Std Dev 3.303
Gaus mean : 29.0886
Gaus Width: 3.5606
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.28
Std Dev 3.307
Gaus mean : 36.1857
Gaus Width: 3.0160
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 40.92
Std Dev 3.289
Gaus mean : 40.9490
Gaus Width: 3.3616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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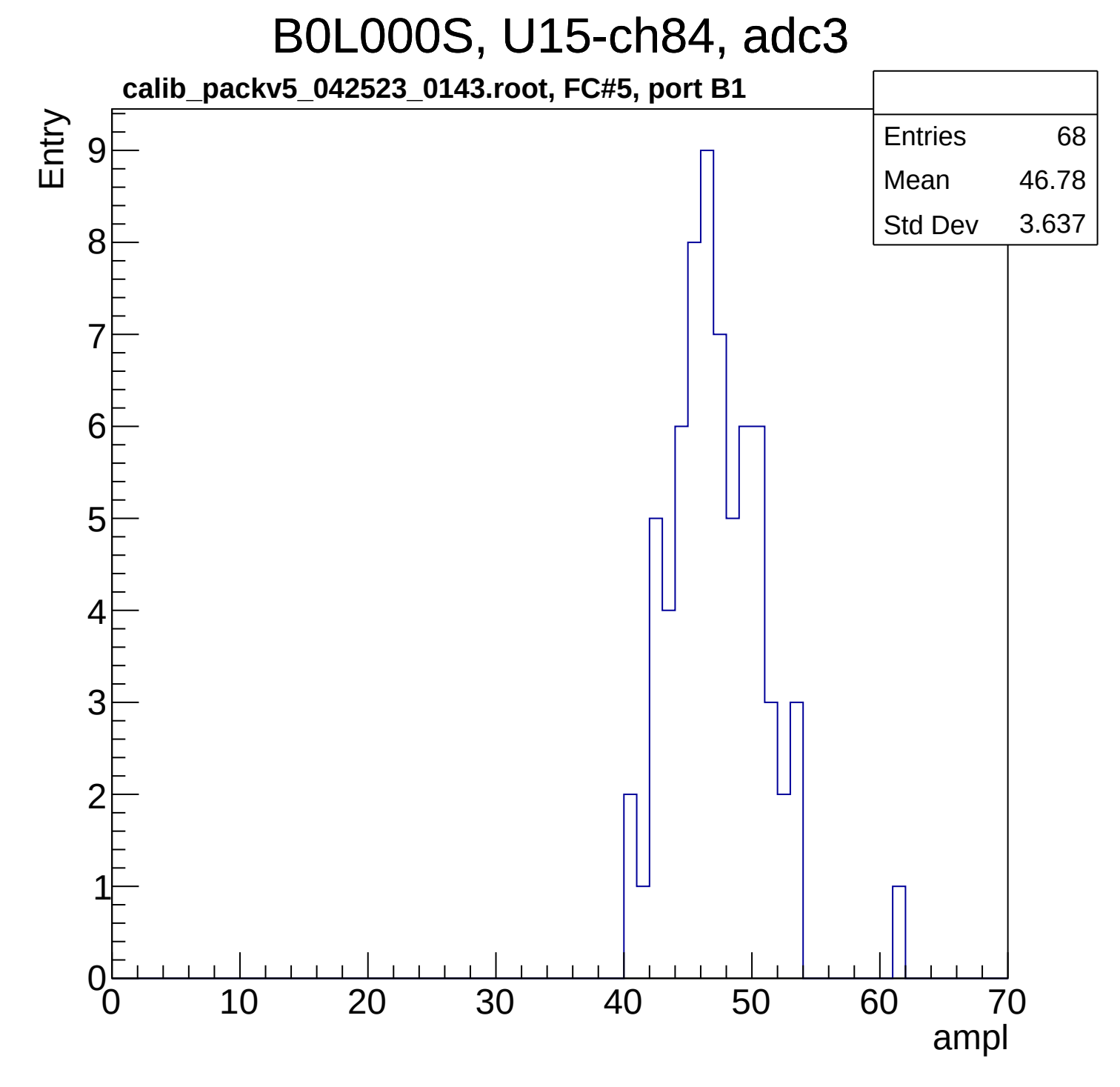
B0L000S, U15-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 46.78
Std Dev 3.637
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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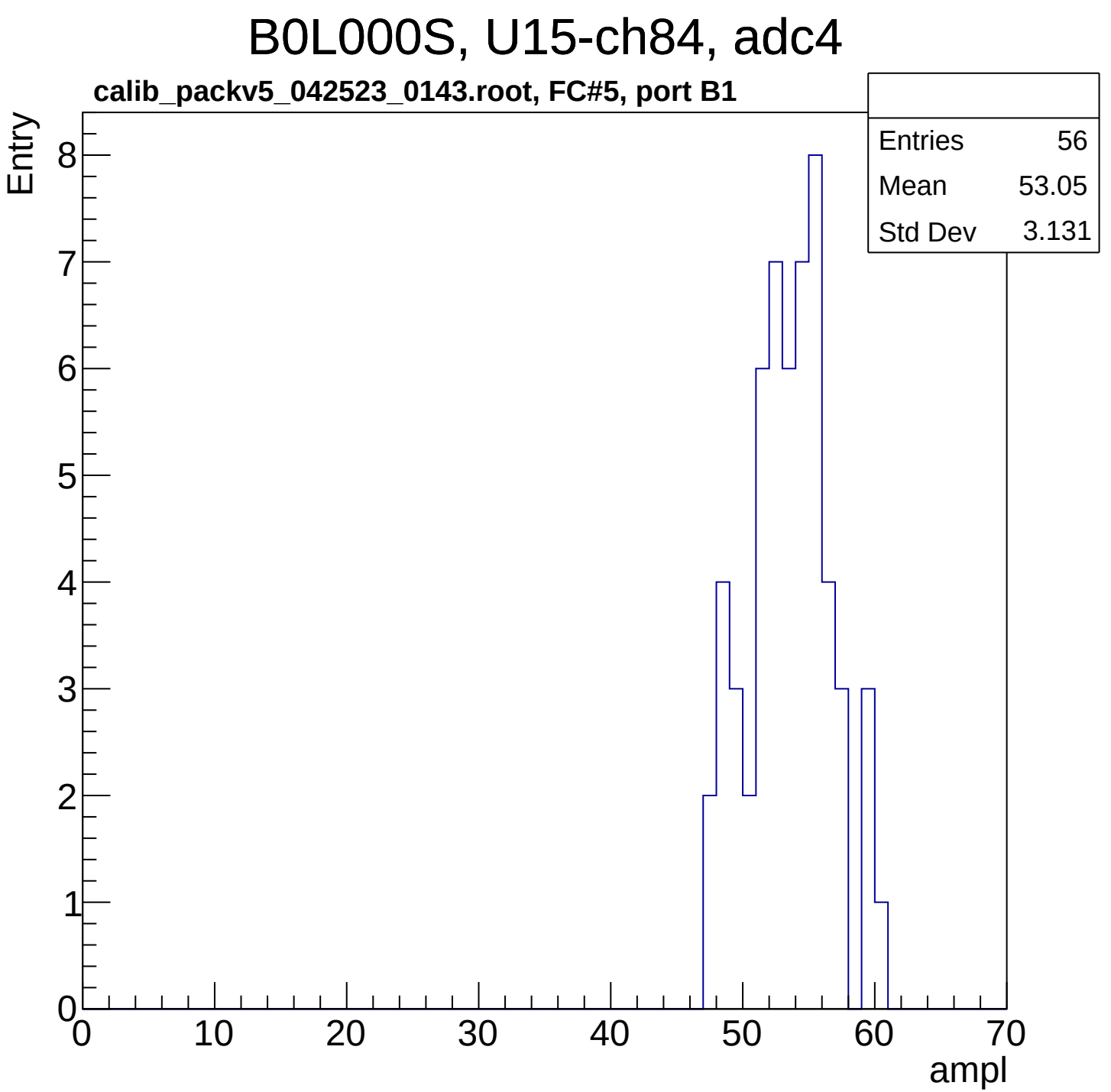
B0L000S, U15-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.05
Std Dev 3.131
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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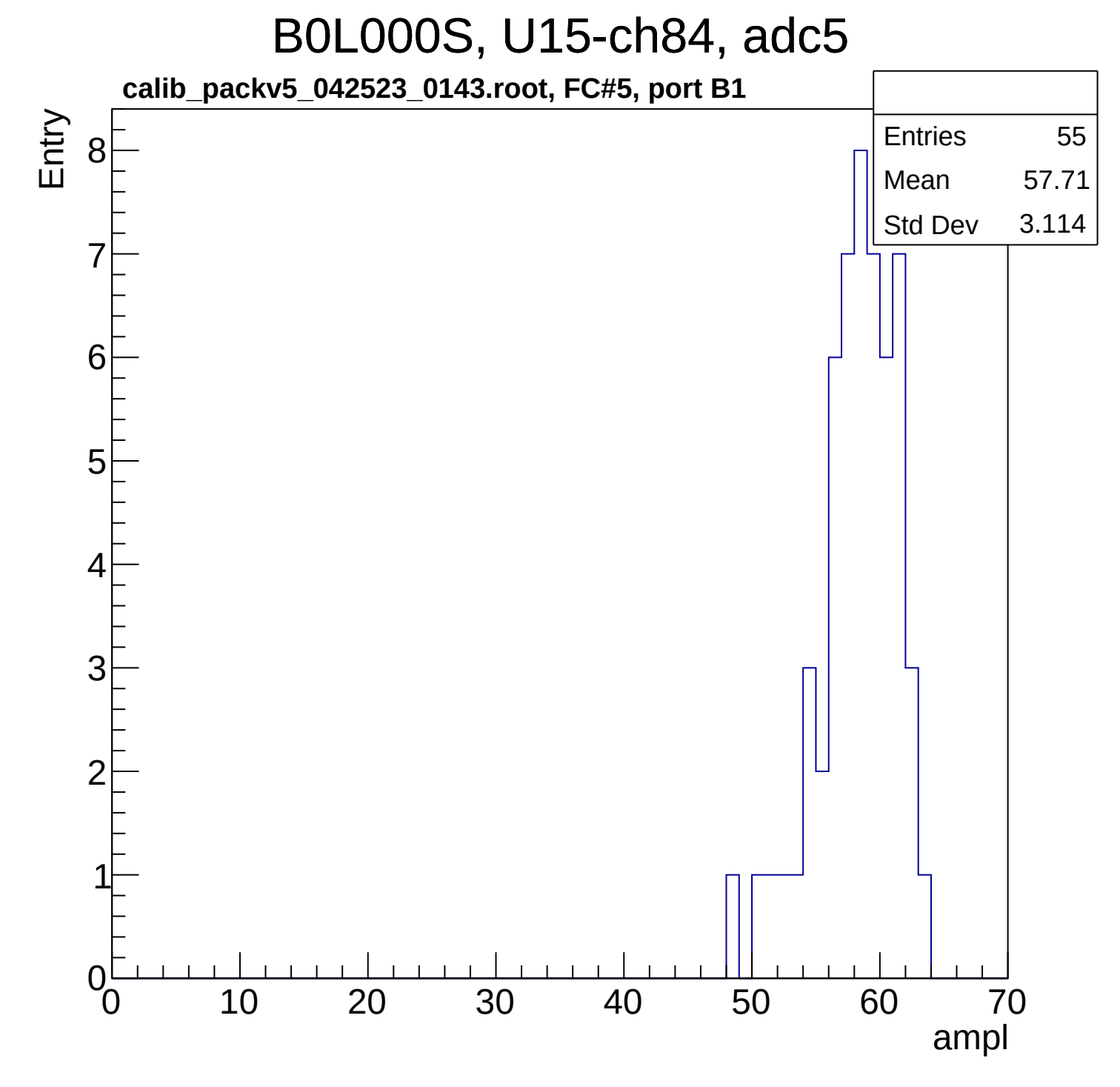
B0L000S, U15-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.71
Std Dev 3.114
Entry
ampl
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1
2
3
4
5
6
7
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20
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40
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60
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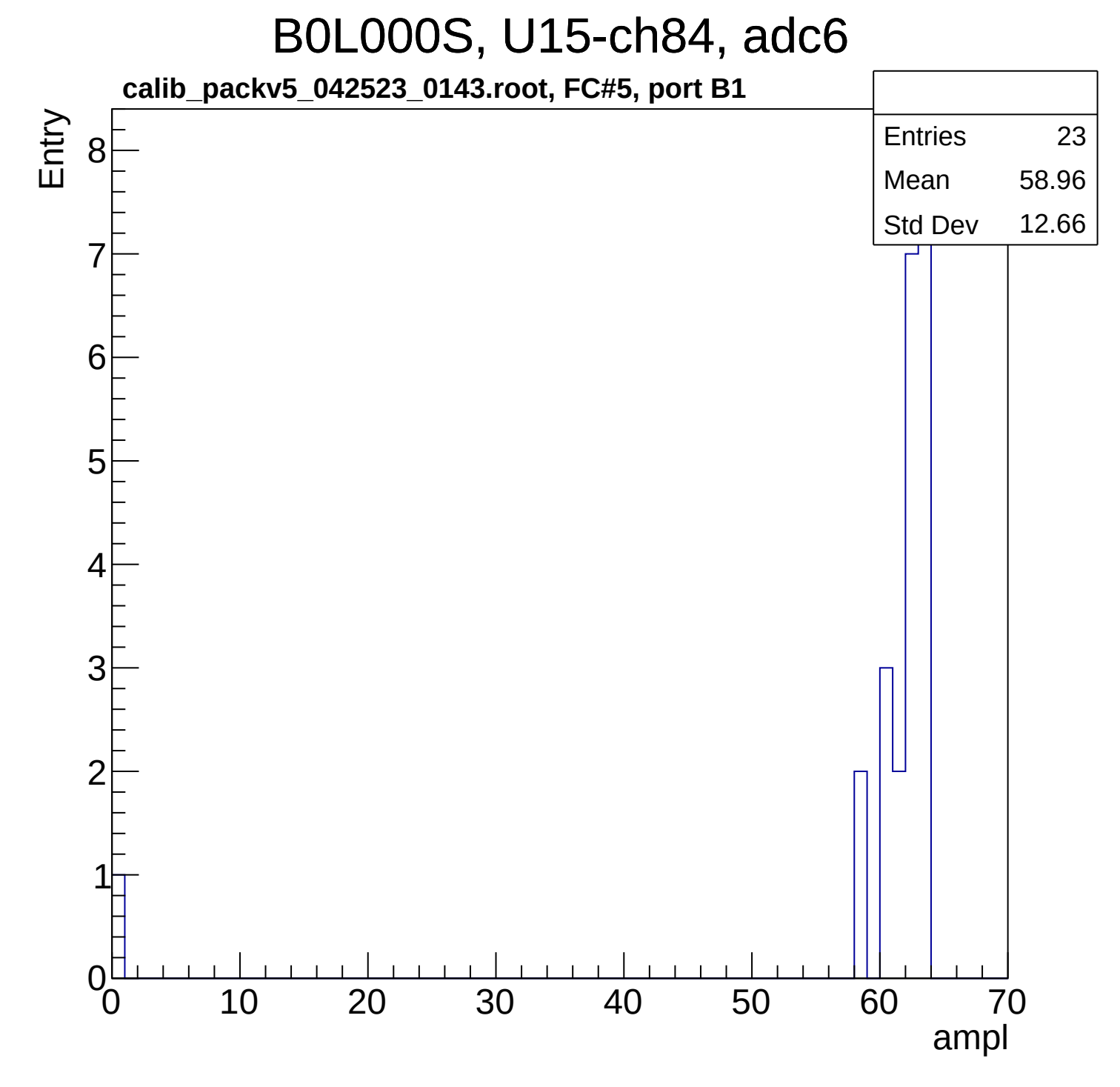
B0L000S, U15-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.96
Std Dev 12.66
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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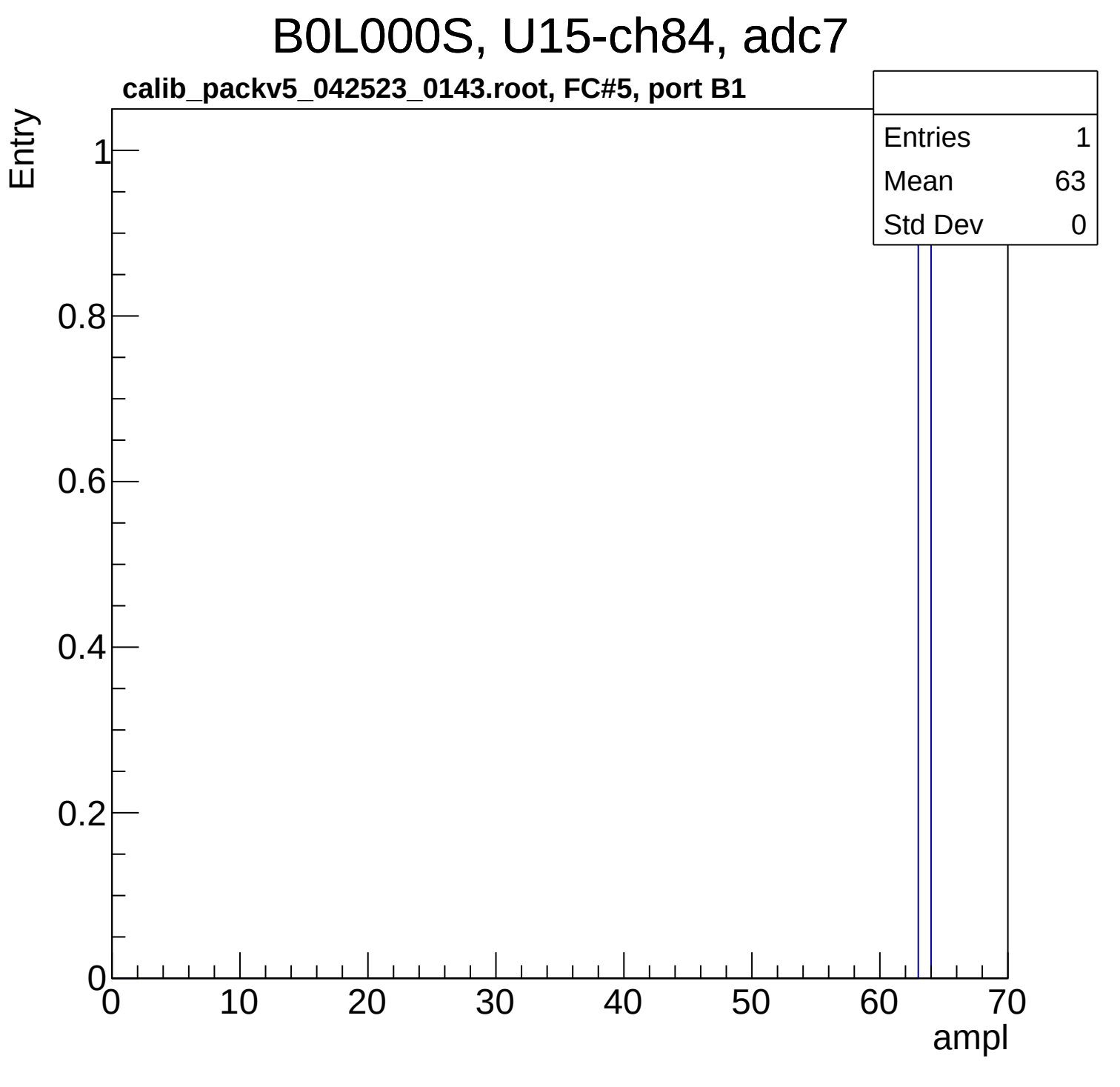
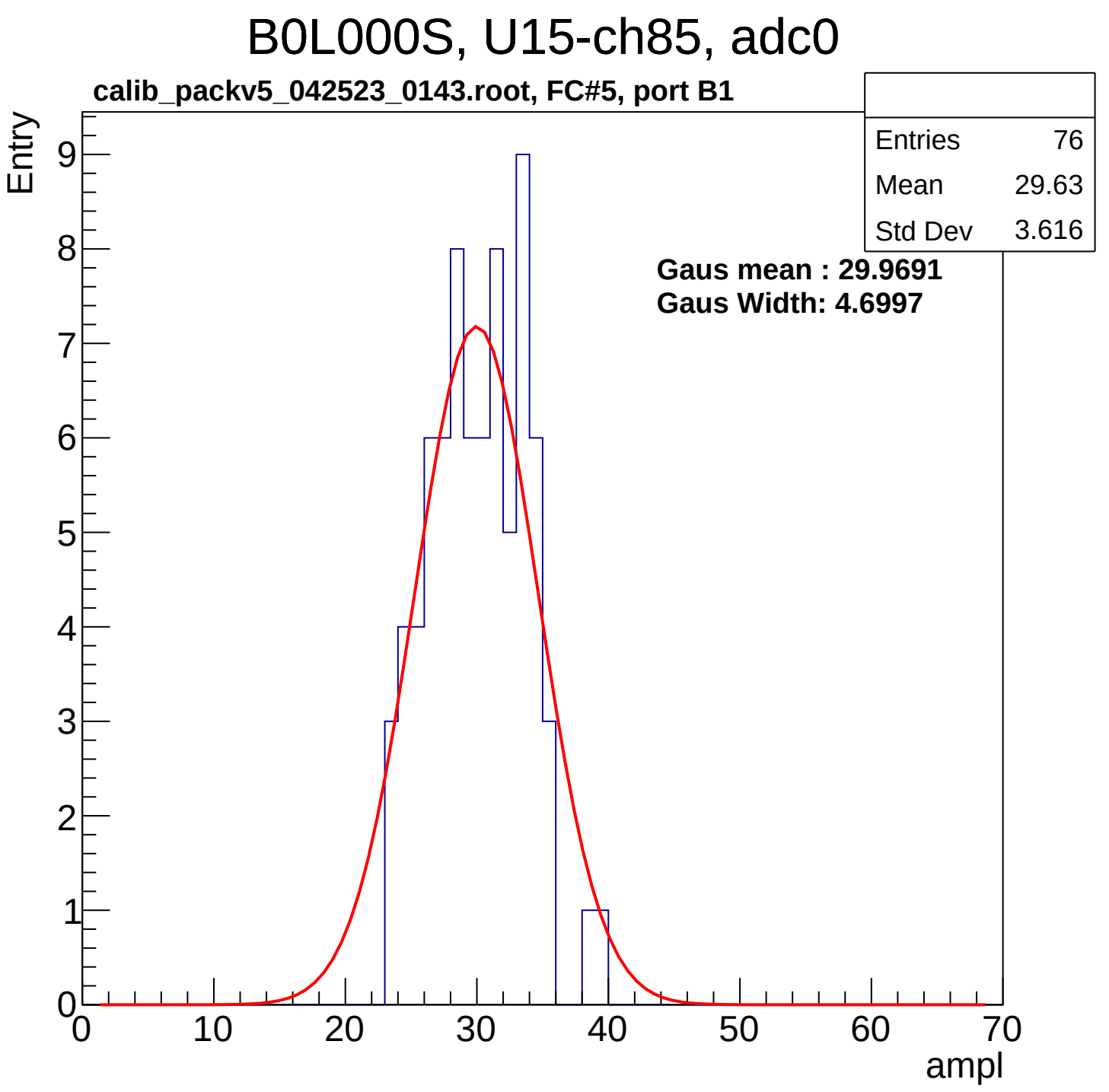

B0L000S, U15-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.63
Std Dev 3.616
Gaus mean : 29.9691
Gaus Width: 4.6997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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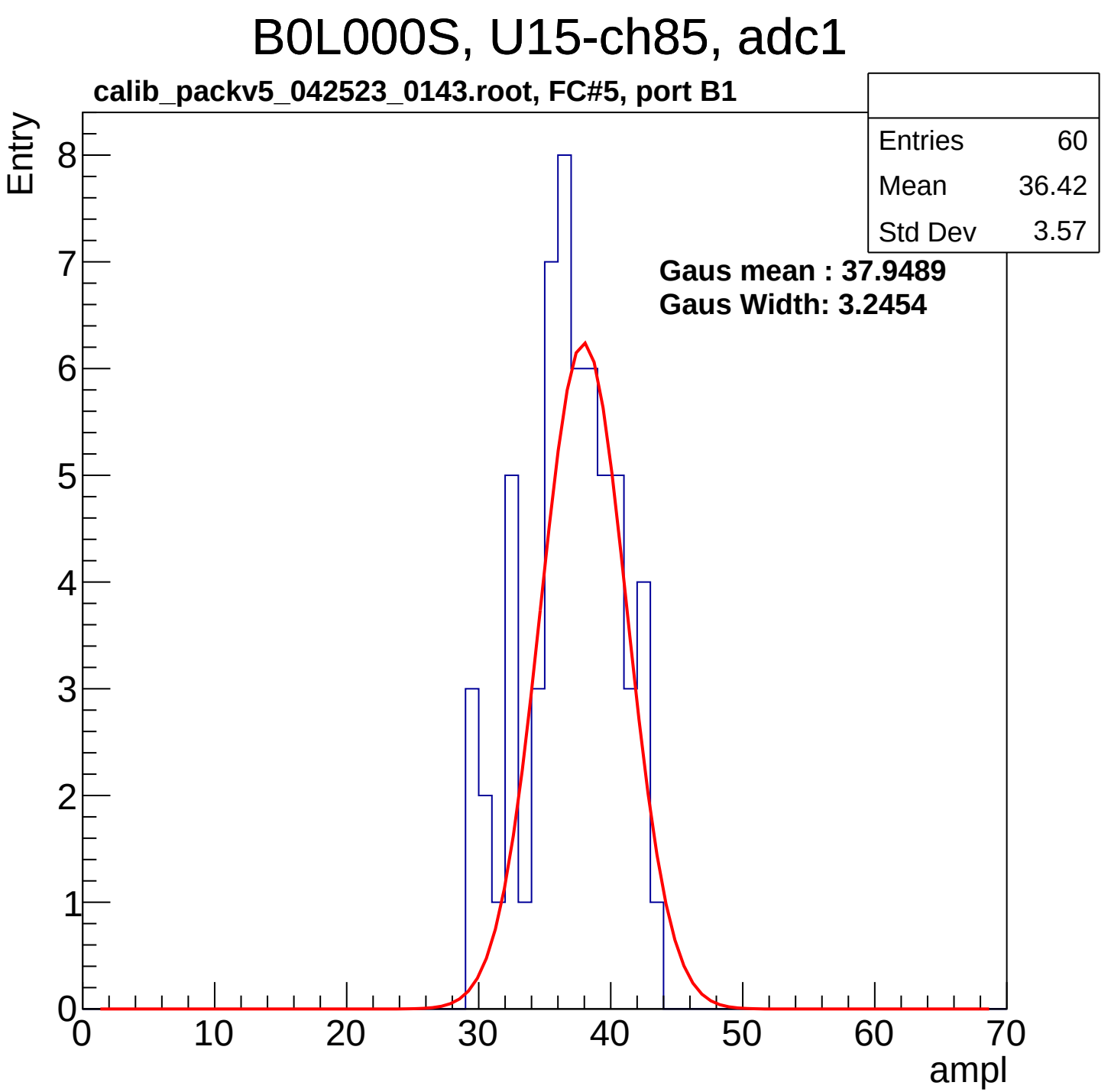
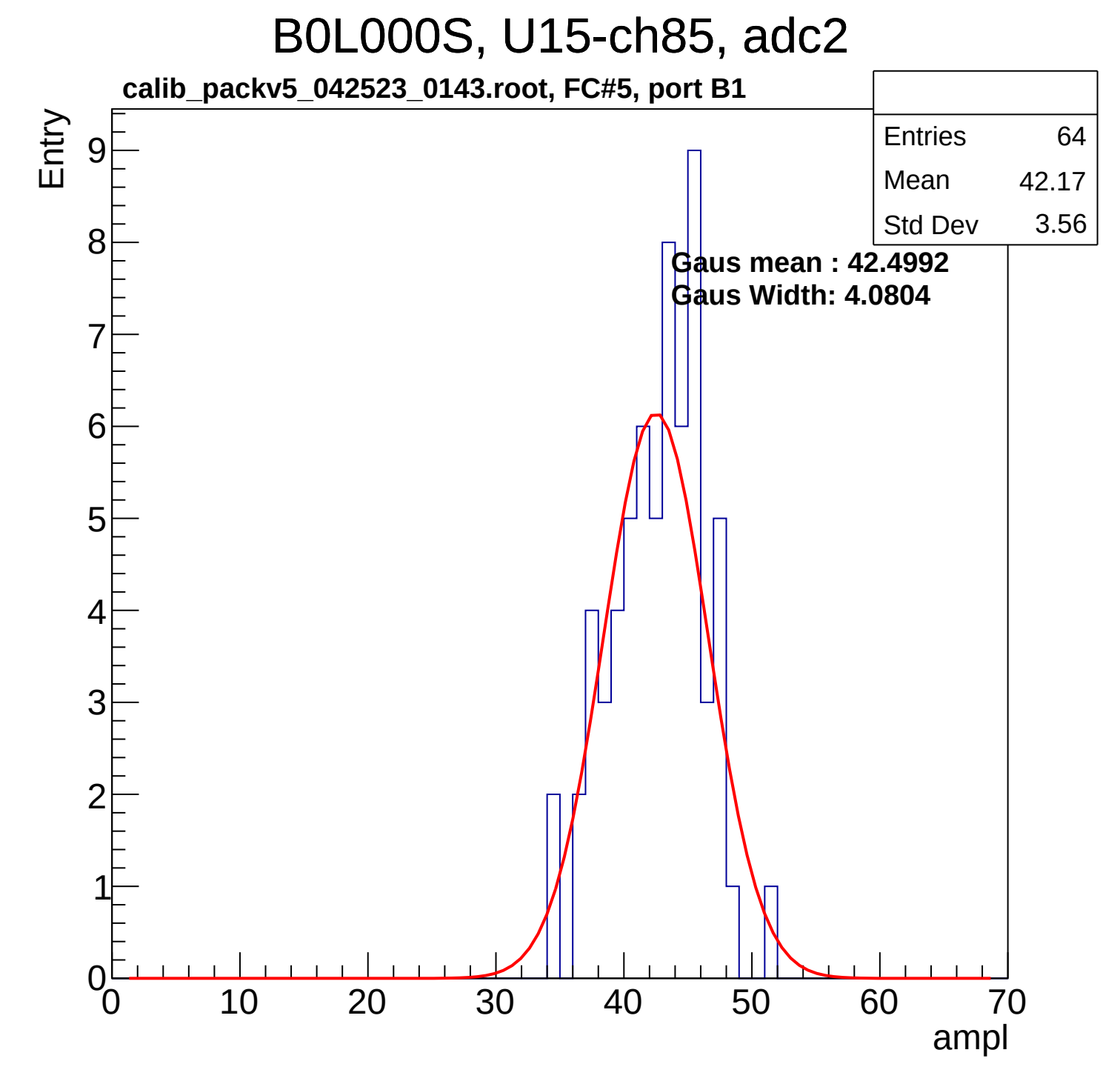

B0L000S, U15-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.42
Std Dev 3.57
Gaus mean : 37.9489
Gaus Width: 3.2454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.17
Std Dev 3.56
Gaus mean : 42.4992
Gaus Width: 4.0804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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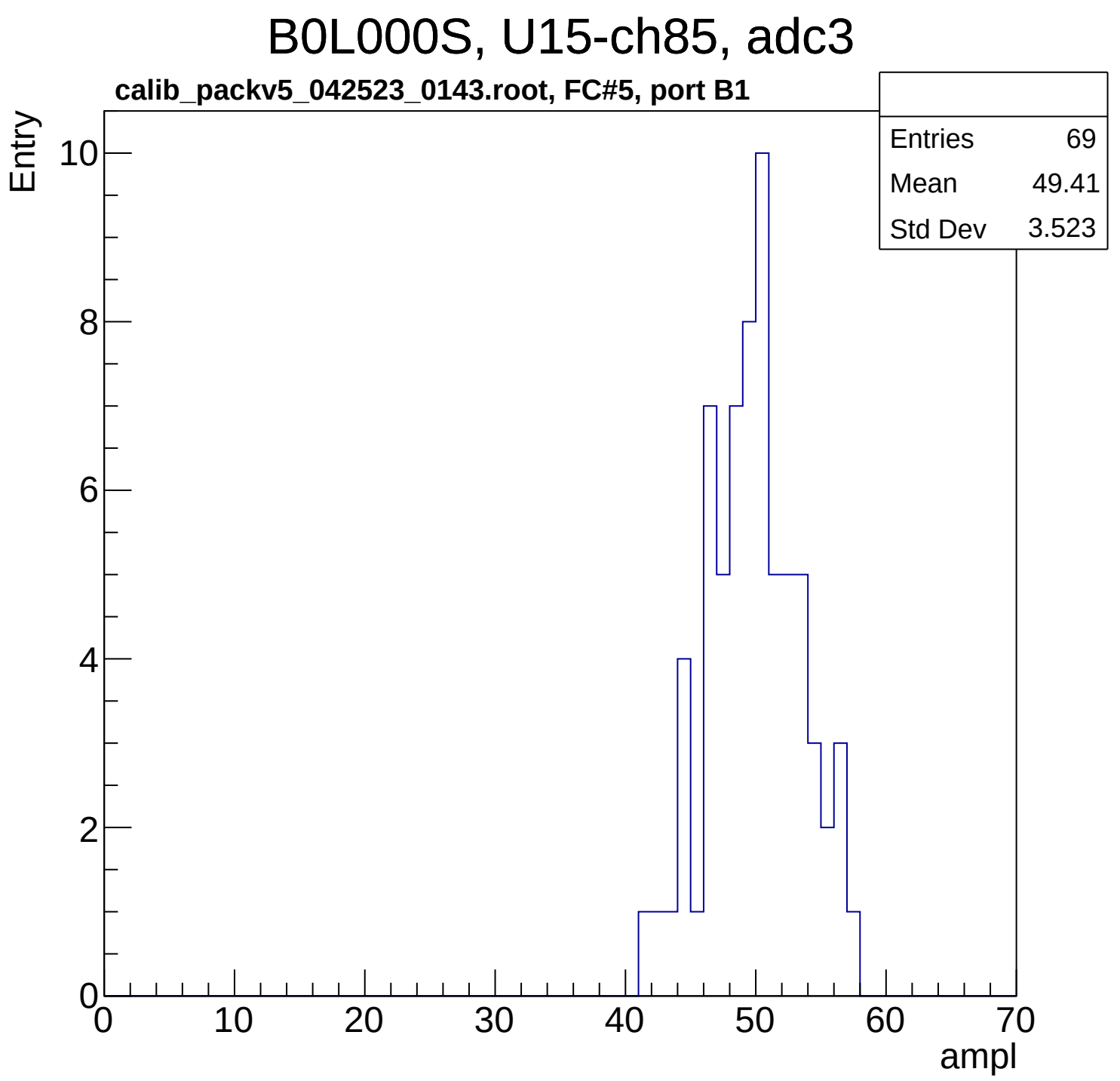
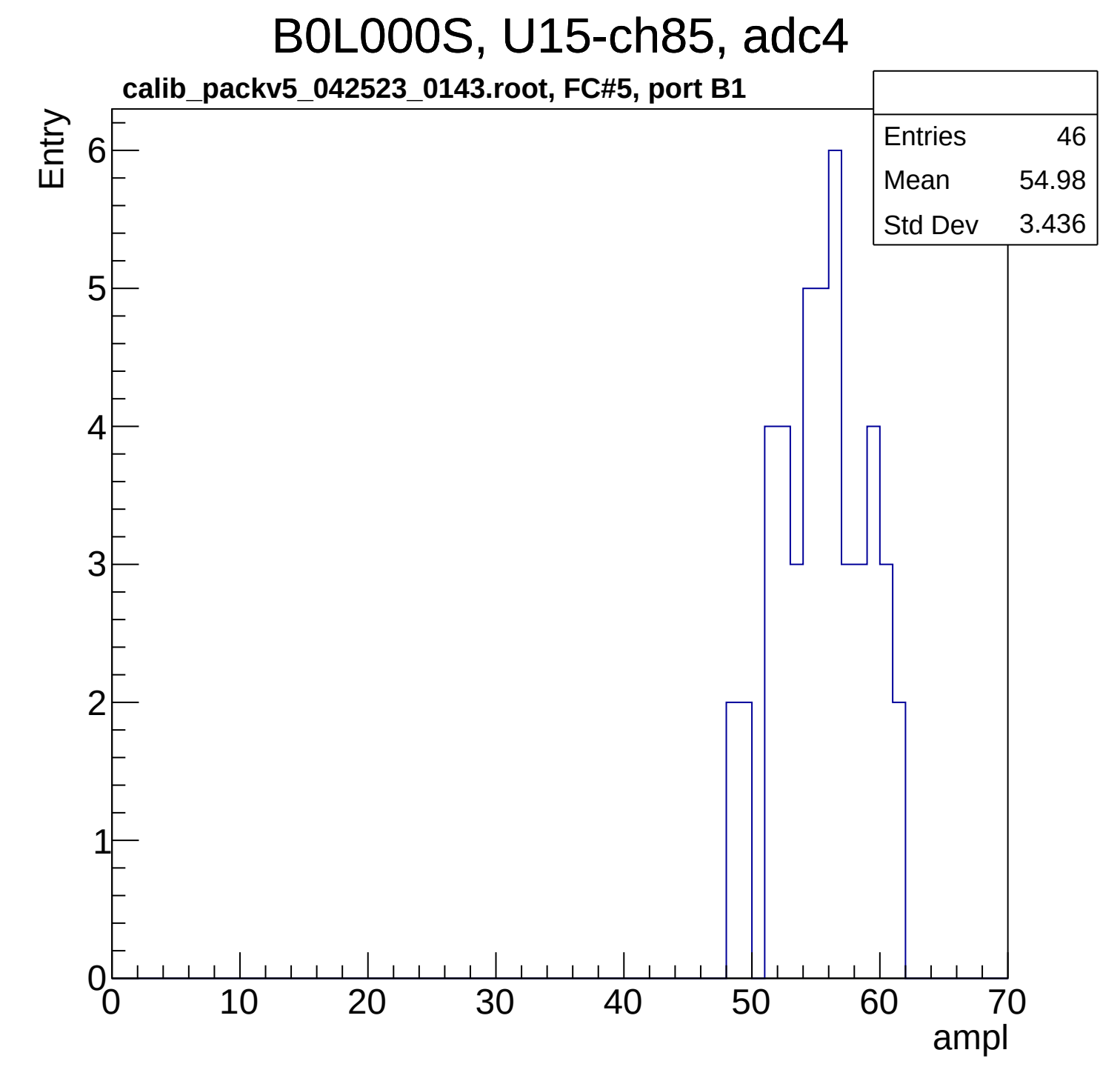
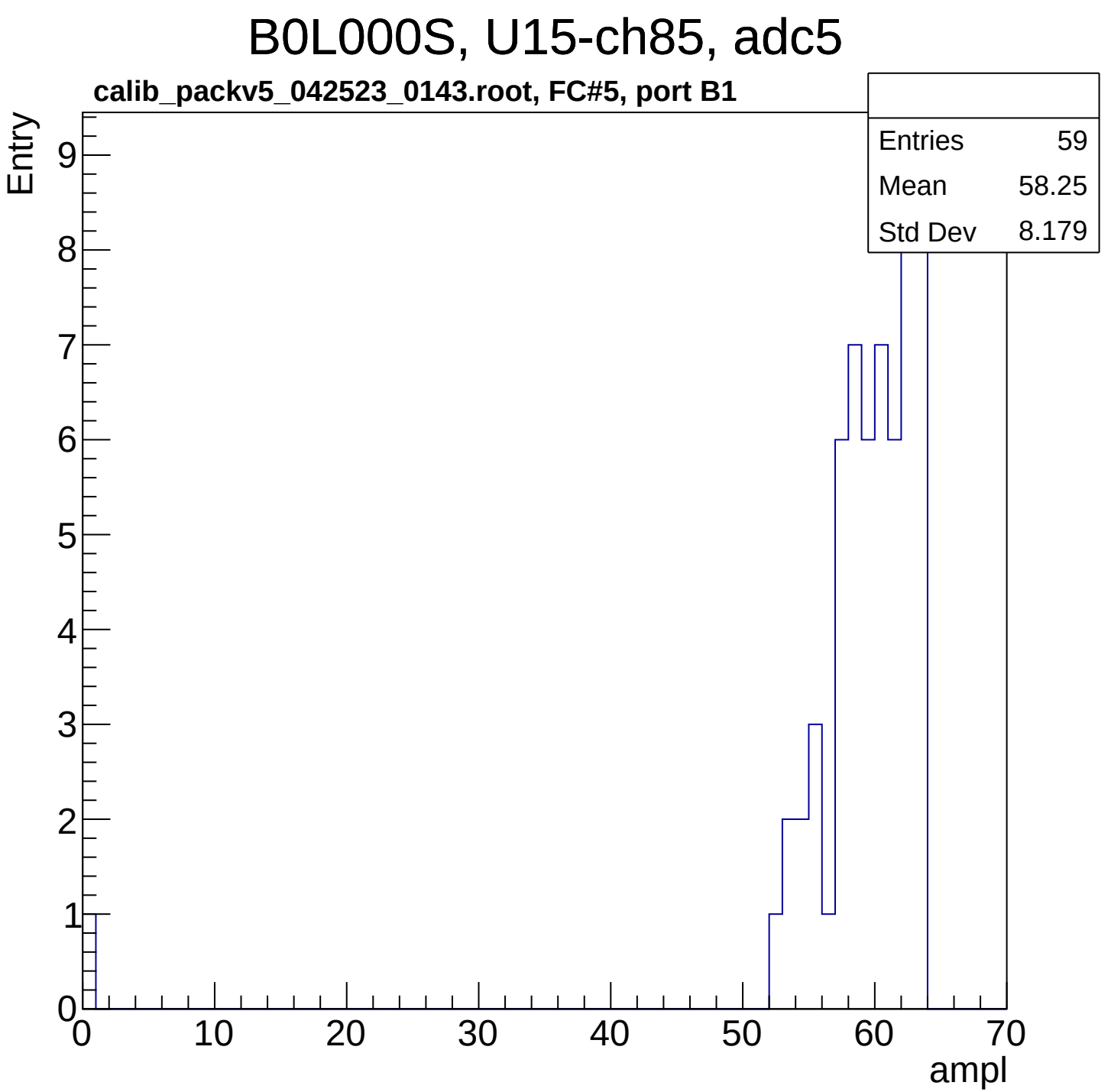
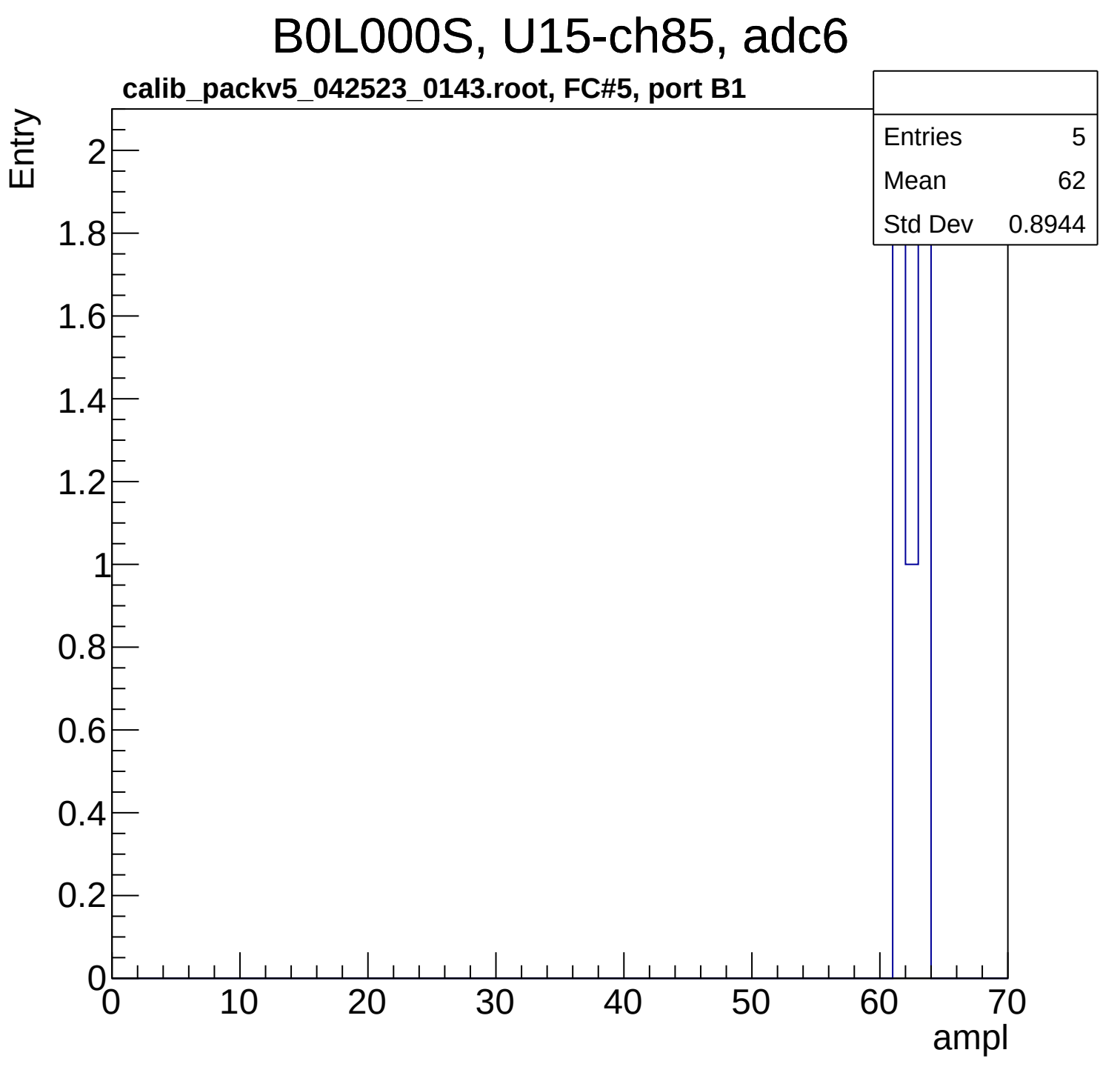

B0L000S, U15-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.41
Std Dev 3.523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.98
Std Dev 3.436
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 58.25
Std Dev 8.179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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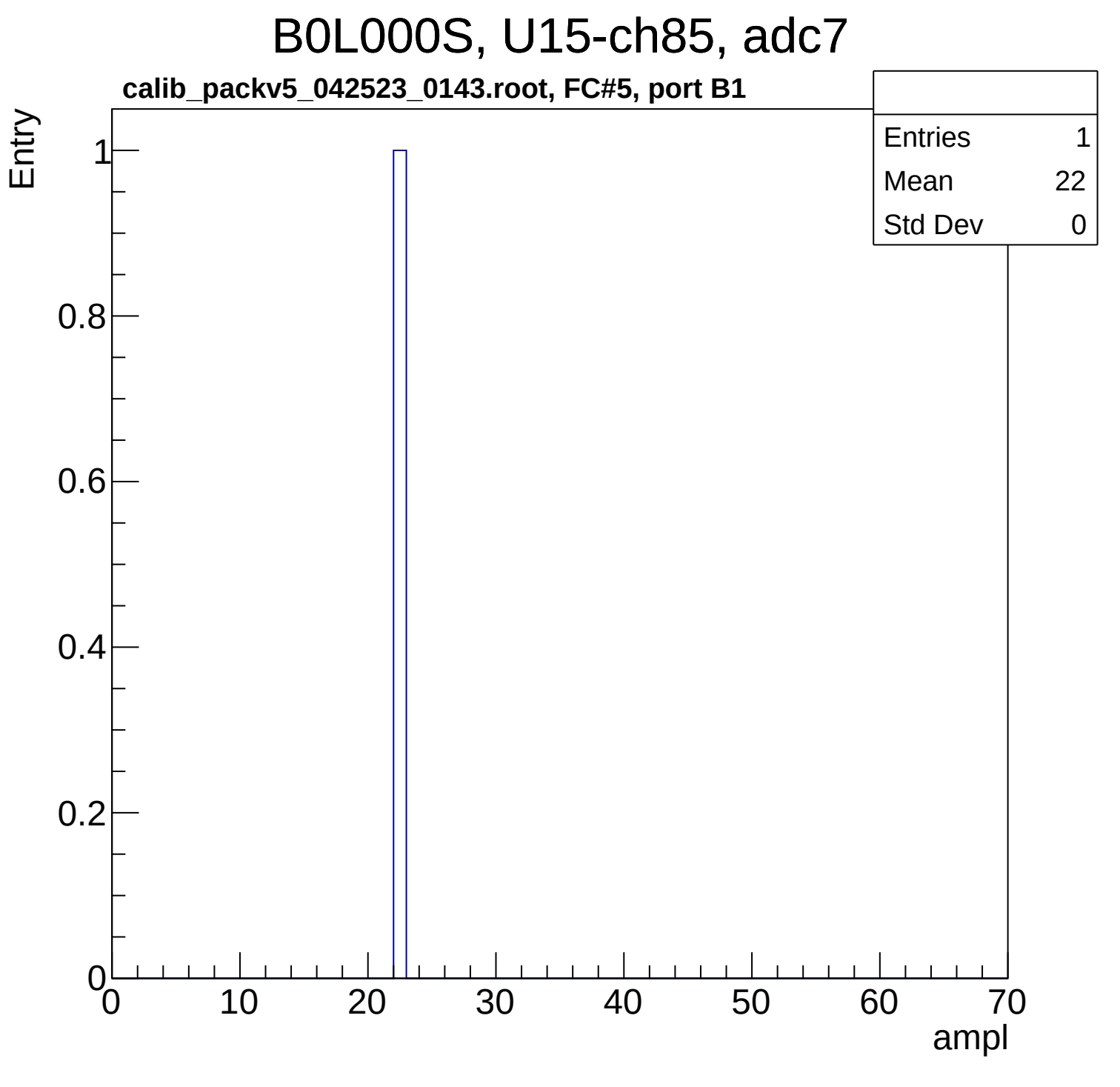
B0L000S, U15-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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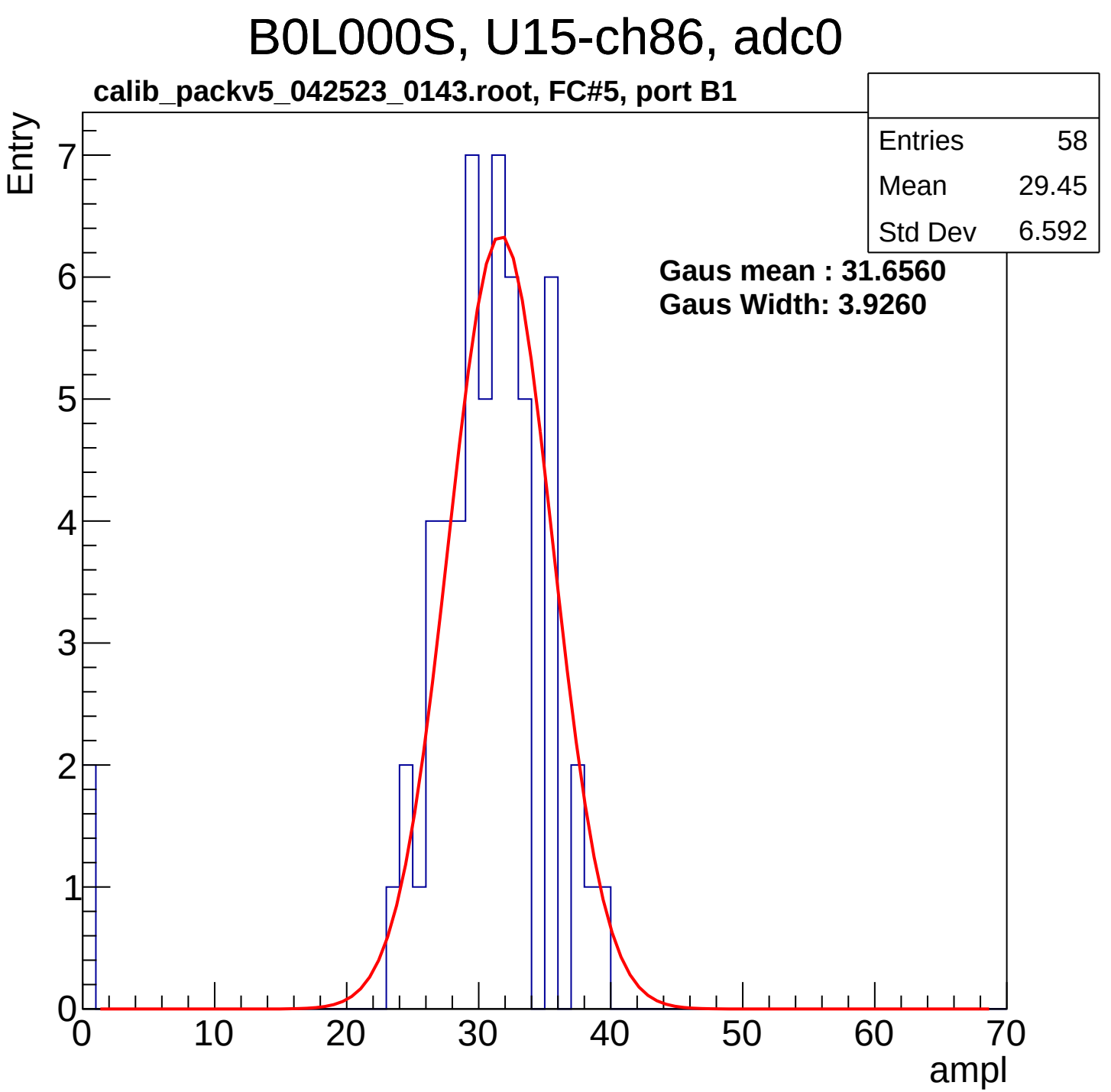
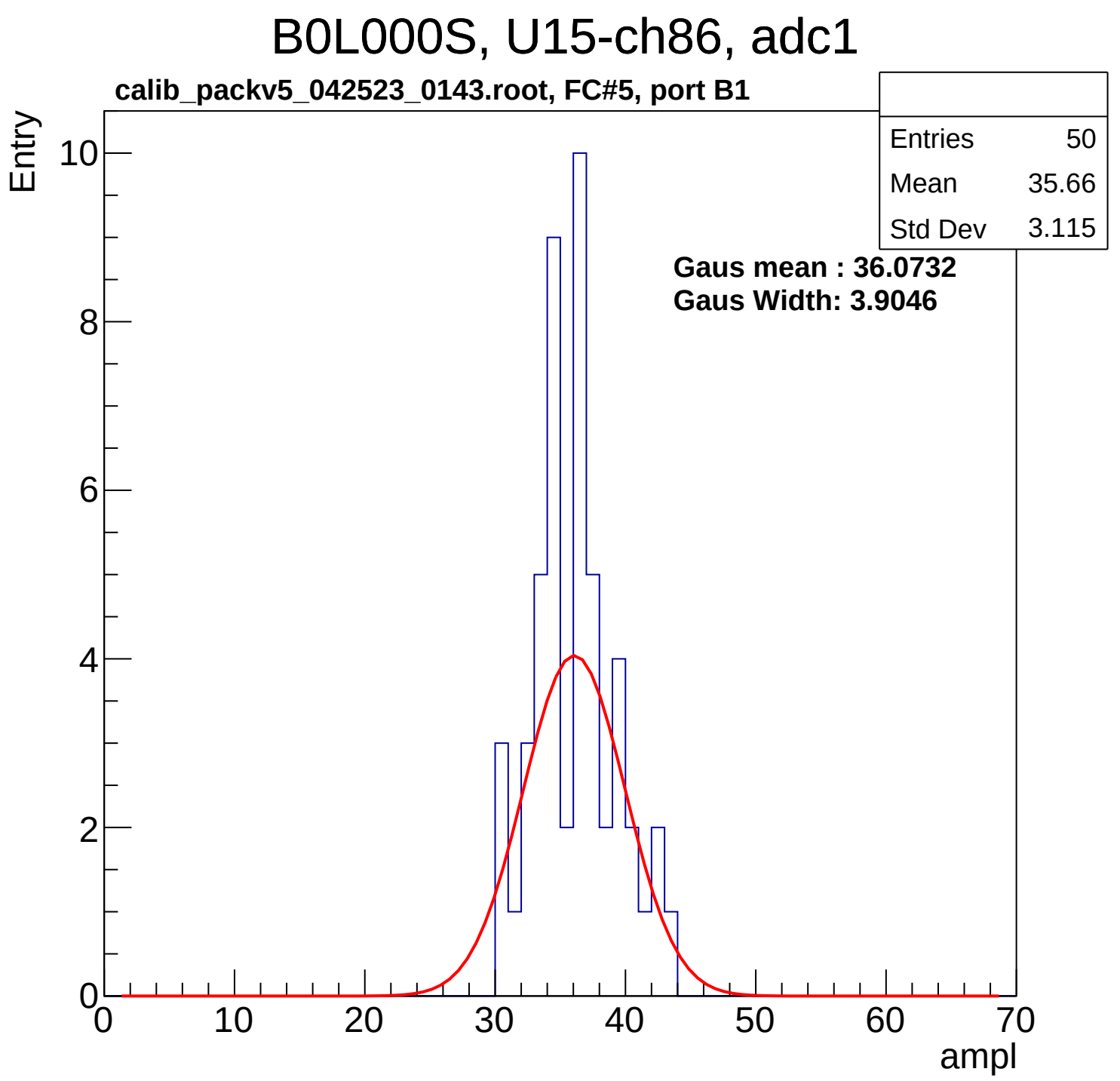
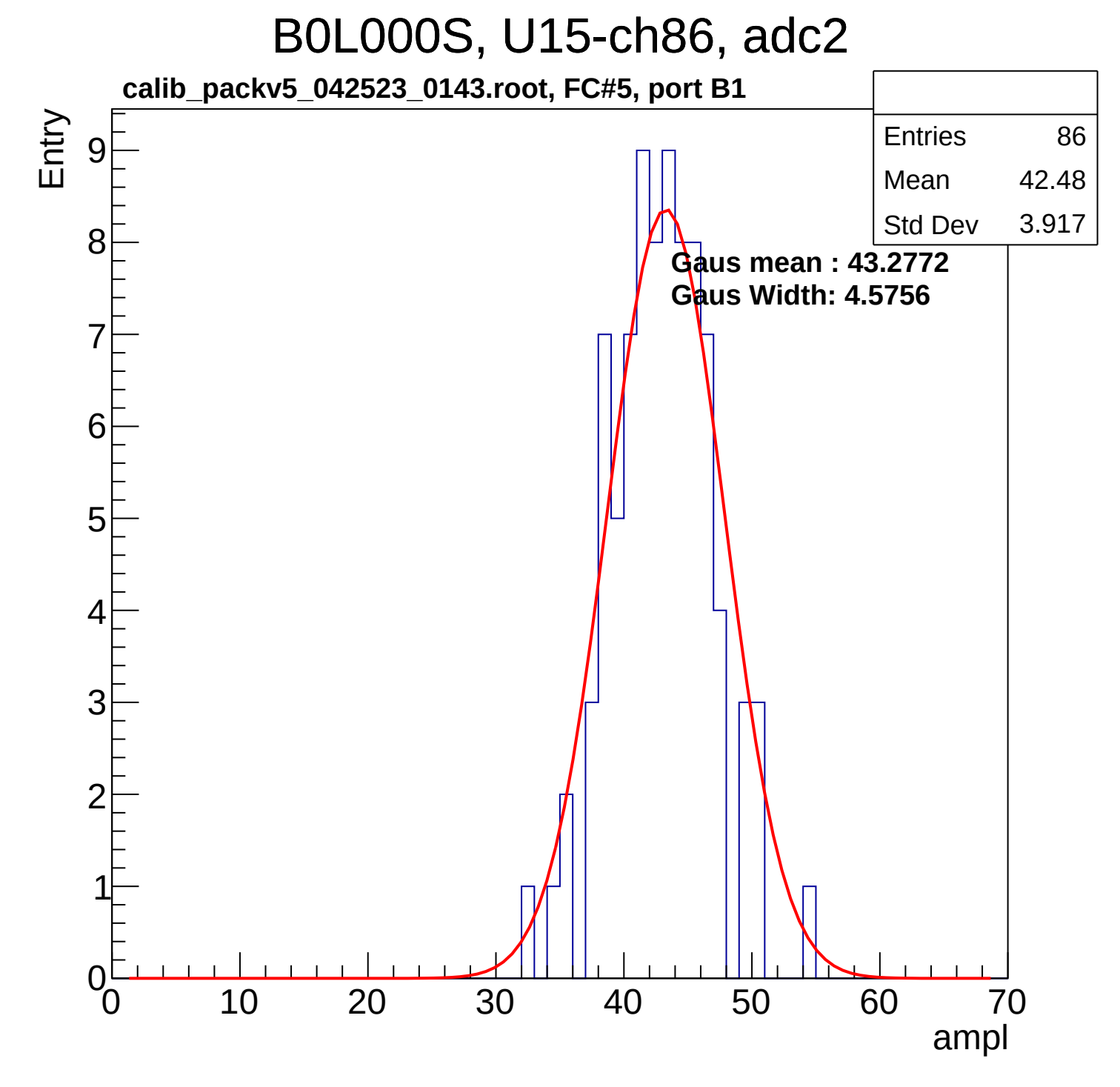
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 29.45
Std Dev 6.592
Gaus mean : 31.6560
Gaus Width: 3.9260
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 35.66
Std Dev 3.115
Gaus mean : 36.0732
Gaus Width: 3.9046
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 42.48
Std Dev 3.917
Gaus mean : 43.2772
Gaus Width: 4.5756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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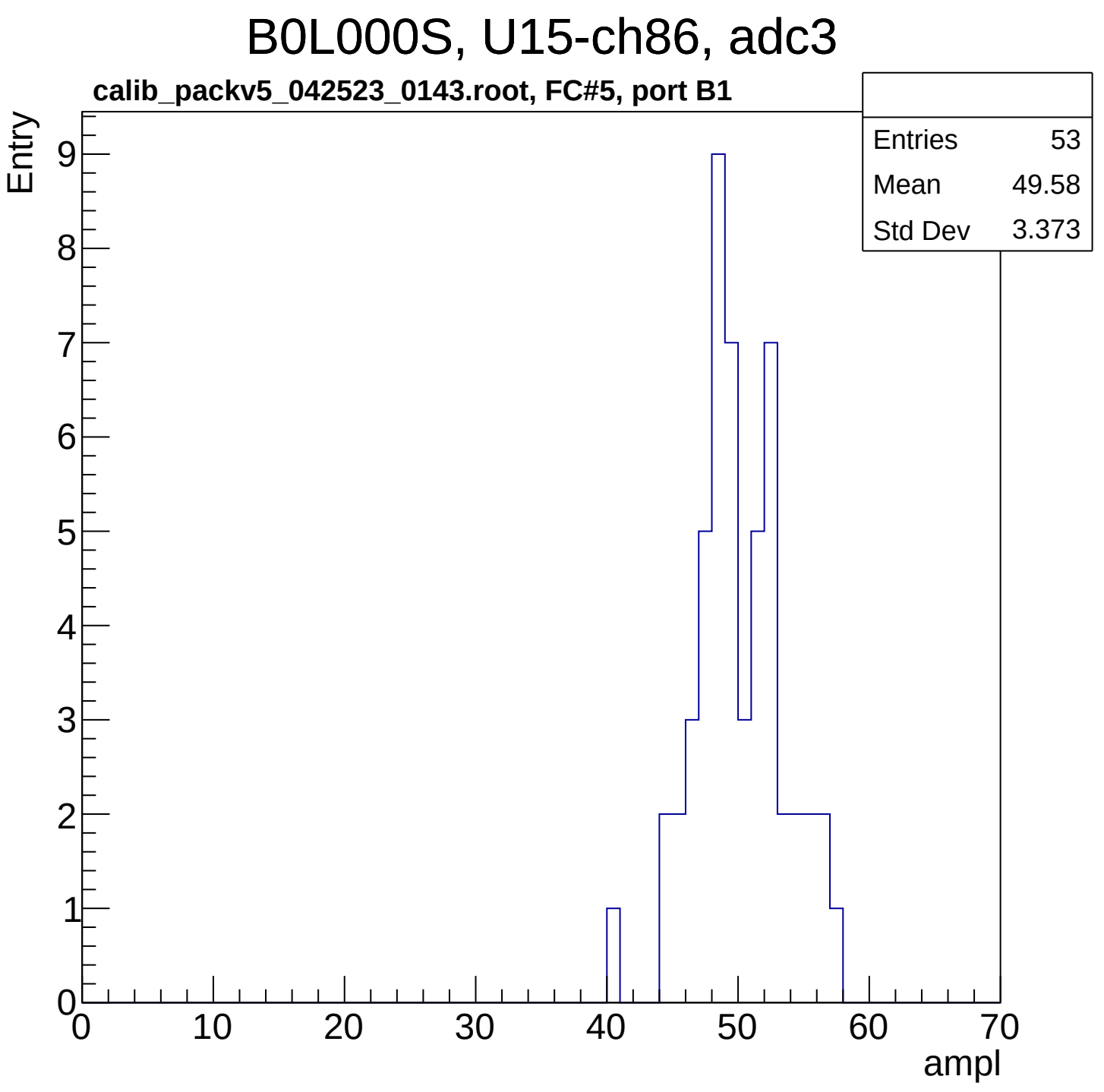
B0L000S, U15-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 49.58
Std Dev 3.373
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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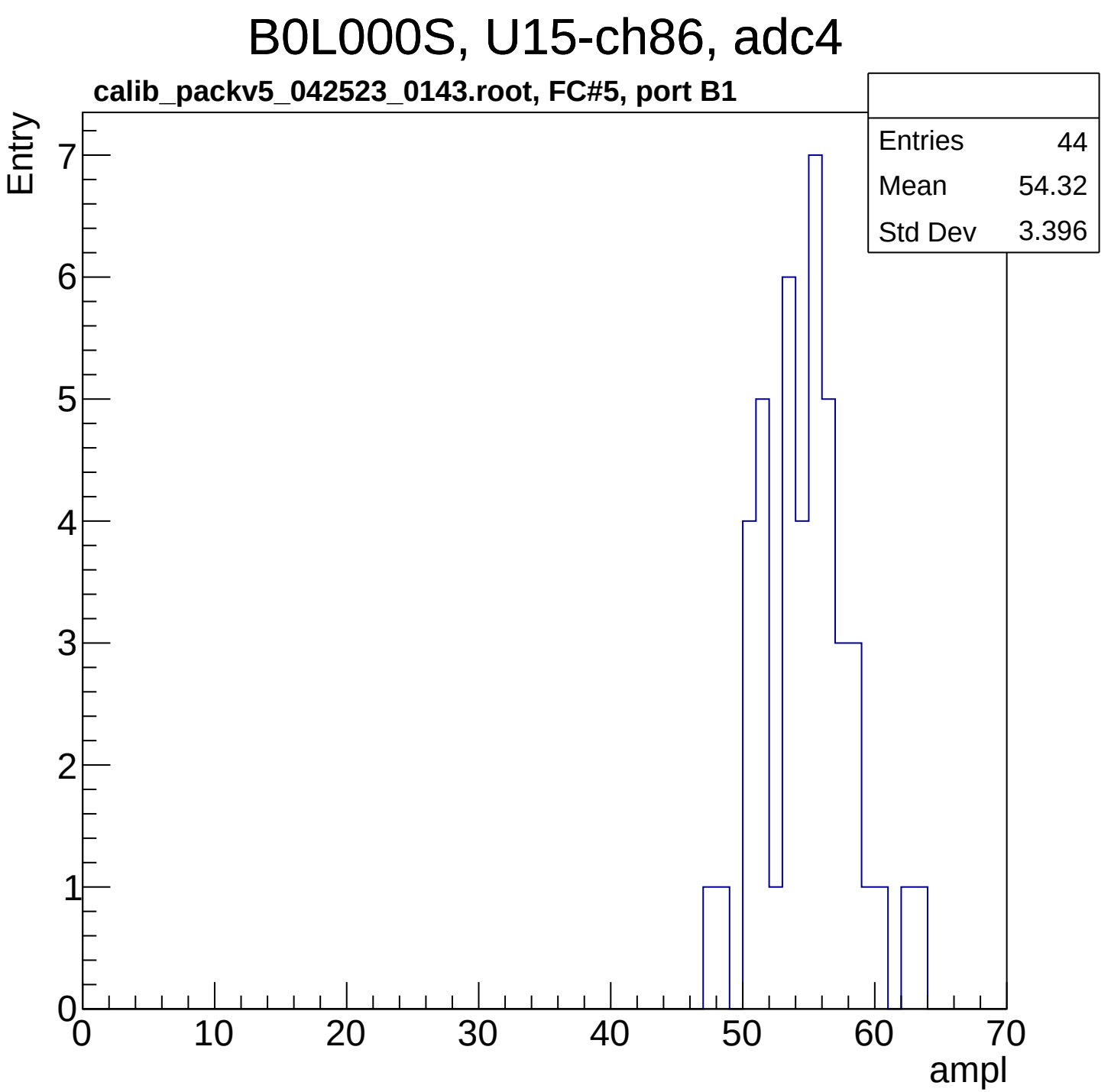
B0L000S, U15-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 54.32
Std Dev 3.396
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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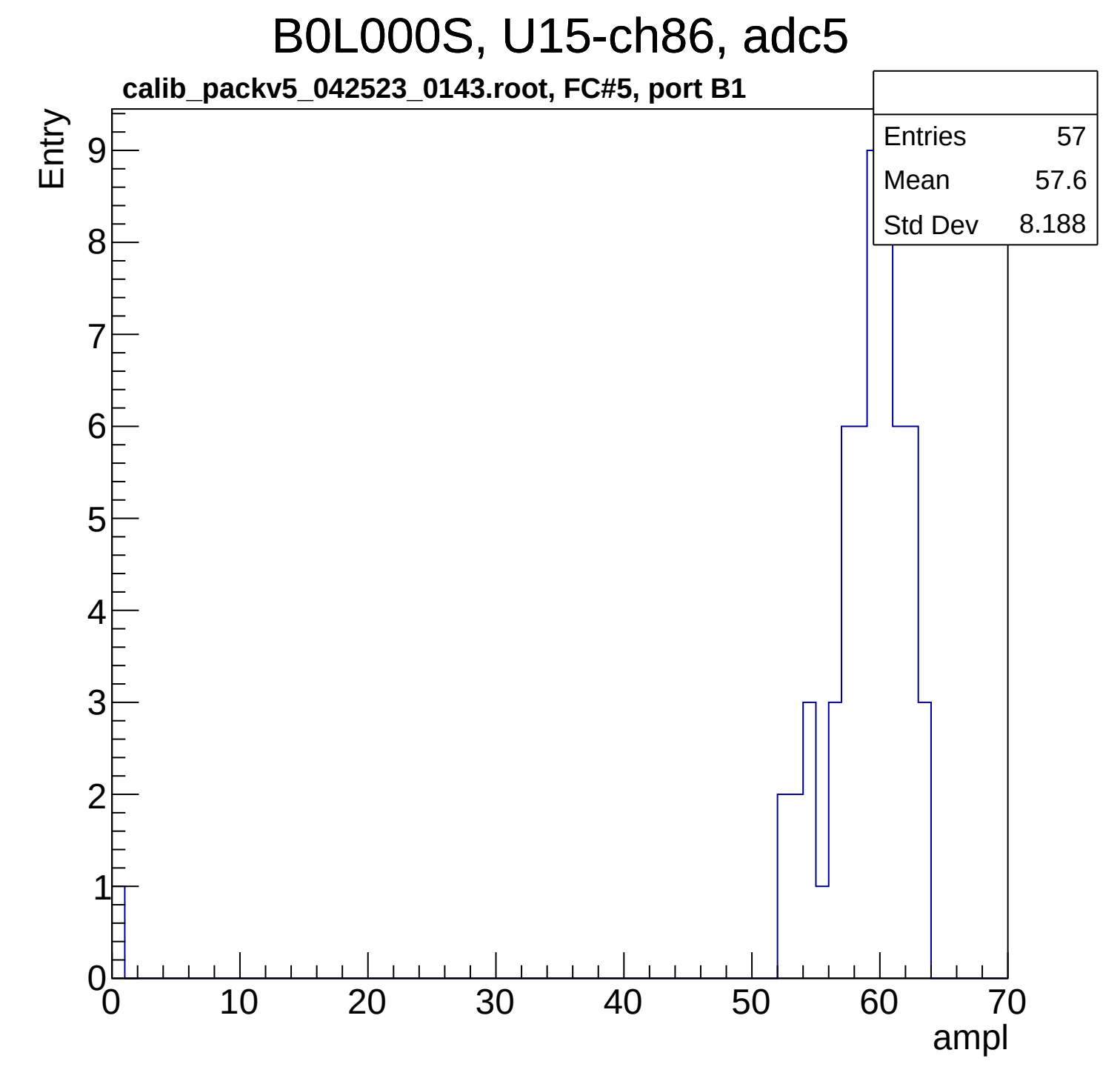
B0L000S, U15-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.6
Std Dev 8.188
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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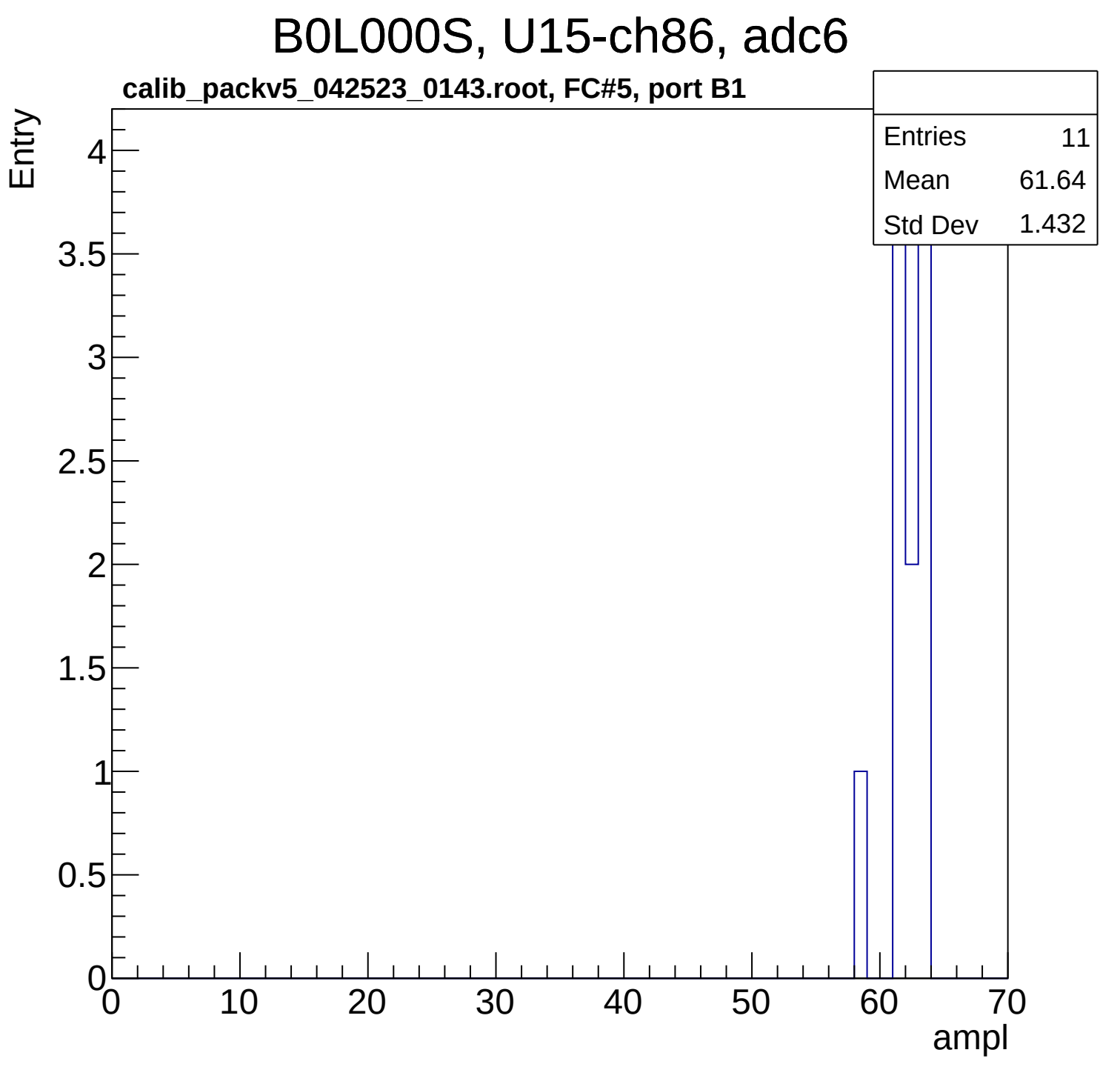
B0L000S, U15-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.64
Std Dev 1.432
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
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50
60
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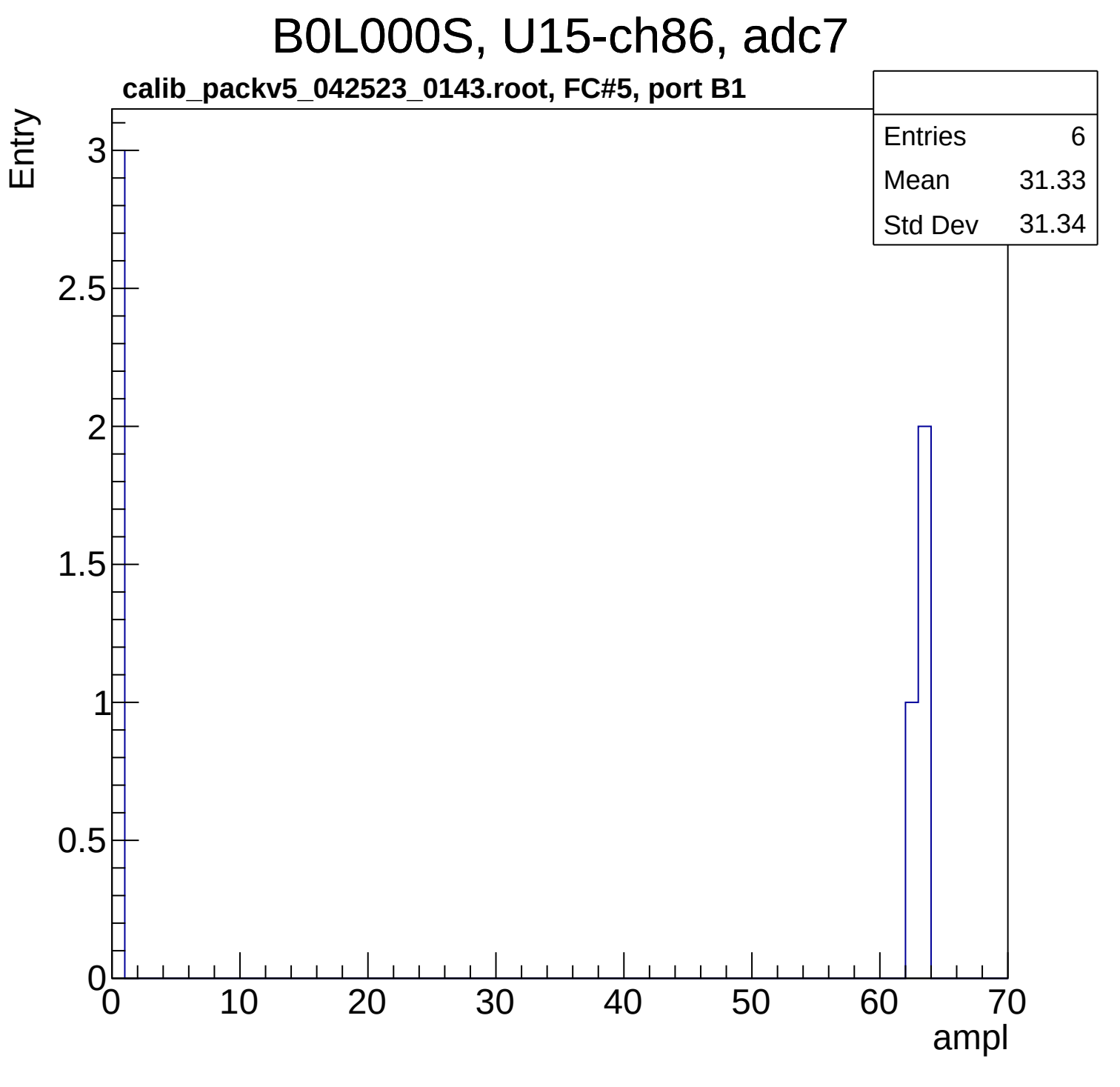
B0L000S, U15-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 31.33
Std Dev 31.34
Entry
ampl
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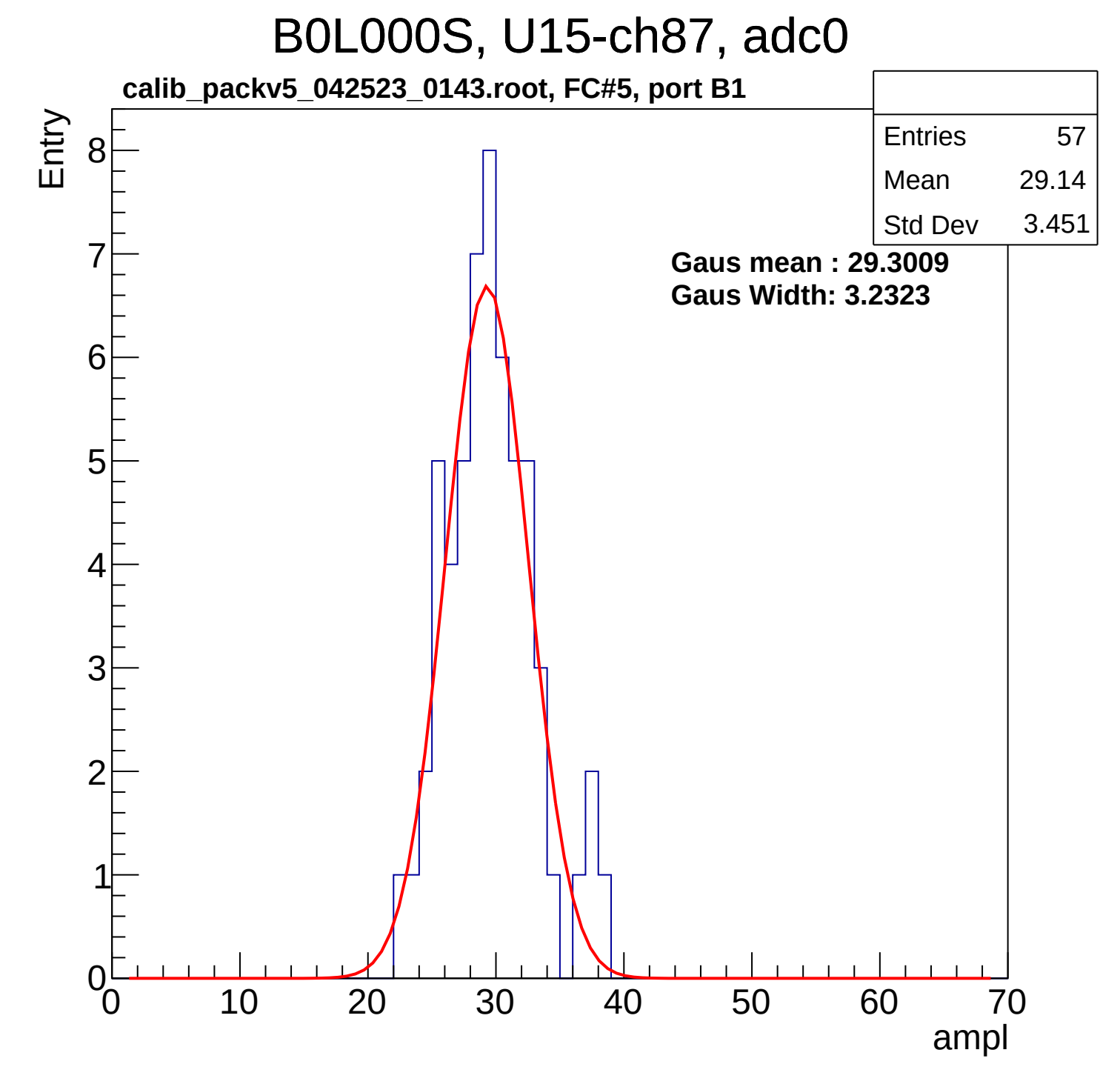
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.14
Std Dev 3.451
Gaus mean : 29.3009
Gaus Width: 3.2323
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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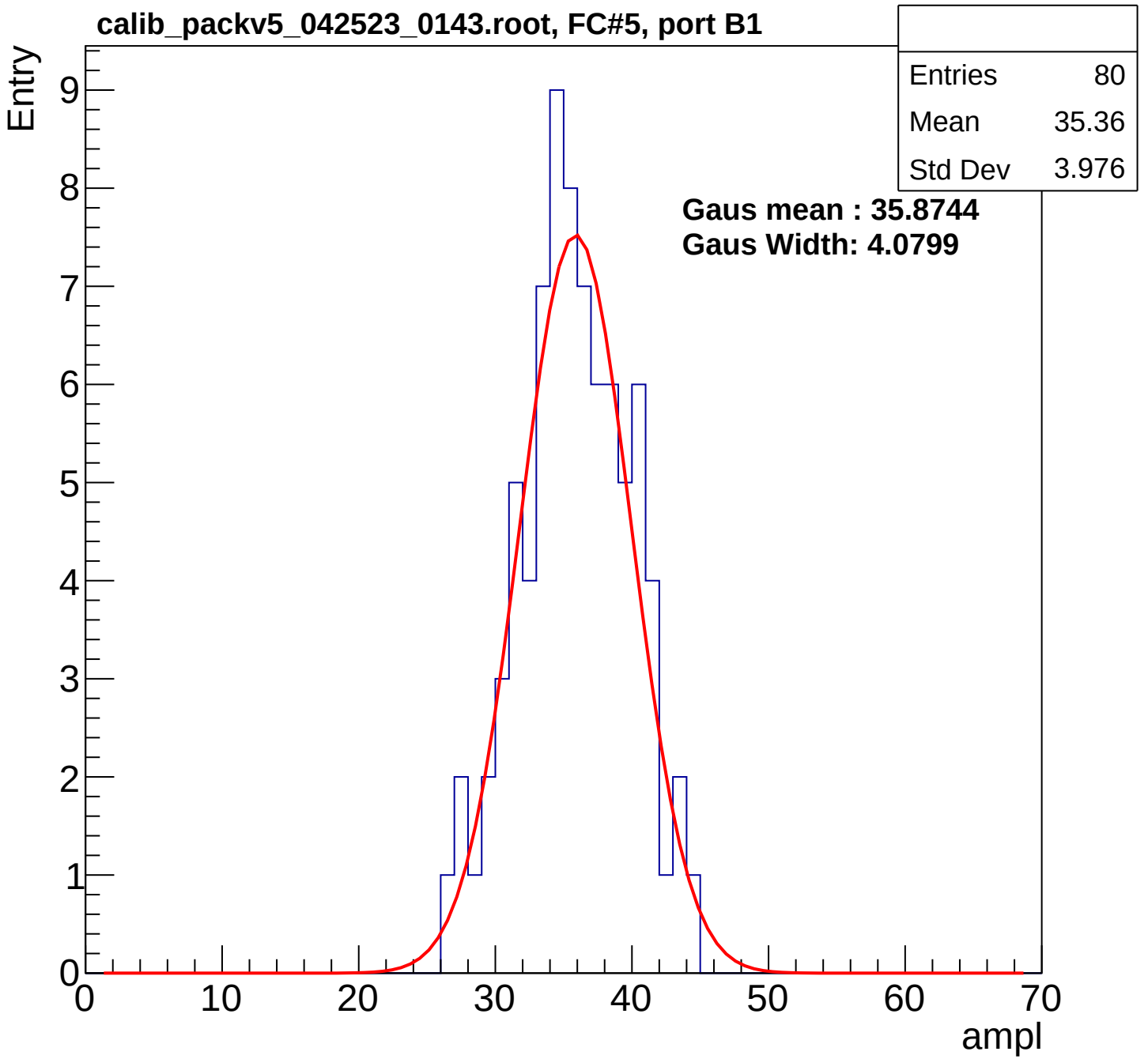
40
50
60
70


B0L000S, U15-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 35.36
Std Dev 3.976
Gaus mean : 35.8744
Gaus Width: 4.0799
Entry
ampl
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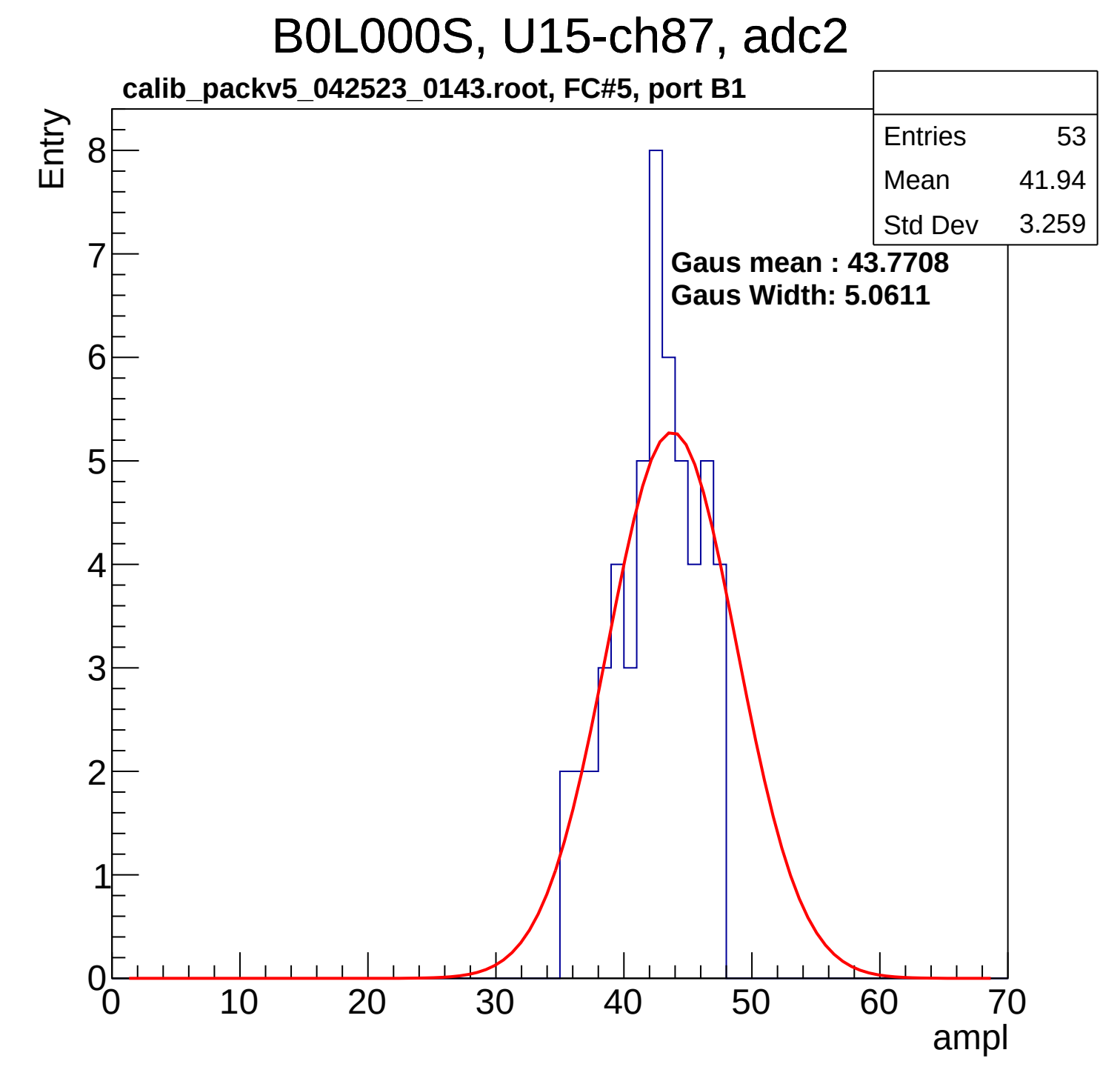
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 41.94
Std Dev 3.259
Gaus mean : 43.7708
Gaus Width: 5.0611
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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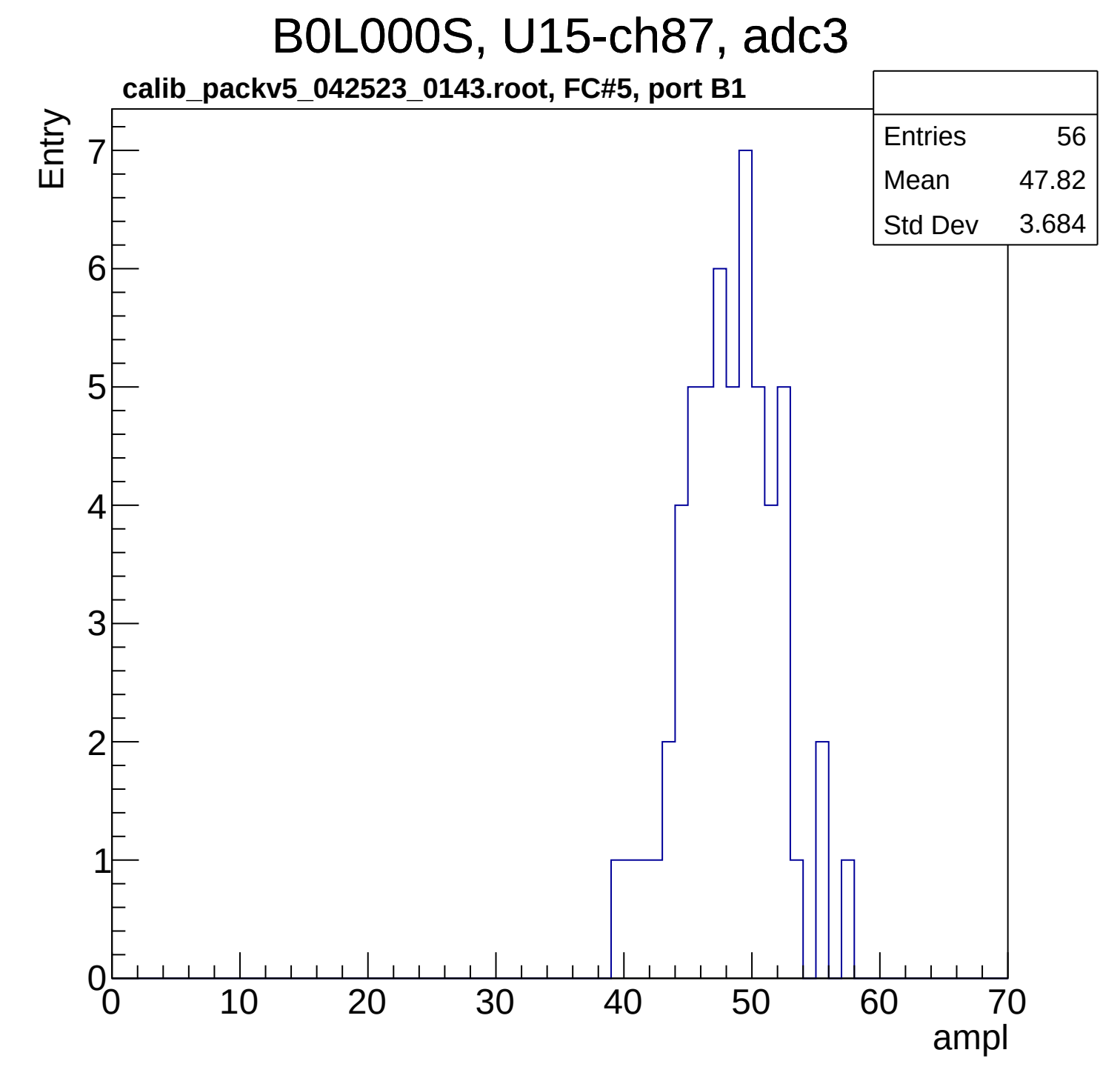
B0L000S, U15-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 47.82
Std Dev 3.684
Entry
ampl
0
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3
4
5
6
7
0
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40
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60
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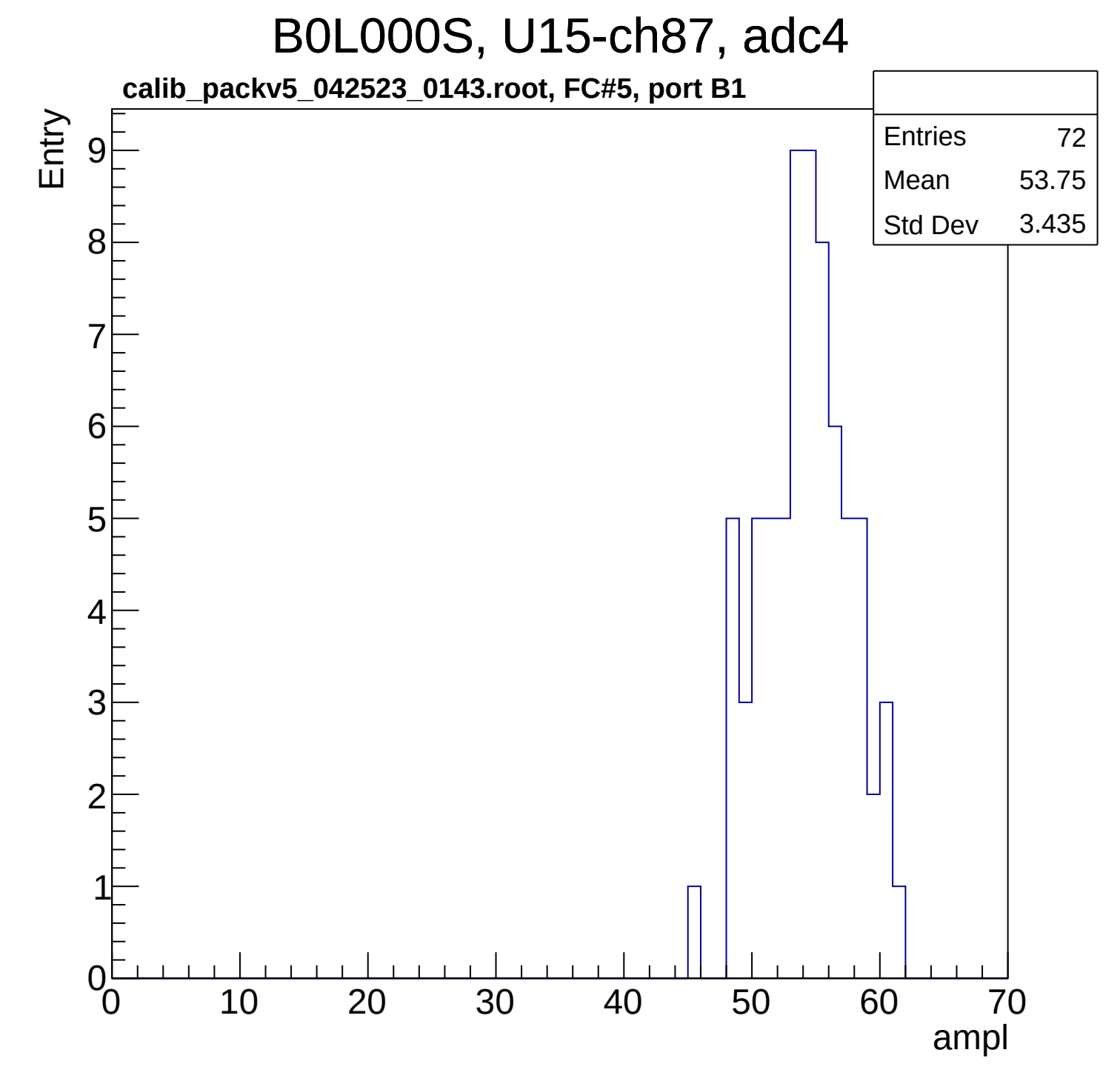
B0L000S, U15-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 53.75
Std Dev 3.435
Entry
ampl
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6
7
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0
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40
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60
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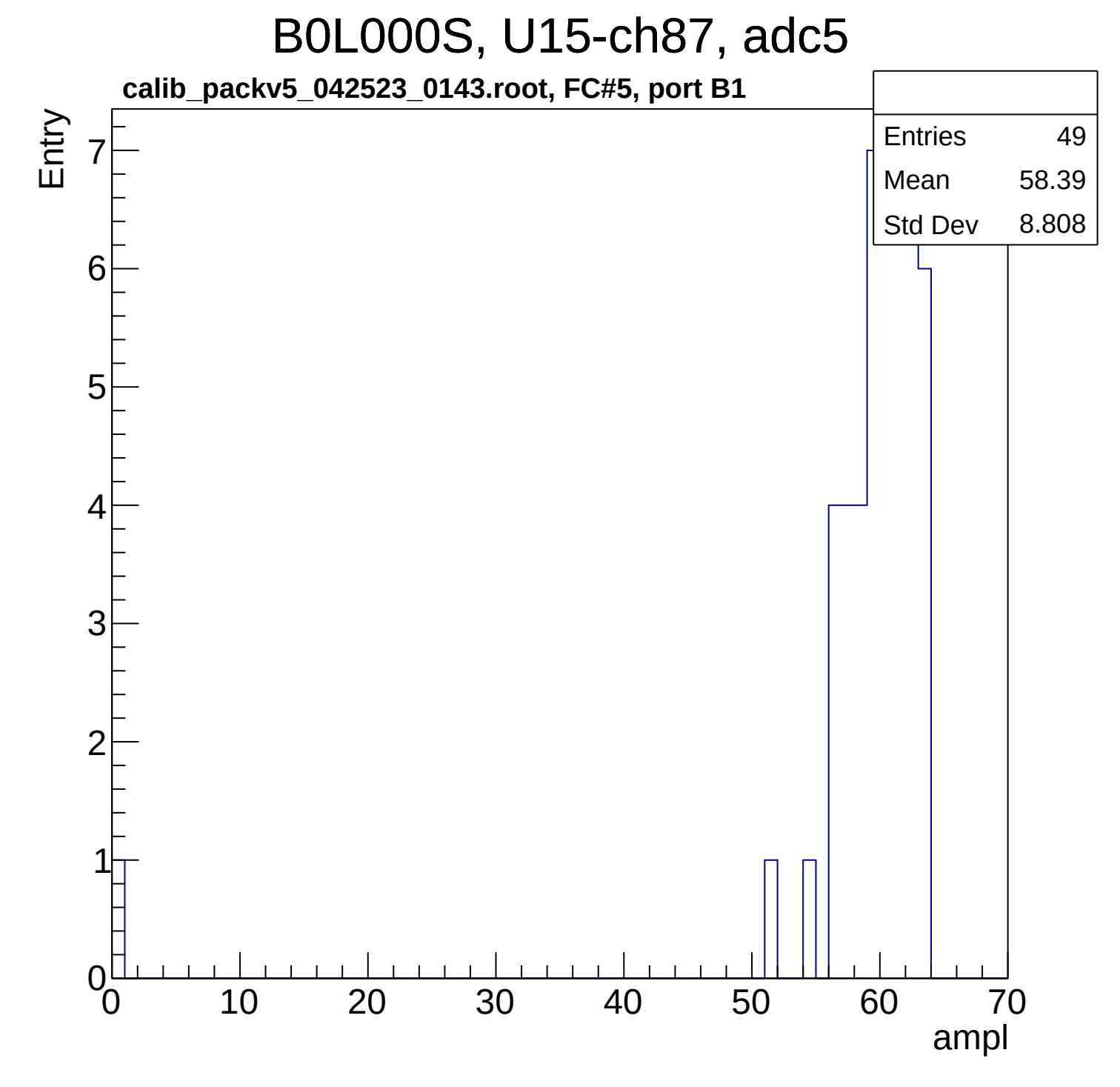
B0L000S, U15-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.39
Std Dev 8.808
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
50
60
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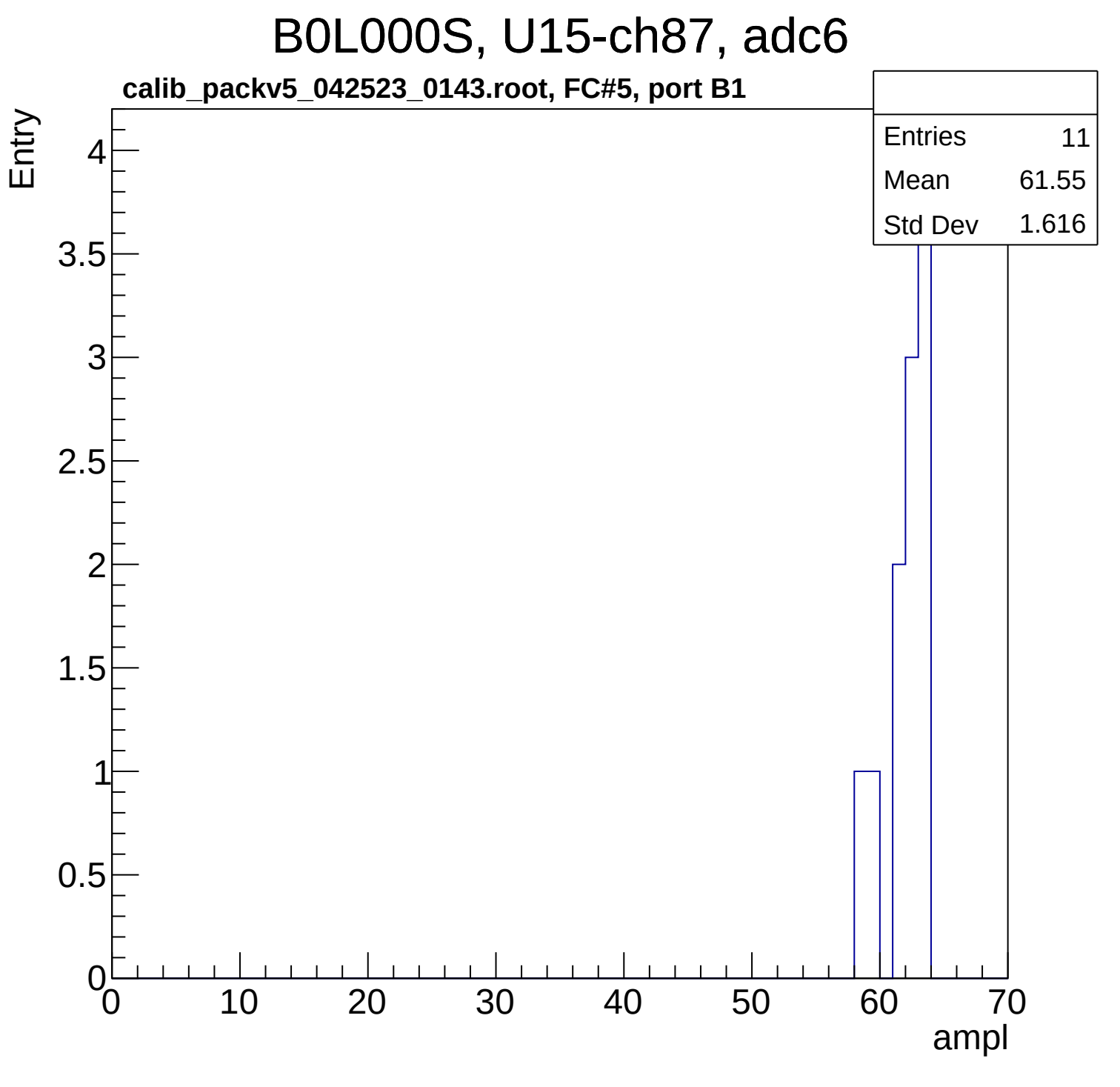
B0L000S, U15-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.55
Std Dev 1.616
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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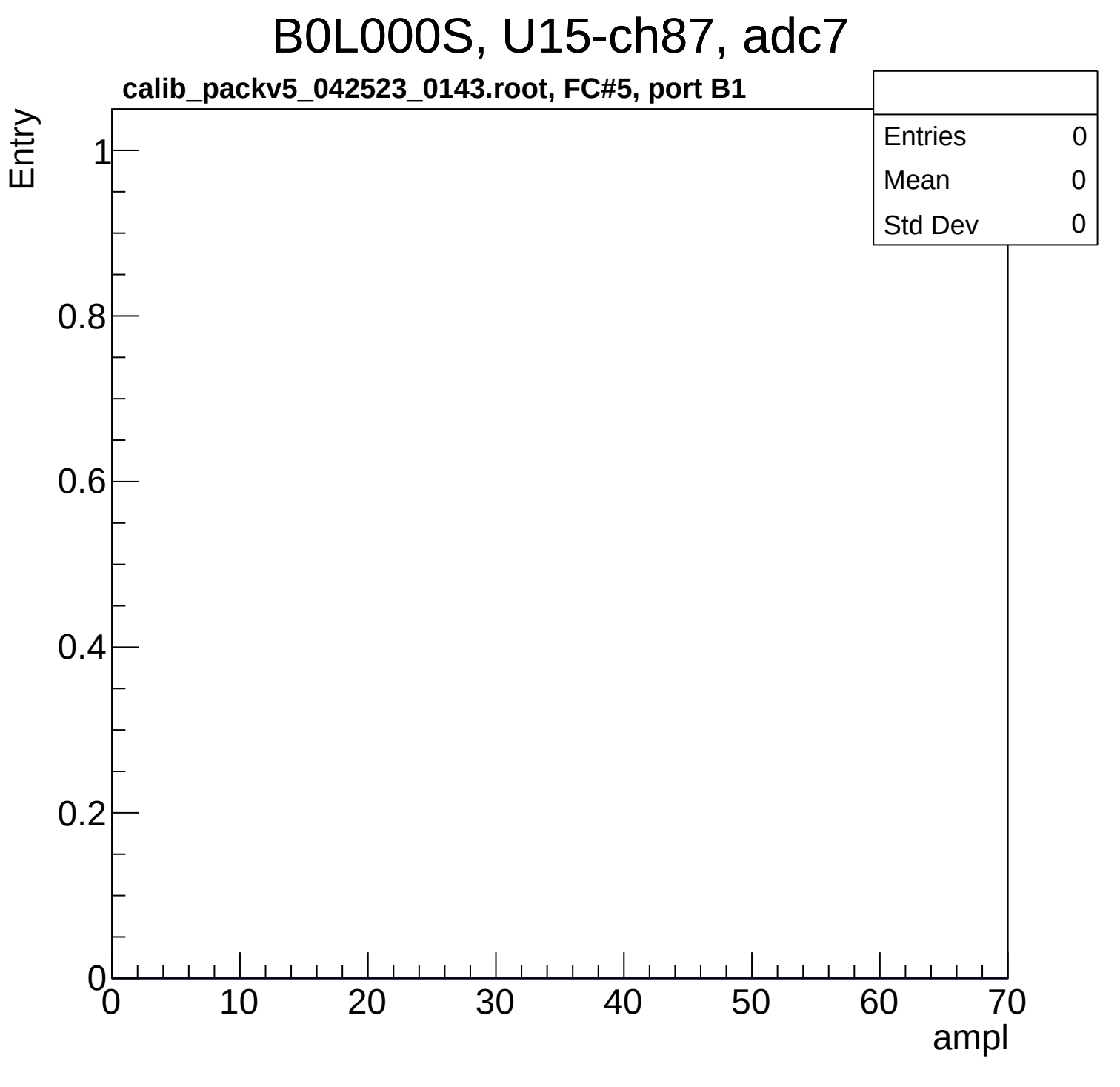
60
70


B0L000S, U15-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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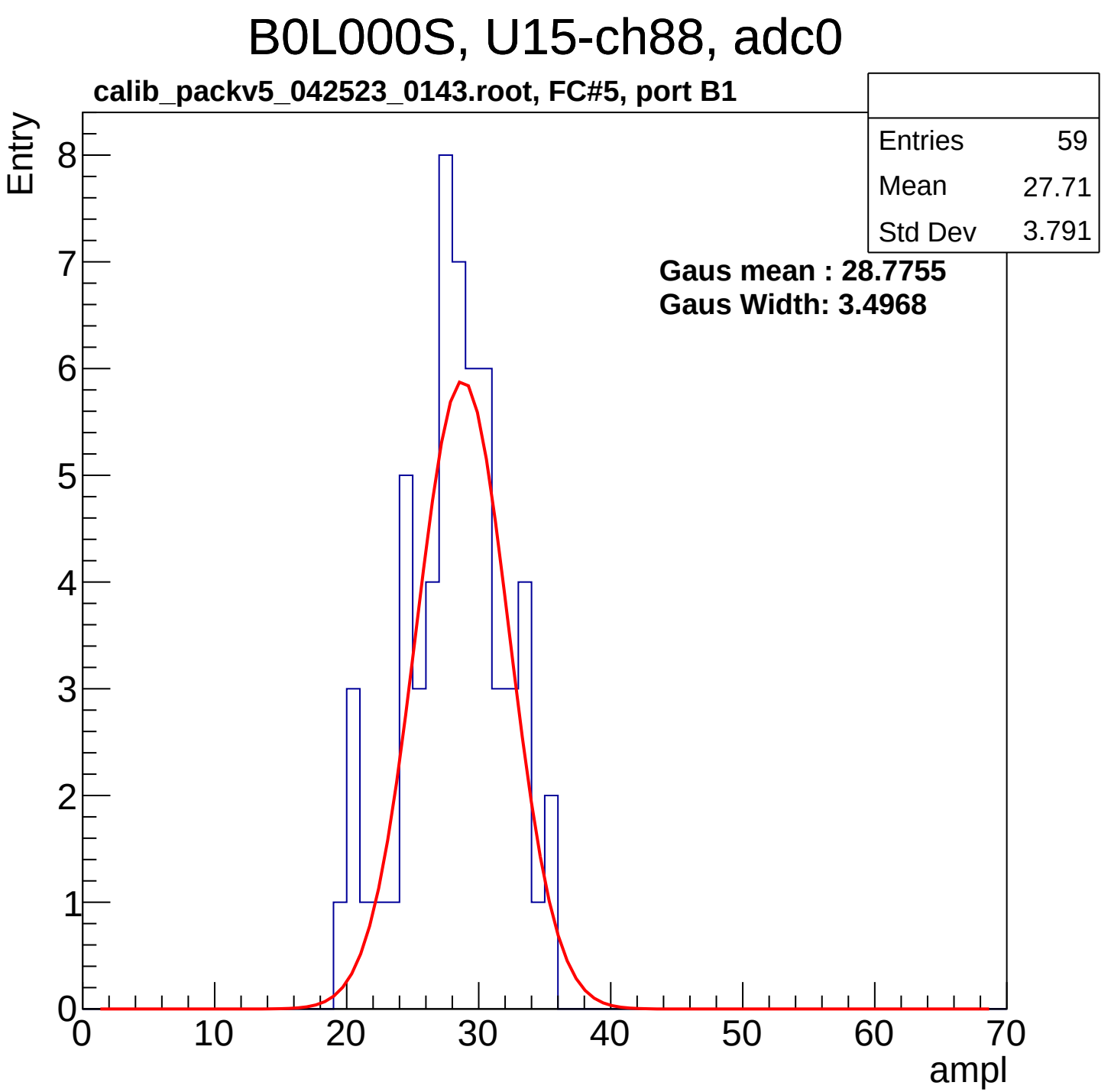
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.71
Std Dev 3.791
Gaus mean : 28.7755
Gaus Width: 3.4968
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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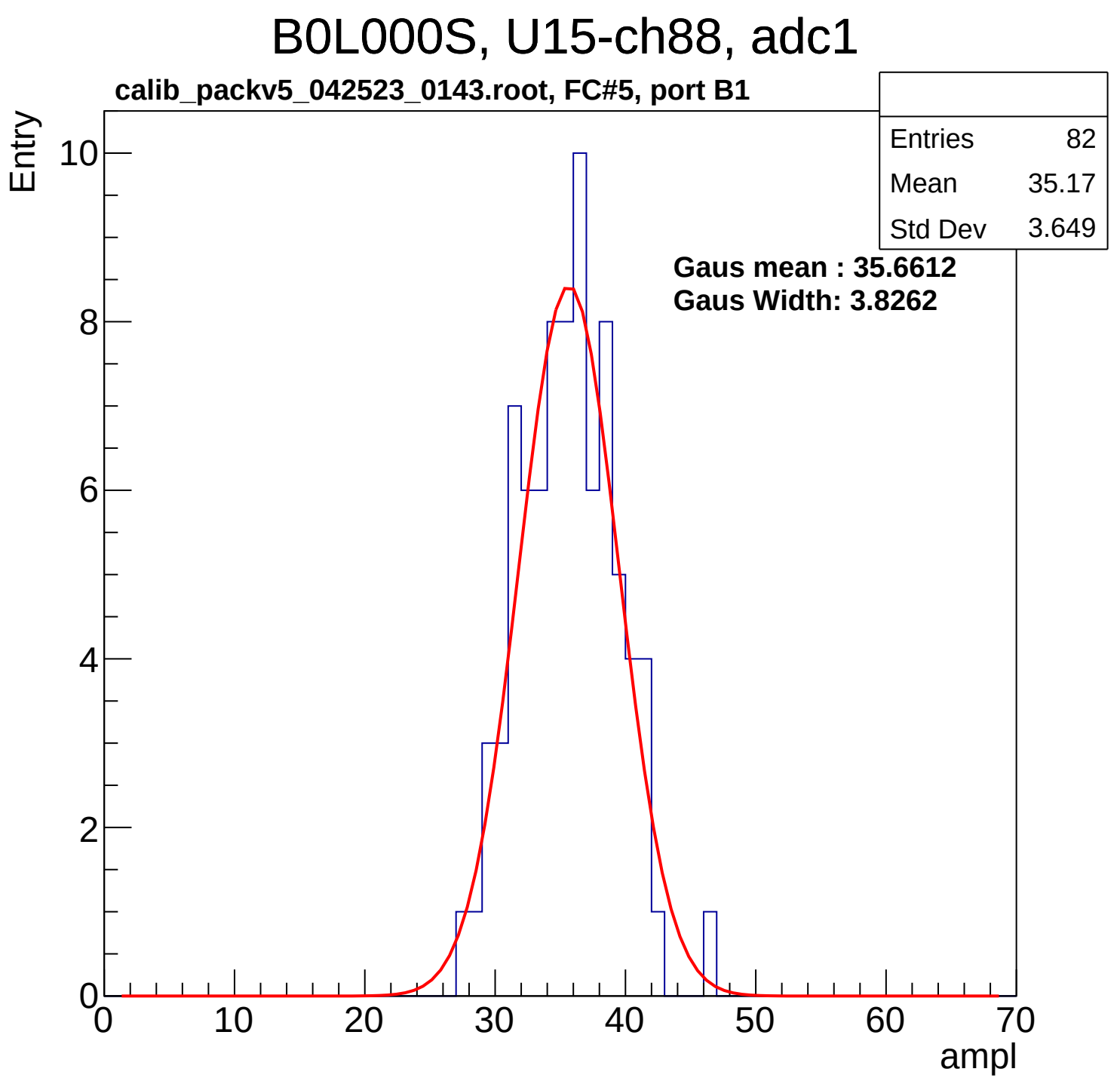
B0L000S, U15-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 35.17
Std Dev 3.649
Gaus mean : 35.6612
Gaus Width: 3.8262
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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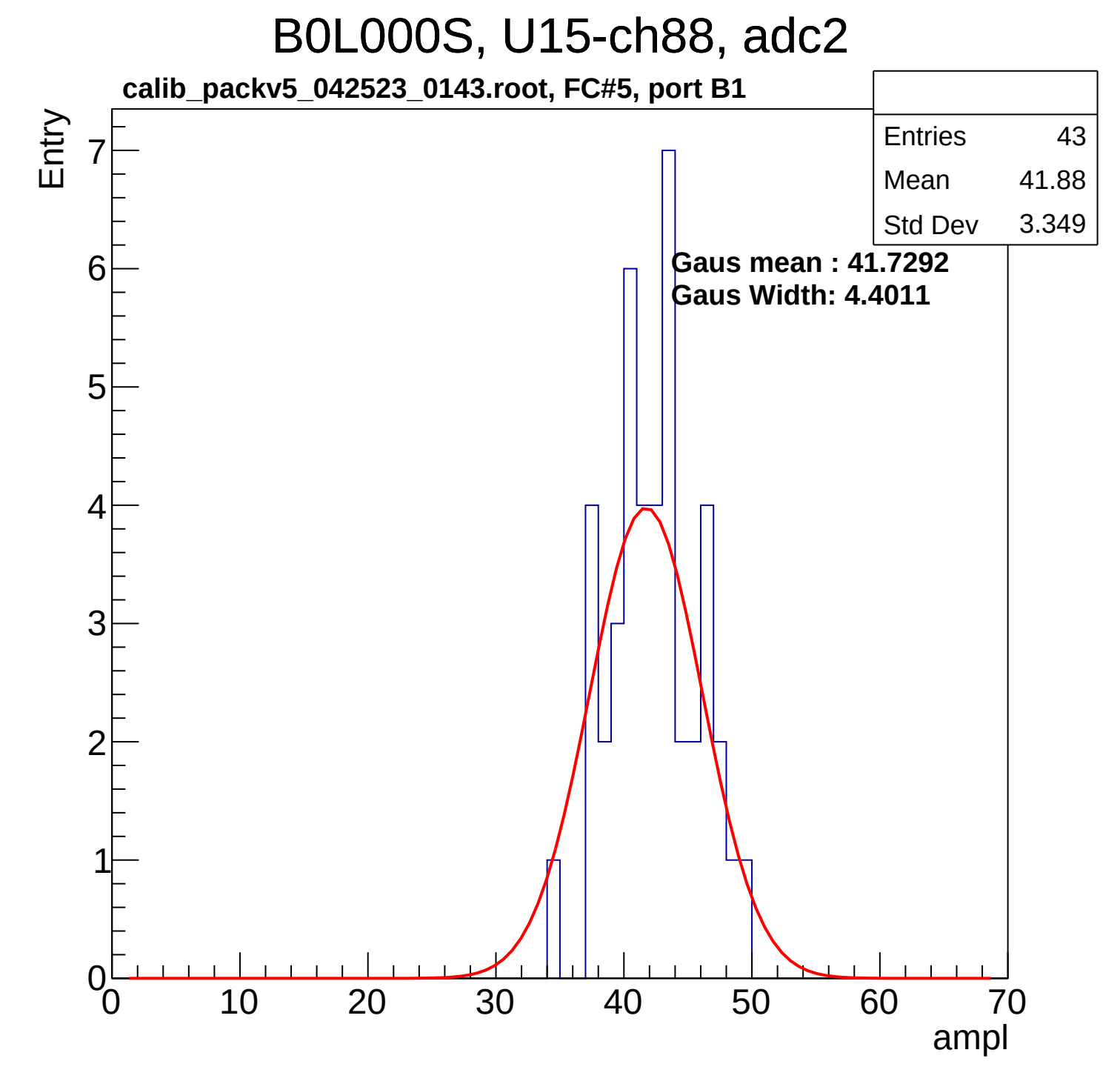
B0L000S, U15-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 41.88
Std Dev 3.349
Gaus mean : 41.7292
Gaus Width: 4.4011
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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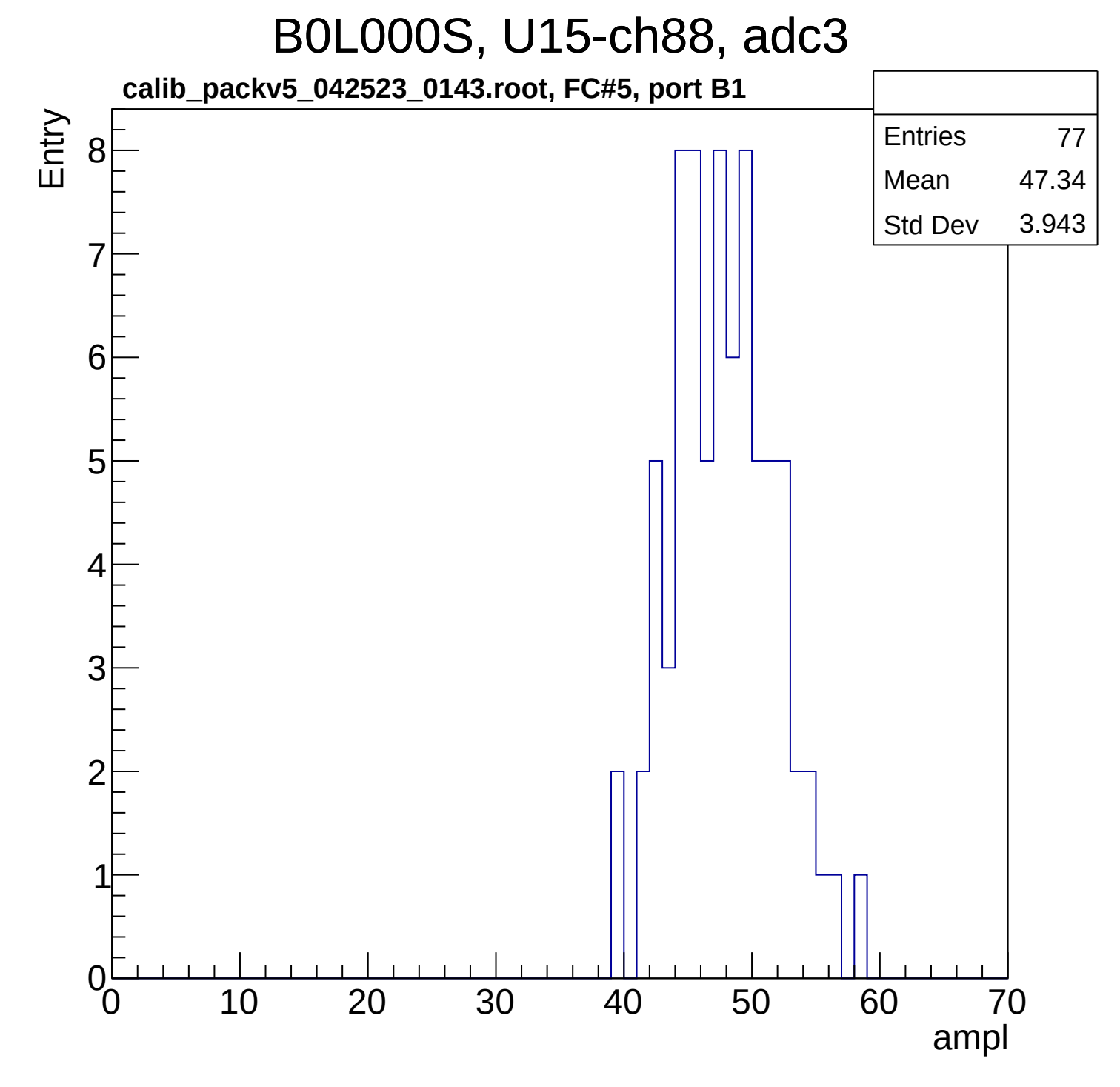
B0L000S, U15-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 47.34
Std Dev 3.943
Entry
ampl
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2
3
4
5
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7
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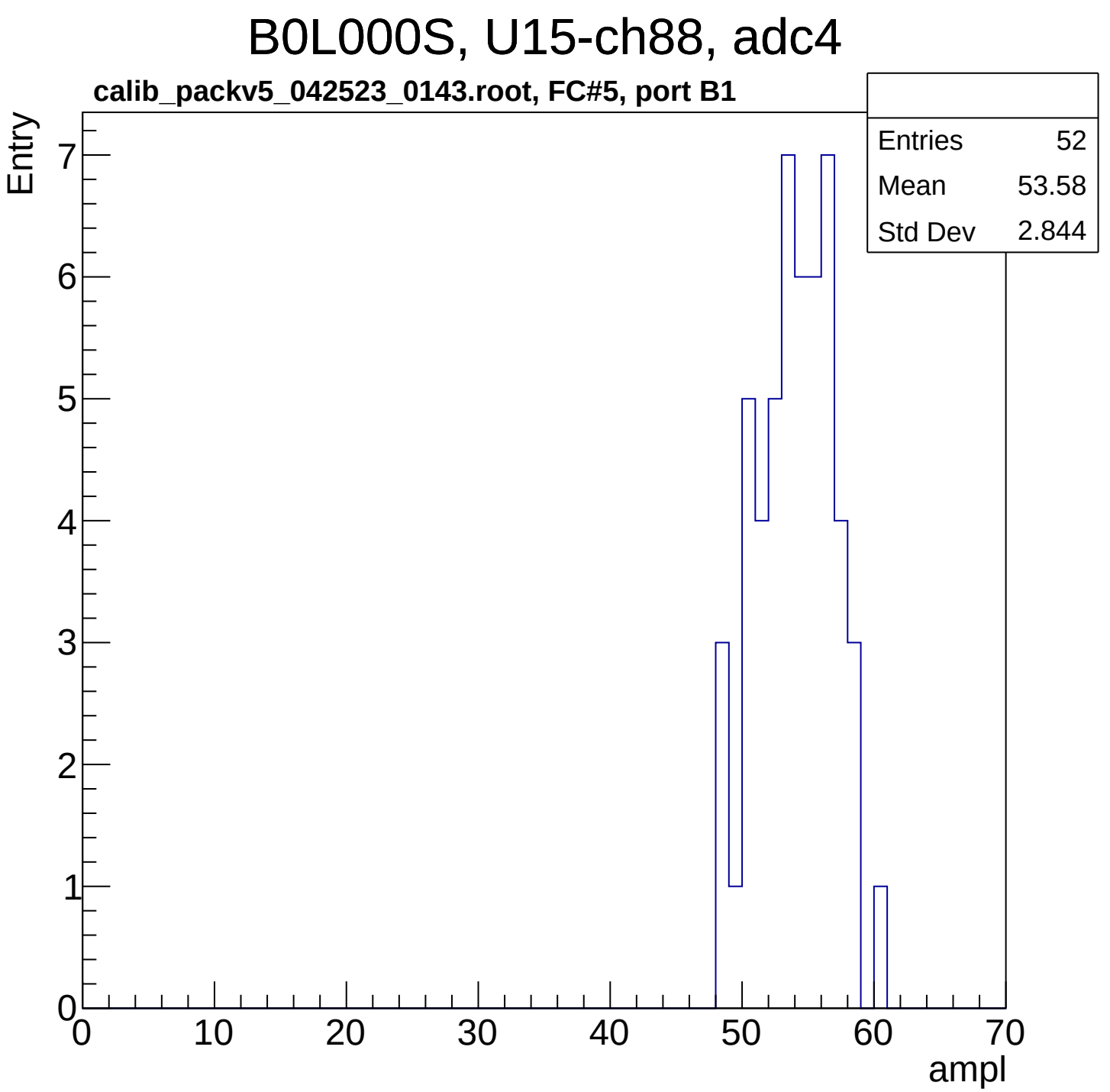
B0L000S, U15-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.58
Std Dev 2.844
Entry
ampl
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3
4
5
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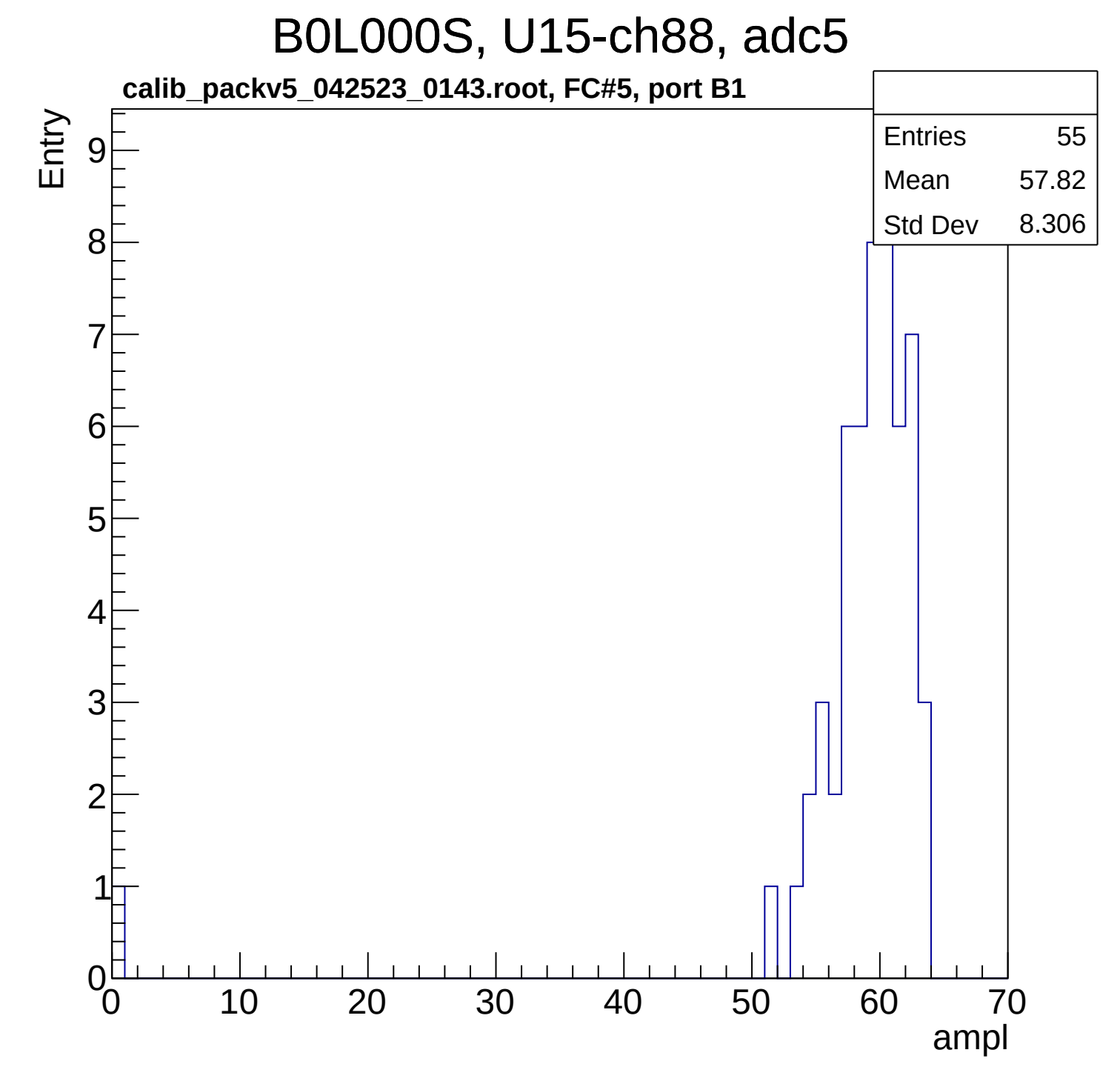
B0L000S, U15-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.82
Std Dev 8.306
Entry
ampl
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3
4
5
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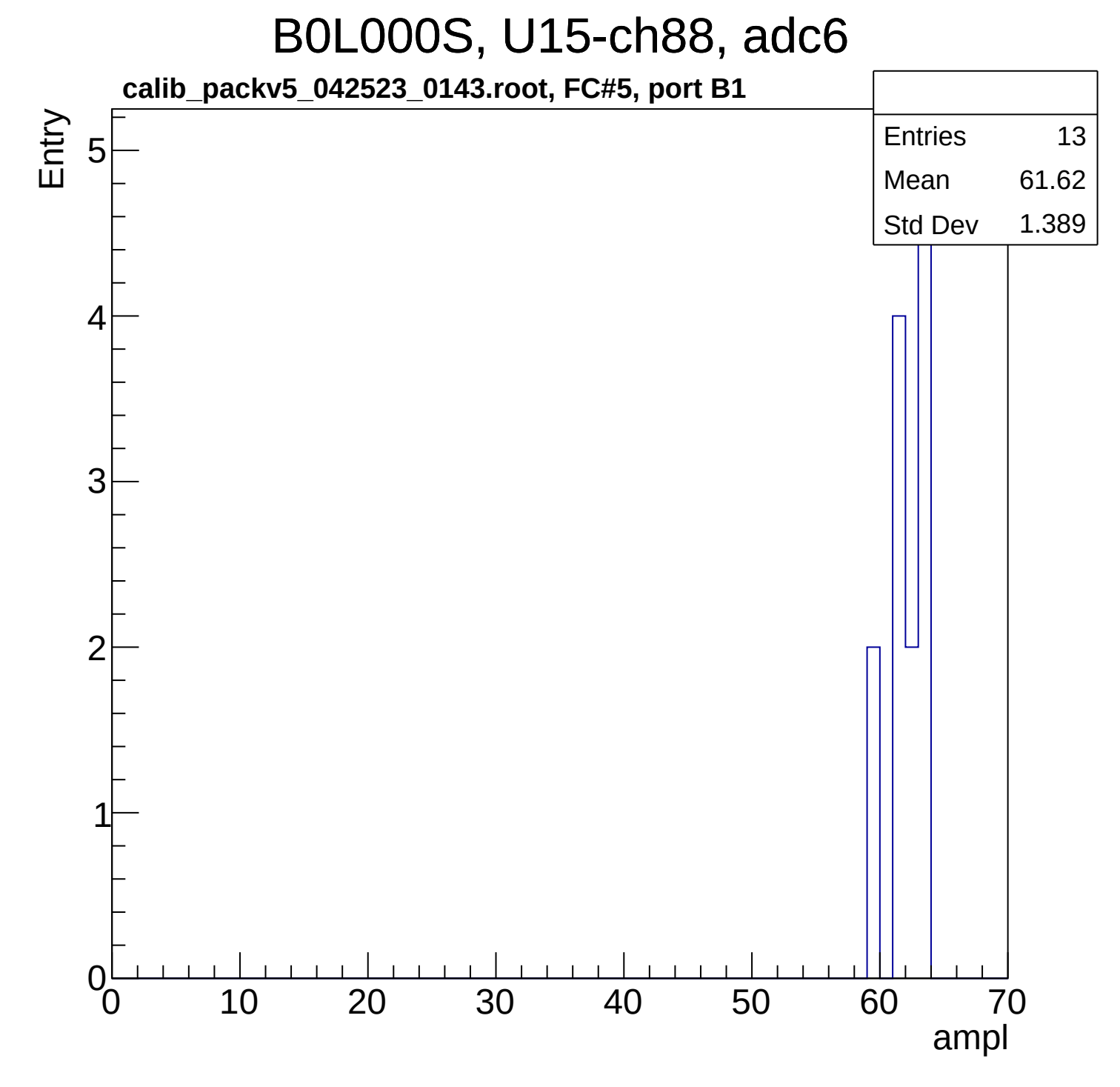
B0L000S, U15-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.62
Std Dev 1.389
Entry
ampl
0
1
2
3
4
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60
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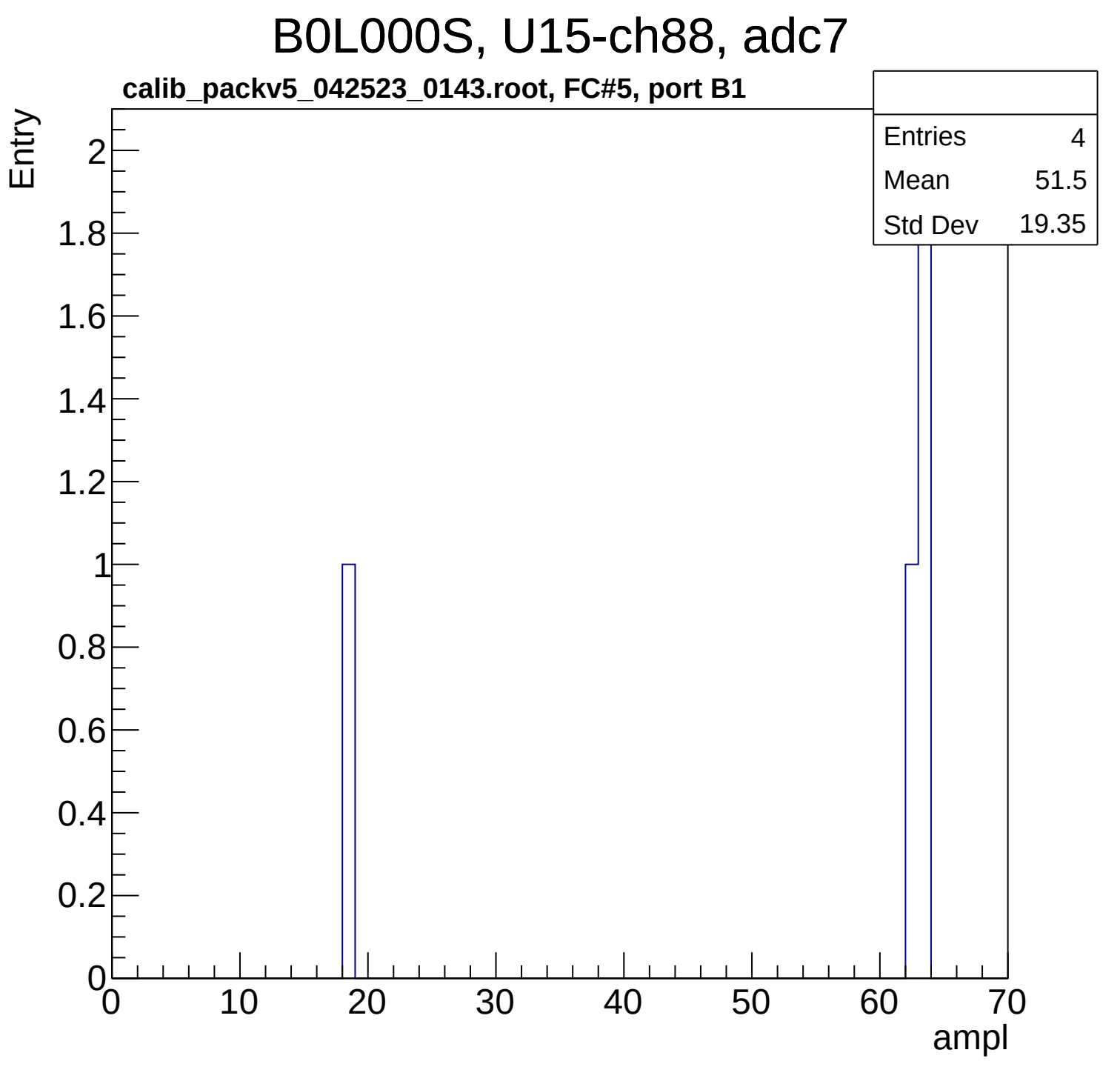
B0L000S, U15-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 51.5
Std Dev 19.35
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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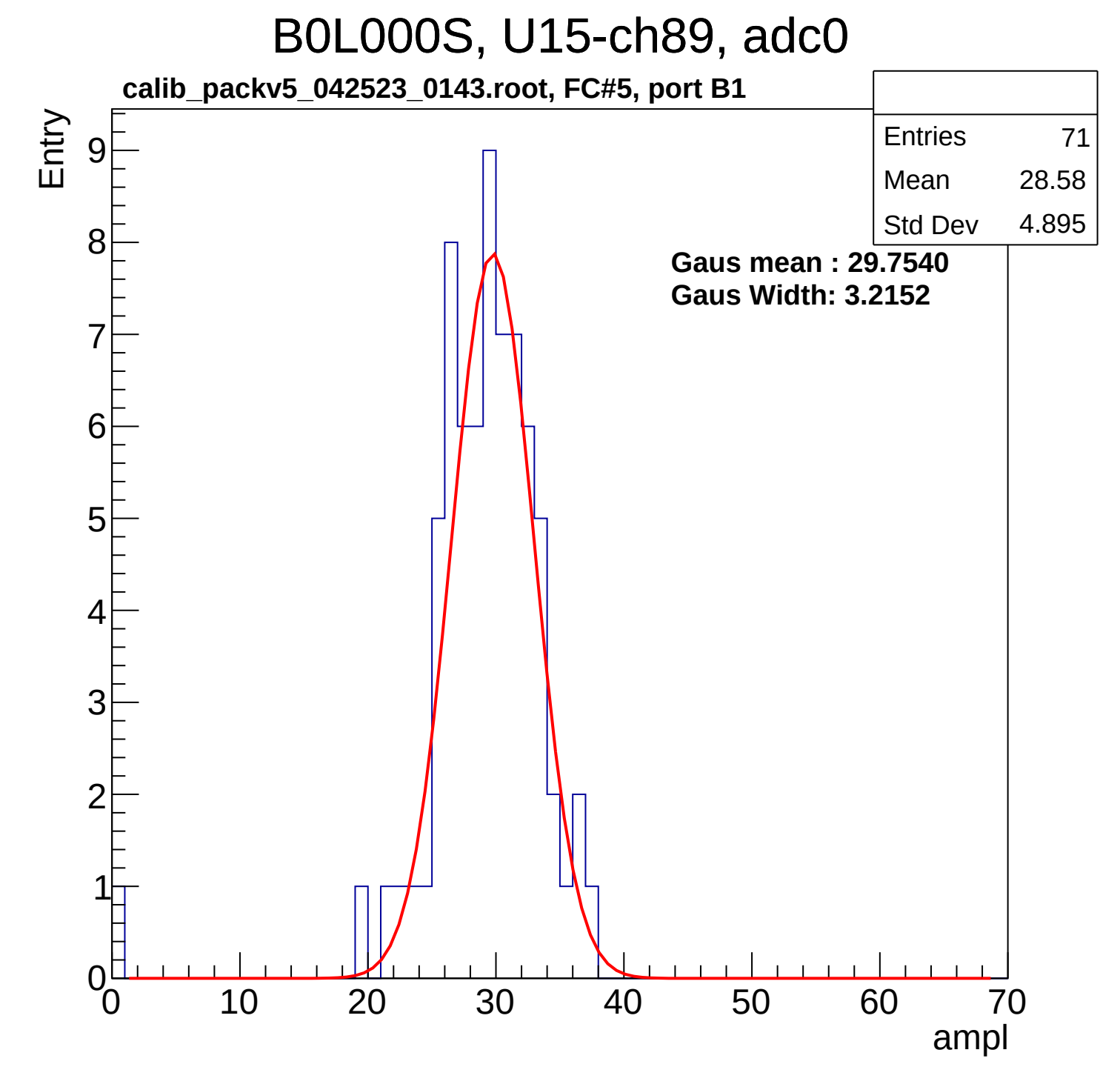
B0L000S, U15-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.58
Std Dev 4.895
Gaus mean : 29.7540
Gaus Width: 3.2152
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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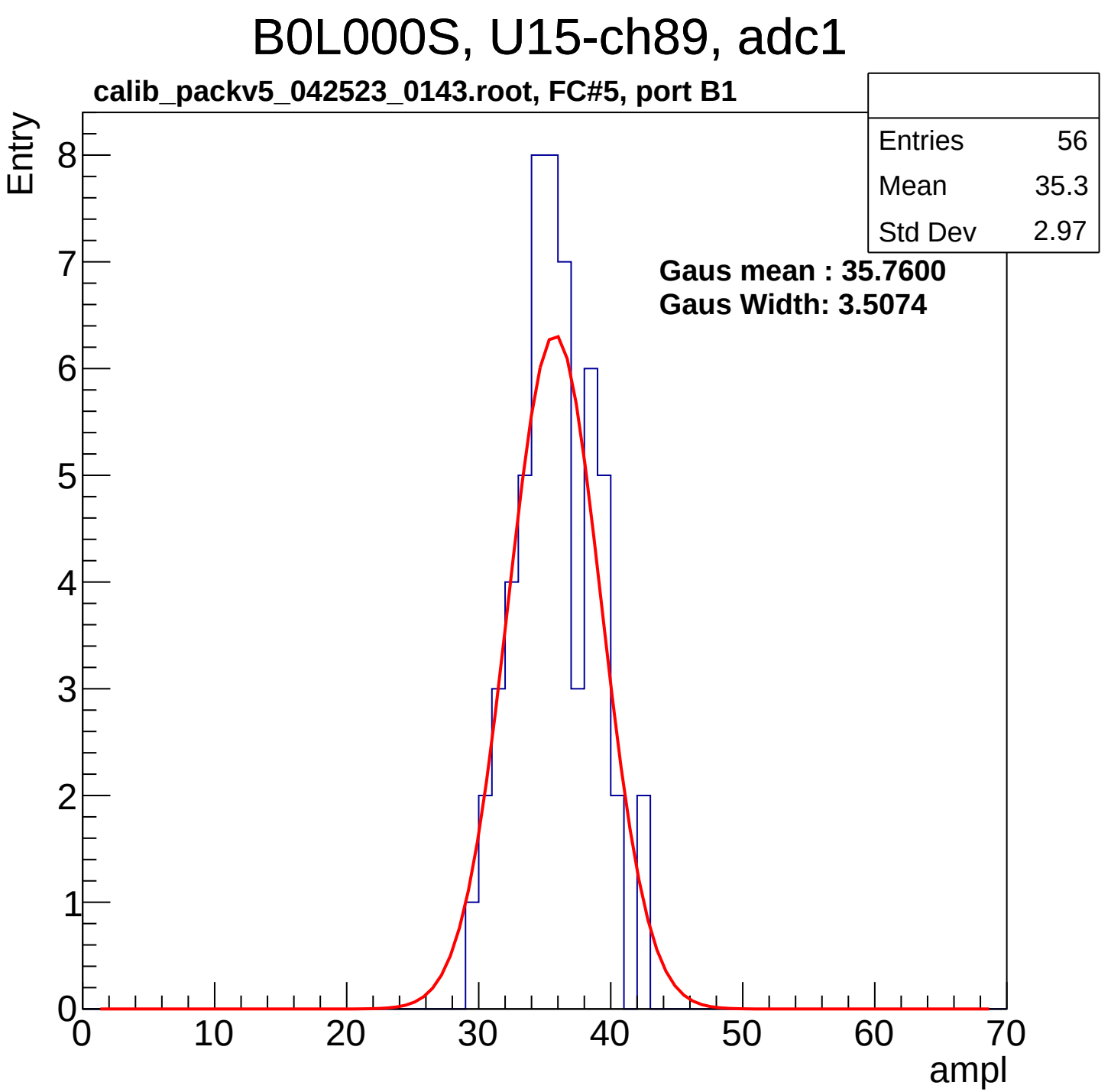
B0L000S, U15-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.3
Std Dev 2.97
Gaus mean : 35.7600
Gaus Width: 3.5074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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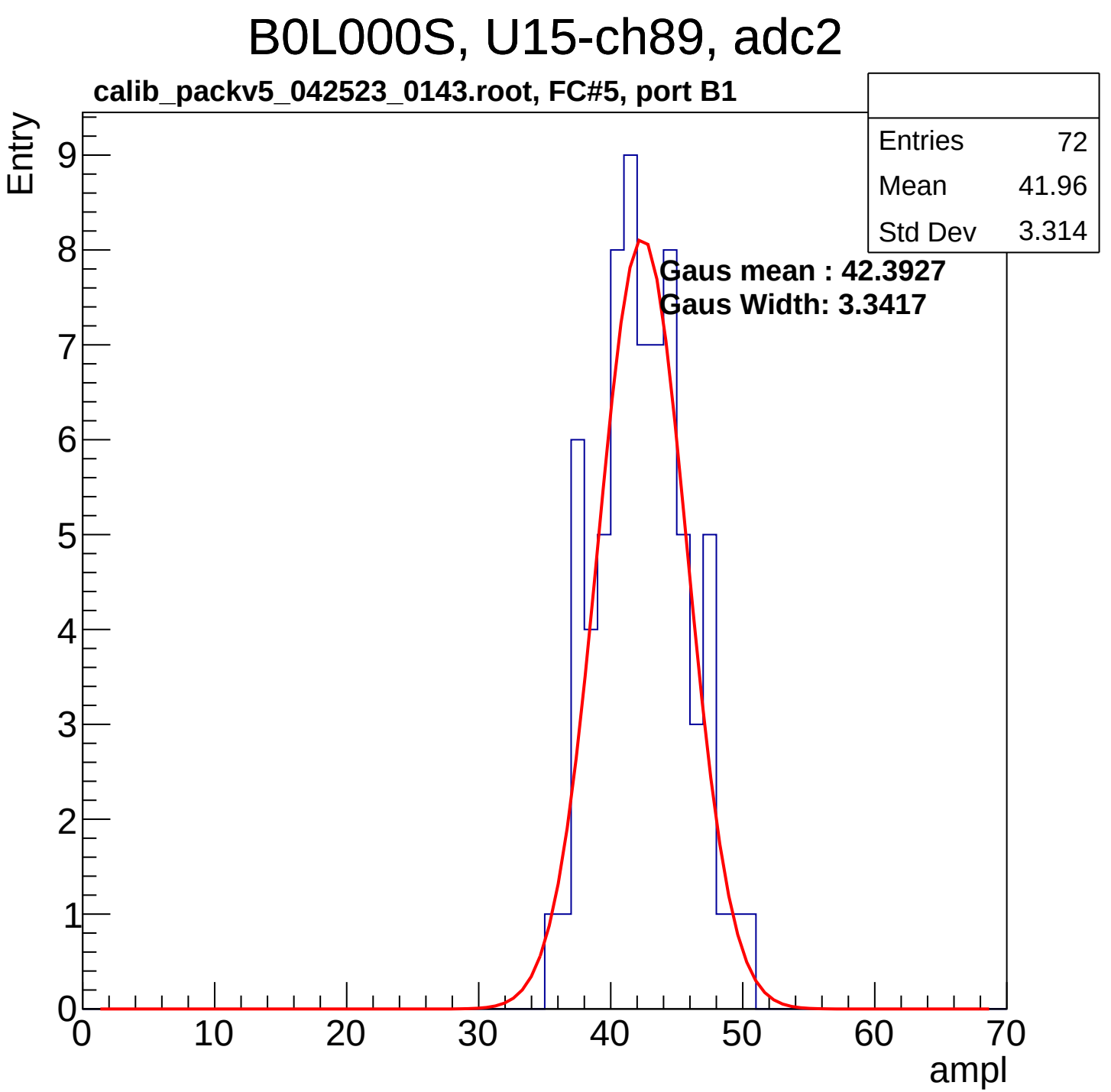
B0L000S, U15-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 41.96
Std Dev 3.314
Gaus mean : 42.3927
Gaus Width: 3.3417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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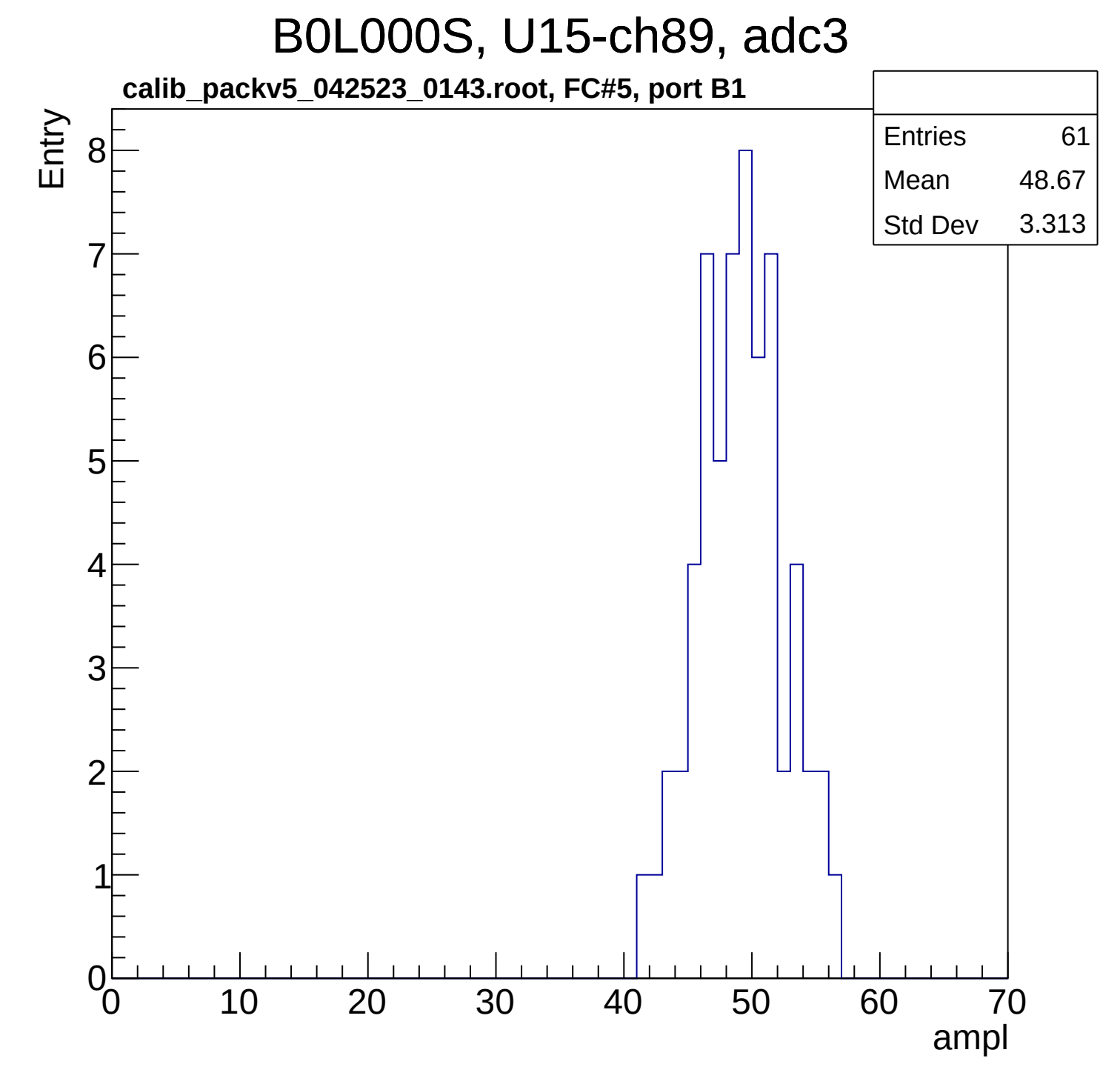
B0L000S, U15-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.67
Std Dev 3.313
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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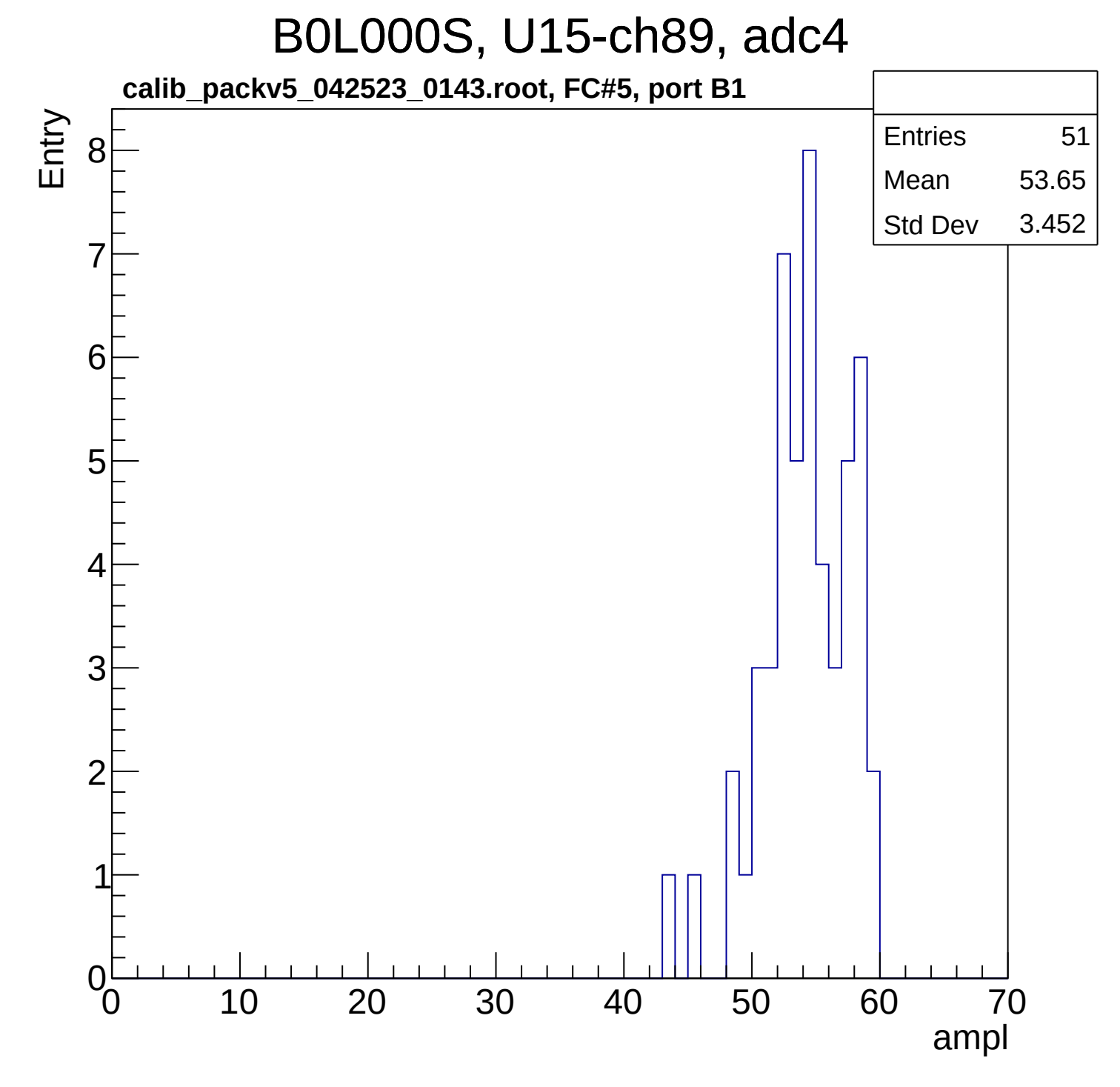
B0L000S, U15-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.65
Std Dev 3.452
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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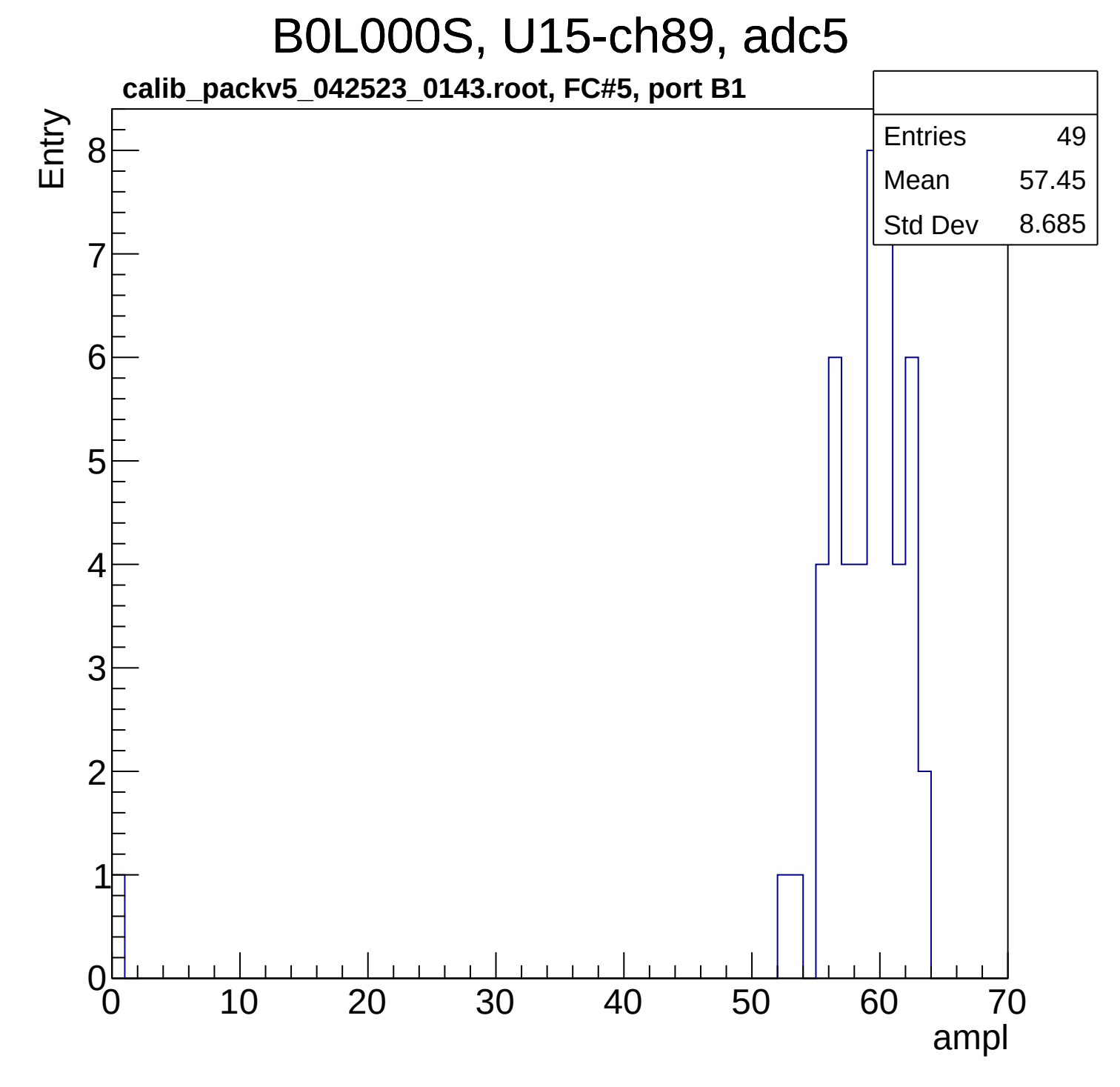
B0L000S, U15-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.45
Std Dev 8.685
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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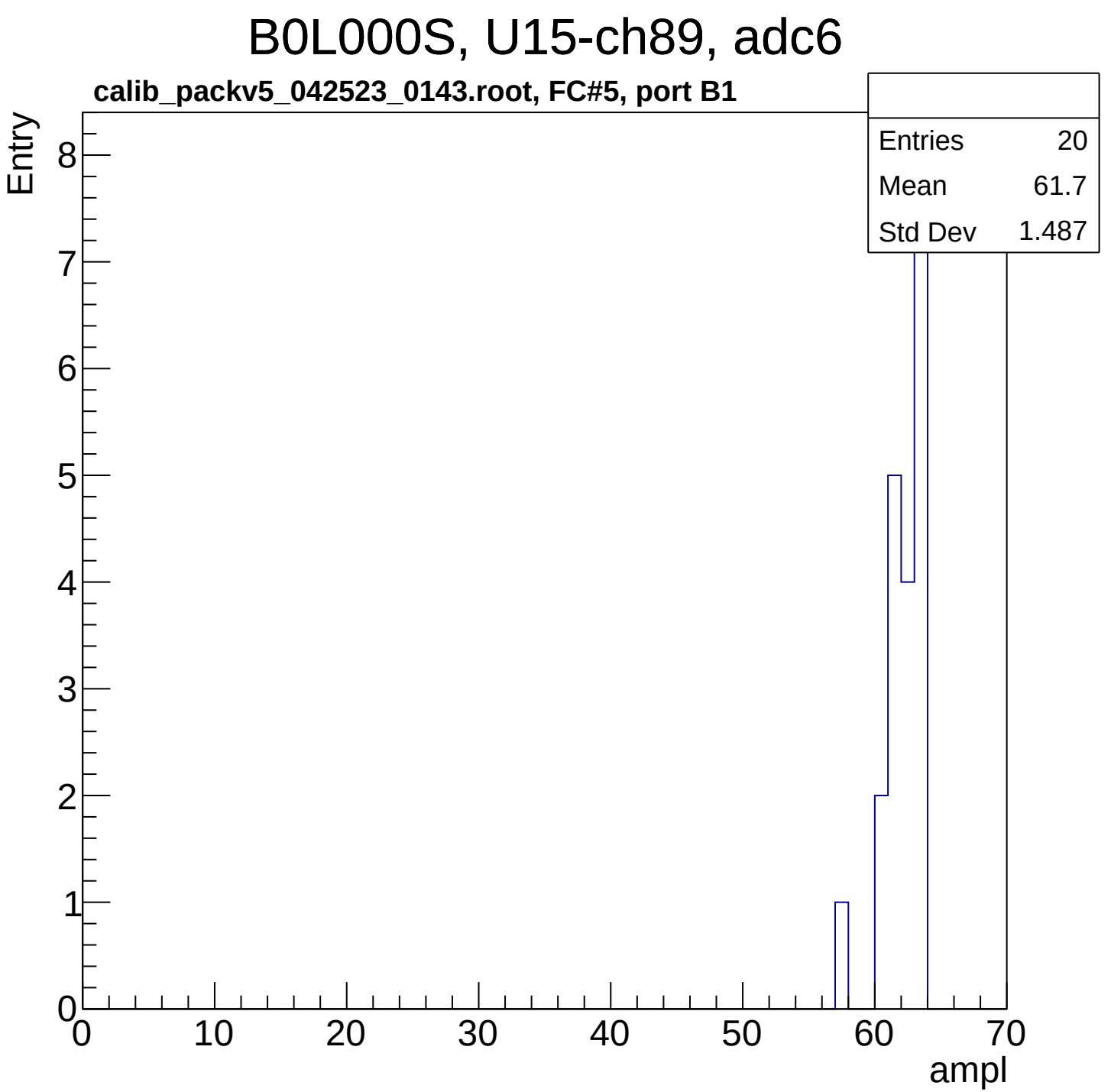
B0L000S, U15-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 61.7
Std Dev 1.487
Entry
ampl
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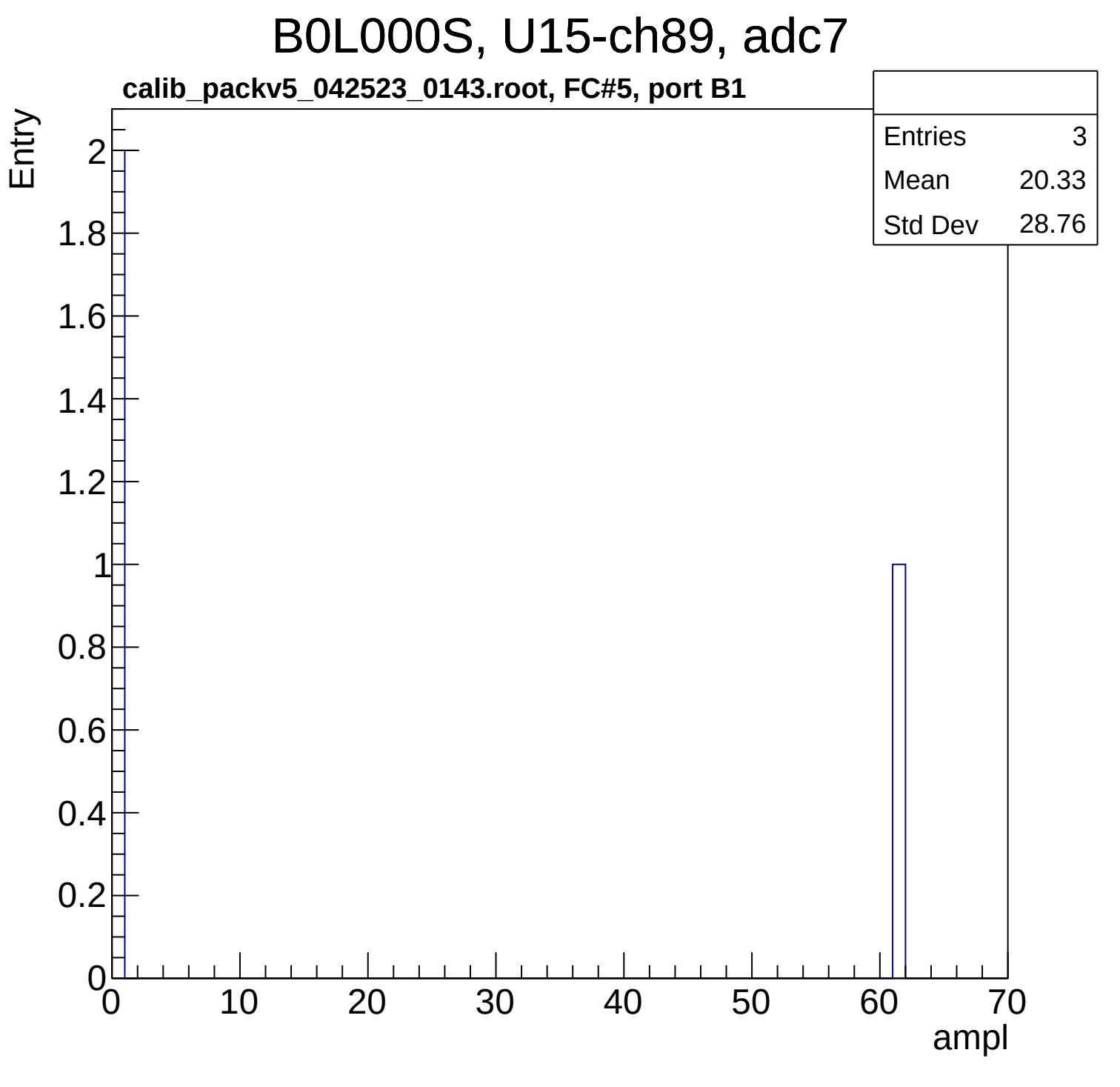
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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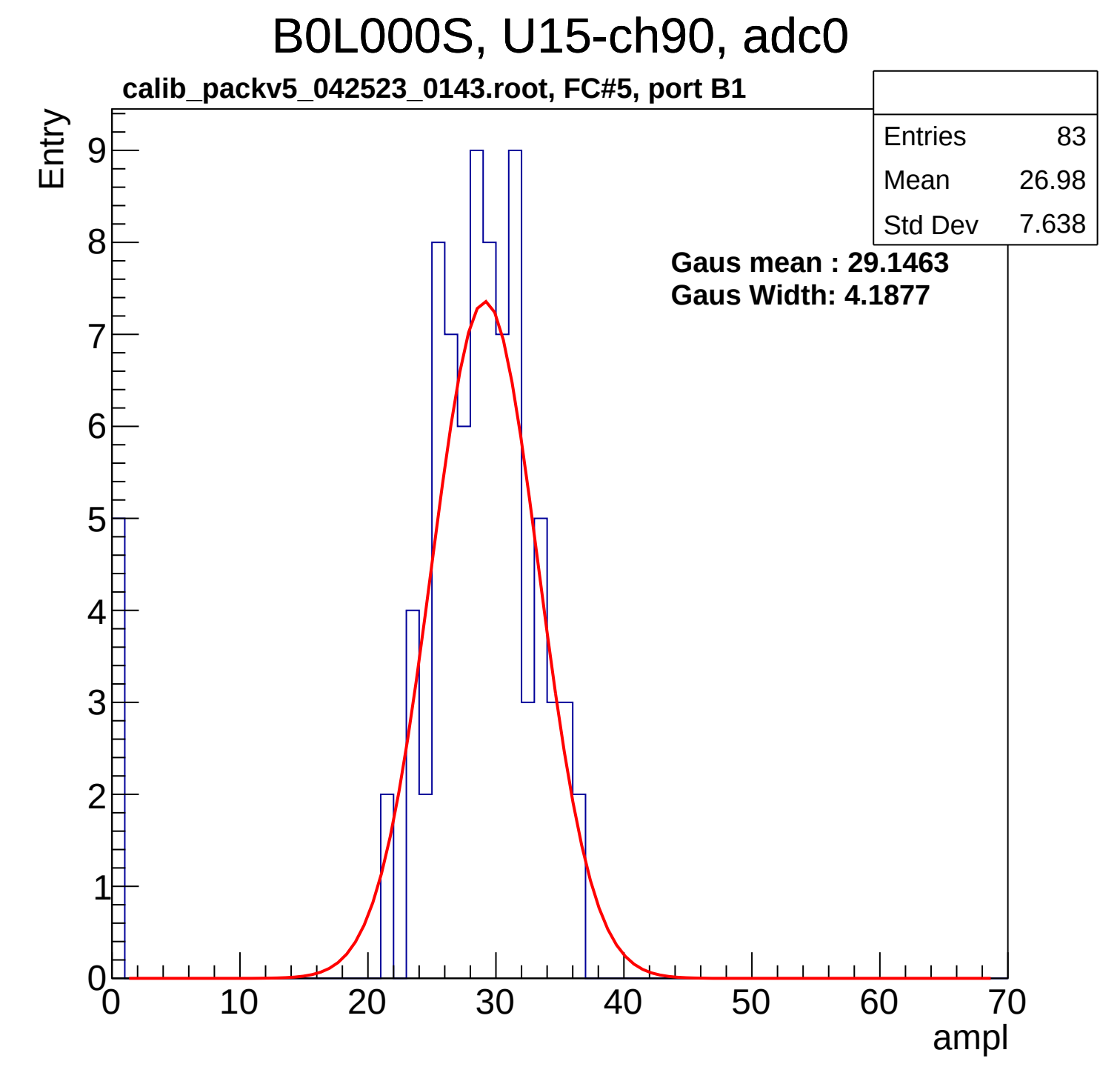
B0L000S, U15-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 26.98
Std Dev 7.638
Gaus mean : 29.1463
Gaus Width: 4.1877
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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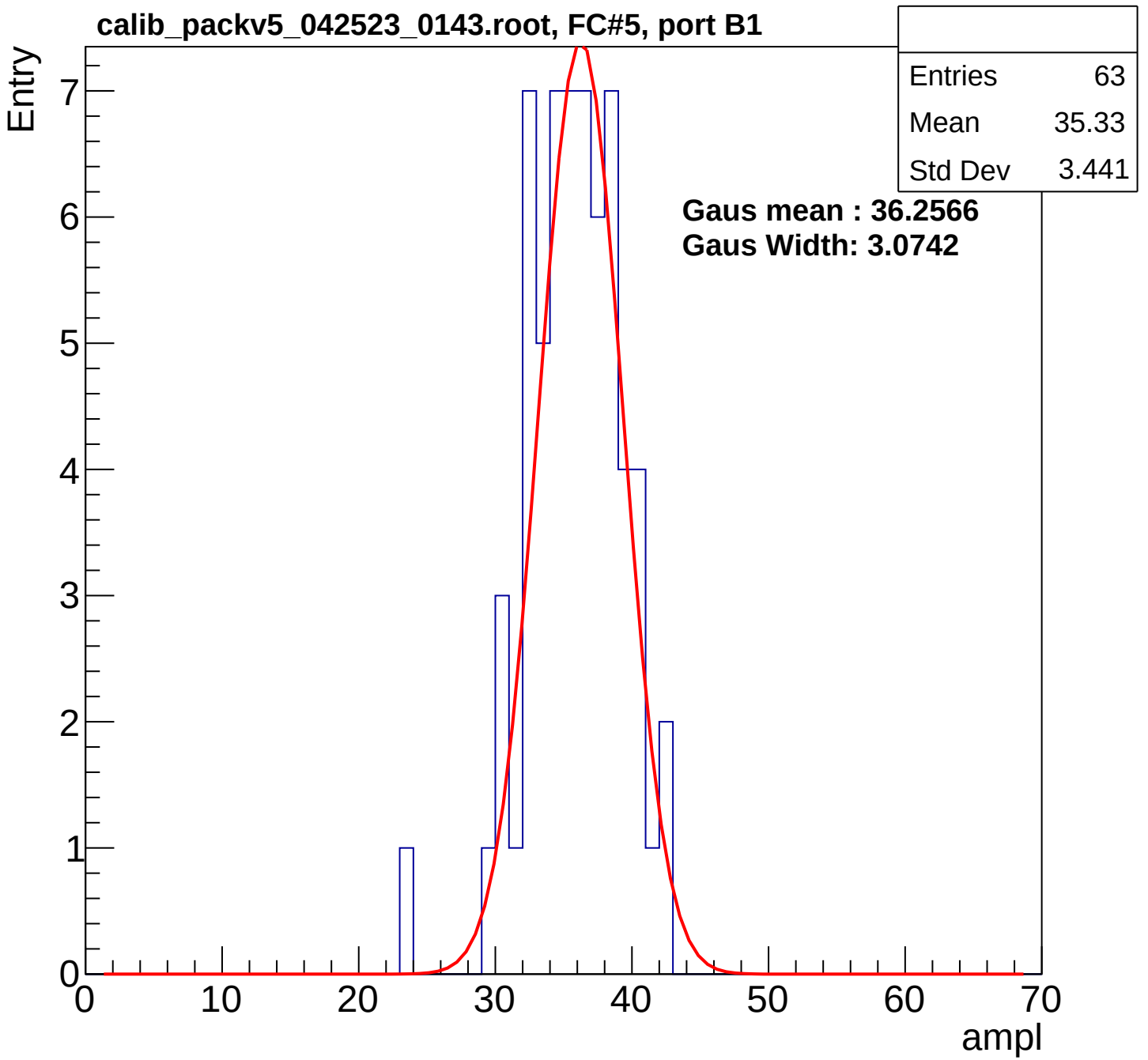
B0L000S, U15-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.33
Std Dev 3.441
Gaus mean : 36.2566
Gaus Width: 3.0742
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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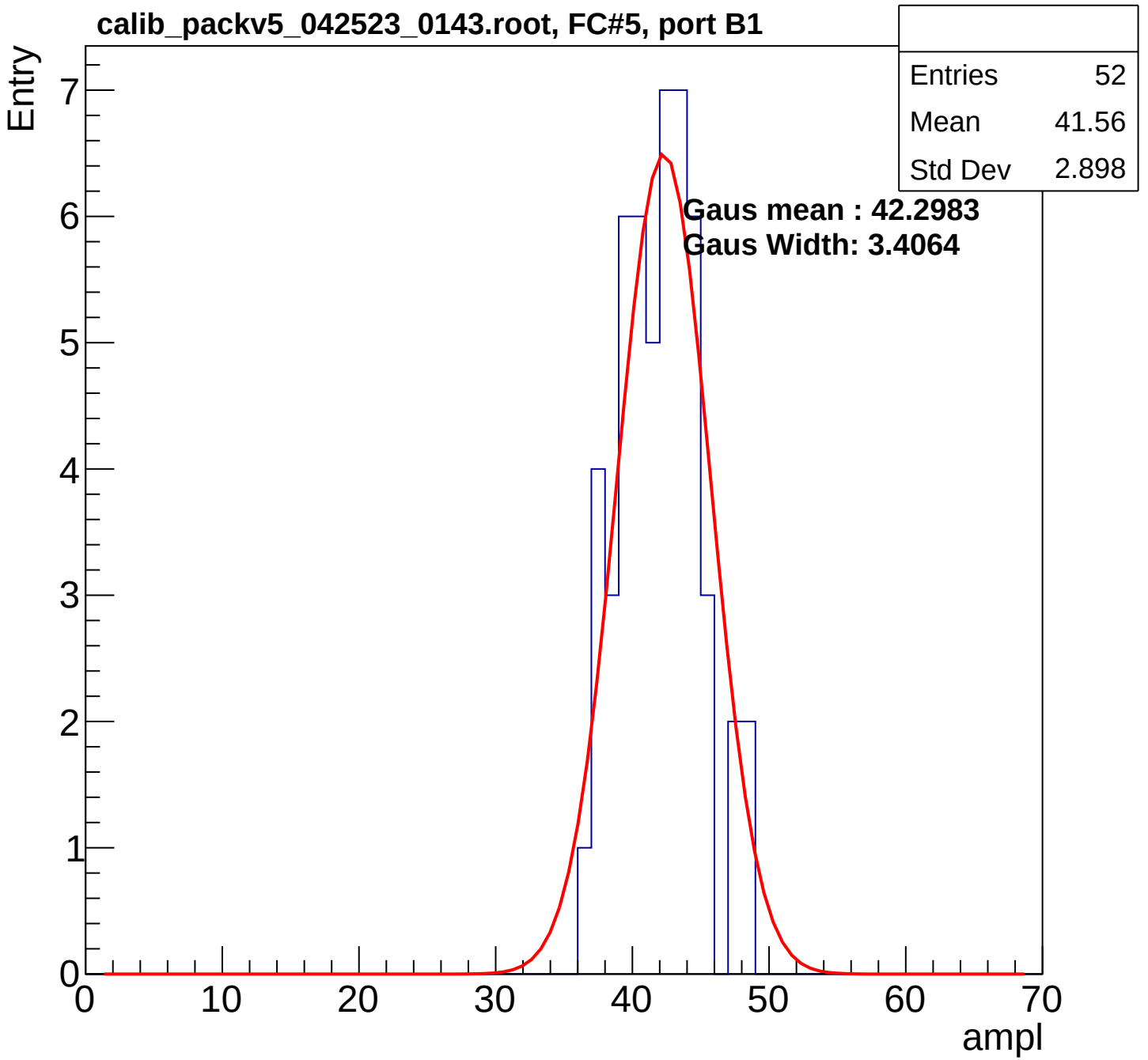
B0L000S, U15-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 41.56
Std Dev 2.898
Gaus mean : 42.2983
Gaus Width: 3.4064
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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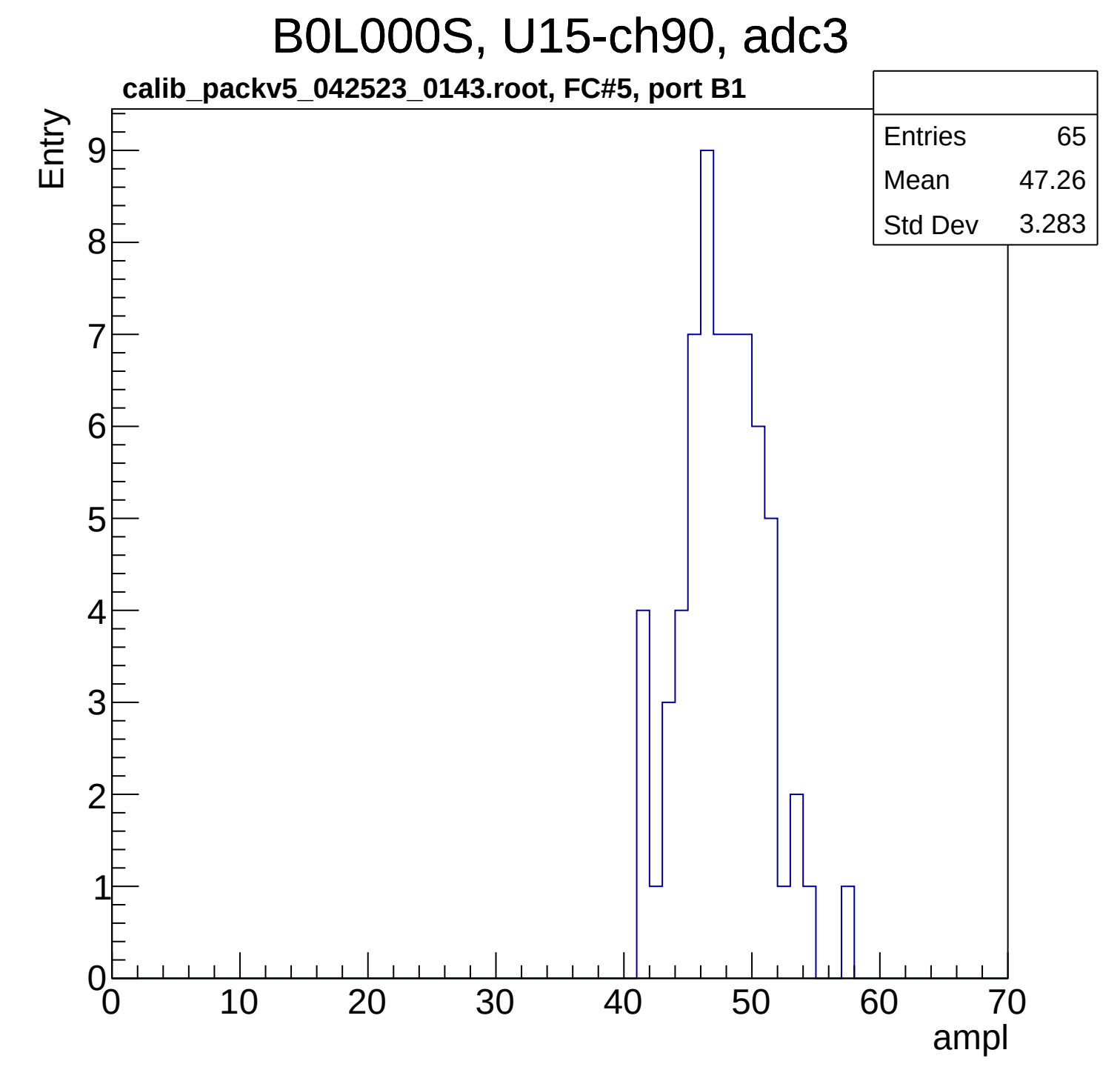
B0L000S, U15-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 47.26
Std Dev 3.283
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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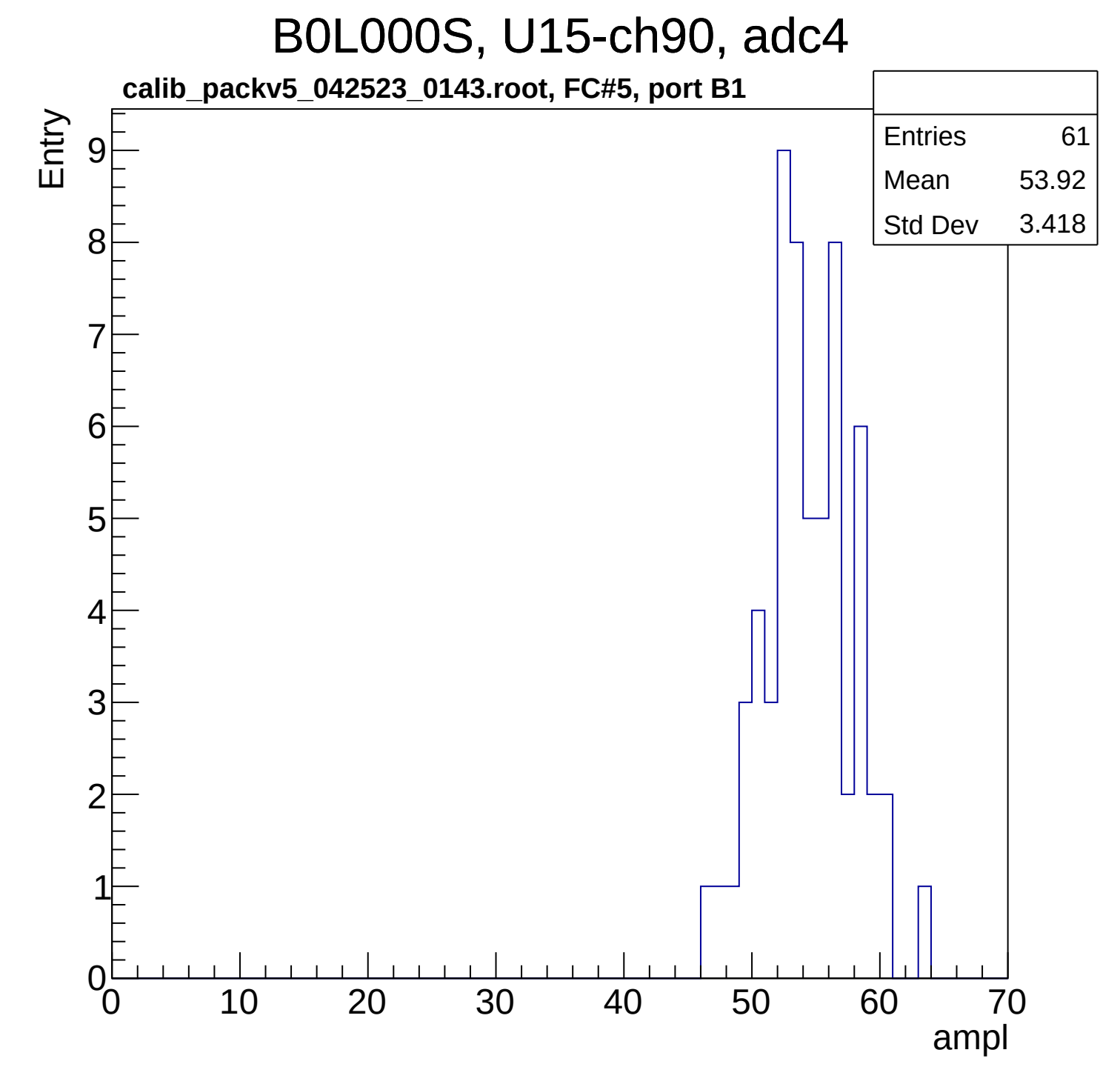
B0L000S, U15-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.92
Std Dev 3.418
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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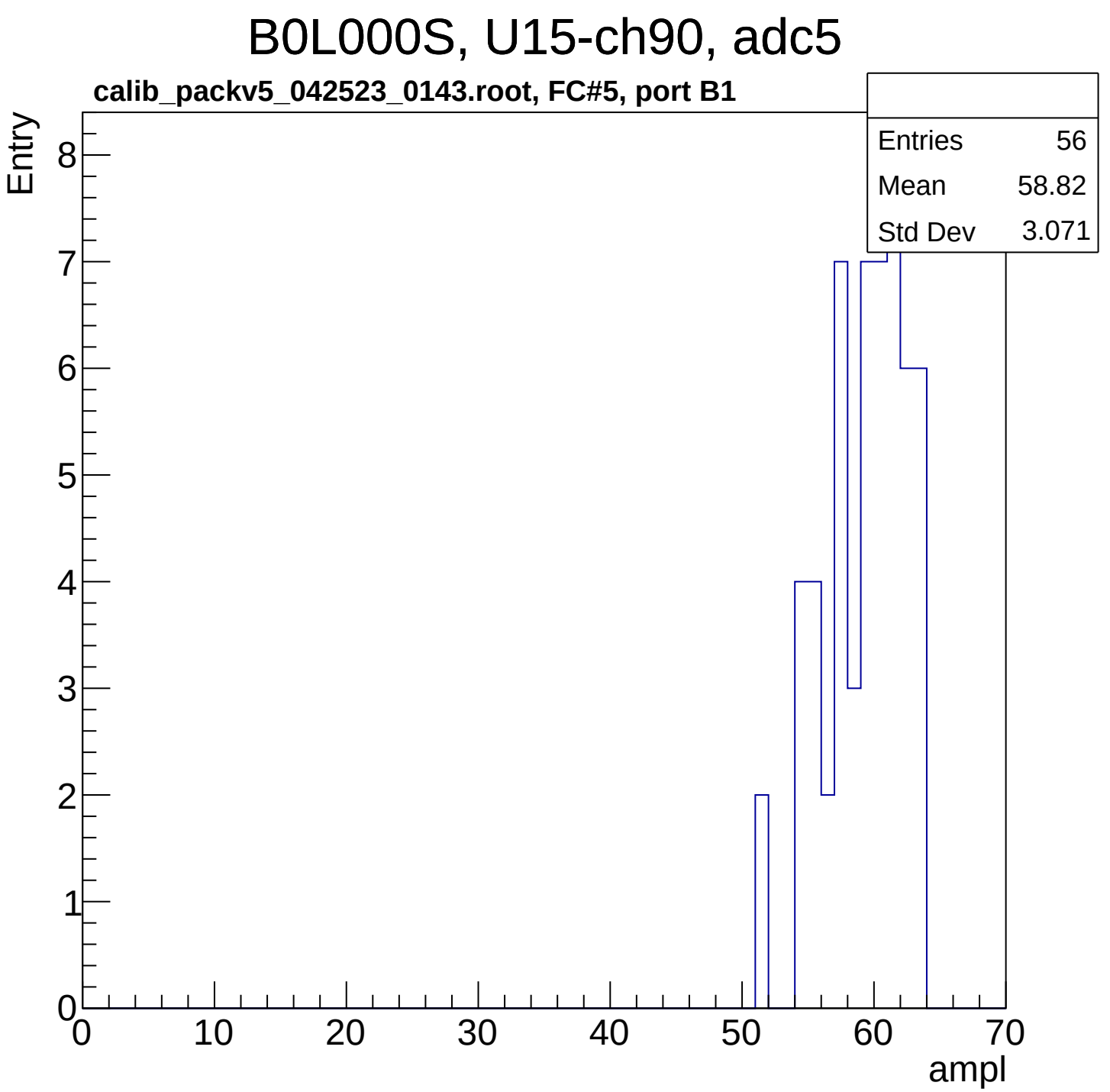
B0L000S, U15-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.82
Std Dev 3.071
Entry
ampl
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1
2
3
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7
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0
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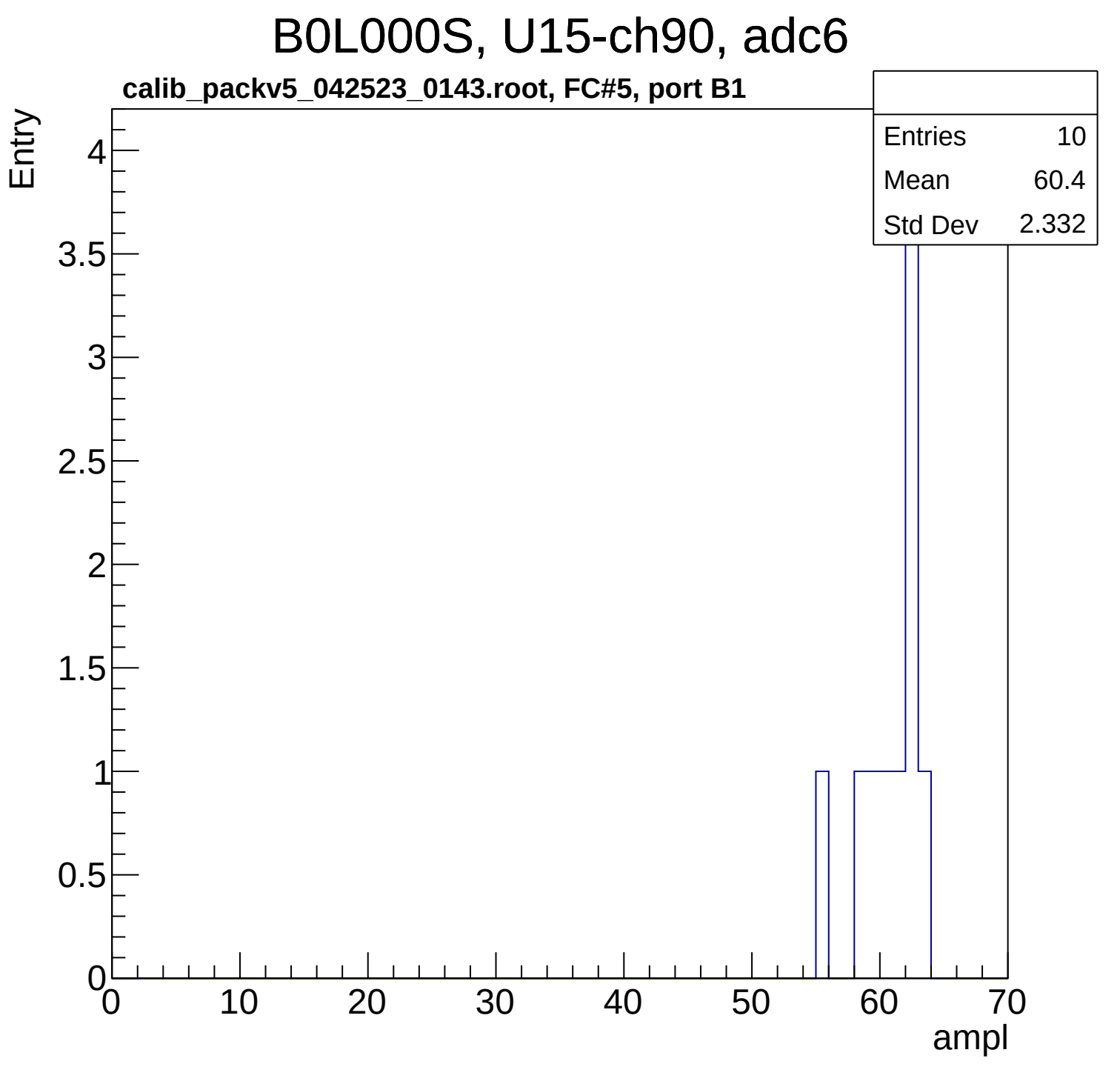
B0L000S, U15-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 60.4
Std Dev 2.332
Entry
ampl
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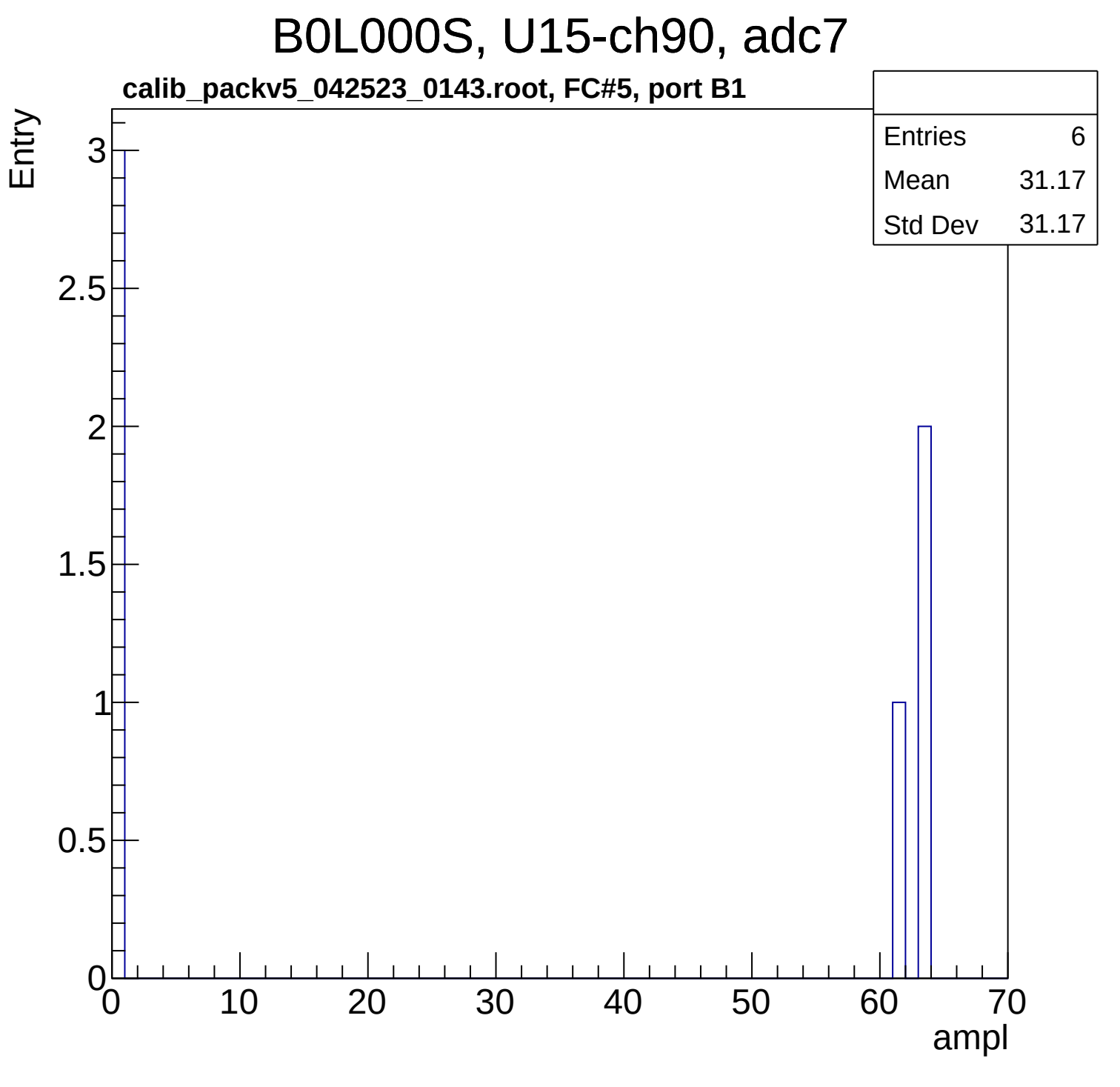
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 31.17
Std Dev 31.17
Entry
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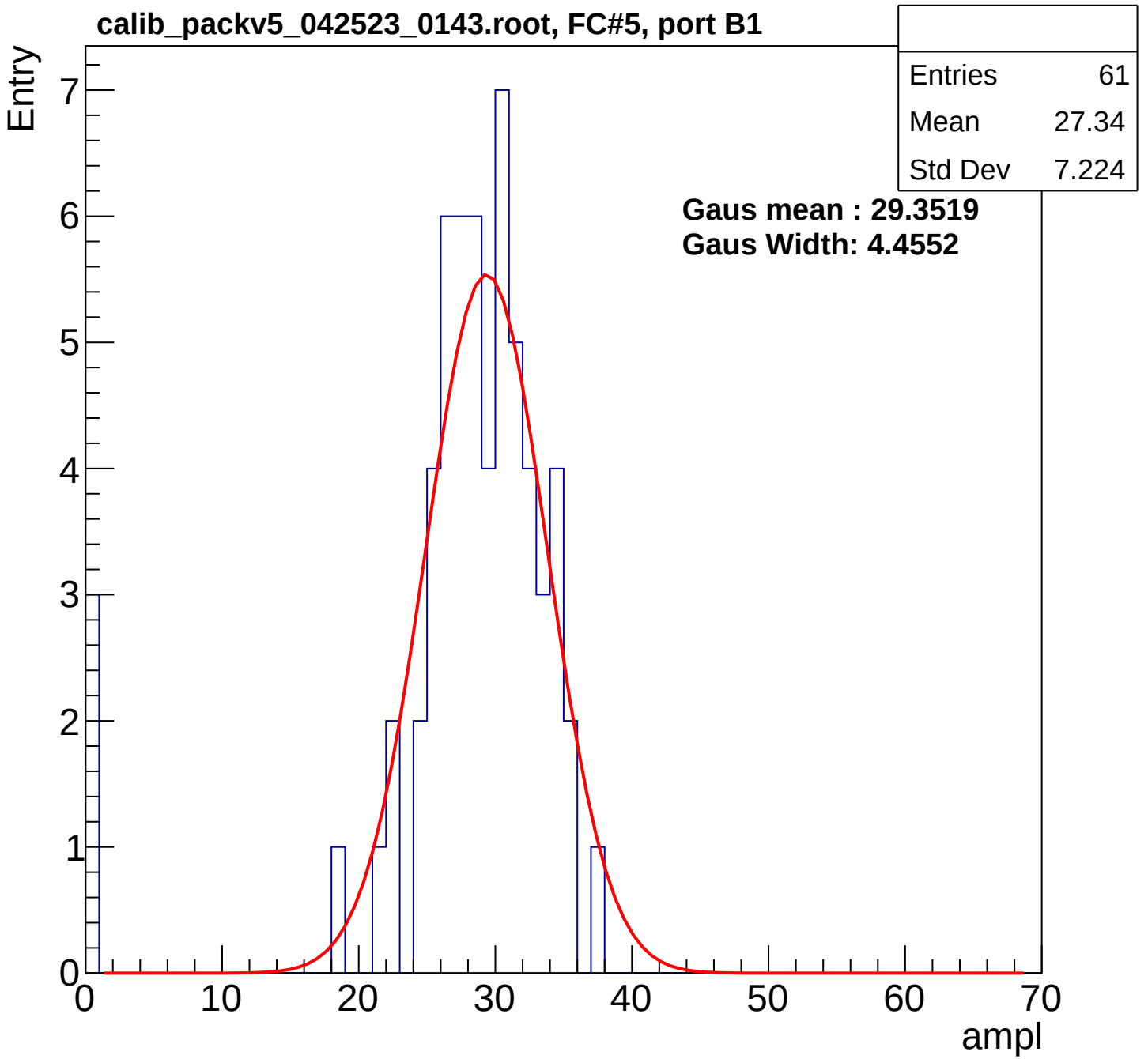
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 27.34
Std Dev 7.224
Gaus mean : 29.3519
Gaus Width: 4.4552
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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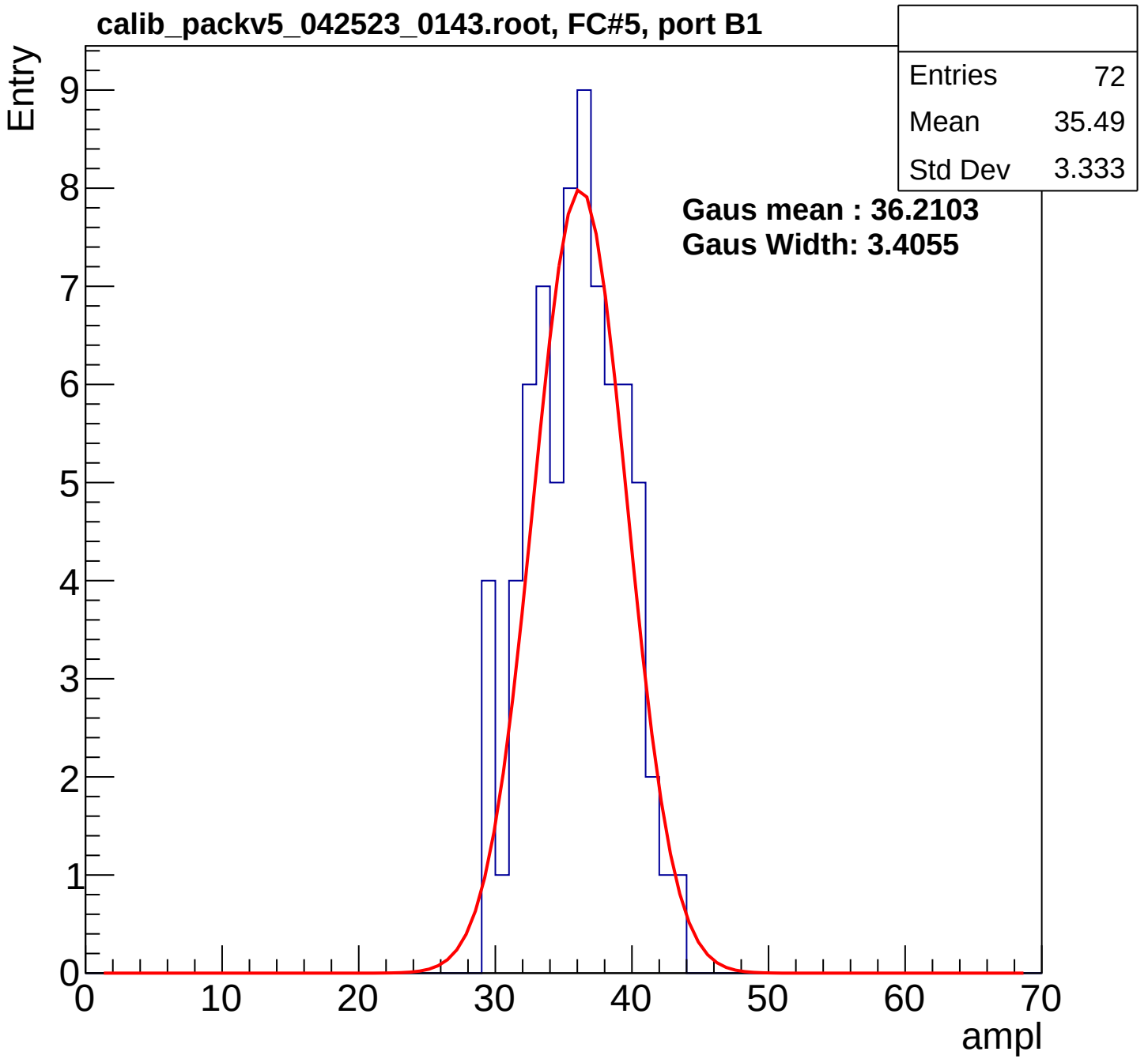
B0L000S, U15-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.49
Std Dev 3.333
Gaus mean : 36.2103
Gaus Width: 3.4055
Entry
ampl
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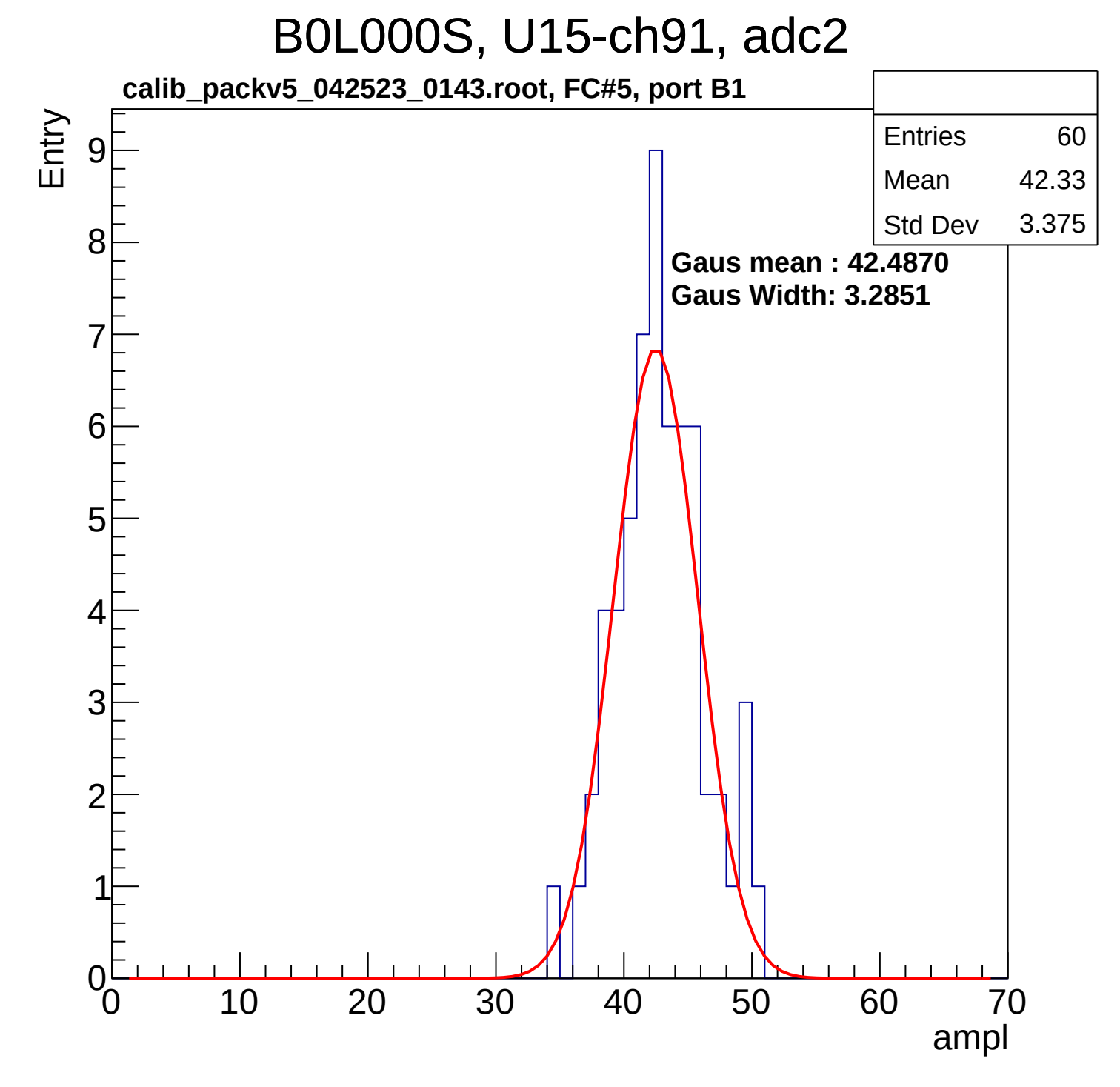
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 42.33
Std Dev 3.375
Gaus mean : 42.4870
Gaus Width: 3.2851
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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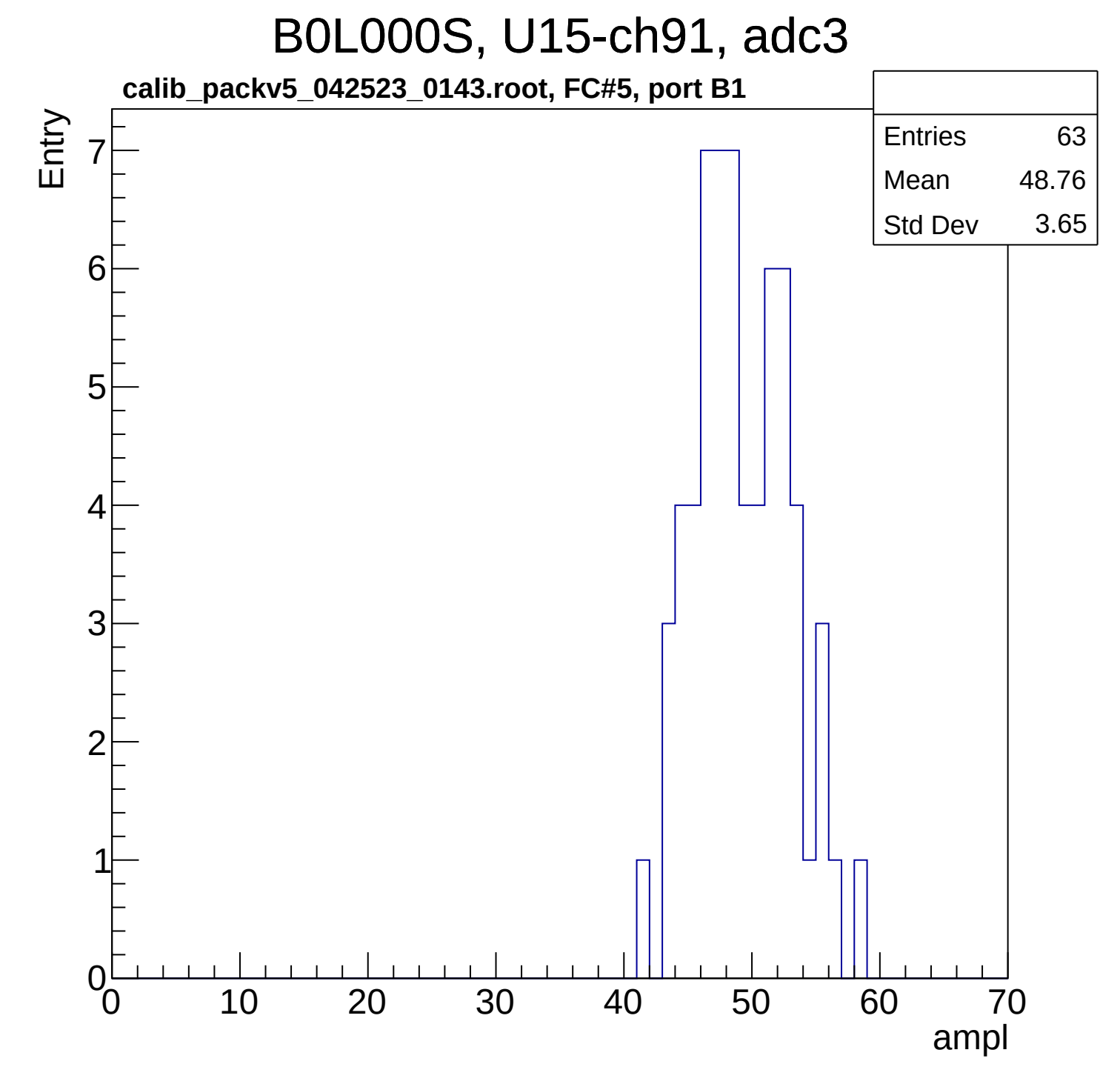
B0L000S, U15-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 48.76
Std Dev 3.65
Entry
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1
2
3
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5
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0
10
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30
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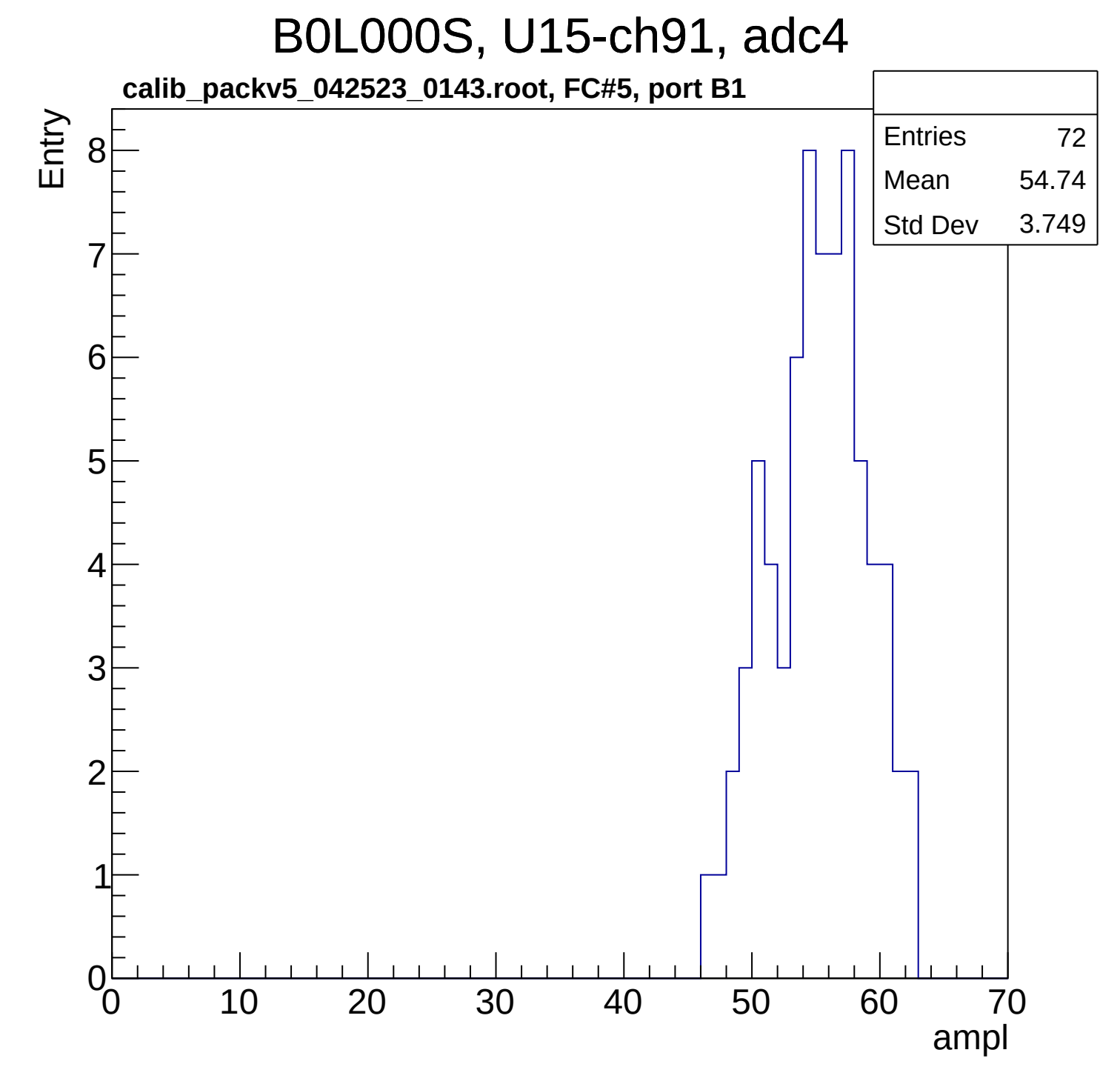
50
60
70
ampl


B0L000S, U15-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 54.74
Std Dev 3.749
Entry
ampl
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5
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0
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20
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60
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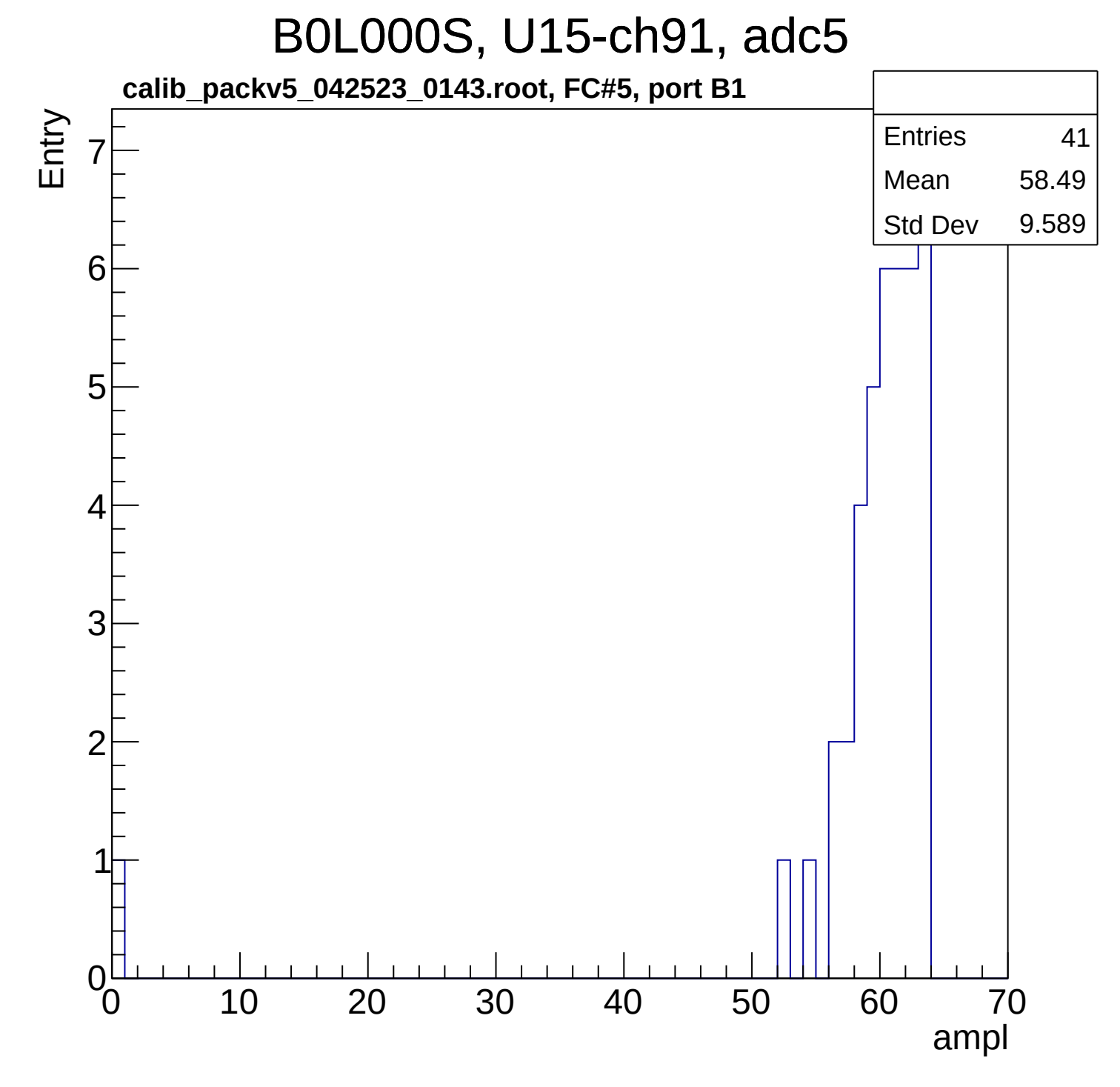
B0L000S, U15-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.49
Std Dev 9.589
Entry
ampl
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3
4
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0
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20
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60
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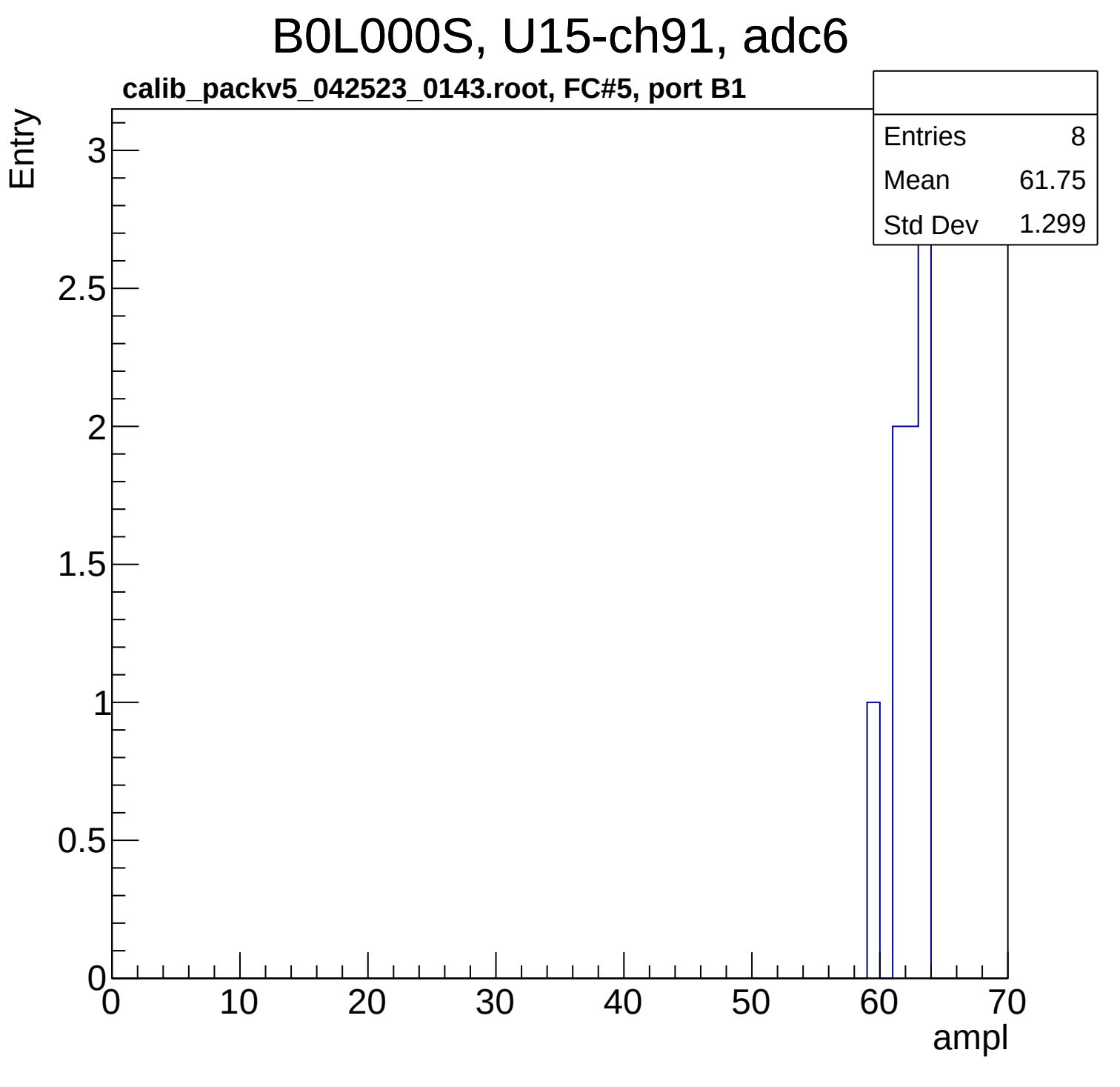
B0L000S, U15-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.75
Std Dev 1.299
Entry
ampl
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0.5
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1.5
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2.5
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0
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60
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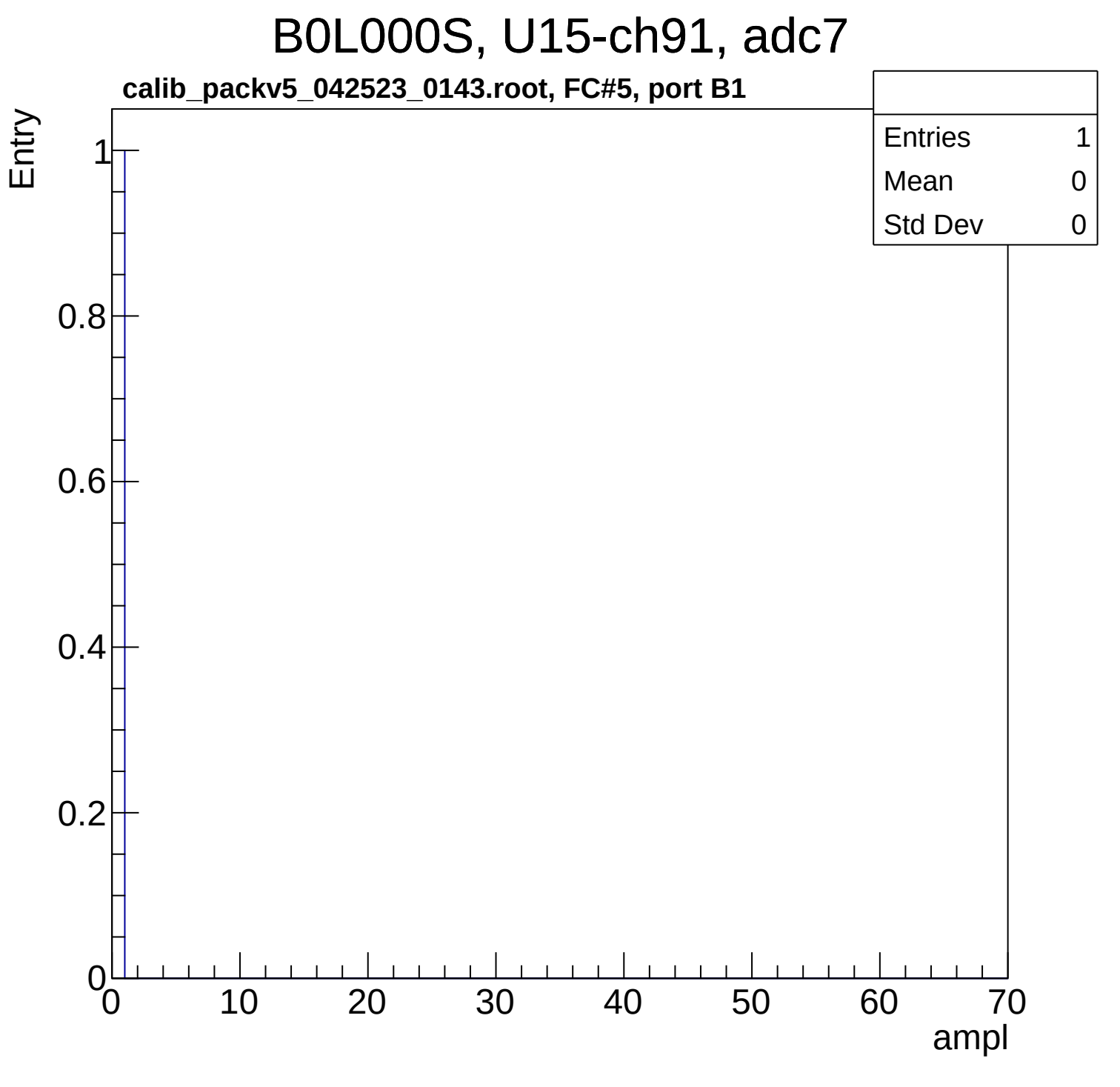
B0L000S, U15-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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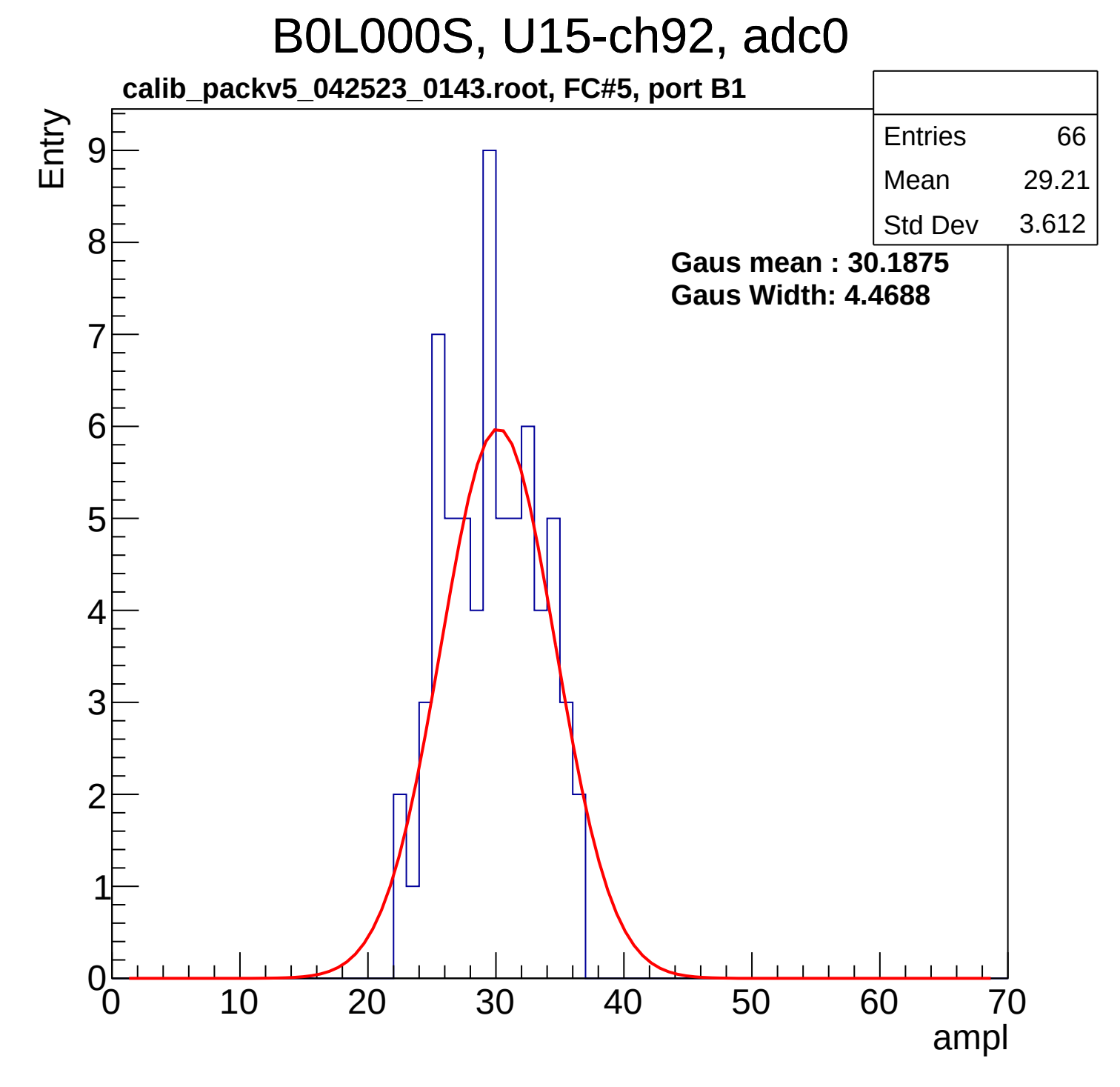
50
60
70
ampl


B0L000S, U15-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.21
Std Dev 3.612
Gaus mean : 30.1875
Gaus Width: 4.4688
Entry
ampl
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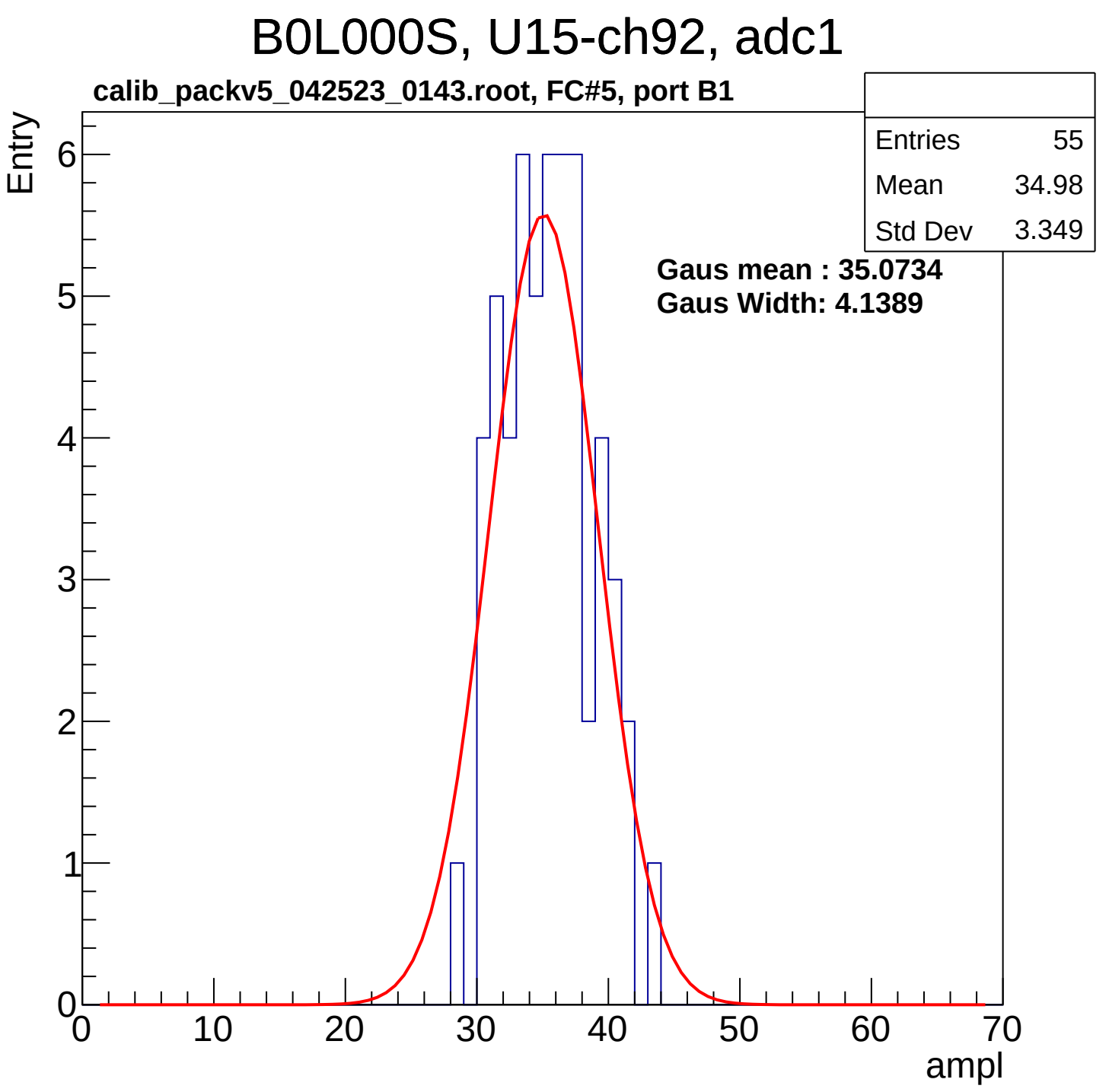
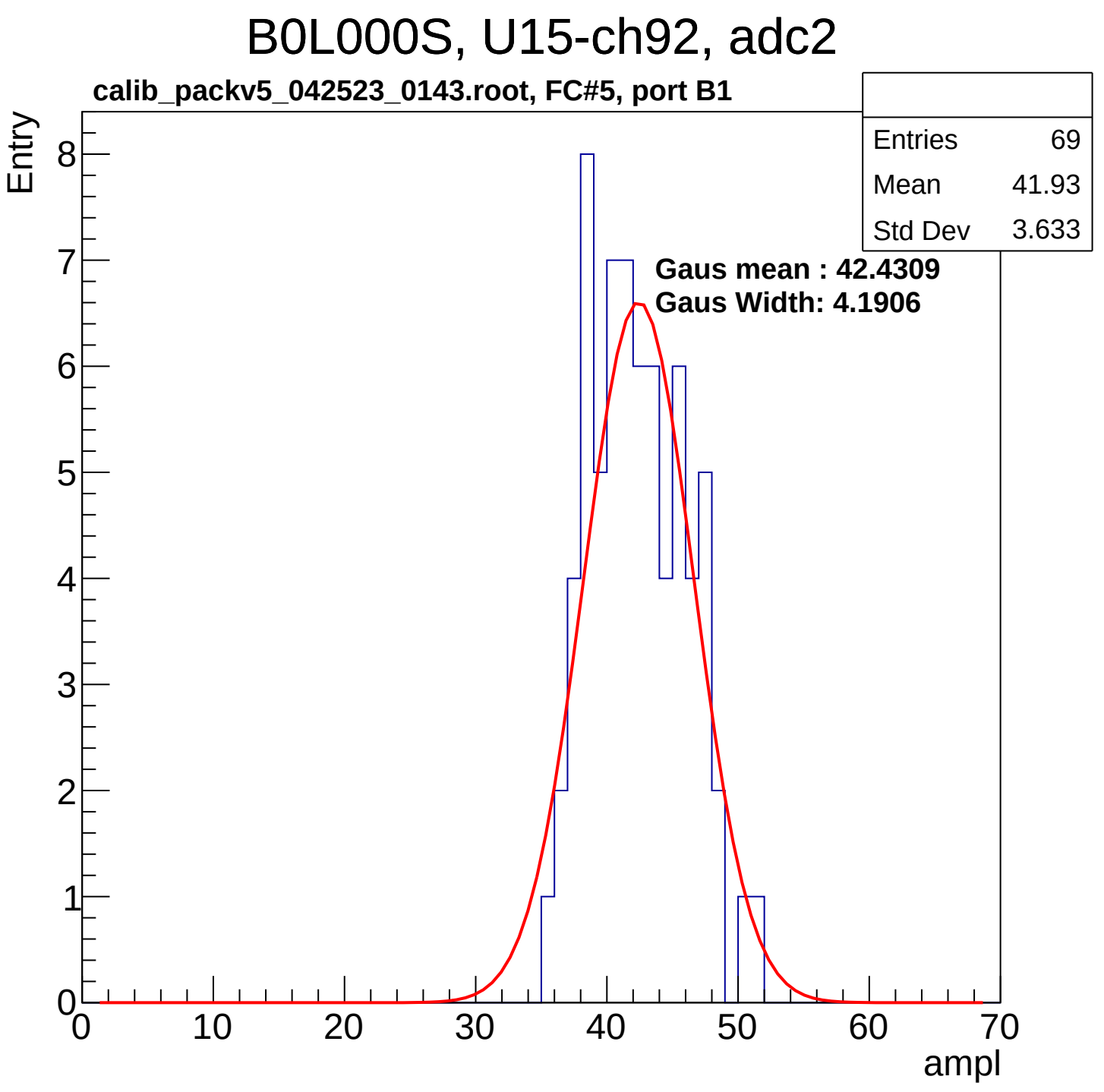
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 34.98
Std Dev 3.349
Gaus mean : 35.0734
Gaus Width: 4.1389
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 41.93
Std Dev 3.633
Gaus mean : 42.4309
Gaus Width: 4.1906
Entry
ampl
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1
2
3
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5
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0
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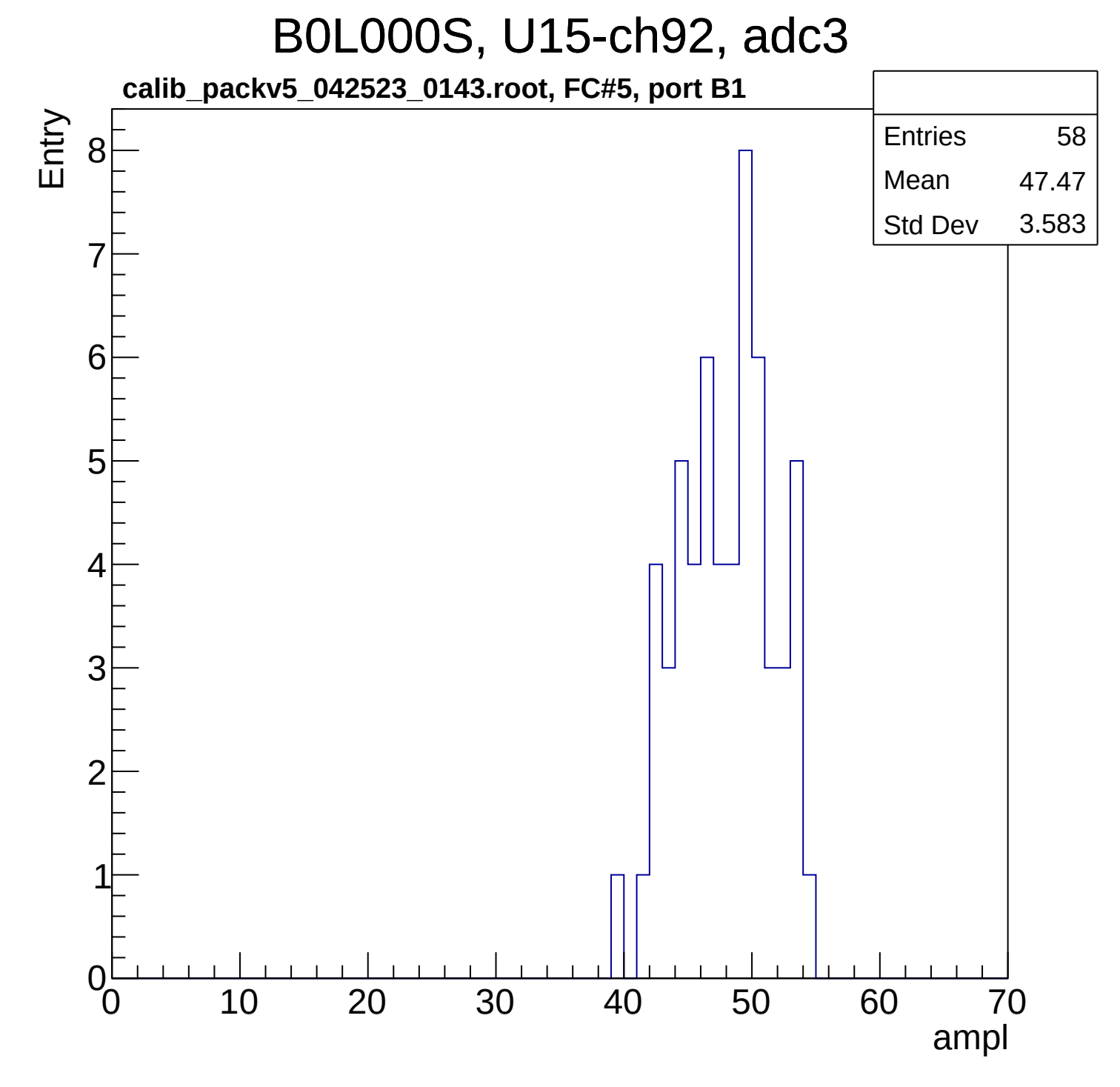
B0L000S, U15-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.47
Std Dev 3.583
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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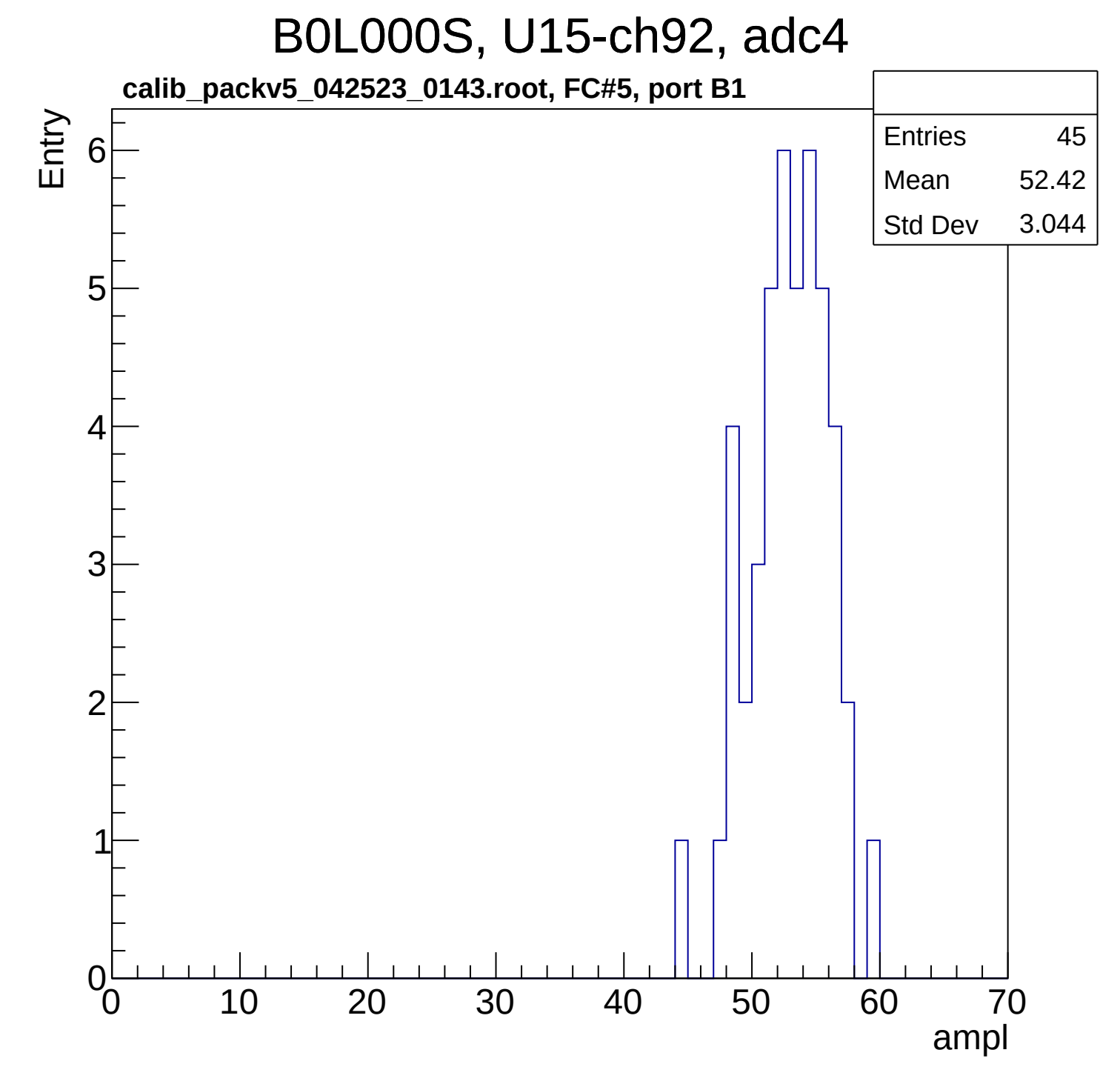
B0L000S, U15-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 52.42
Std Dev 3.044
Entry
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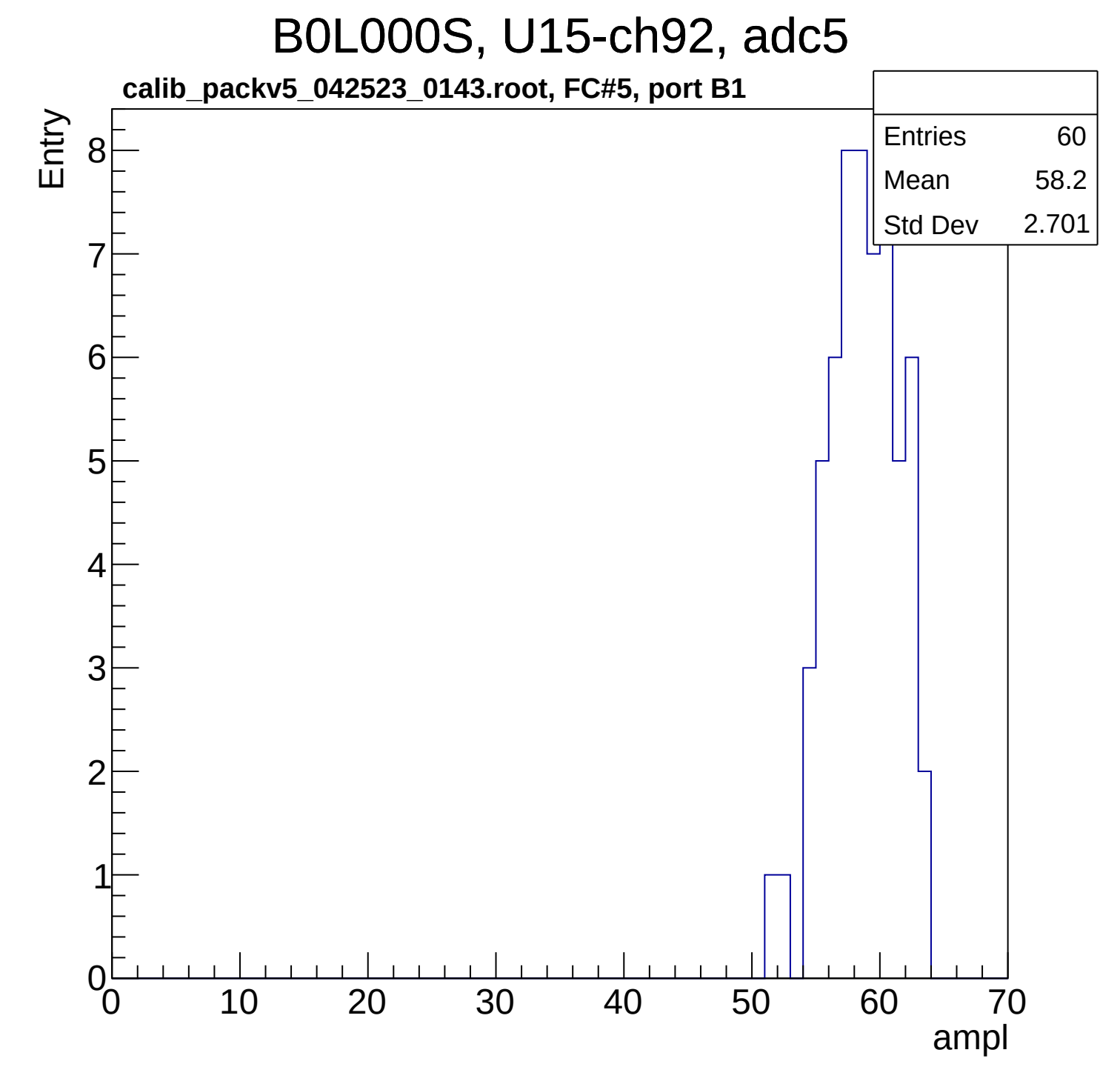
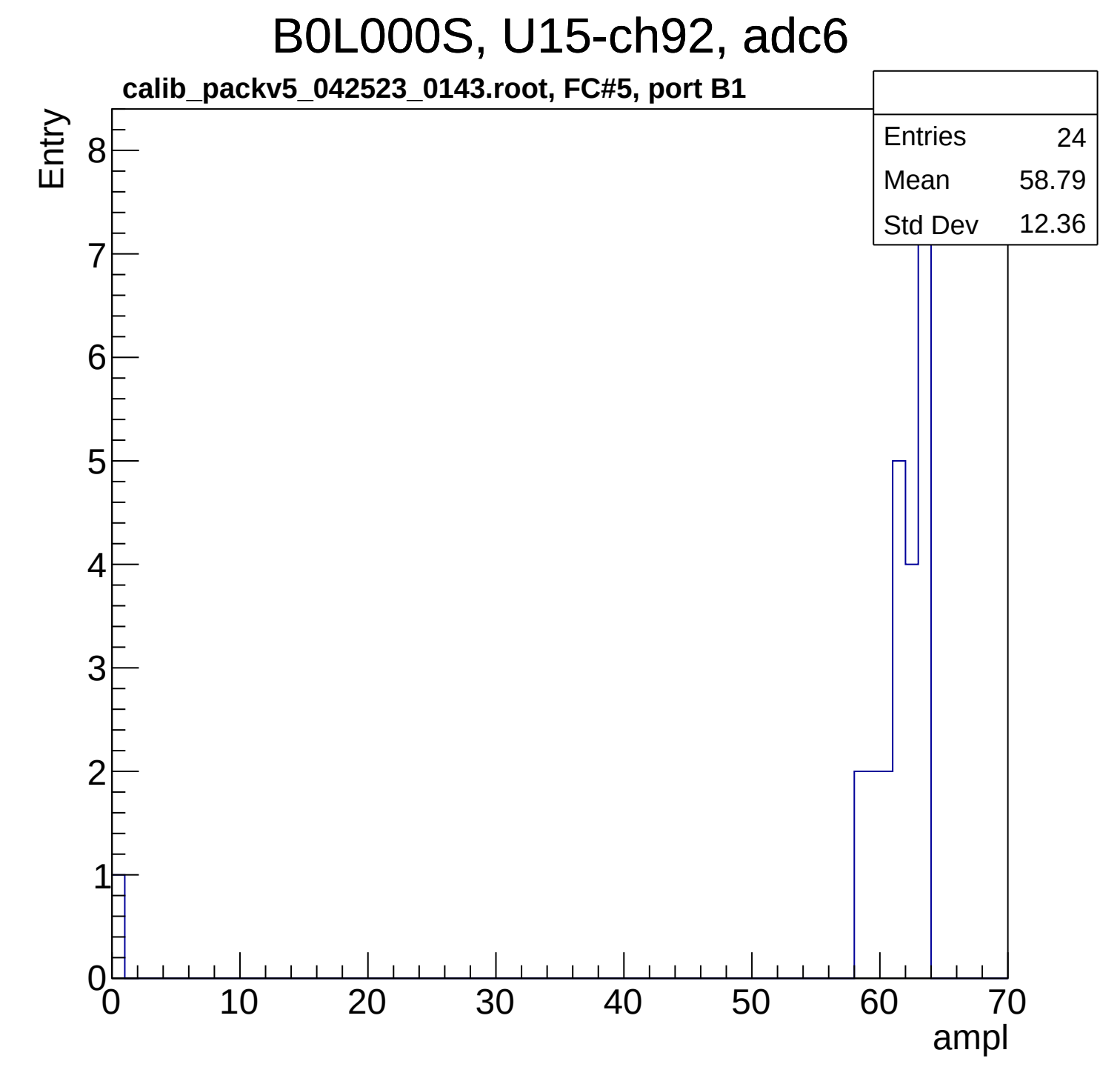
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.2
Std Dev 2.701
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.79
Std Dev 12.36
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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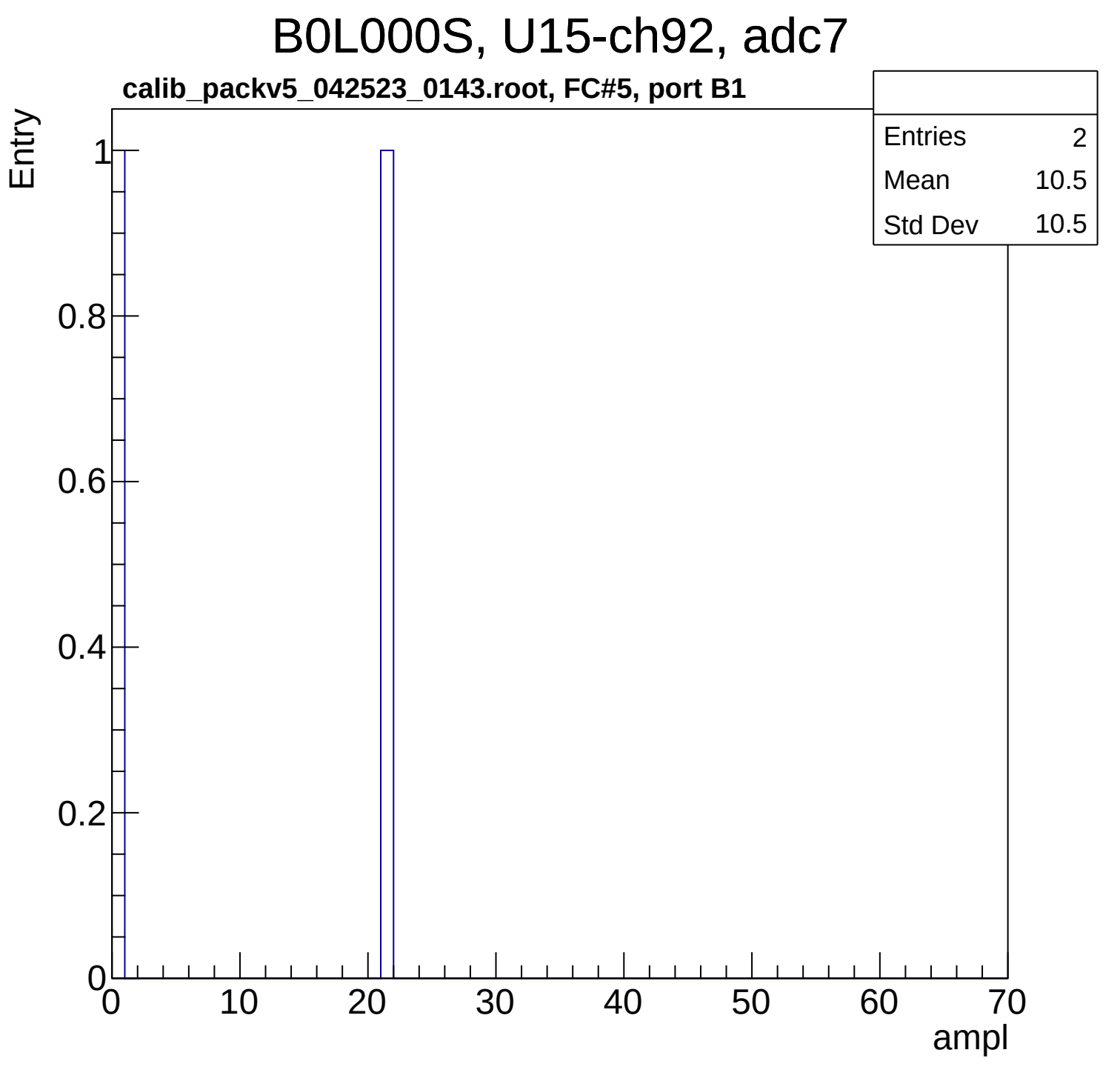
B0L000S, U15-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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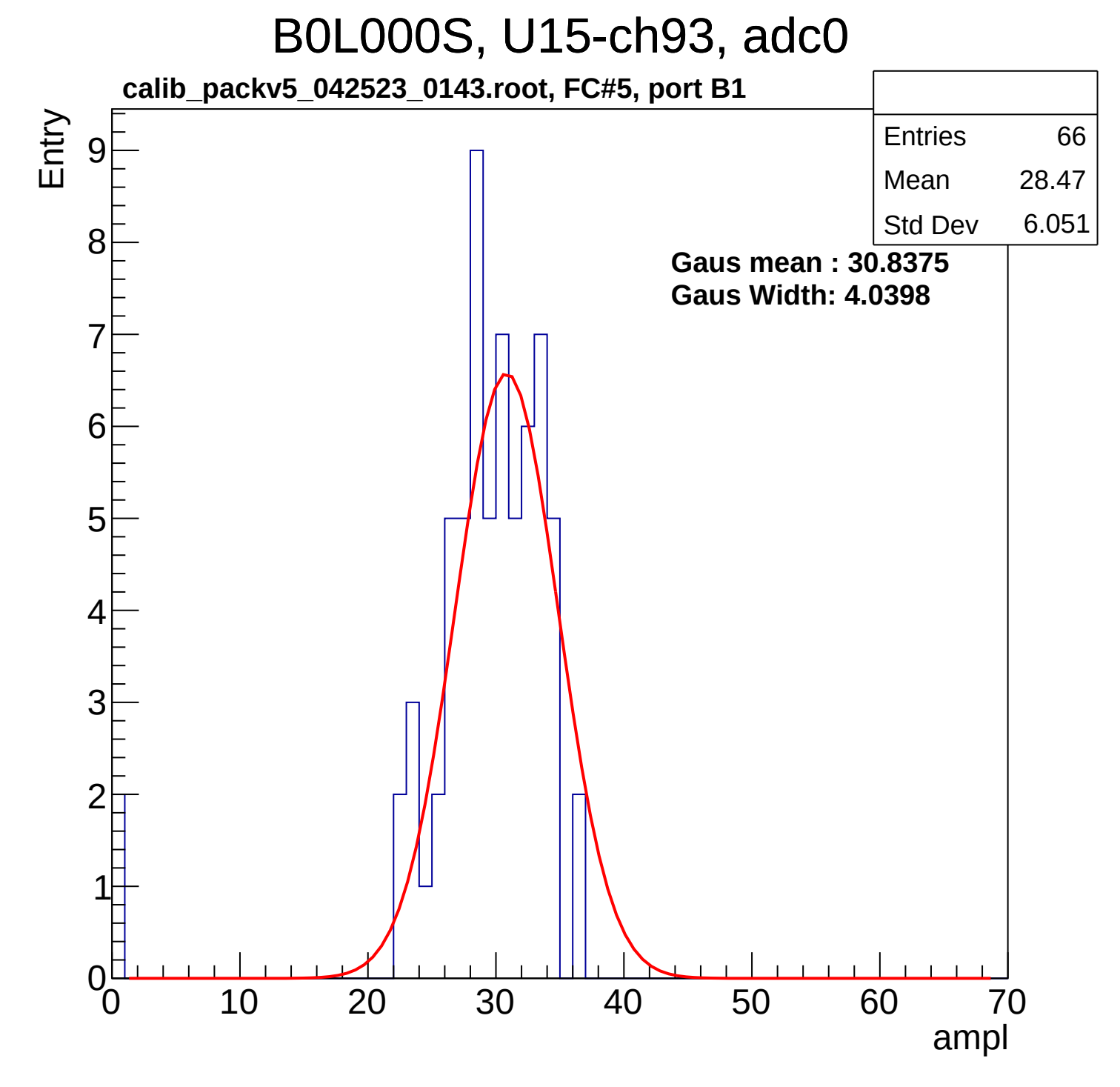
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.47
Std Dev 6.051
Gaus mean : 30.8375
Gaus Width: 4.0398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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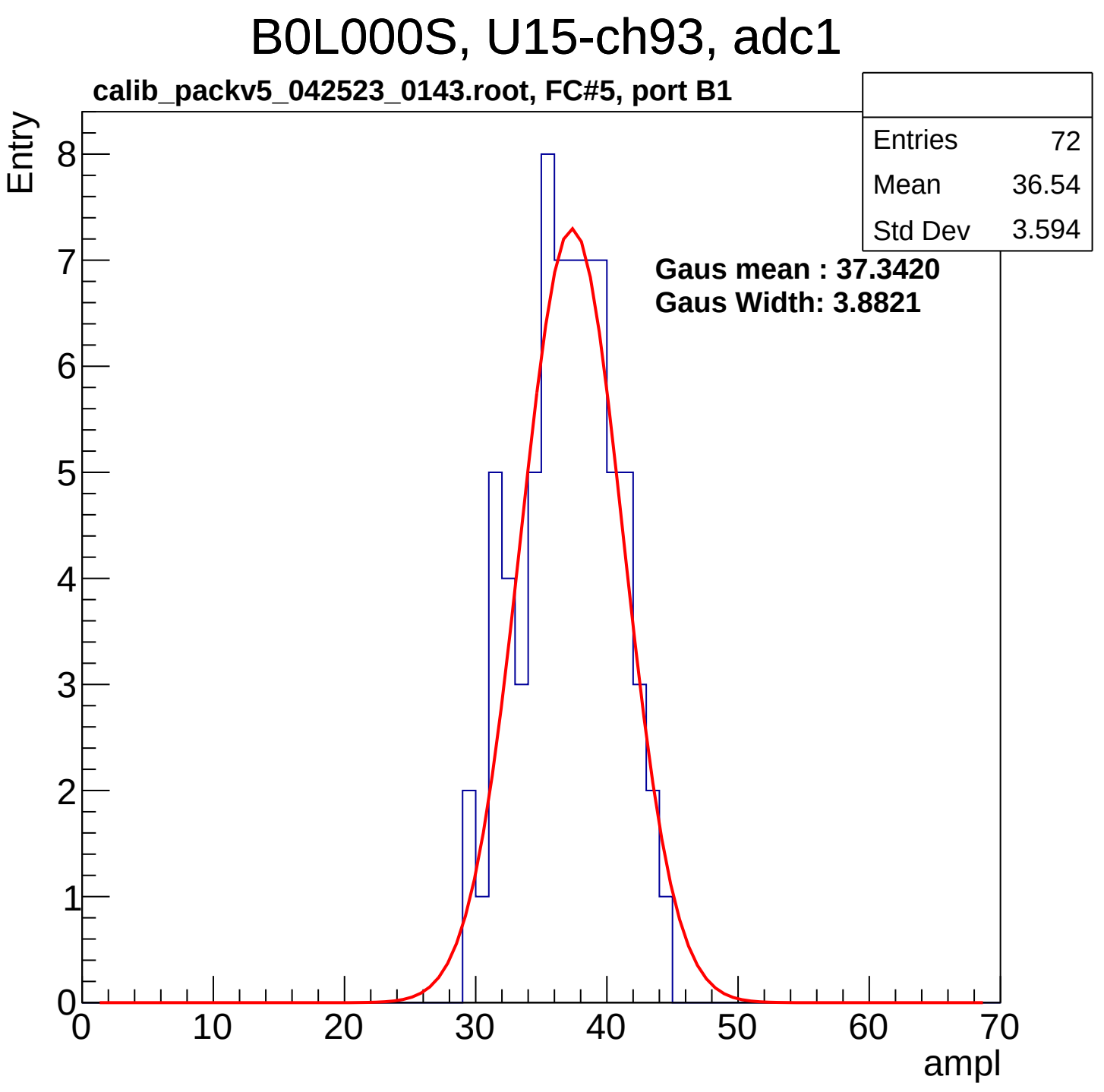
B0L000S, U15-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.54
Std Dev 3.594
Gaus mean : 37.3420
Gaus Width: 3.8821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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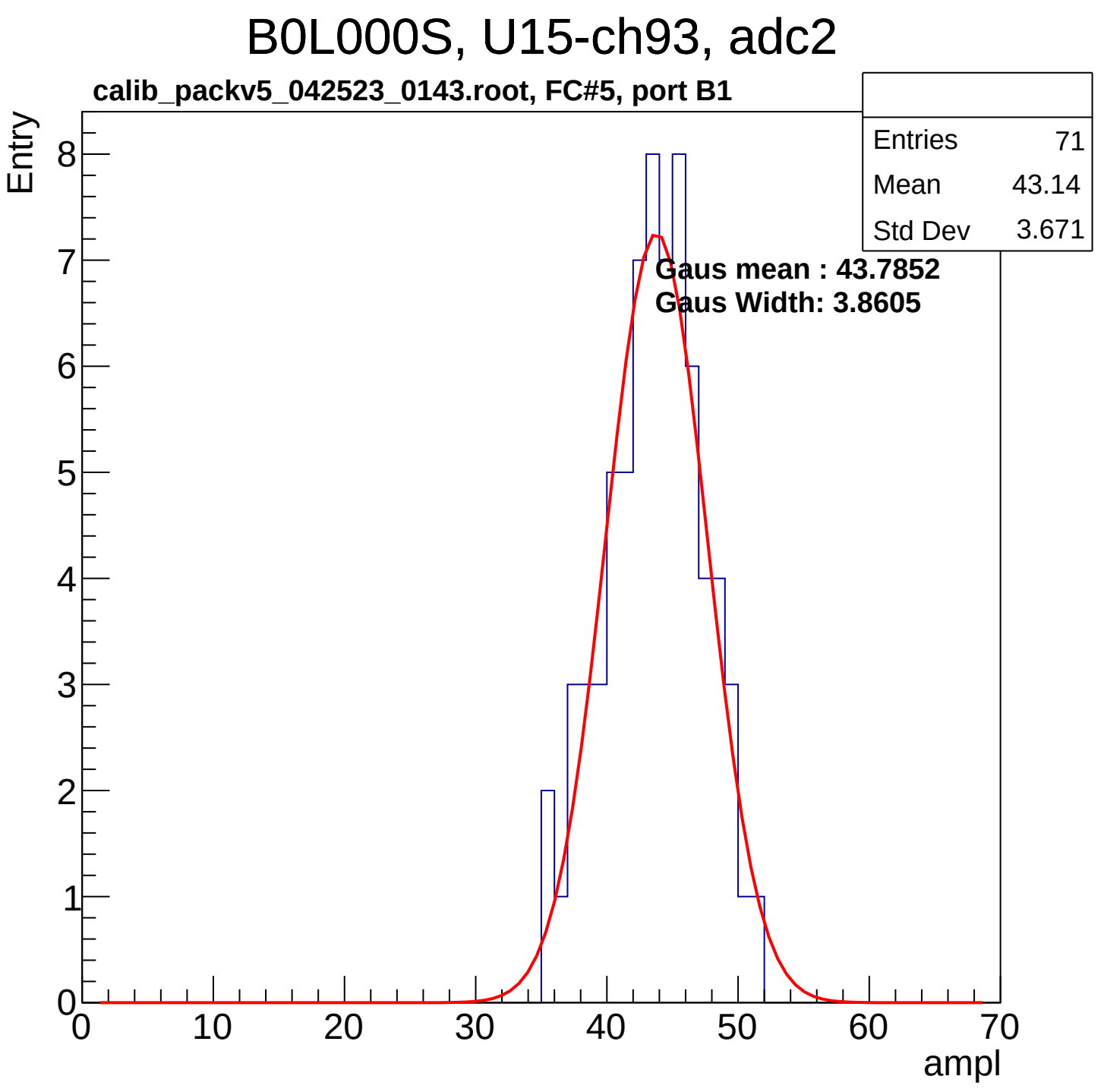
B0L000S, U15-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.14
Std Dev 3.671
Gaus mean : 43.7852
Gaus Width: 3.8605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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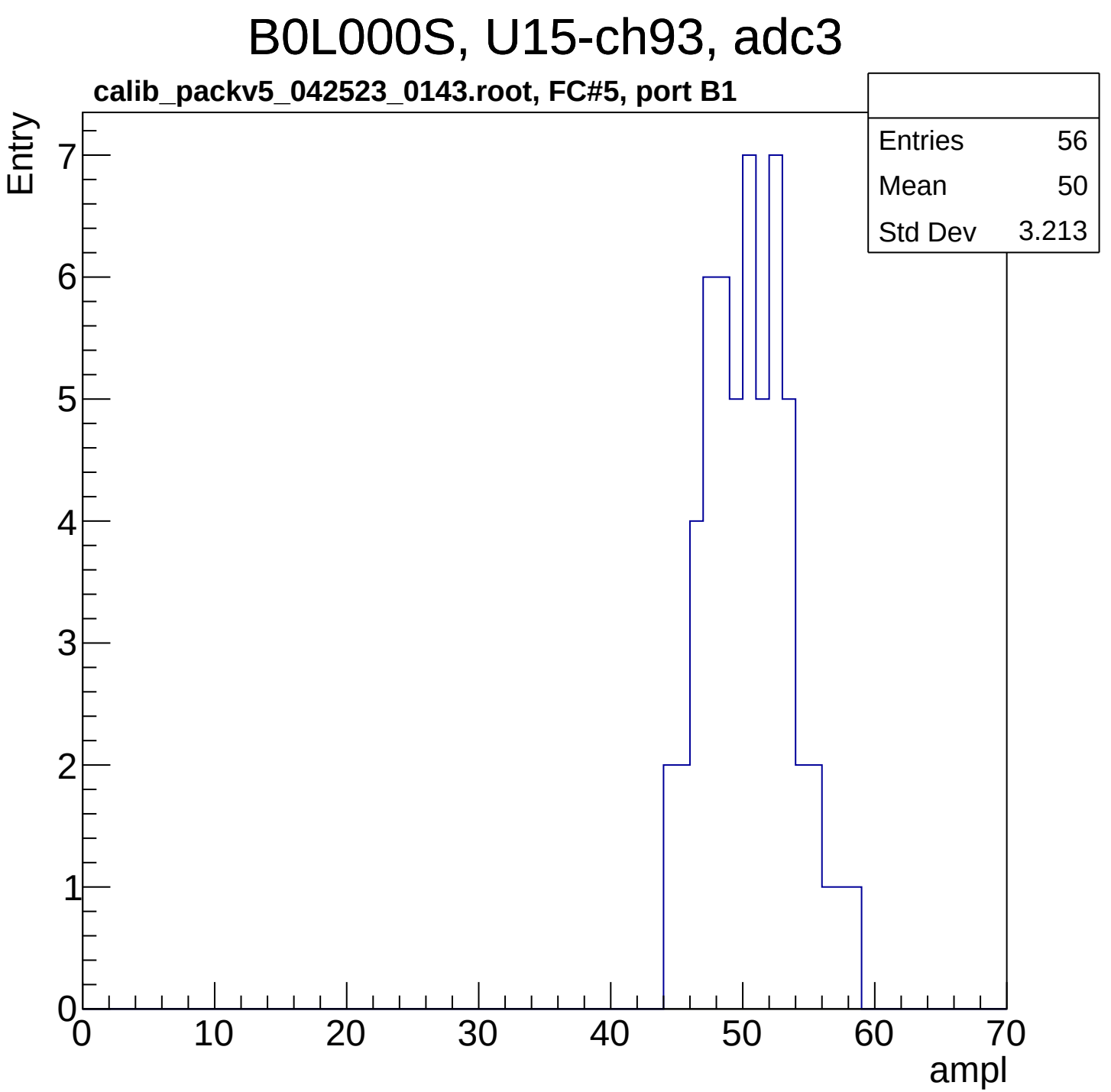
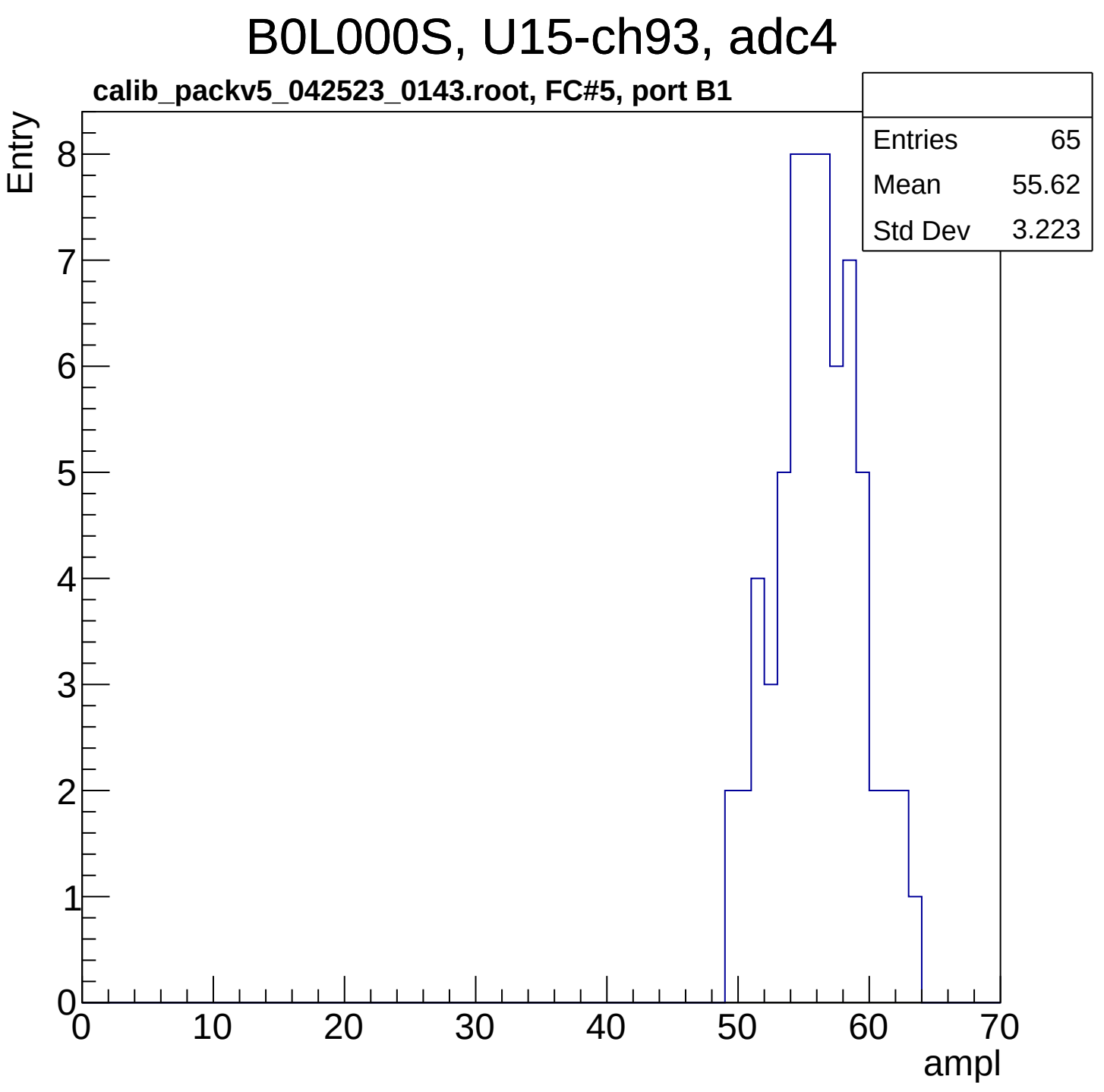

B0L000S, U15-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 50
Std Dev 3.213
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.62
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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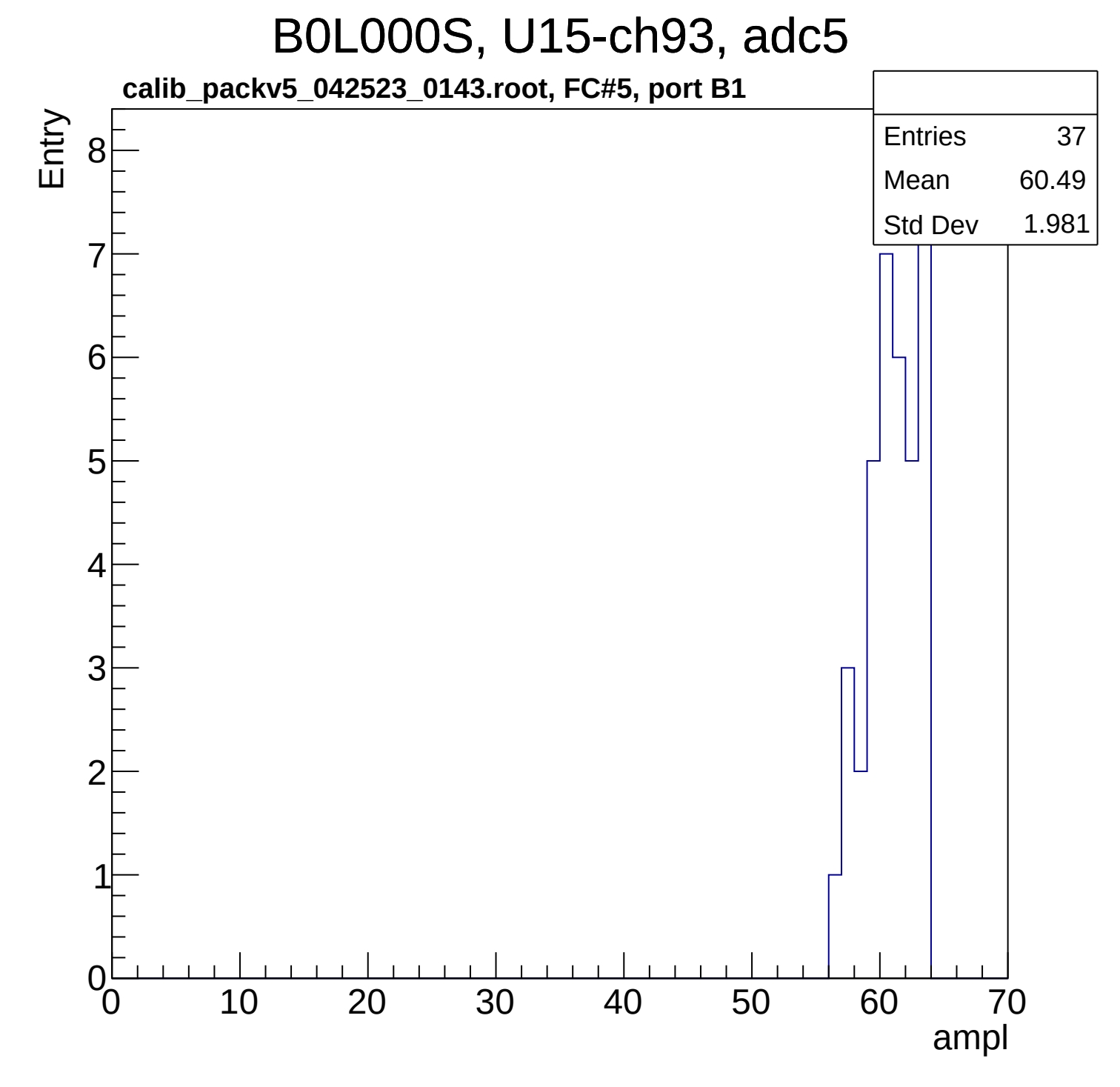
B0L000S, U15-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.49
Std Dev 1.981
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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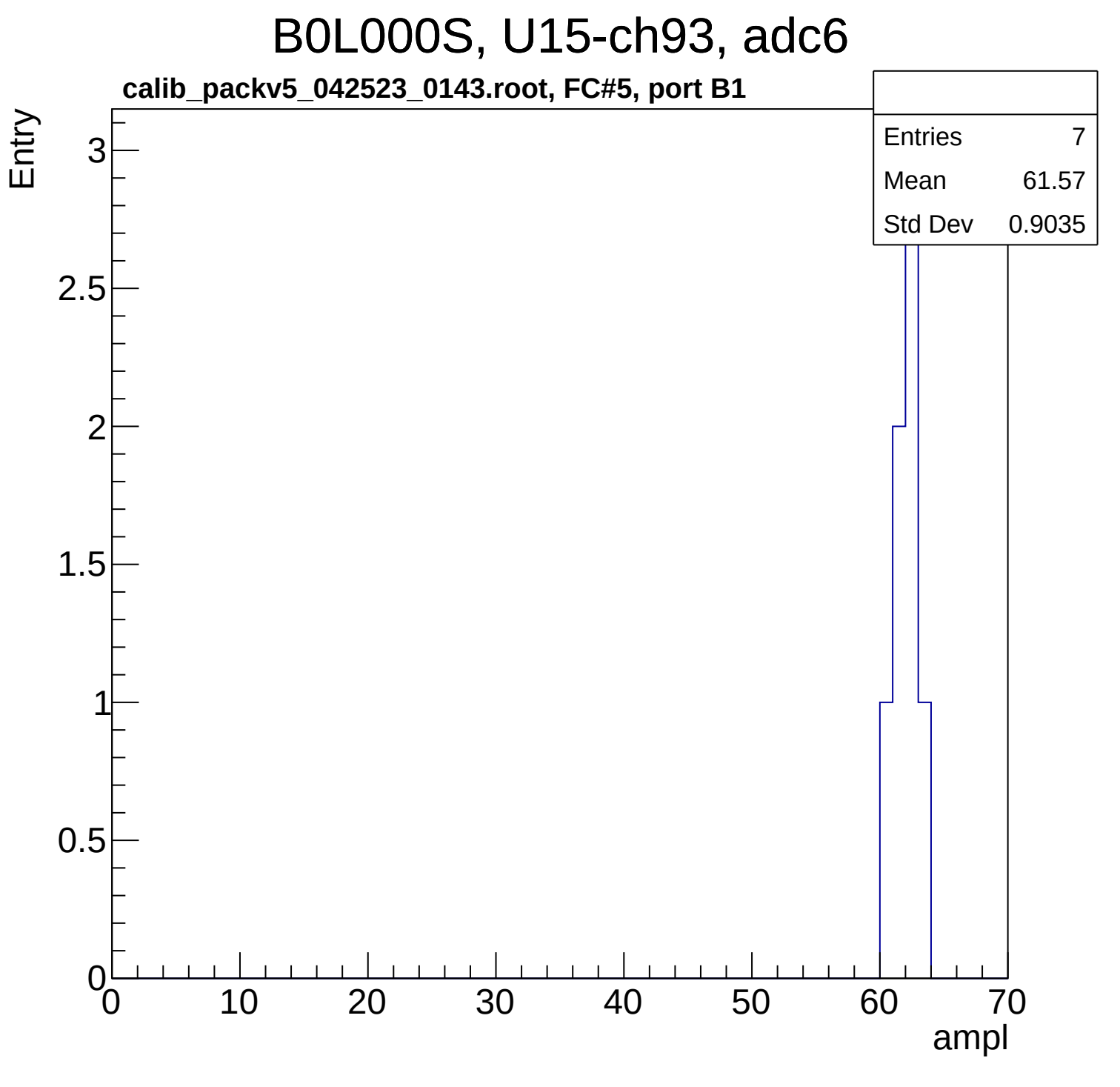
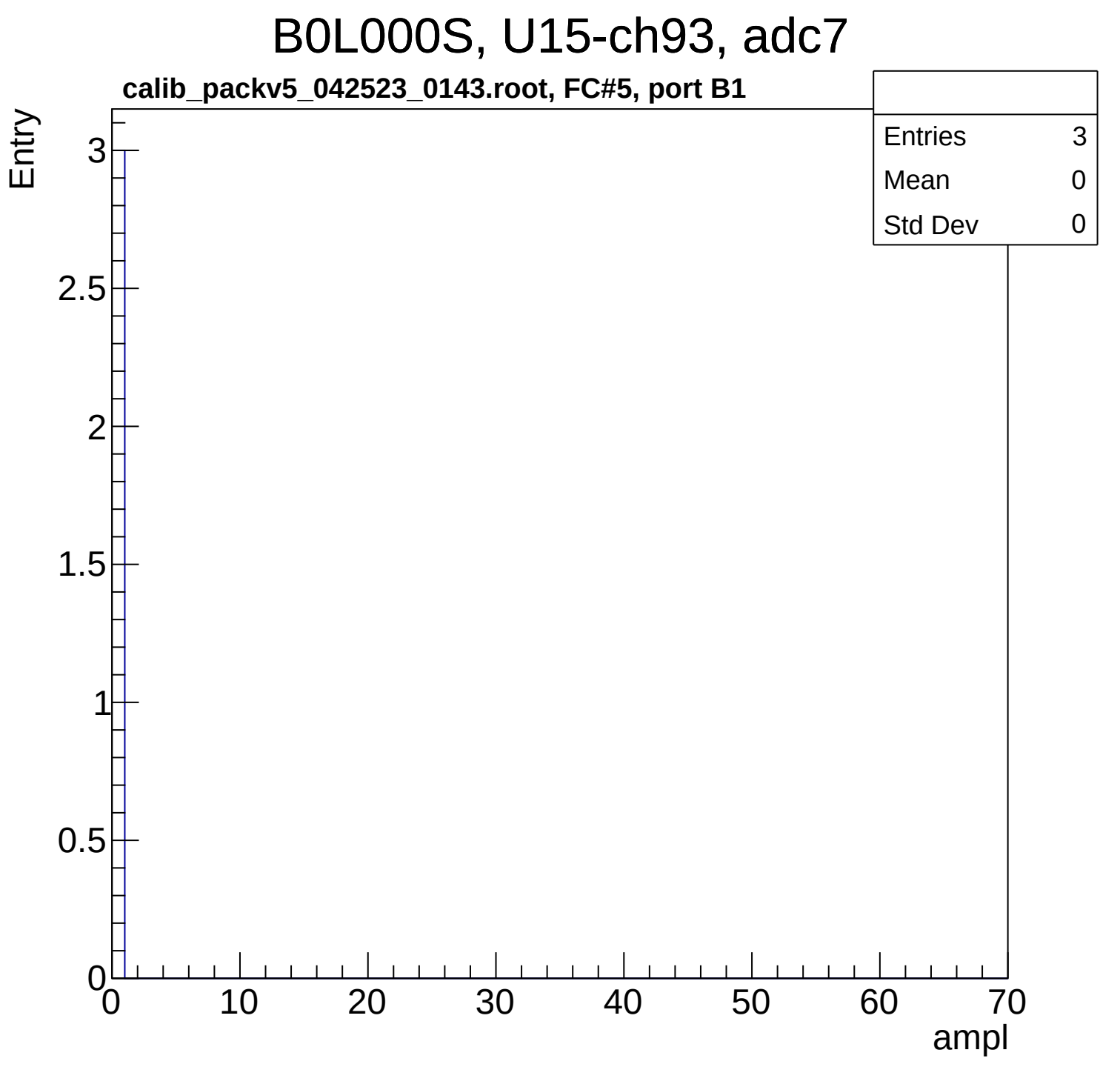
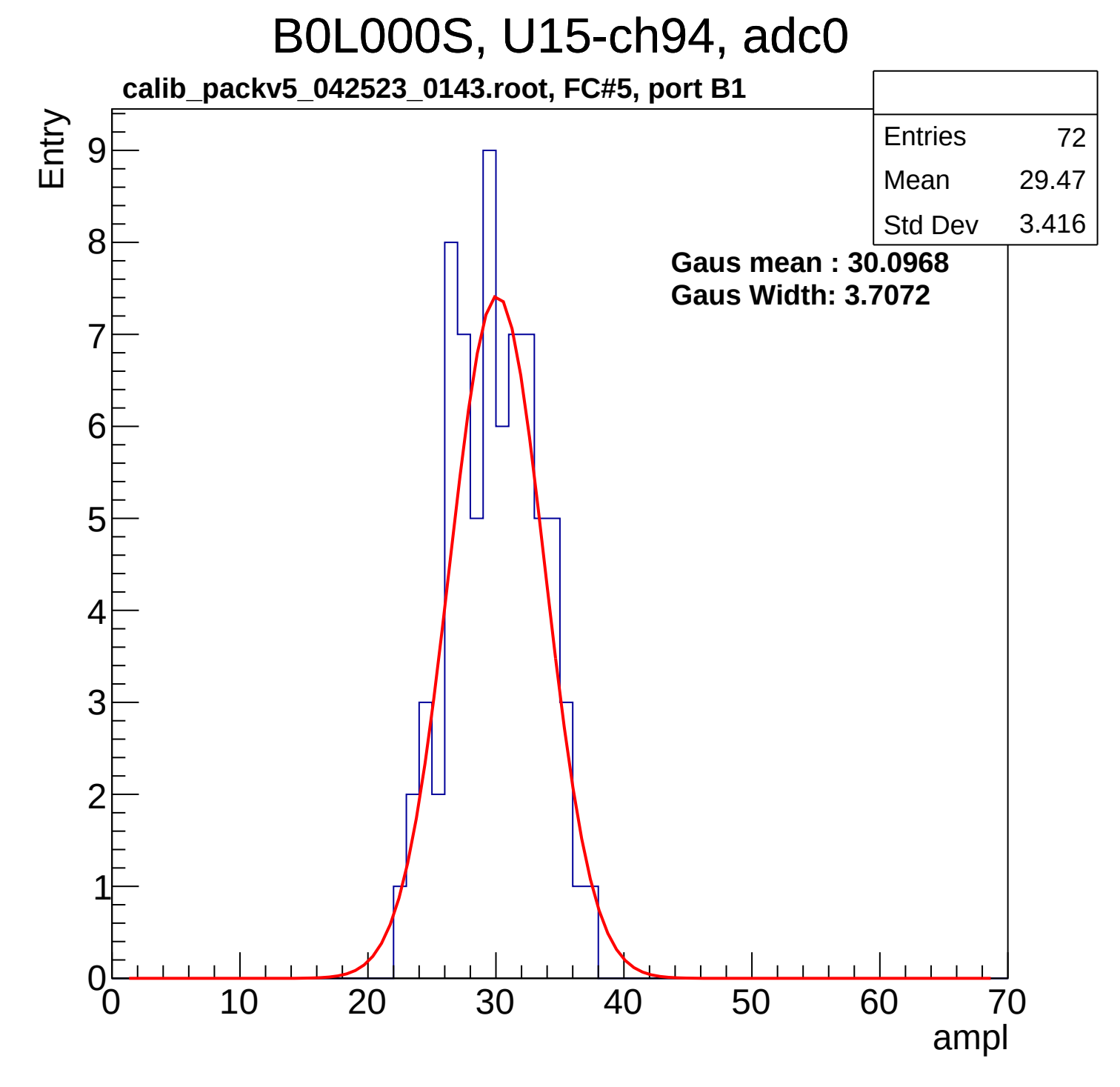

B0L000S, U15-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.57
Std Dev 0.9035
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.47
Std Dev 3.416
Gaus mean : 30.0968
Gaus Width: 3.7072
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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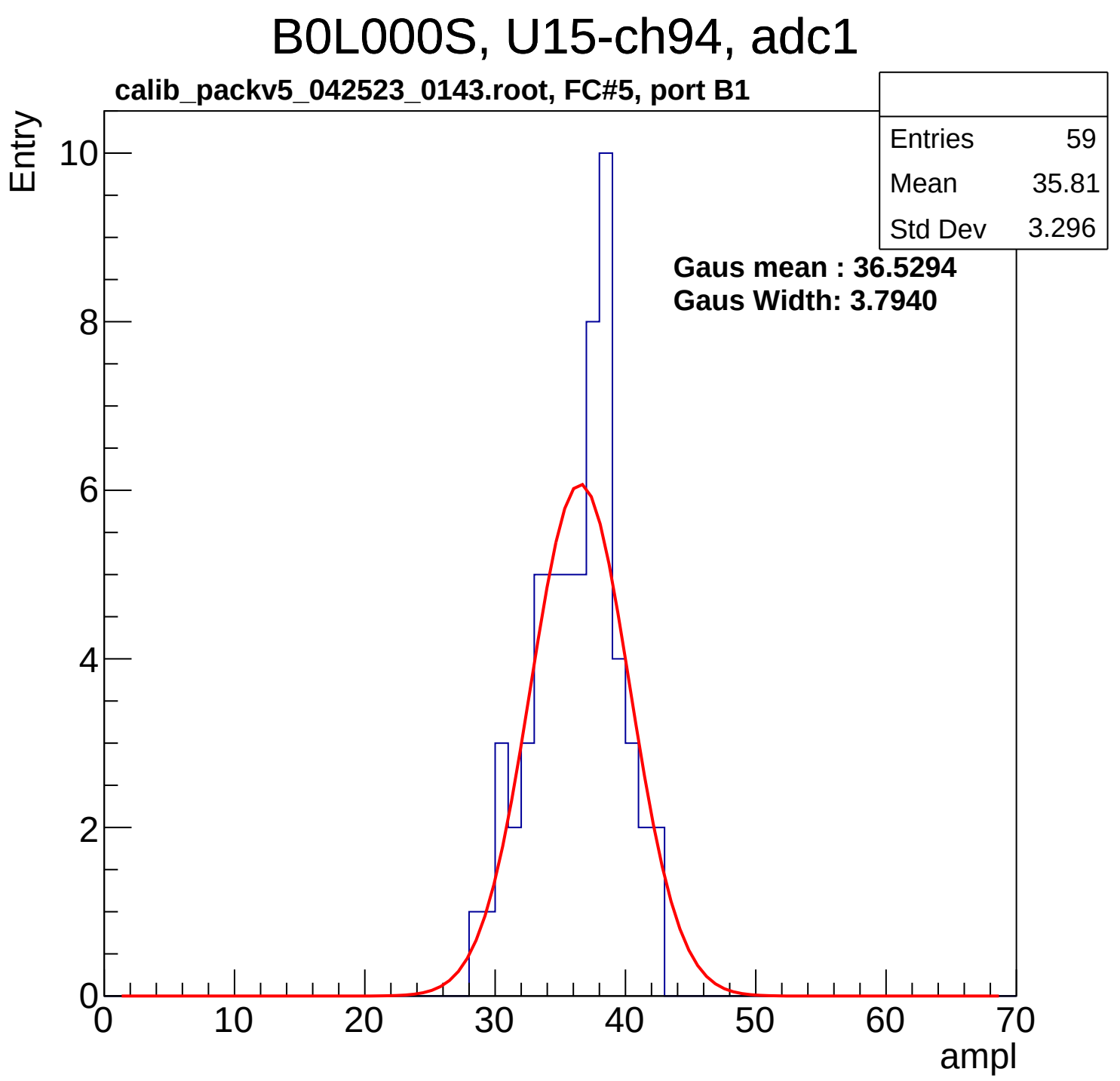
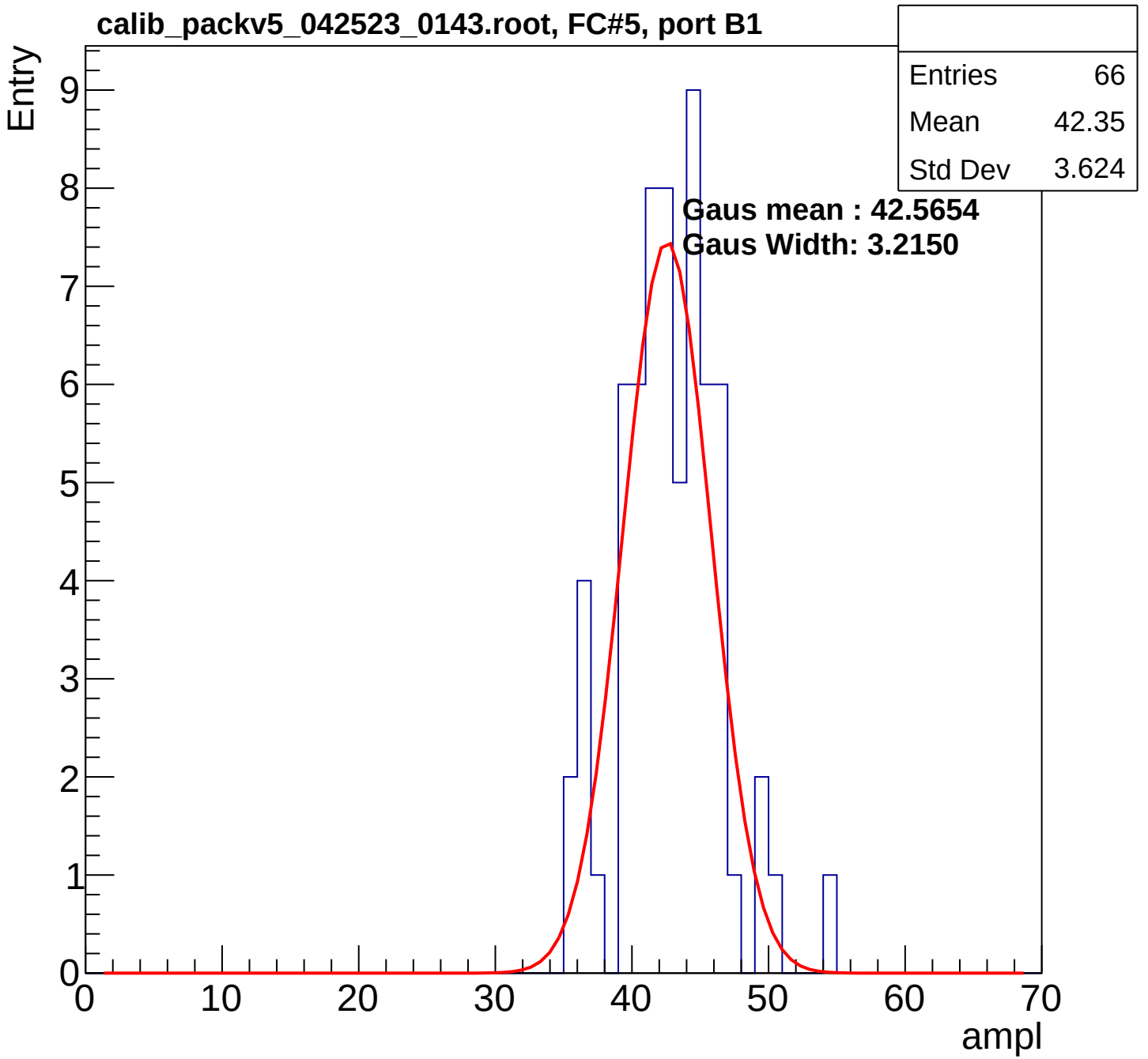

B0L000S, U15-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.81
Std Dev 3.296
Gaus mean : 36.5294
Gaus Width: 3.7940
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.35
Std Dev 3.624
Gaus mean : 42.5654
Gaus Width: 3.2150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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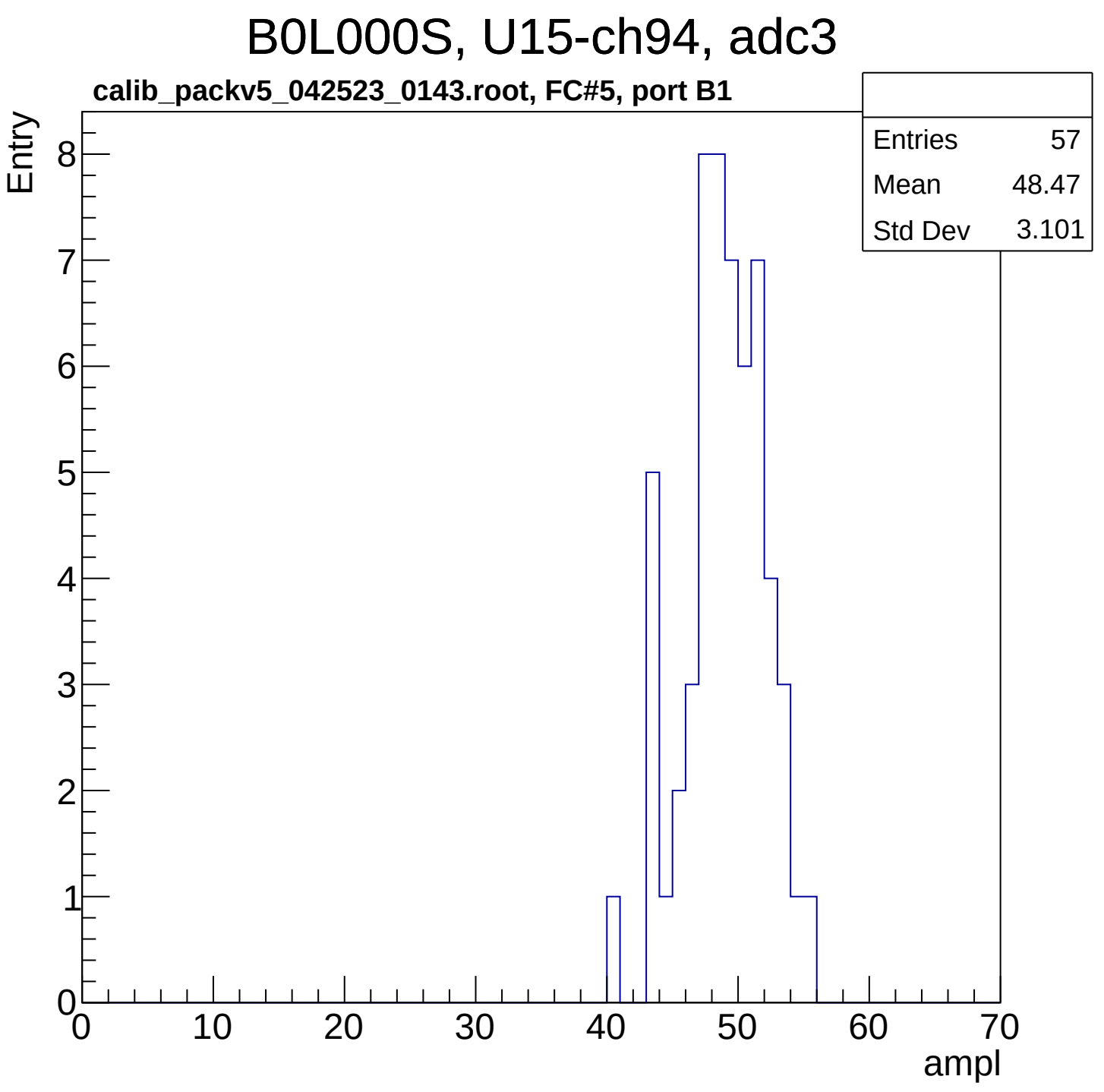
B0L000S, U15-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 48.47
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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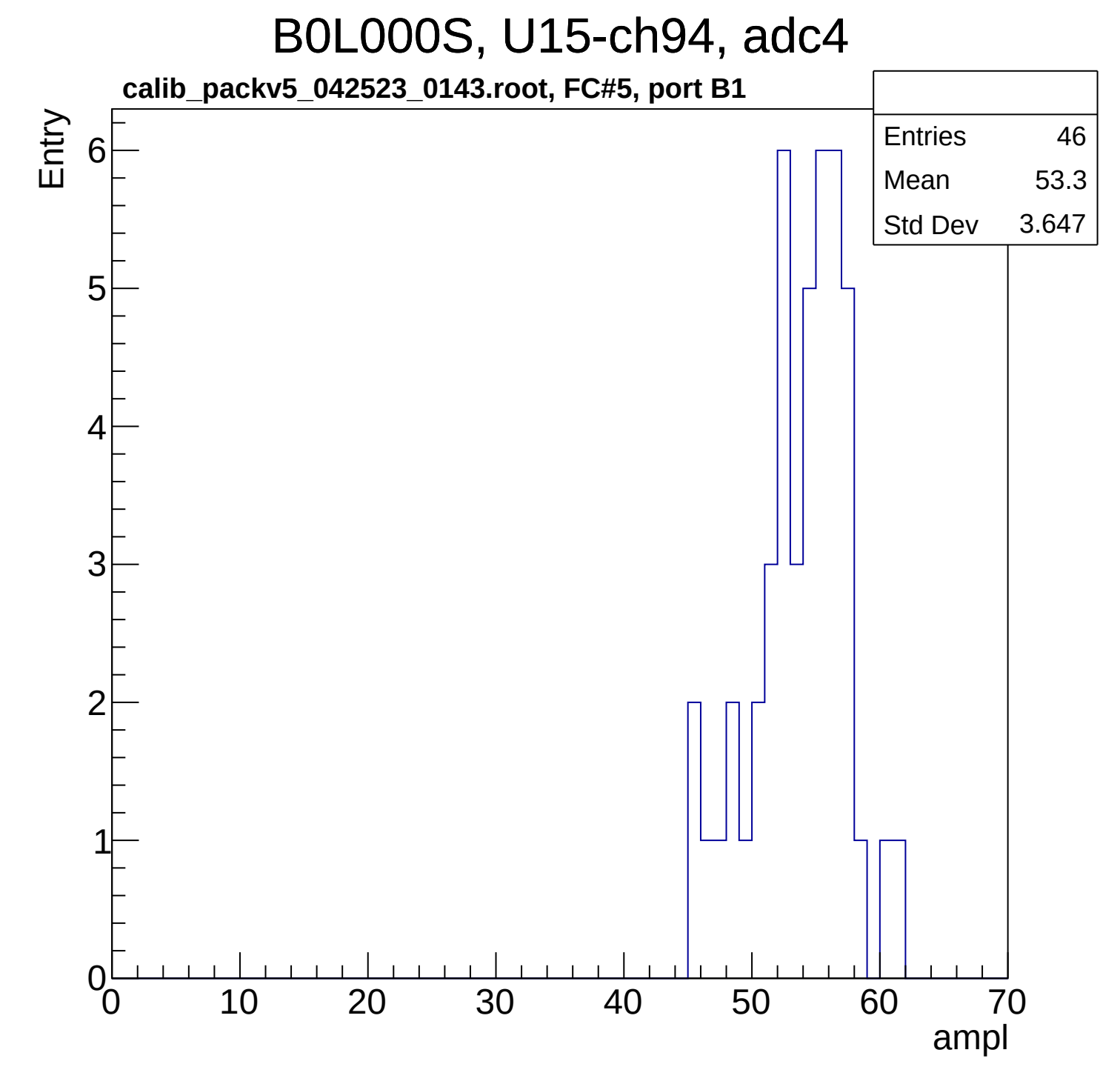
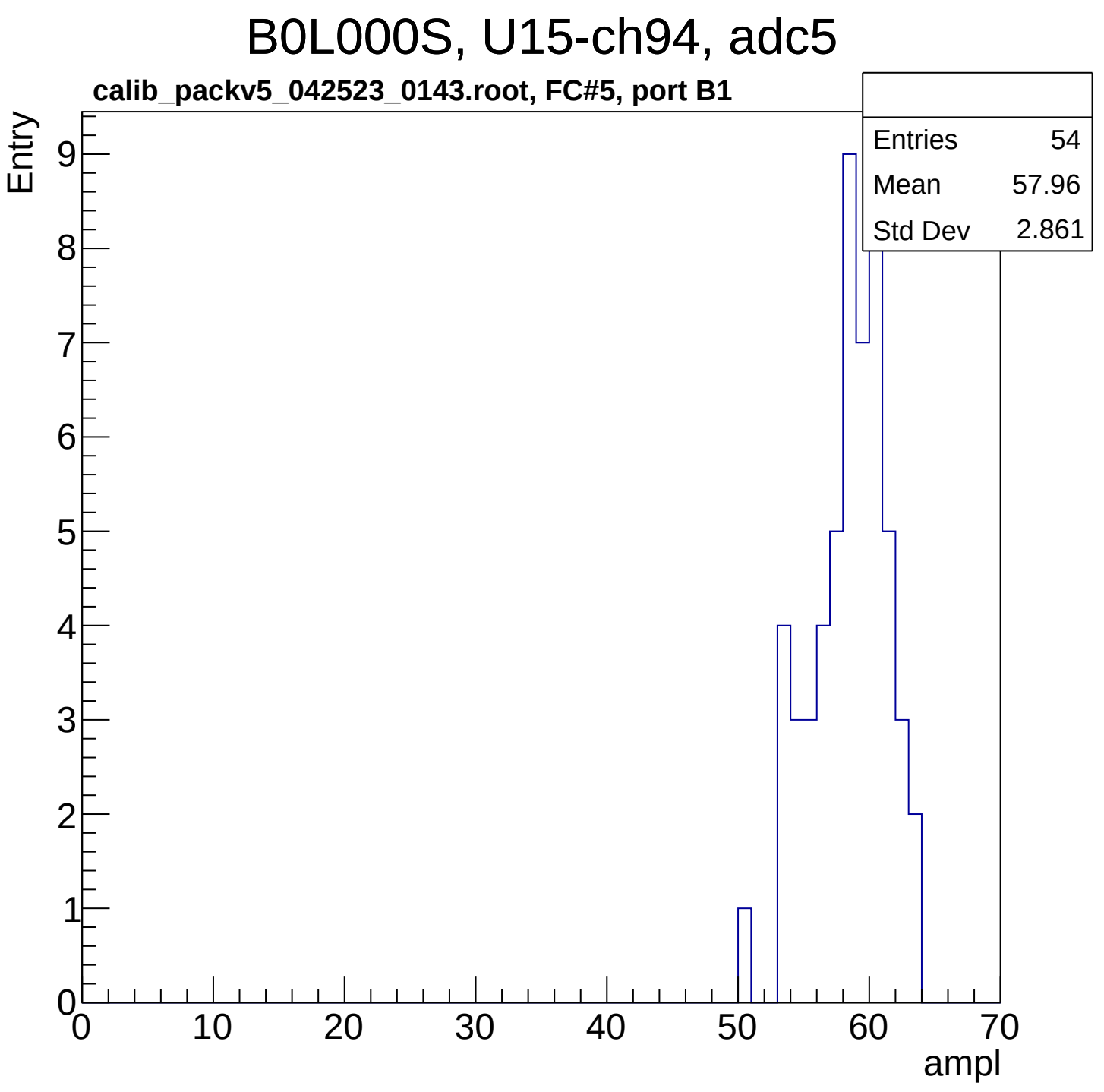

B0L000S, U15-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 53.3
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 57.96
Std Dev 2.861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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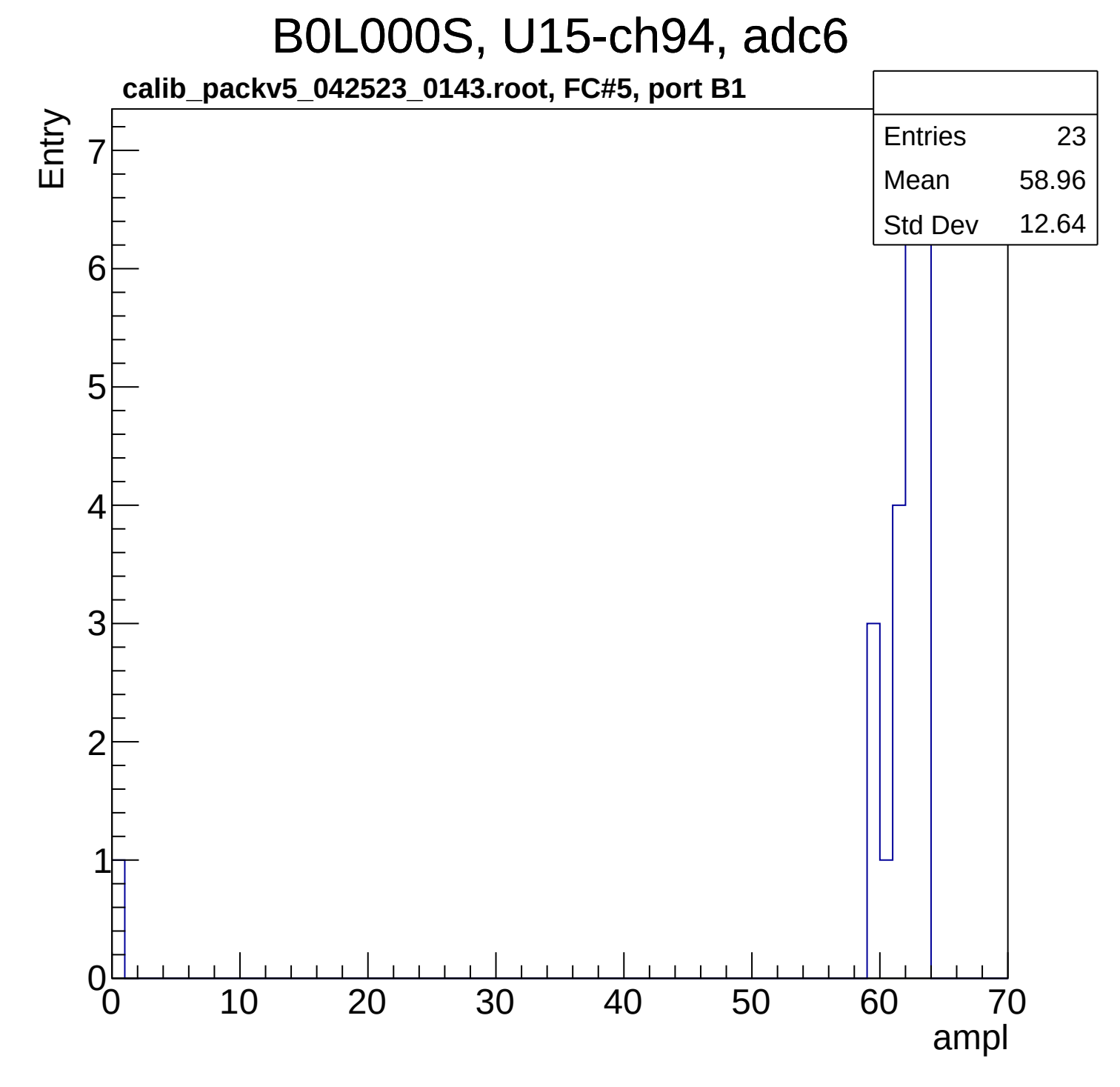
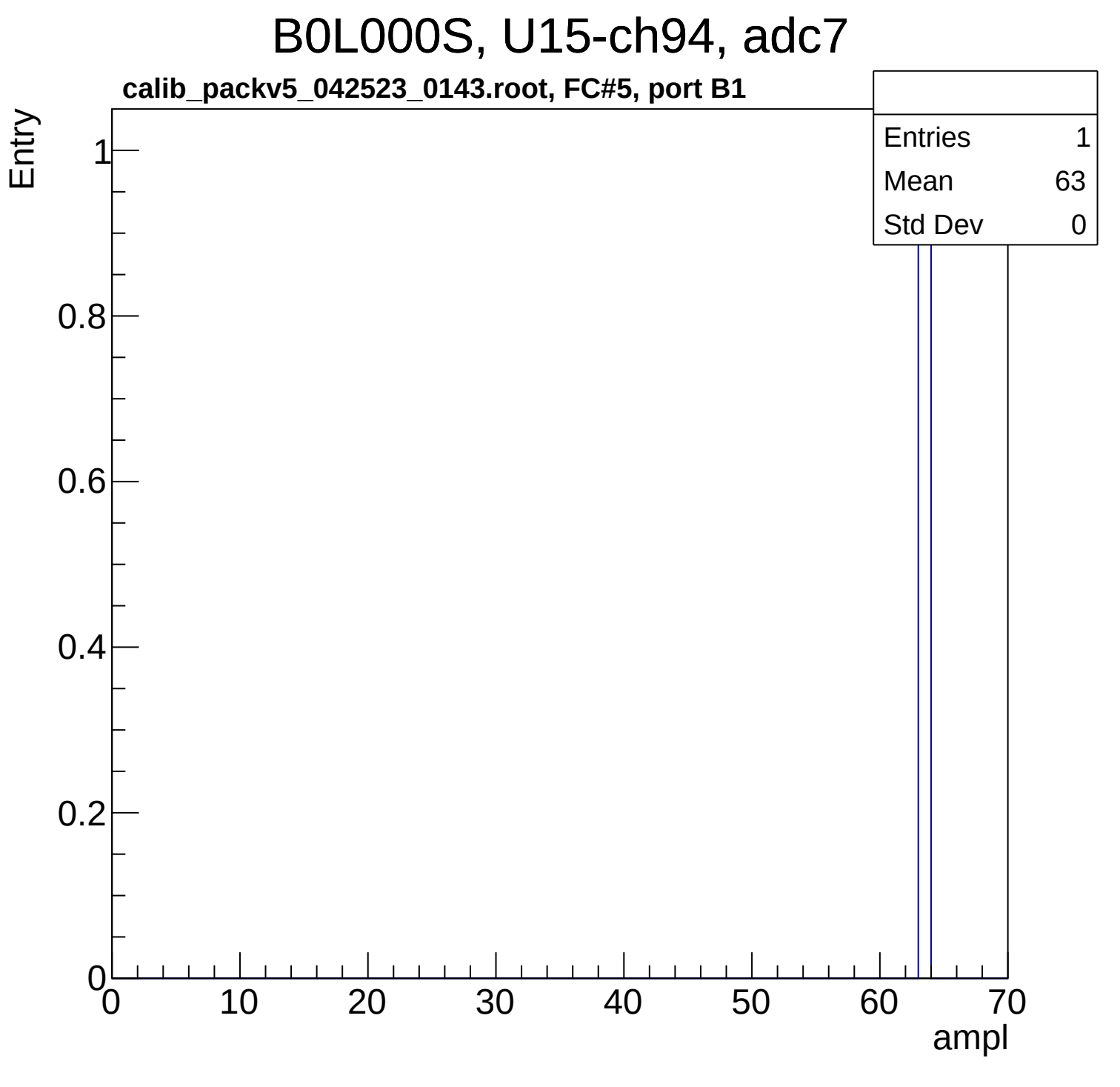

B0L000S, U15-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.96
Std Dev 12.64
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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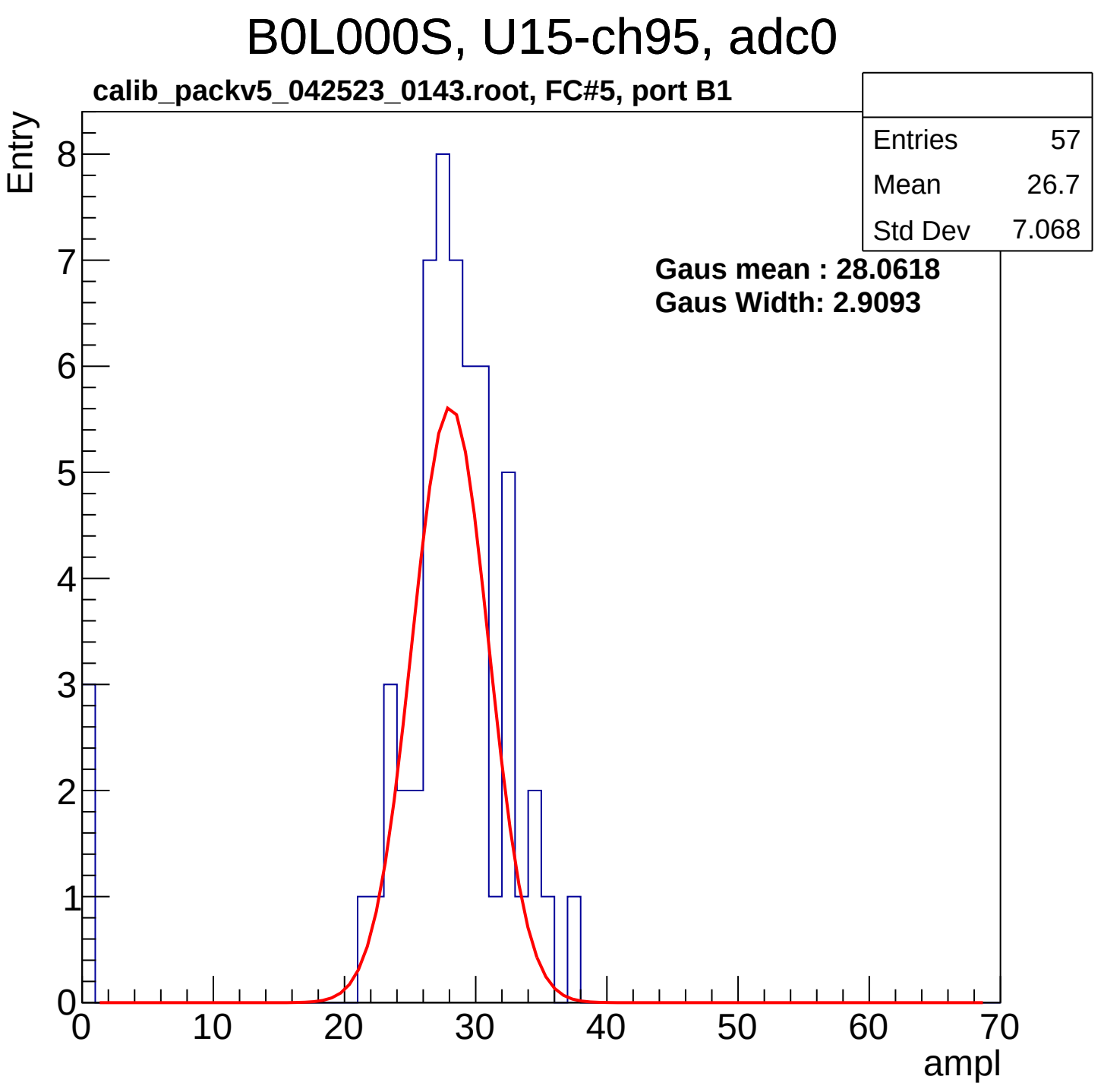
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 26.7
Std Dev 7.068
Gaus mean : 28.0618
Gaus Width: 2.9093
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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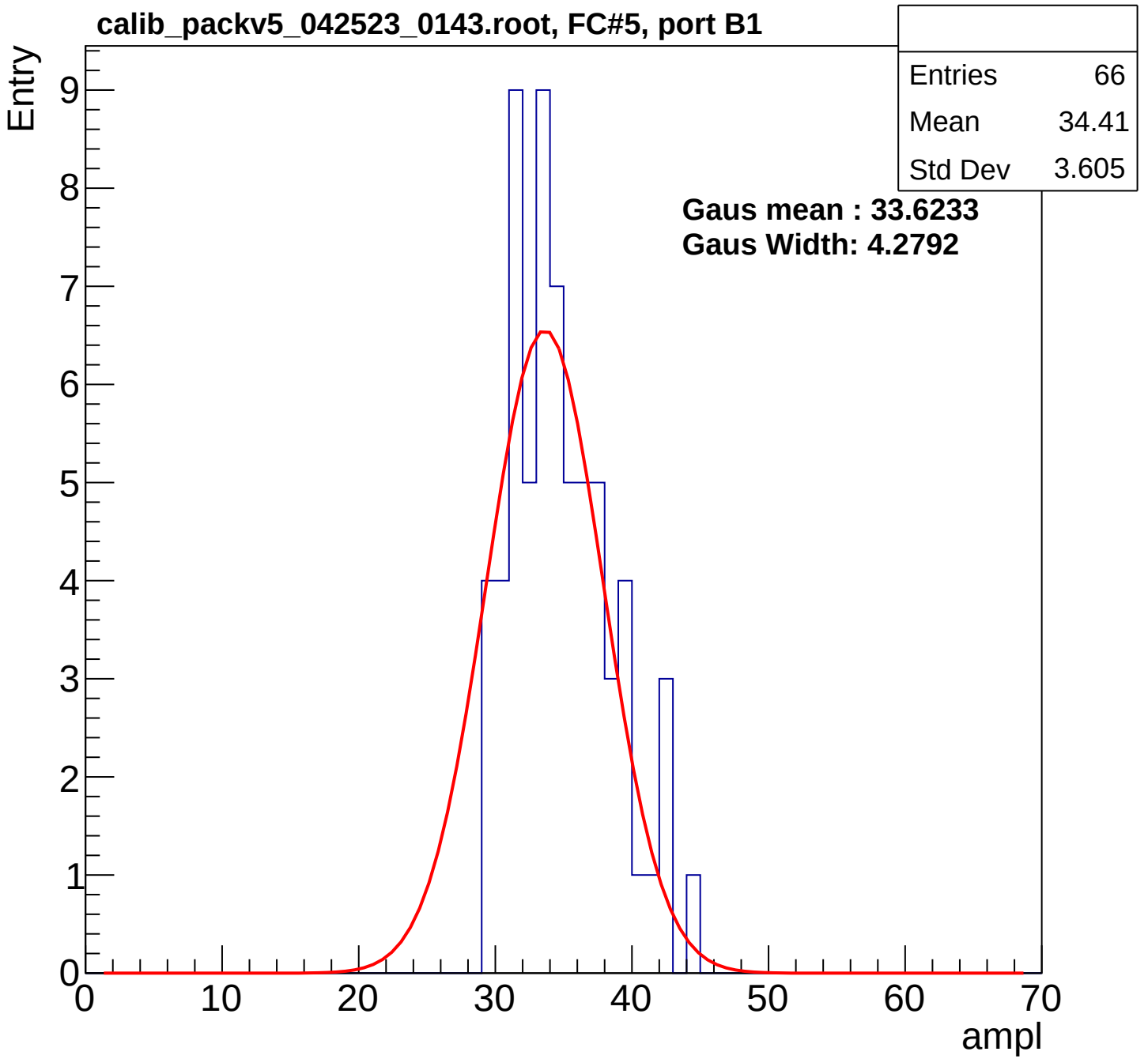
B0L000S, U15-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 34.41
Std Dev 3.605
Gaus mean : 33.6233
Gaus Width: 4.2792
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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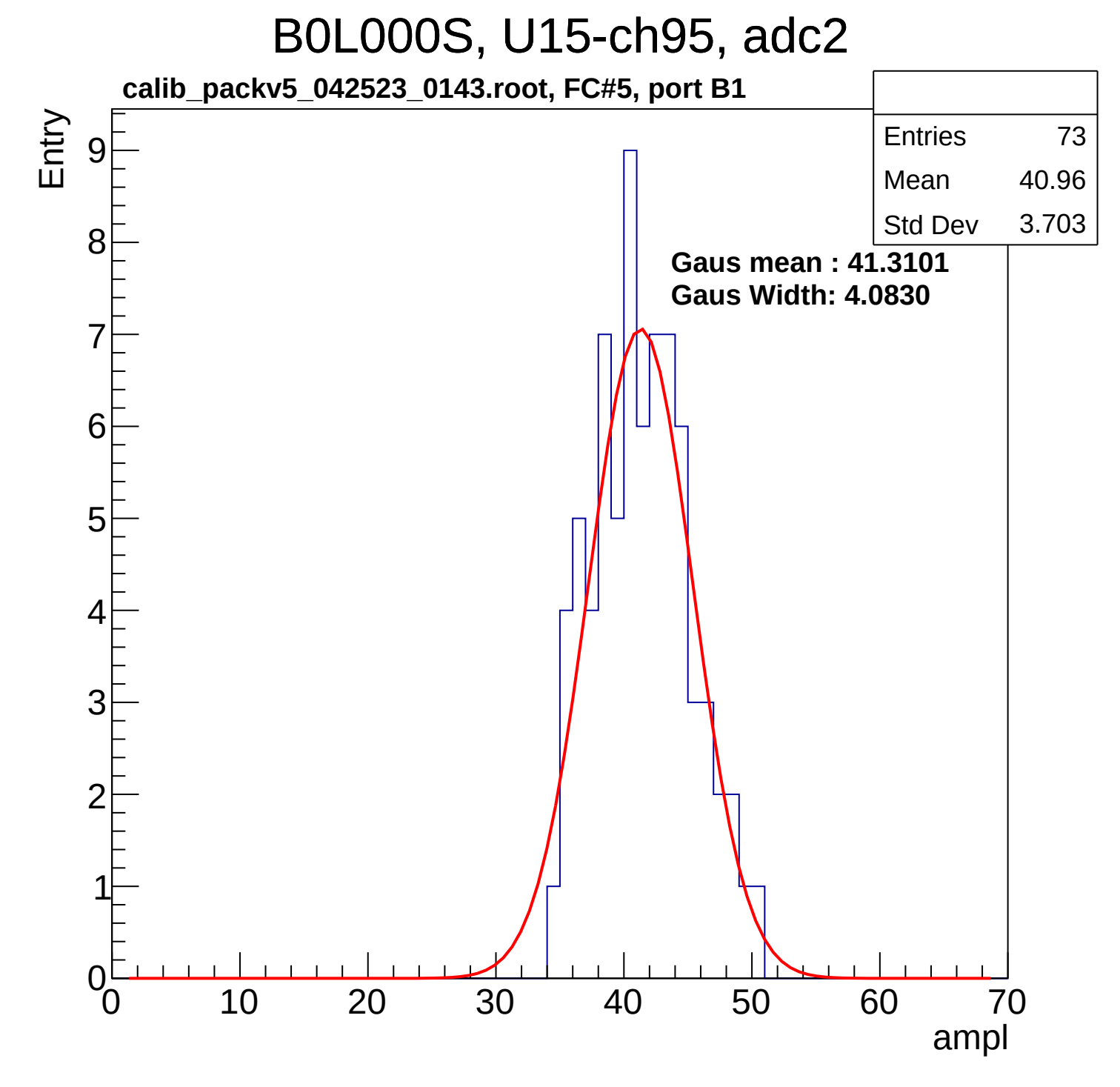
B0L000S, U15-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 40.96
Std Dev 3.703
Gaus mean : 41.3101
Gaus Width: 4.0830
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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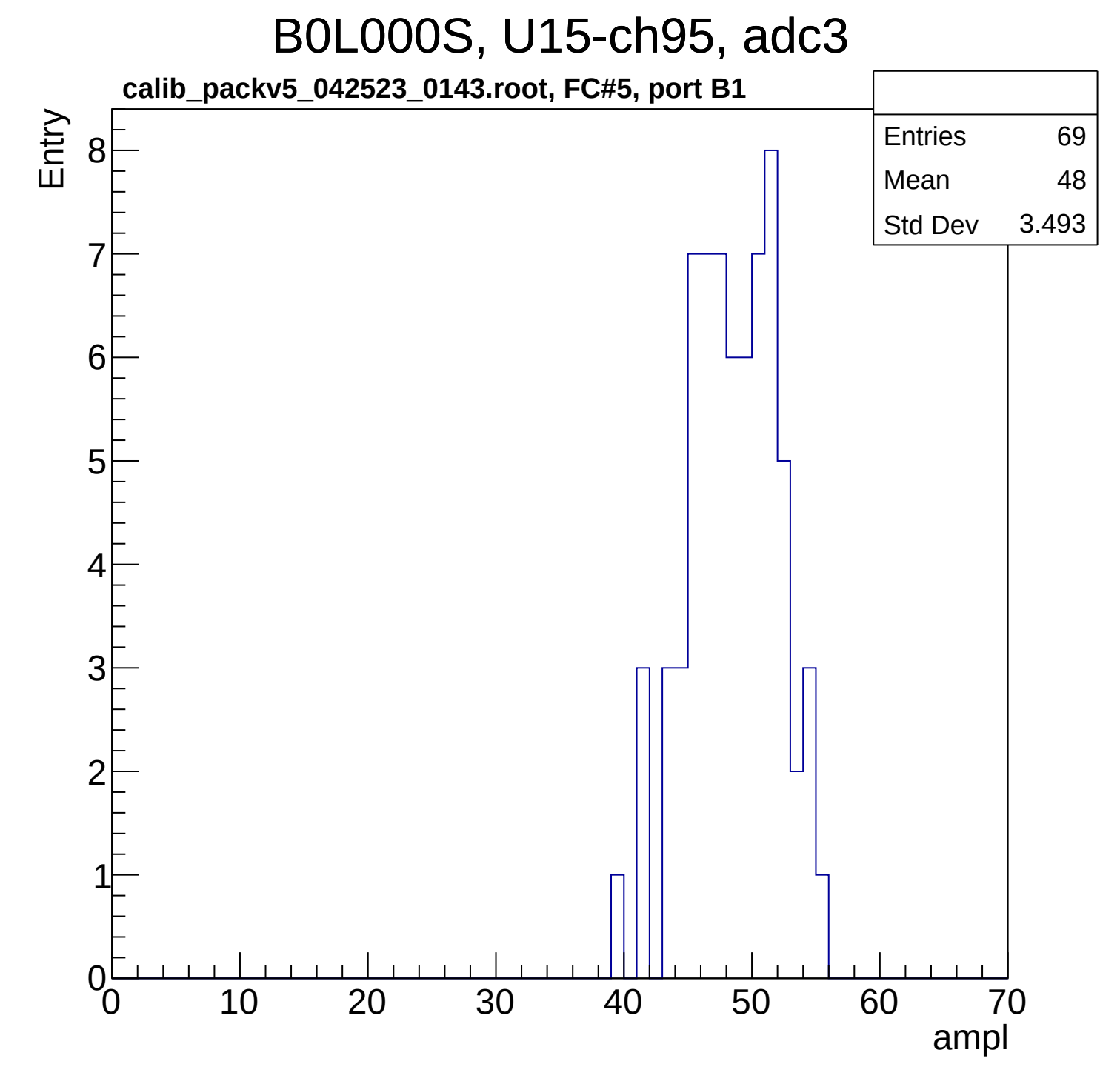
B0L000S, U15-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48
Std Dev 3.493
Entry
ampl
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4
5
6
7
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0
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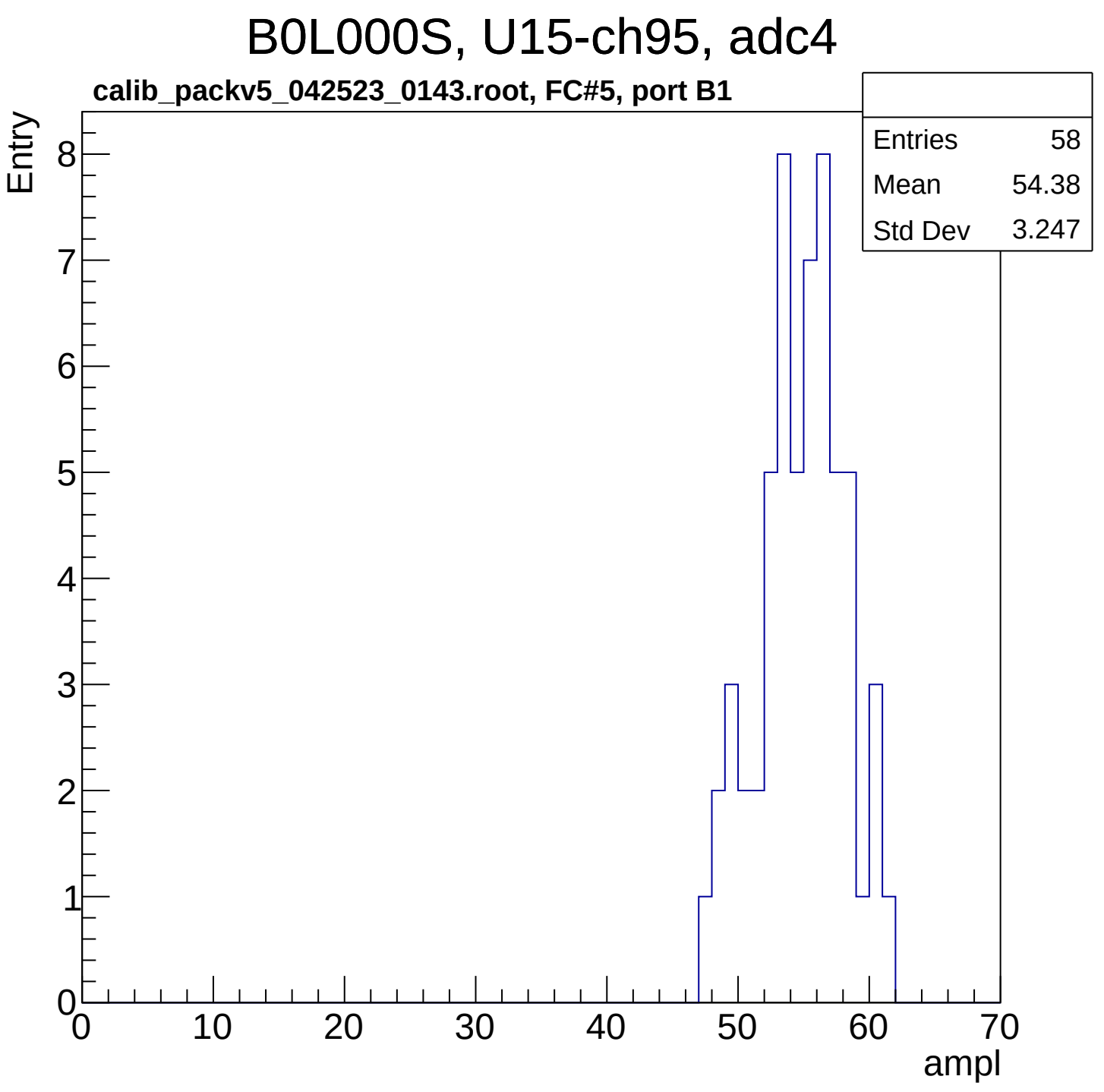
B0L000S, U15-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.38
Std Dev 3.247
Entry
ampl
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4
5
6
7
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0
10
20
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40
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60
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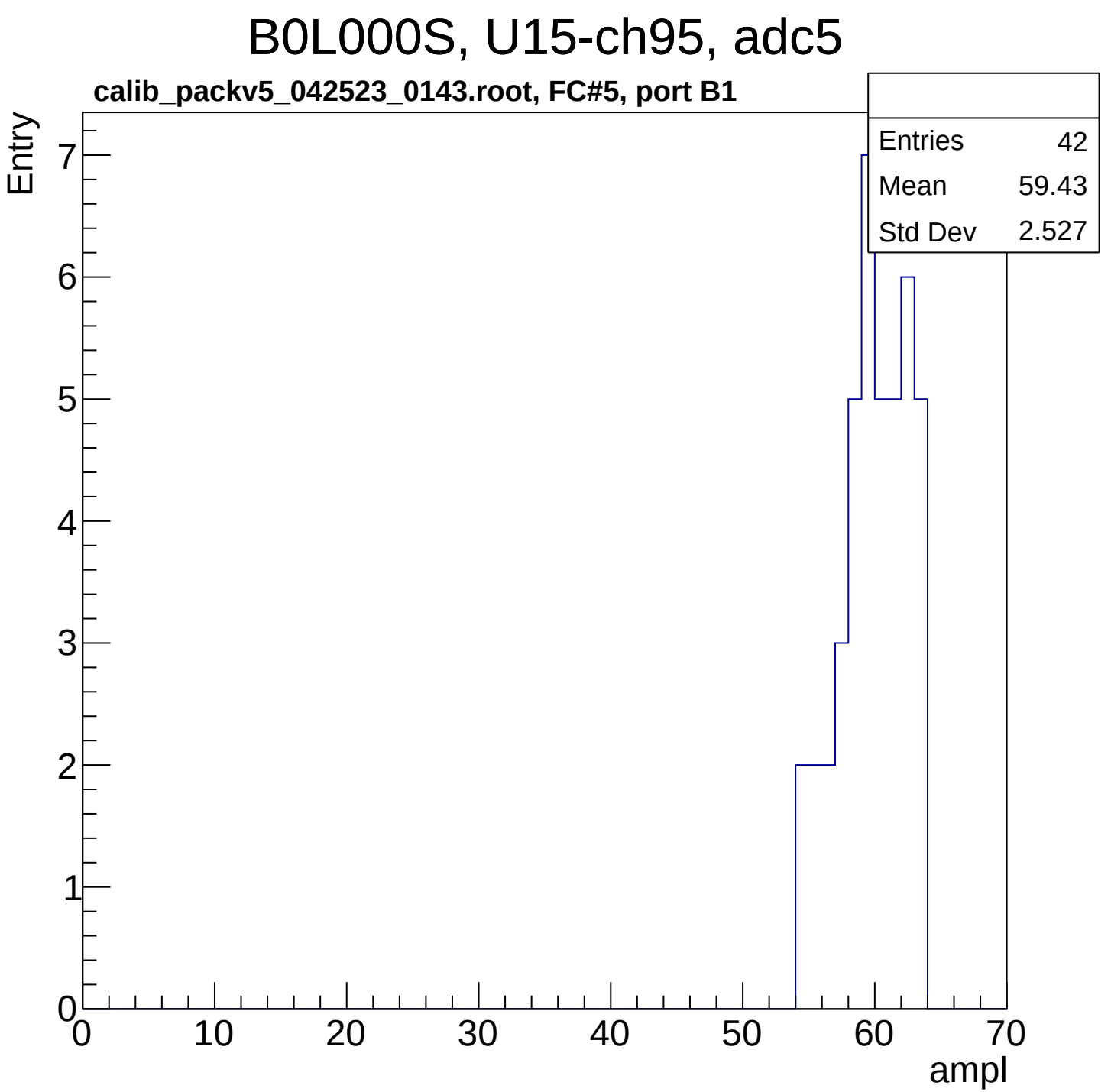
B0L000S, U15-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.43
Std Dev 2.527
Entry
ampl
0
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3
4
5
6
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0
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40
50
60
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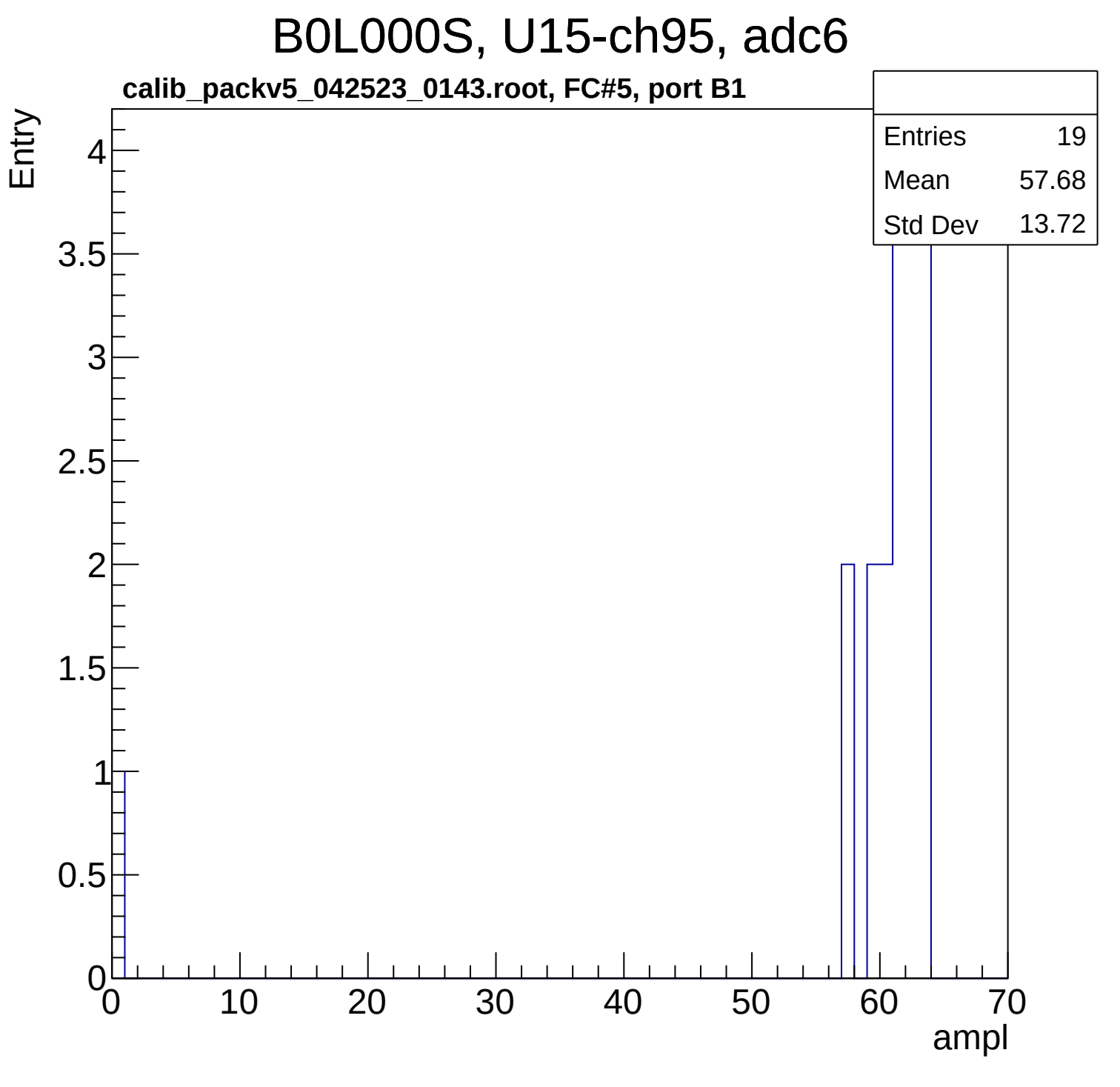
B0L000S, U15-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 57.68
Std Dev 13.72
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
50
60
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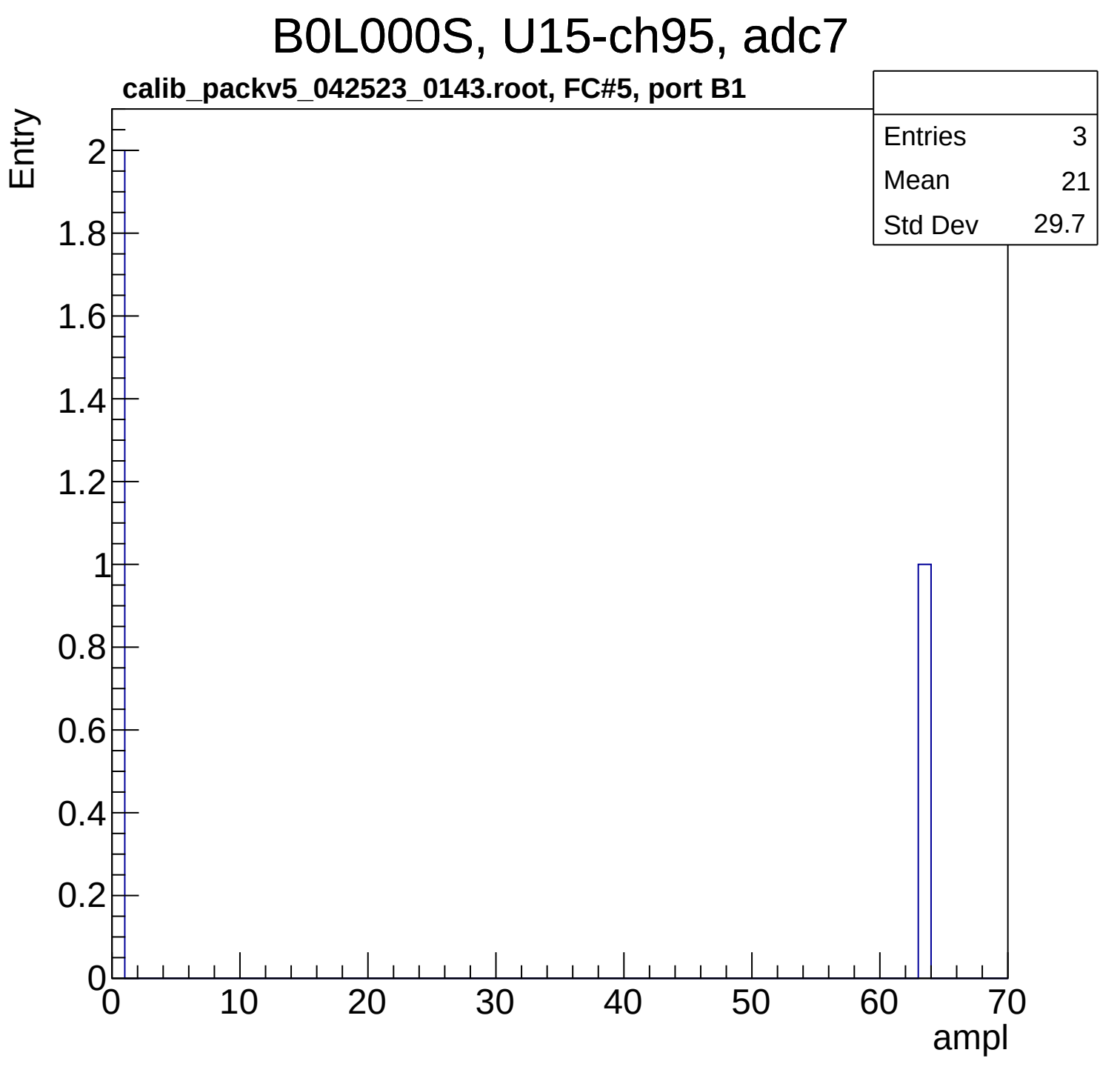
B0L000S, U15-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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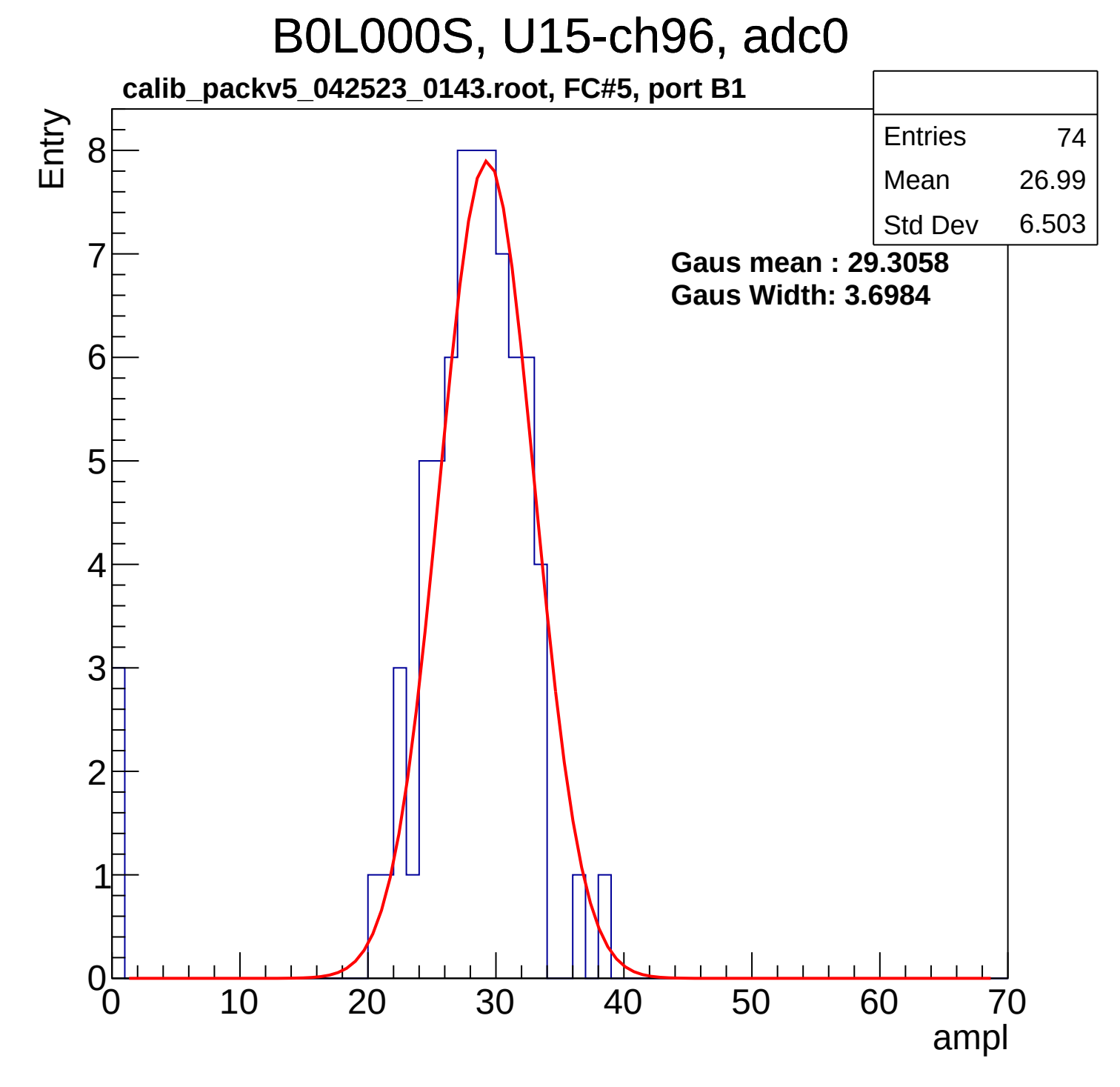
B0L000S, U15-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 26.99
Std Dev 6.503
Gaus mean : 29.3058
Gaus Width: 3.6984
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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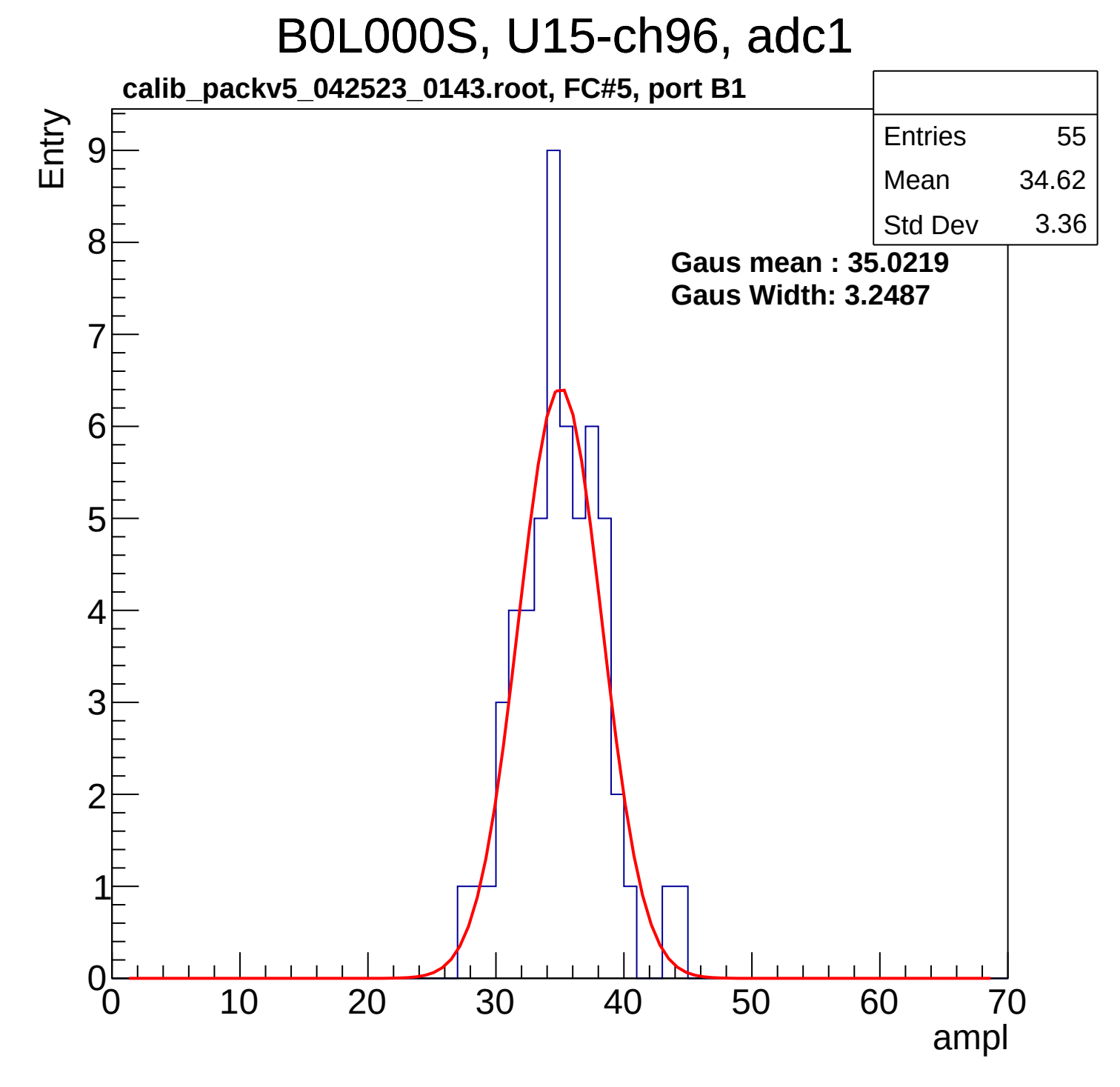
40
50
60
70


B0L000S, U15-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 34.62
Std Dev 3.36
Gaus mean : 35.0219
Gaus Width: 3.2487
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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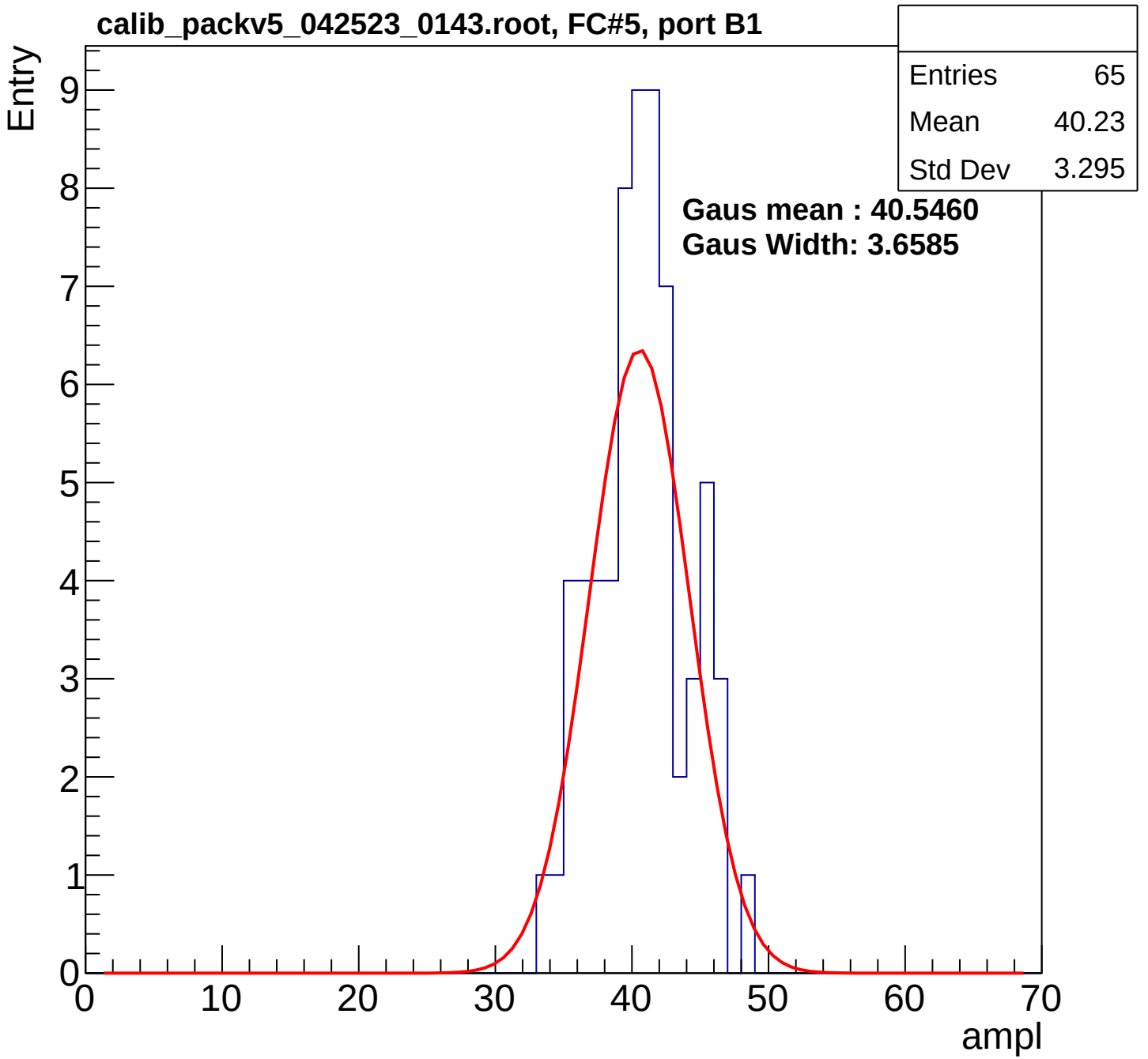
B0L000S, U15-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 40.23
Std Dev 3.295
Gaus mean : 40.5460
Gaus Width: 3.6585
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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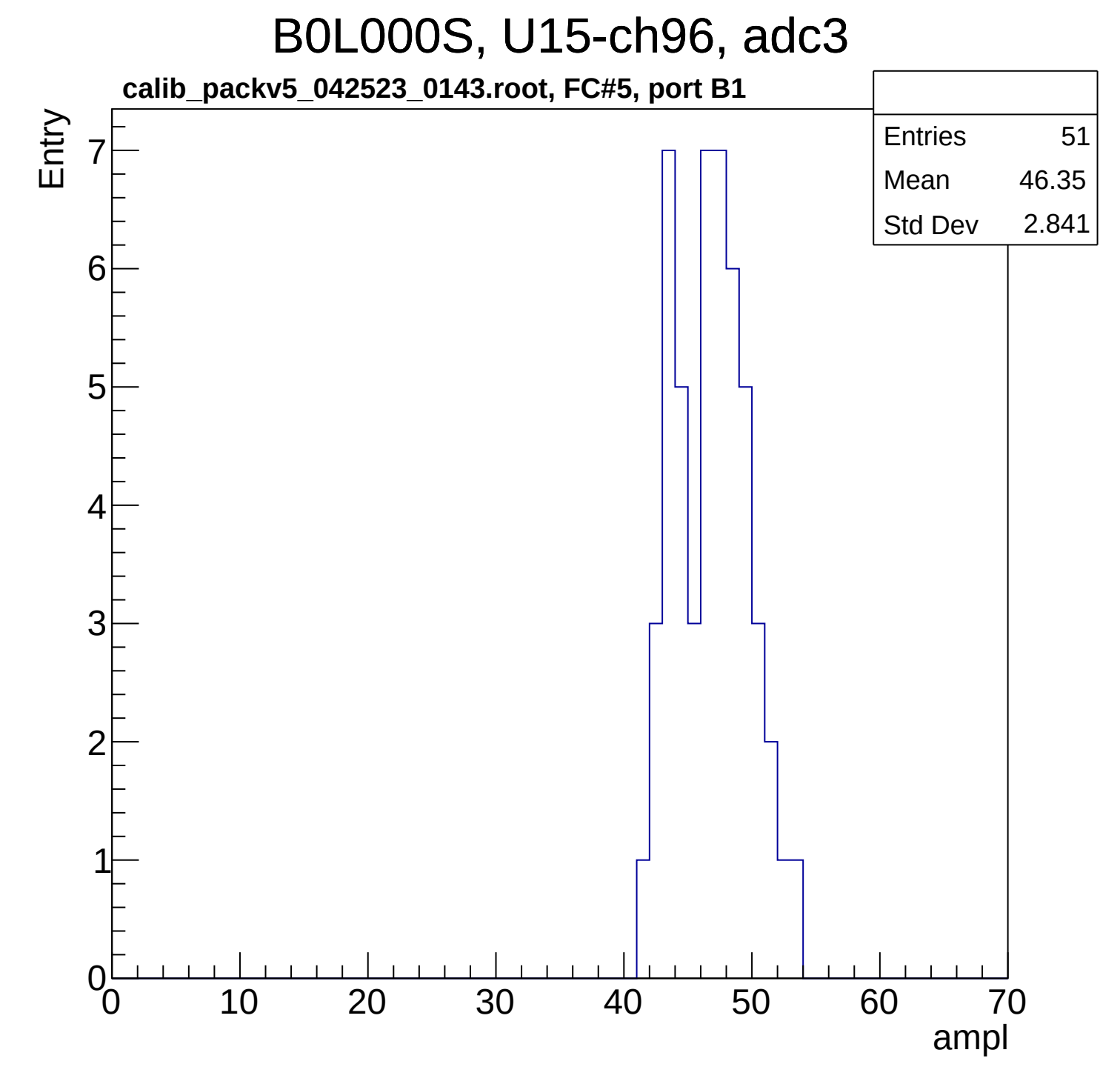
B0L000S, U15-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 46.35
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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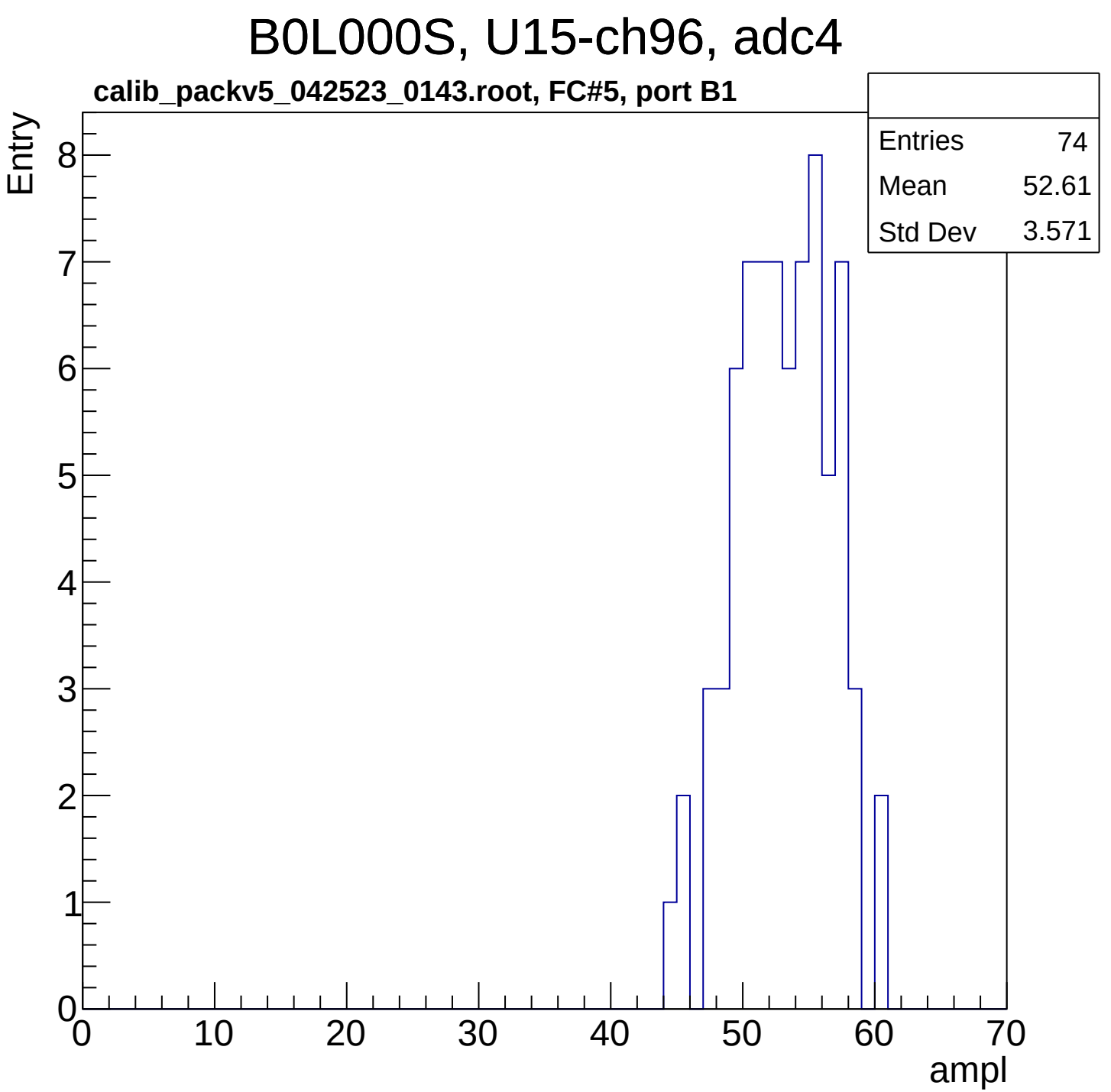
B0L000S, U15-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 52.61
Std Dev 3.571
Entry
ampl
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1
2
3
4
5
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7
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0
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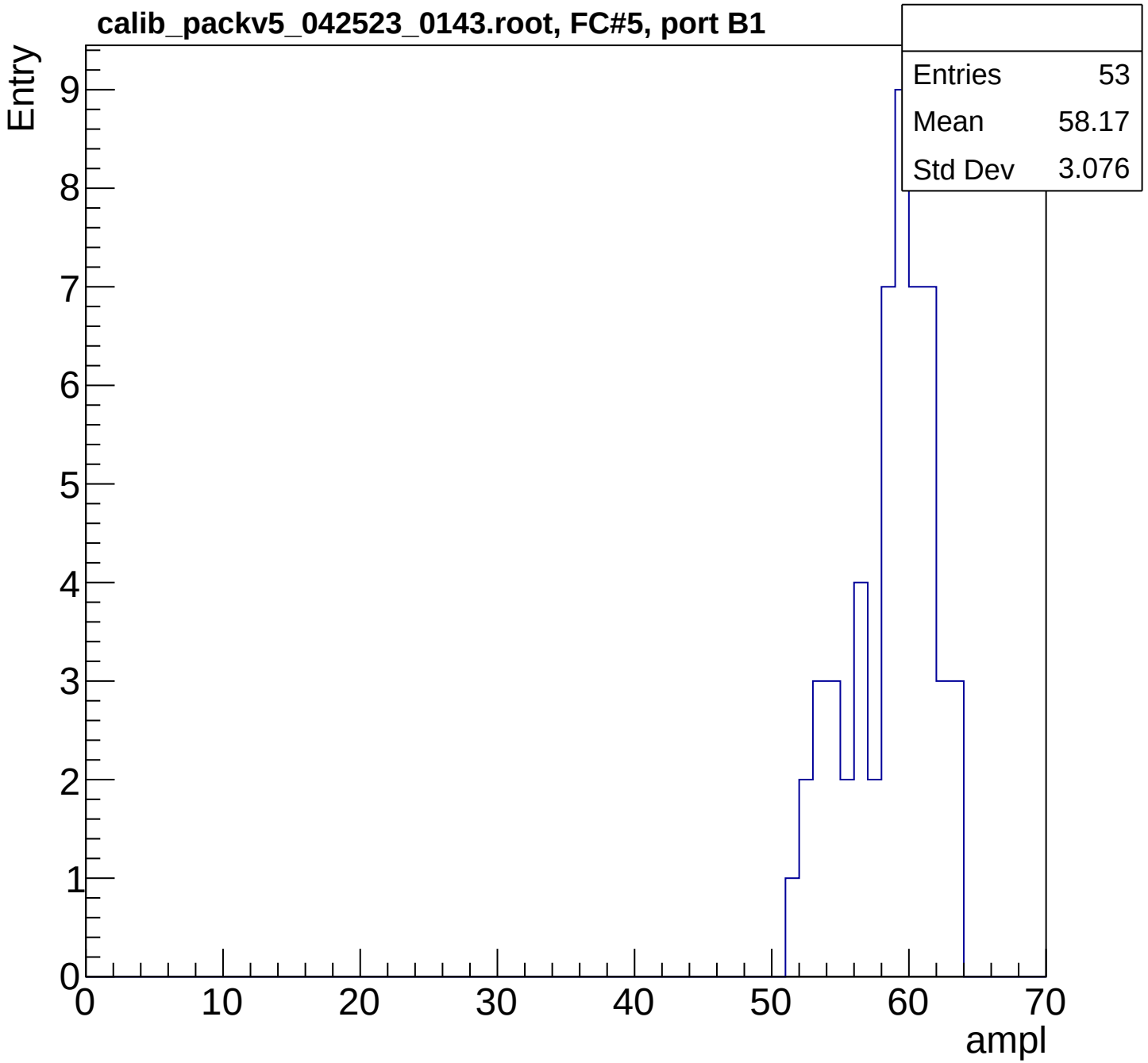
B0L000S, U15-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.17
Std Dev 3.076
Entry
ampl
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3
4
5
6
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8
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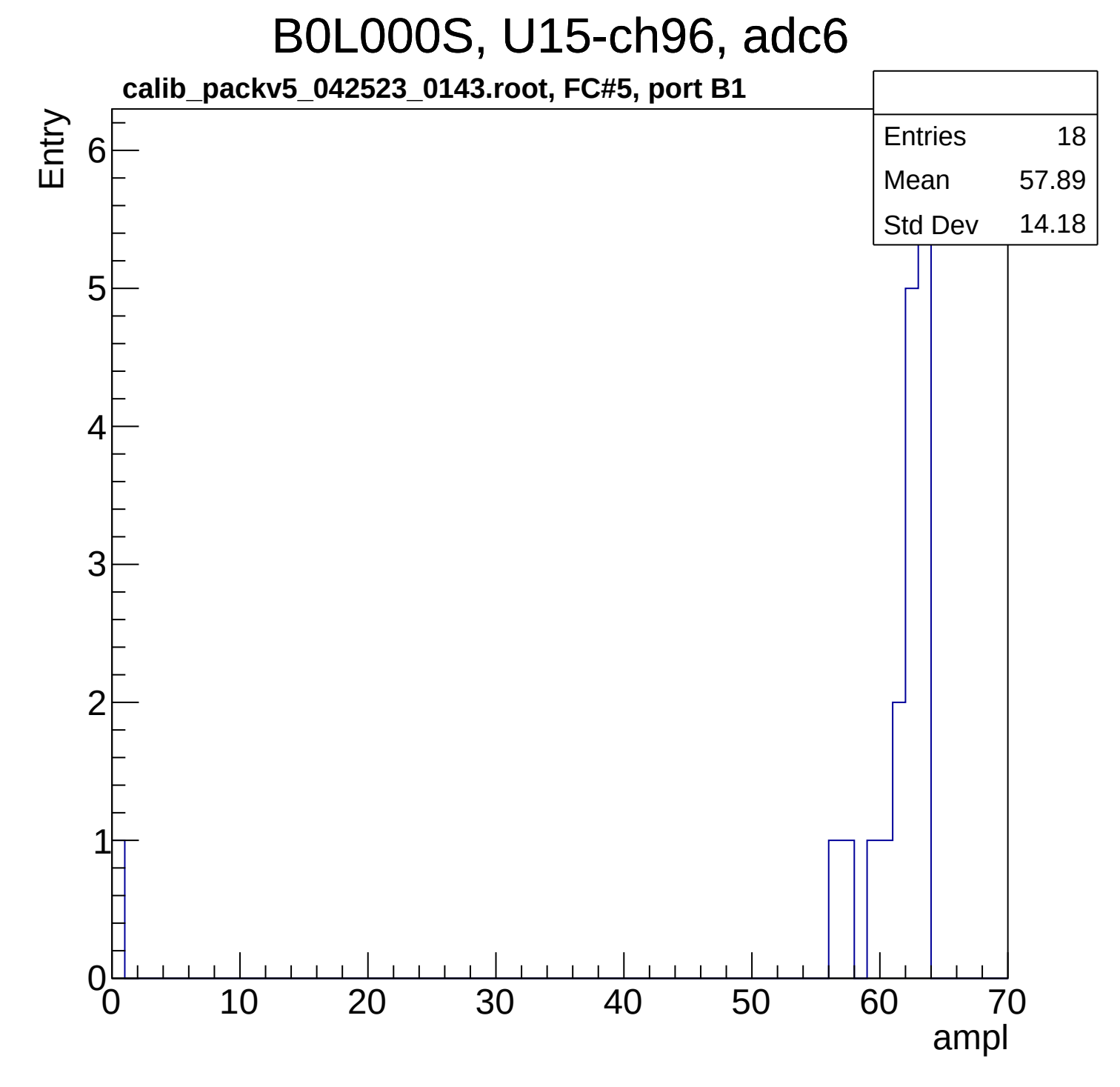
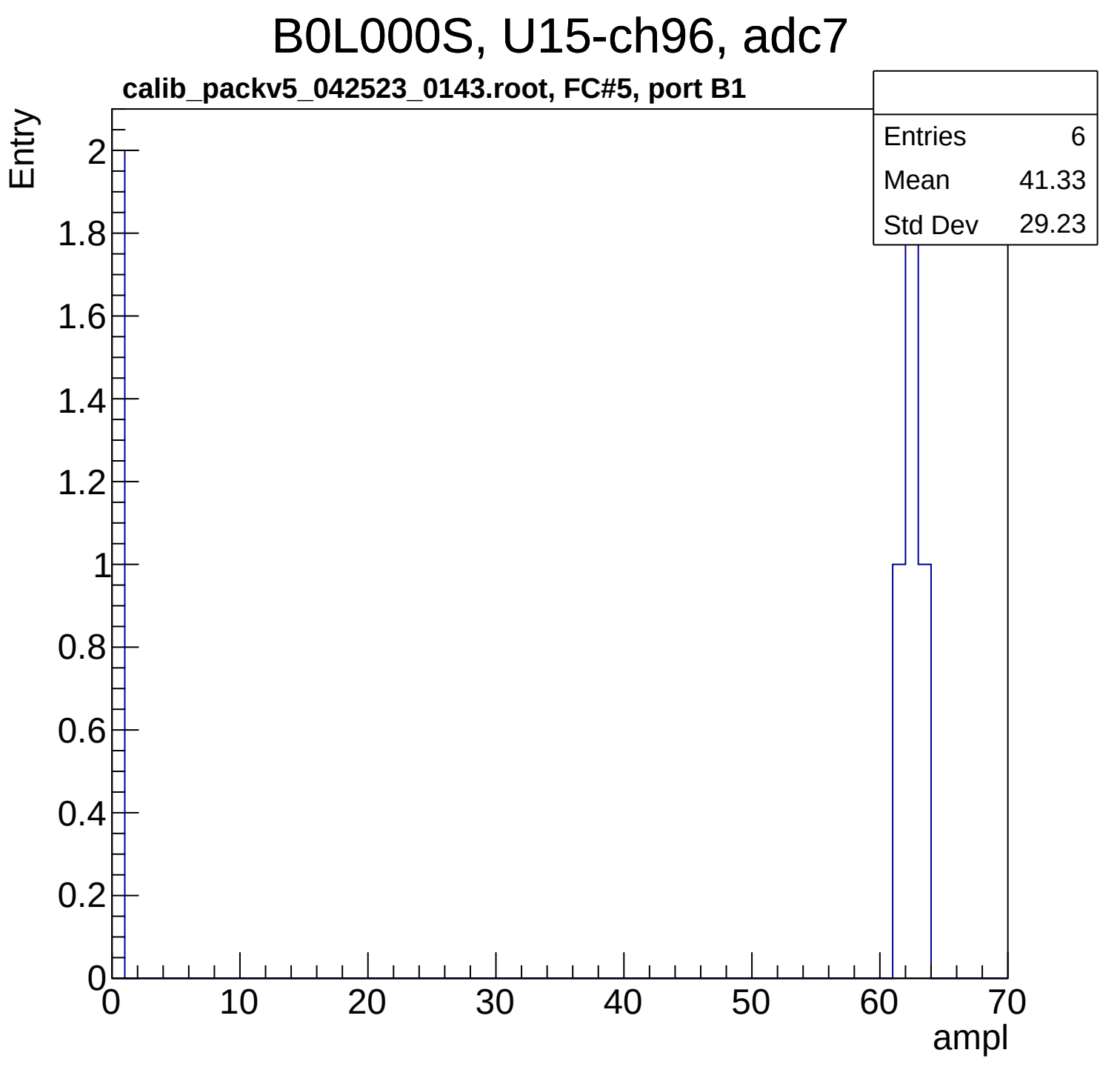

B0L000S, U15-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 57.89
Std Dev 14.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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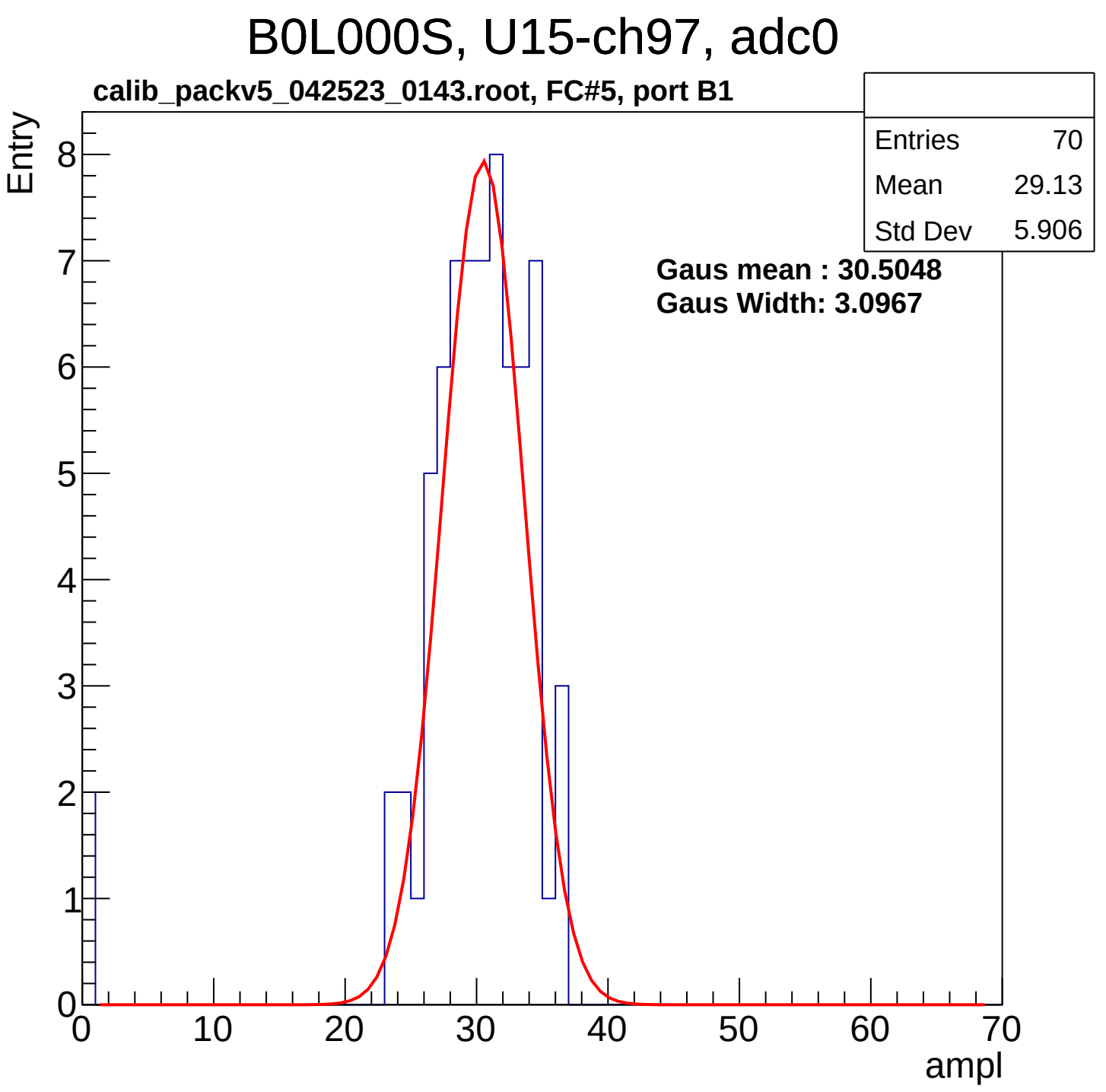
B0L000S, U15-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.13
Std Dev 5.906
Gaus mean : 30.5048
Gaus Width: 3.0967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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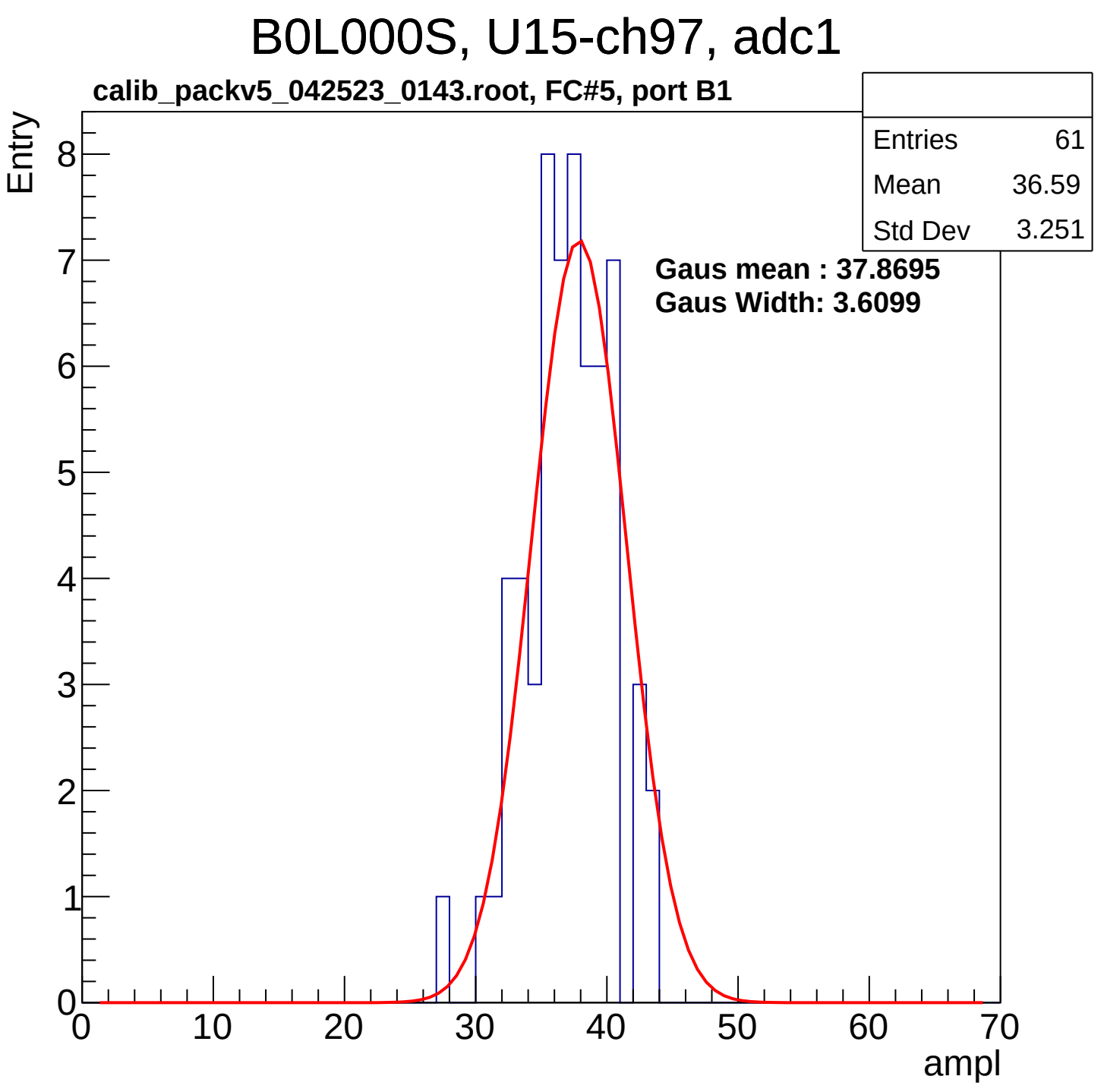
B0L000S, U15-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.59
Std Dev 3.251
Gaus mean : 37.8695
Gaus Width: 3.6099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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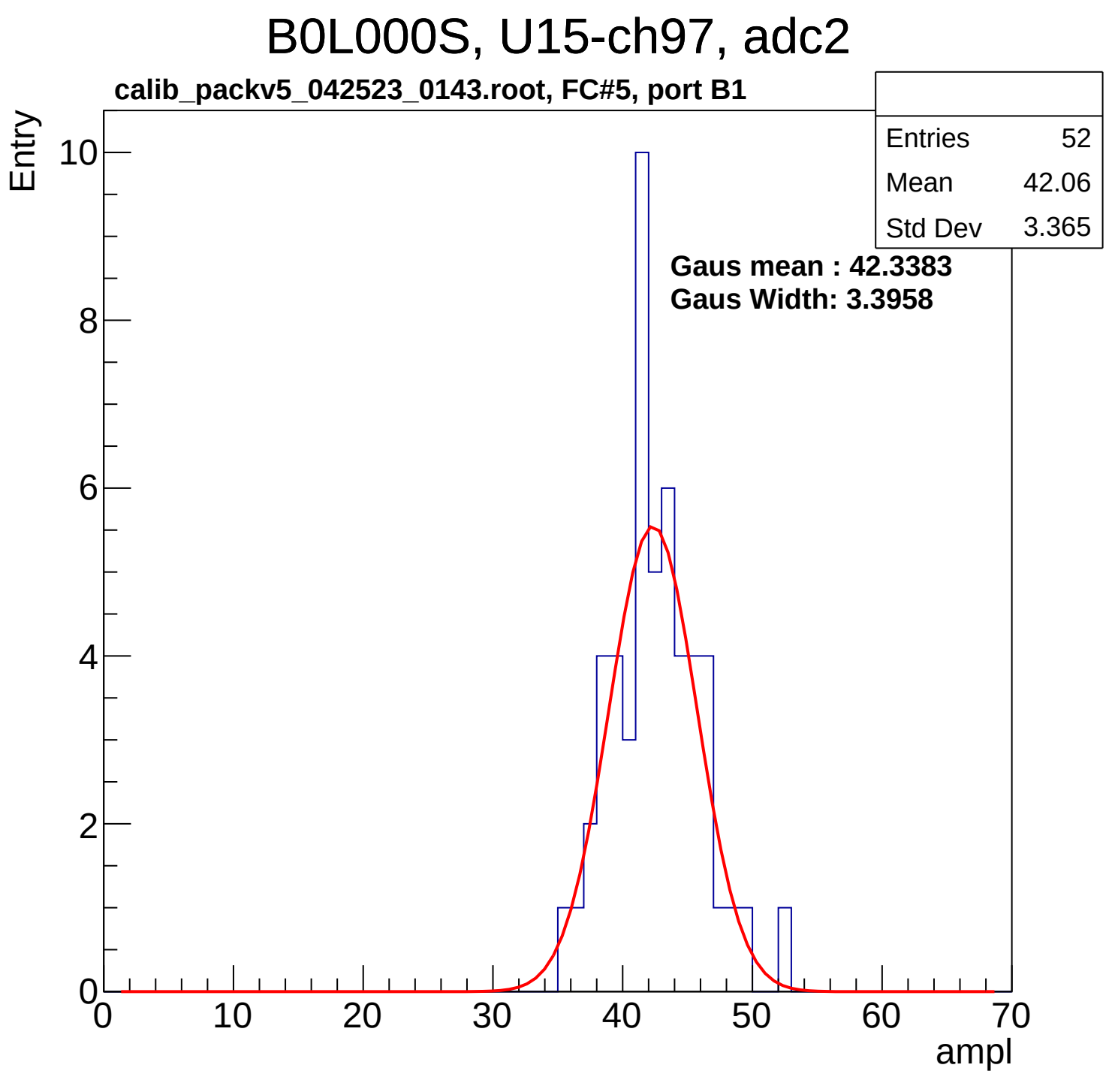
B0L000S, U15-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.06
Std Dev 3.365
Gaus mean : 42.3383
Gaus Width: 3.3958
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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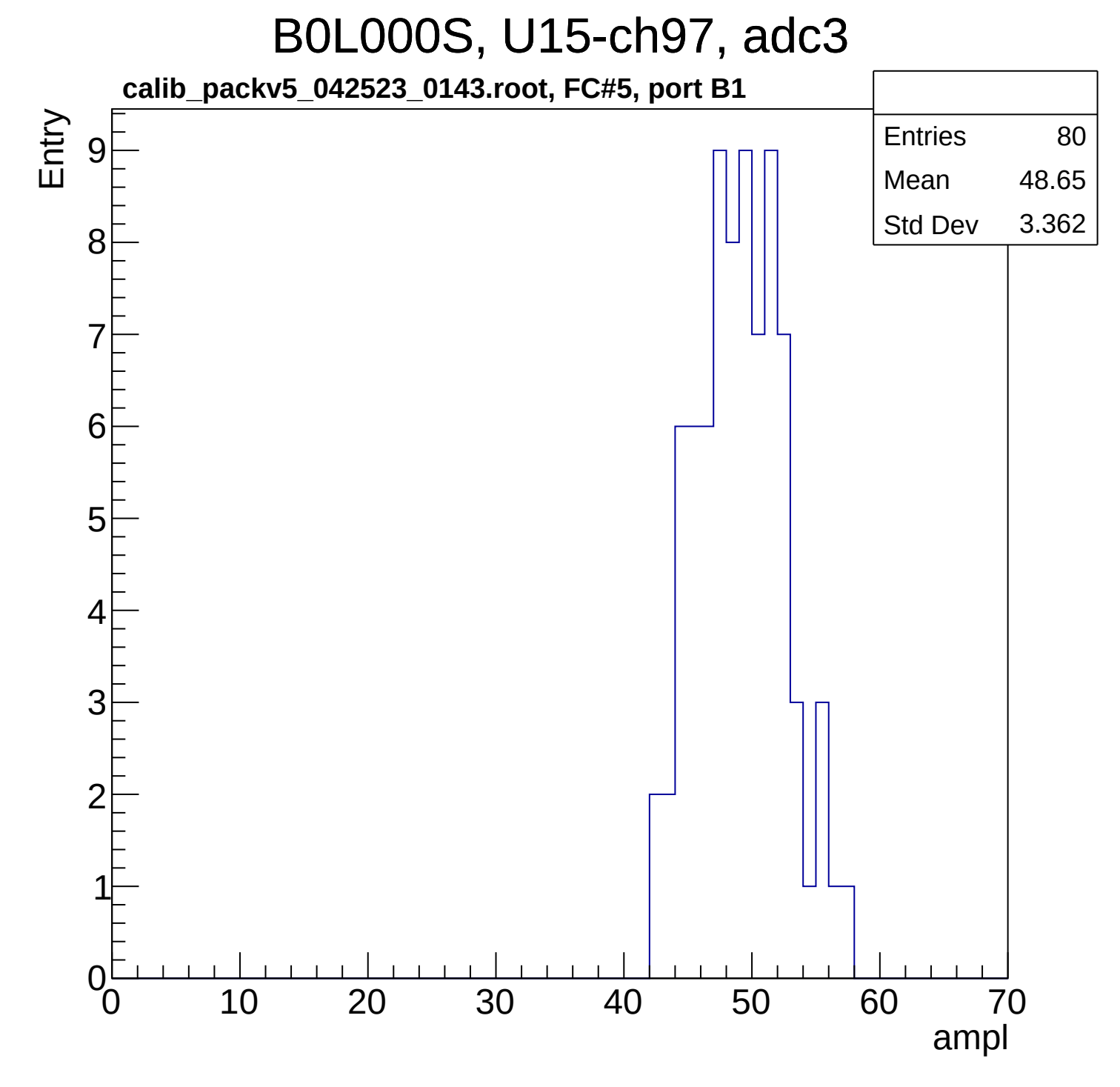
B0L000S, U15-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 48.65
Std Dev 3.362
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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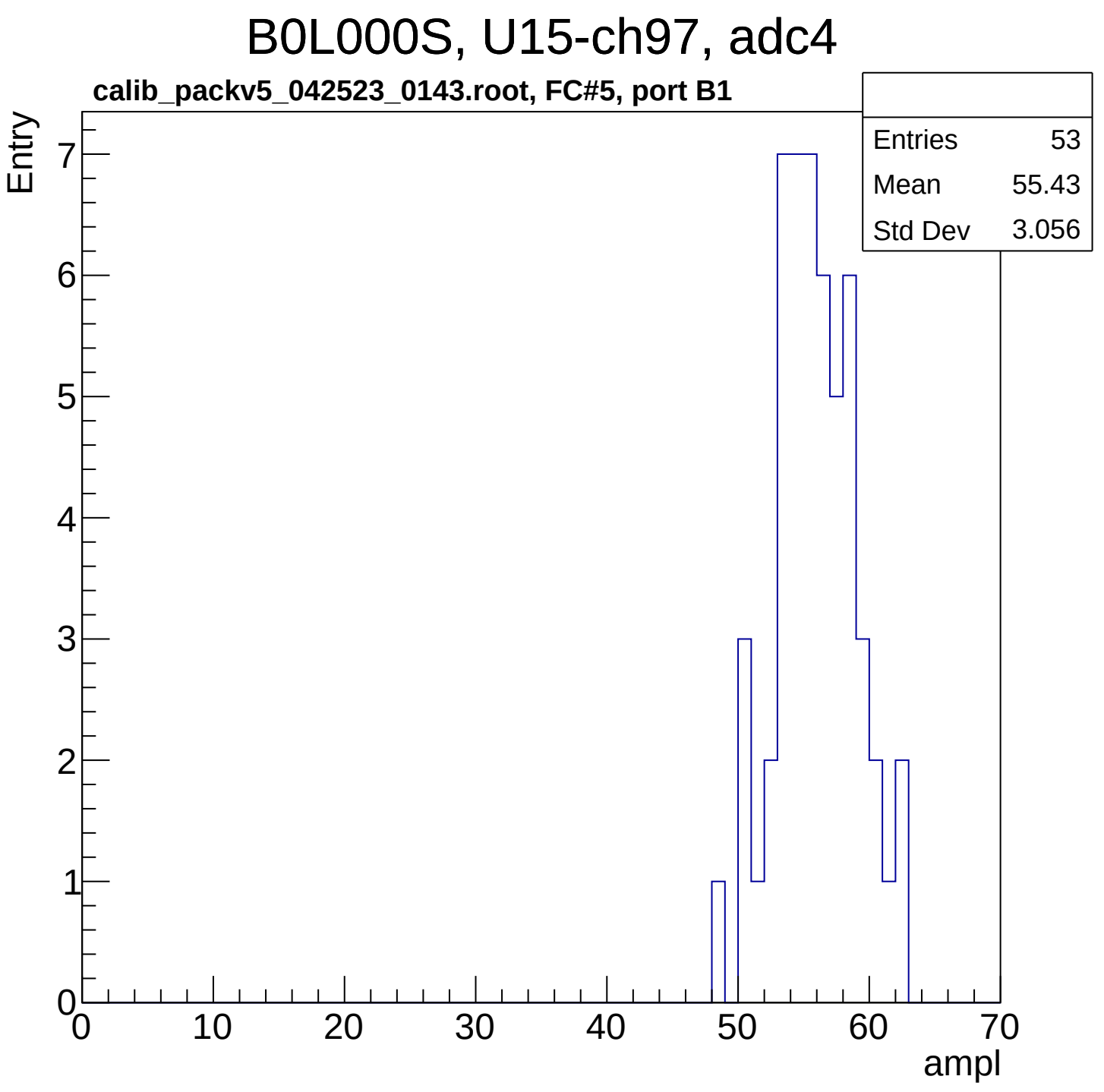
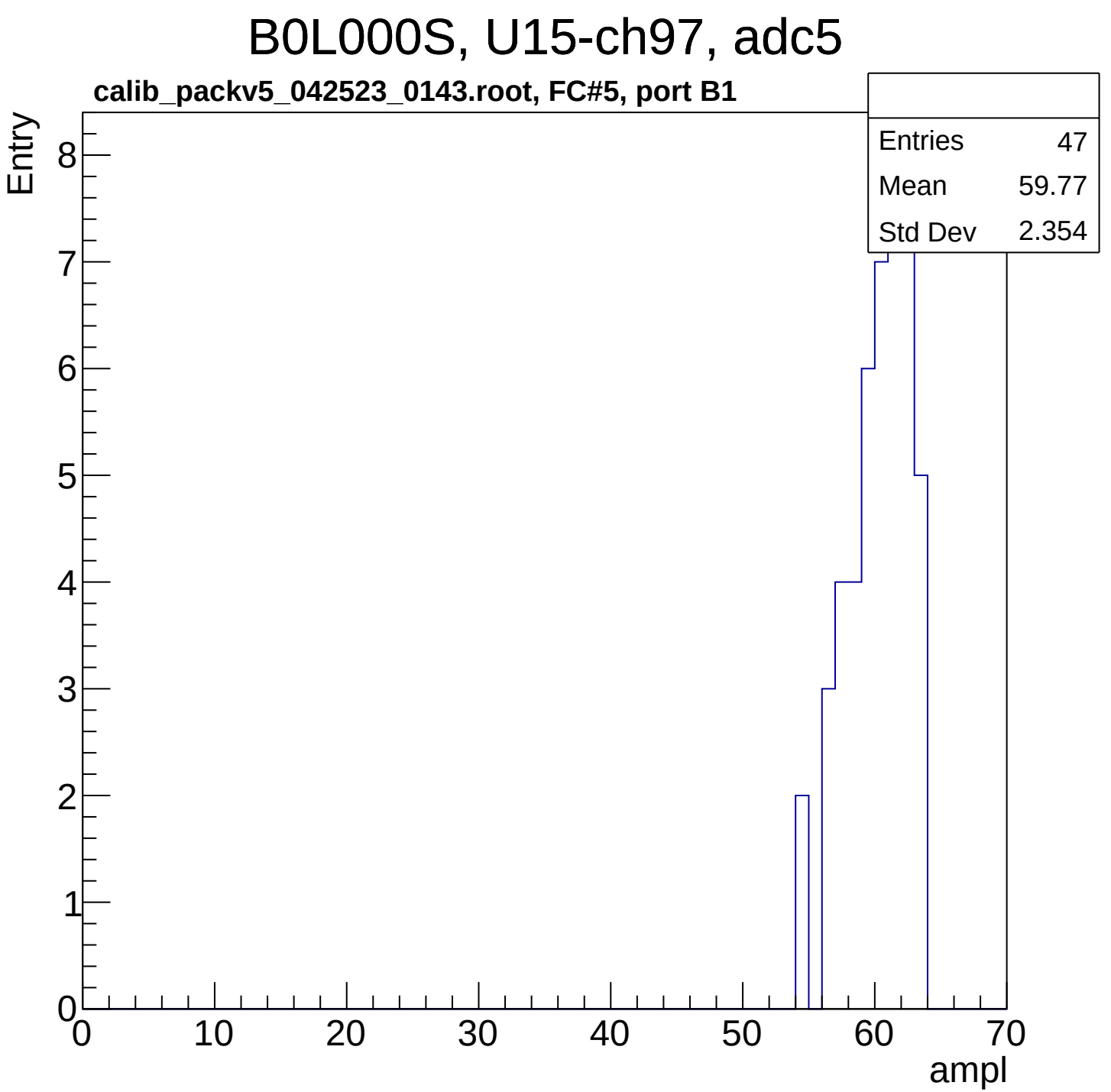

B0L000S, U15-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.43
Std Dev 3.056
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.77
Std Dev 2.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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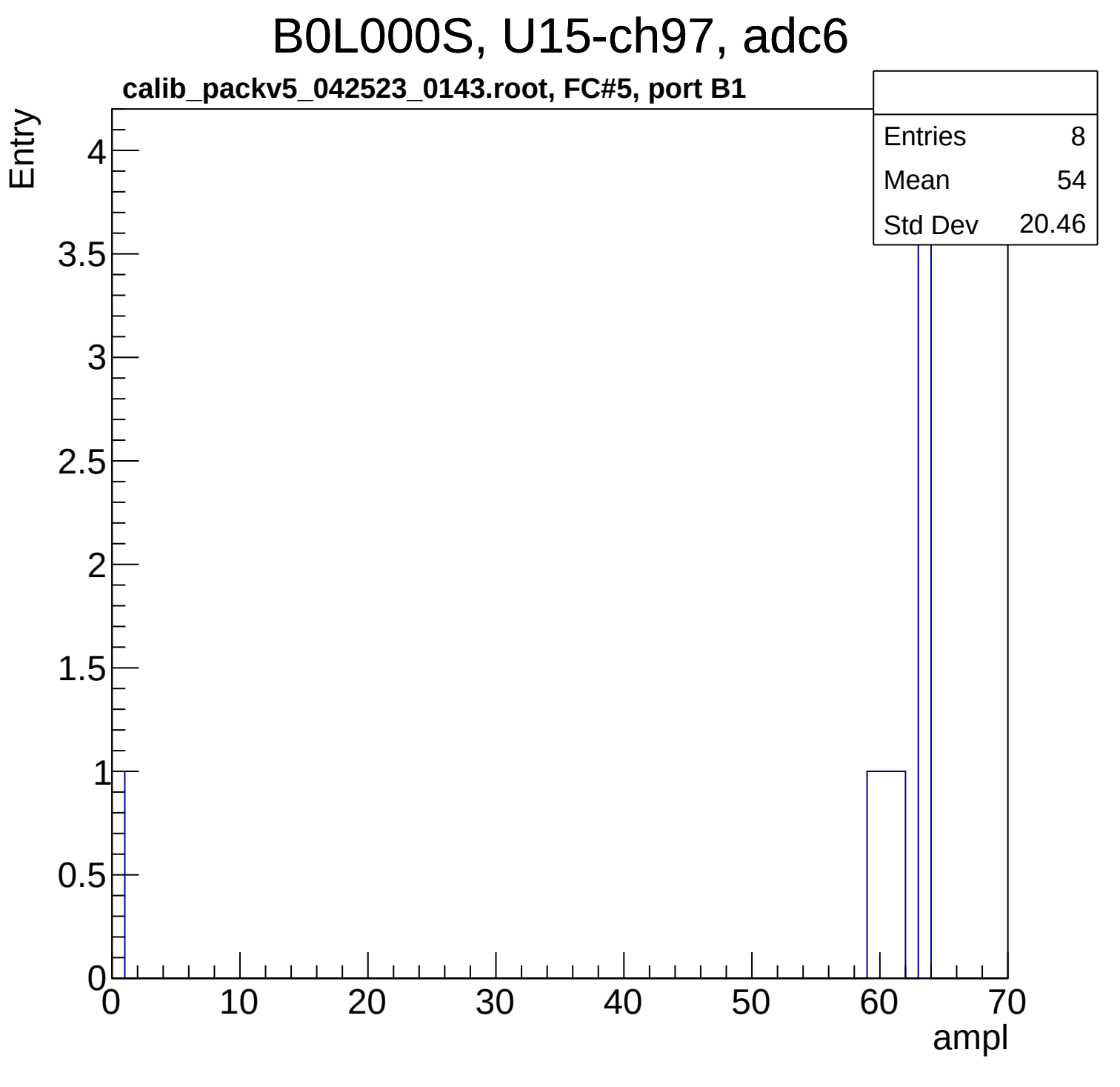
B0L000S, U15-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54
Std Dev 20.46
Entry
ampl
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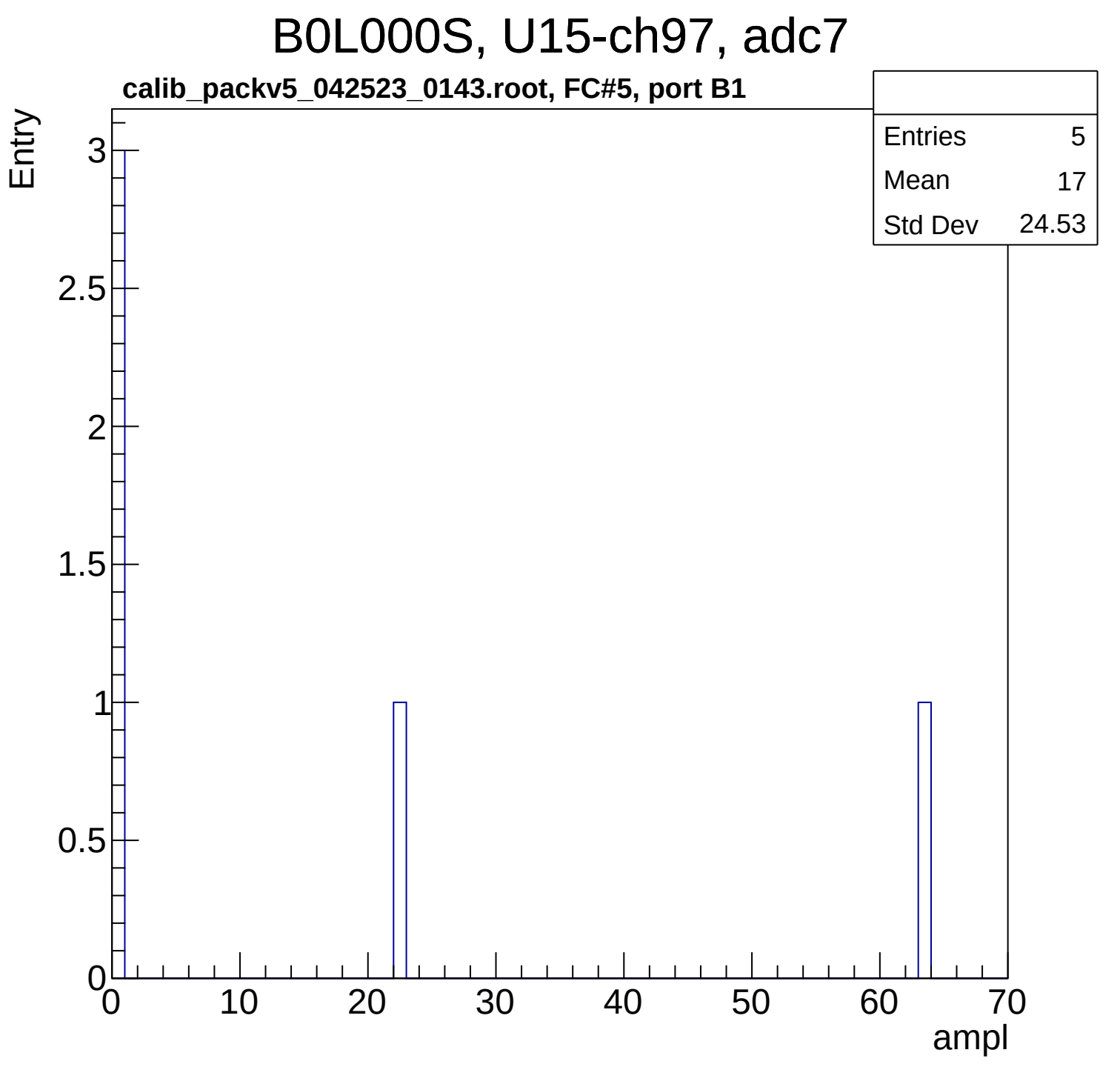
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 17
Std Dev 24.53
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
30
40
50
60
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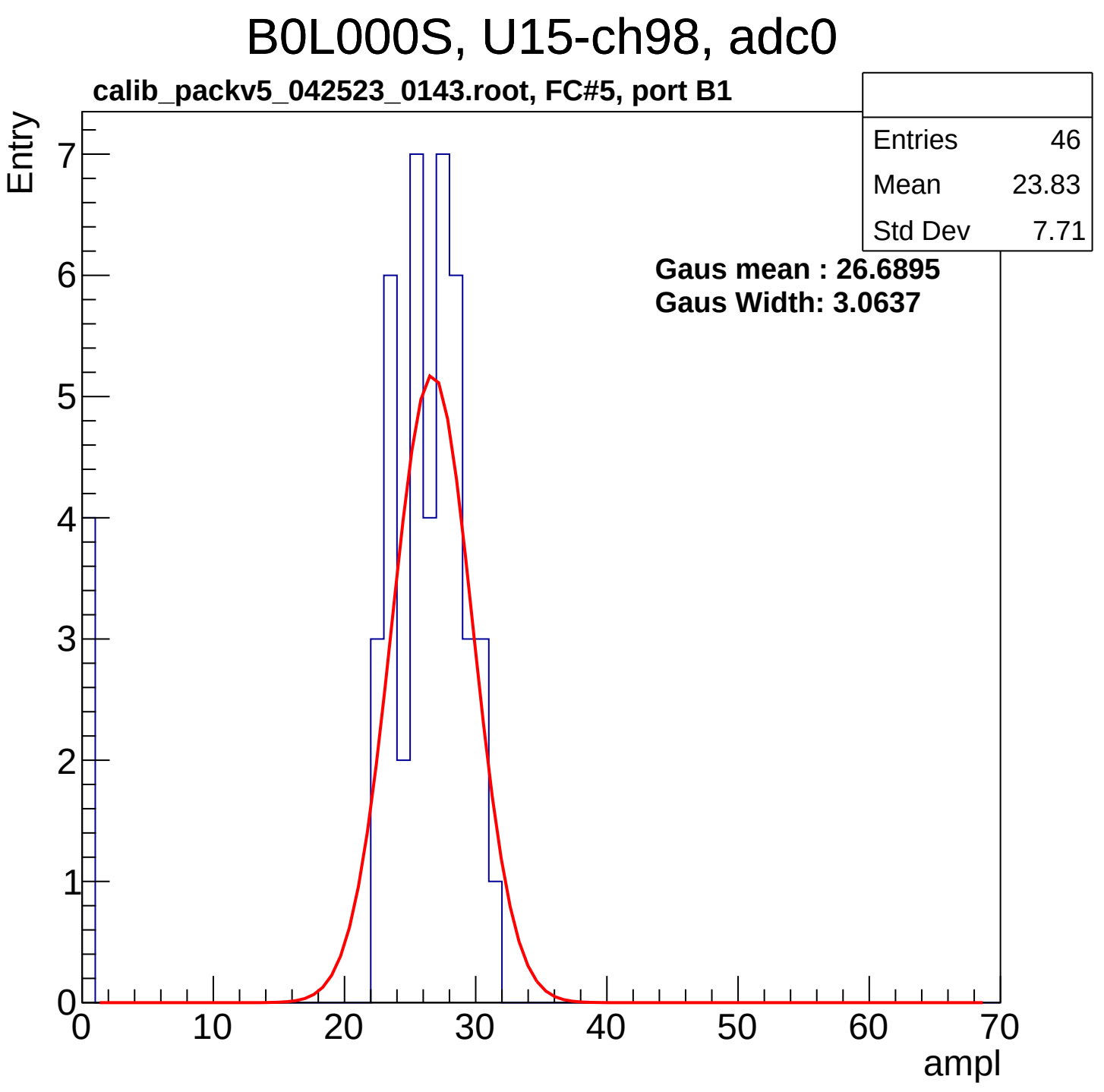
B0L000S, U15-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 23.83
Std Dev 7.71
Gaus mean : 26.6895
Gaus Width: 3.0637
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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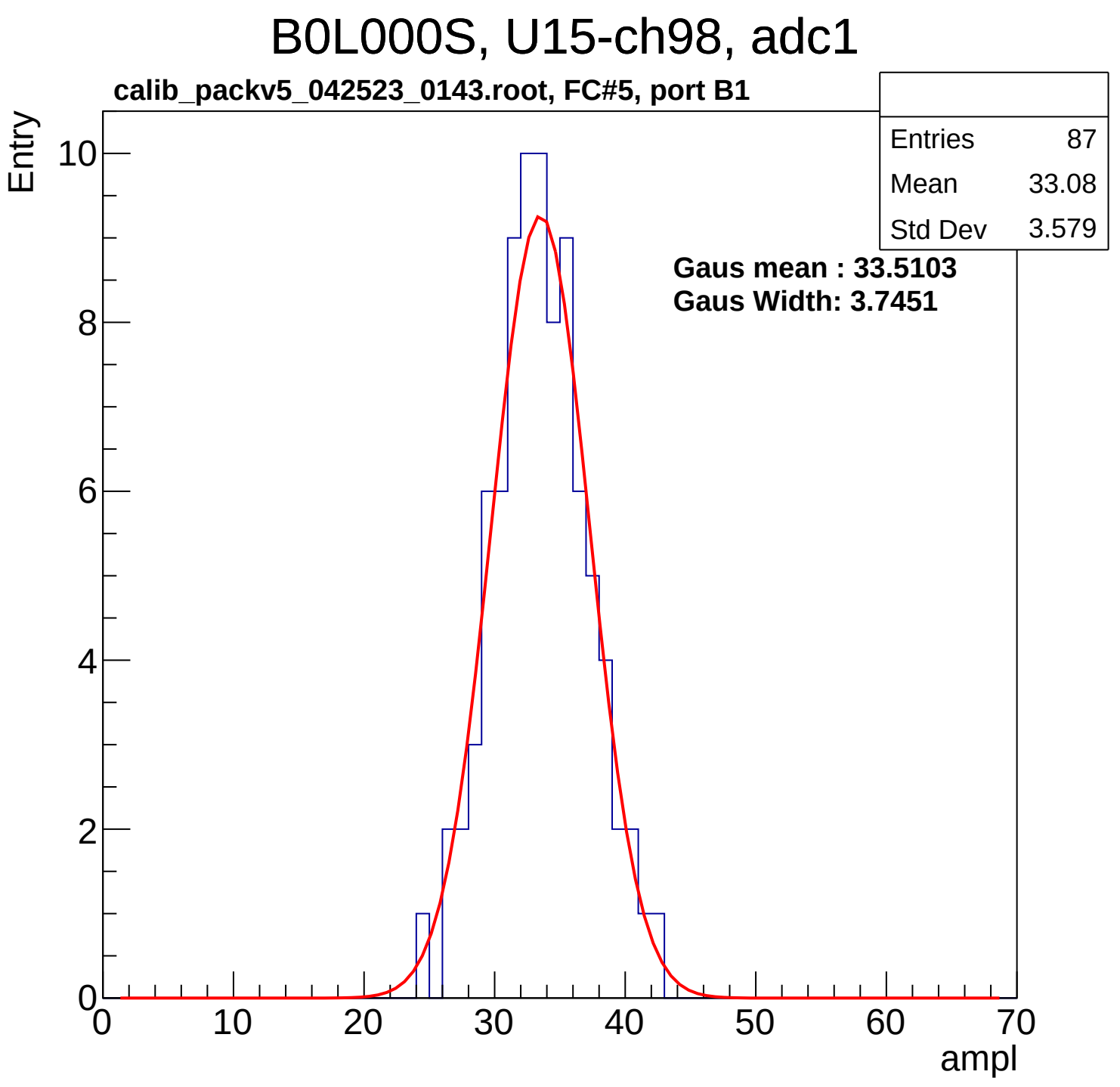
B0L000S, U15-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 33.08
Std Dev 3.579
Gaus mean : 33.5103
Gaus Width: 3.7451
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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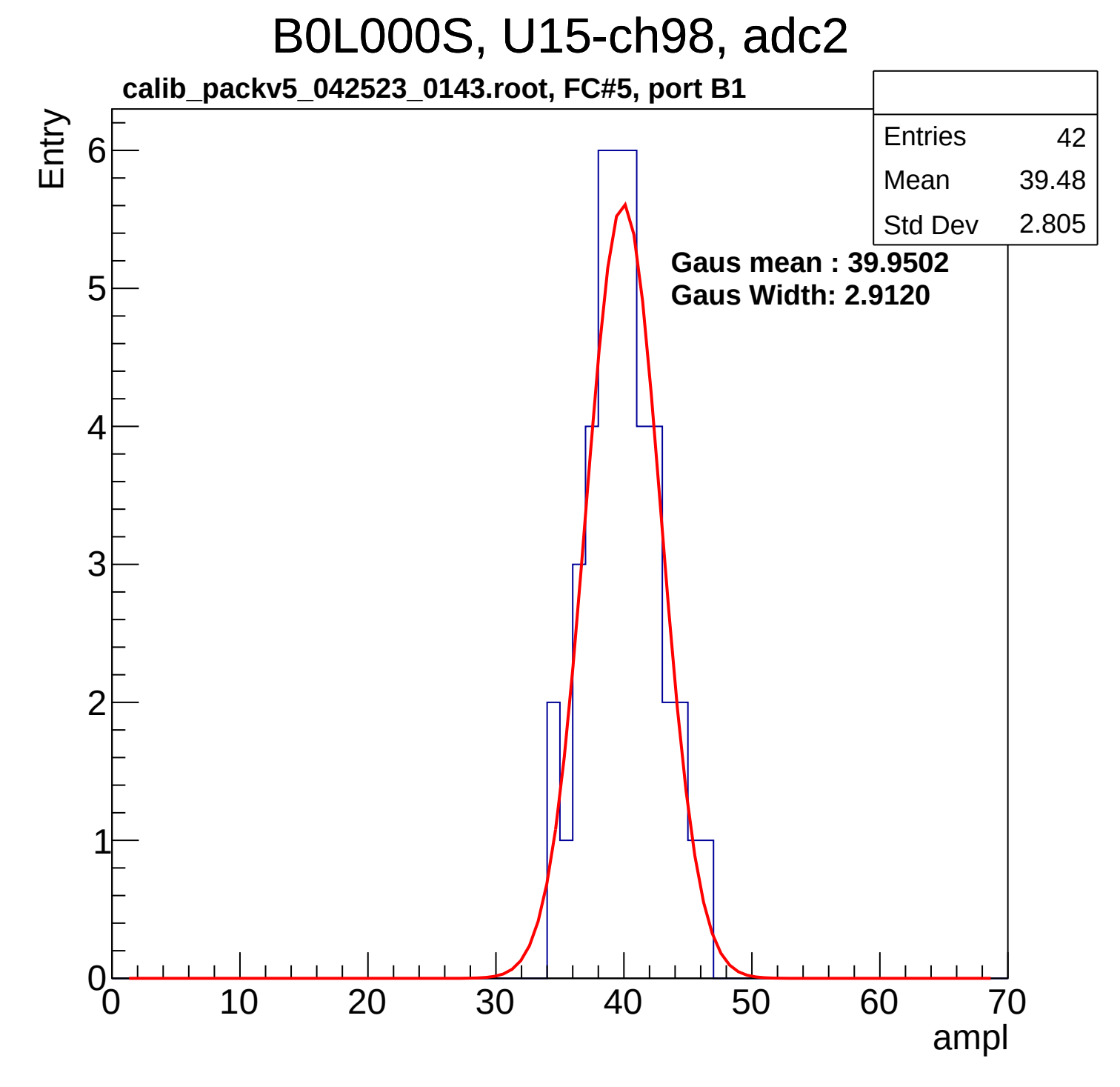
B0L000S, U15-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 39.48
Std Dev 2.805
Gaus mean : 39.9502
Gaus Width: 2.9120
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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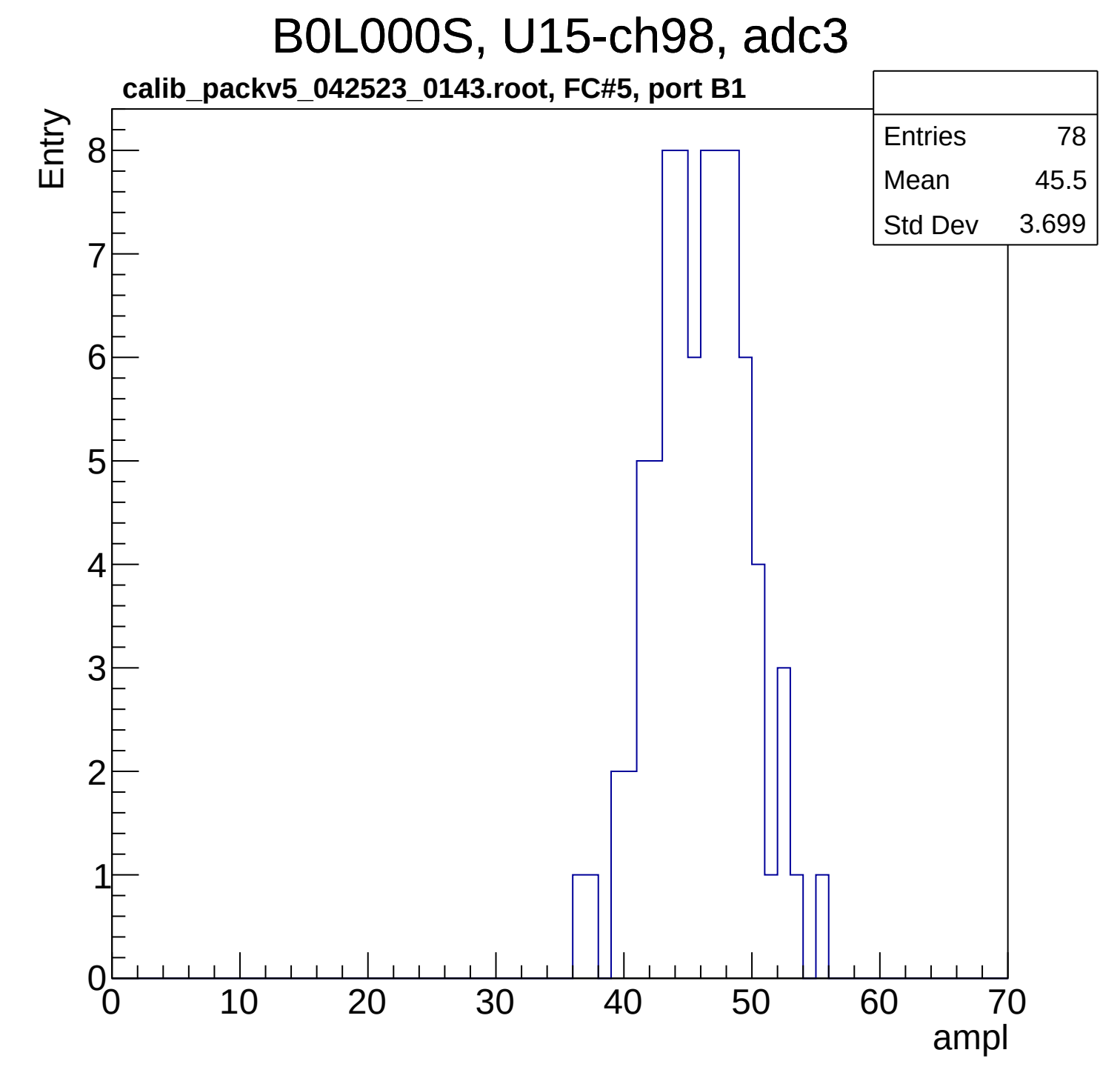
B0L000S, U15-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 45.5
Std Dev 3.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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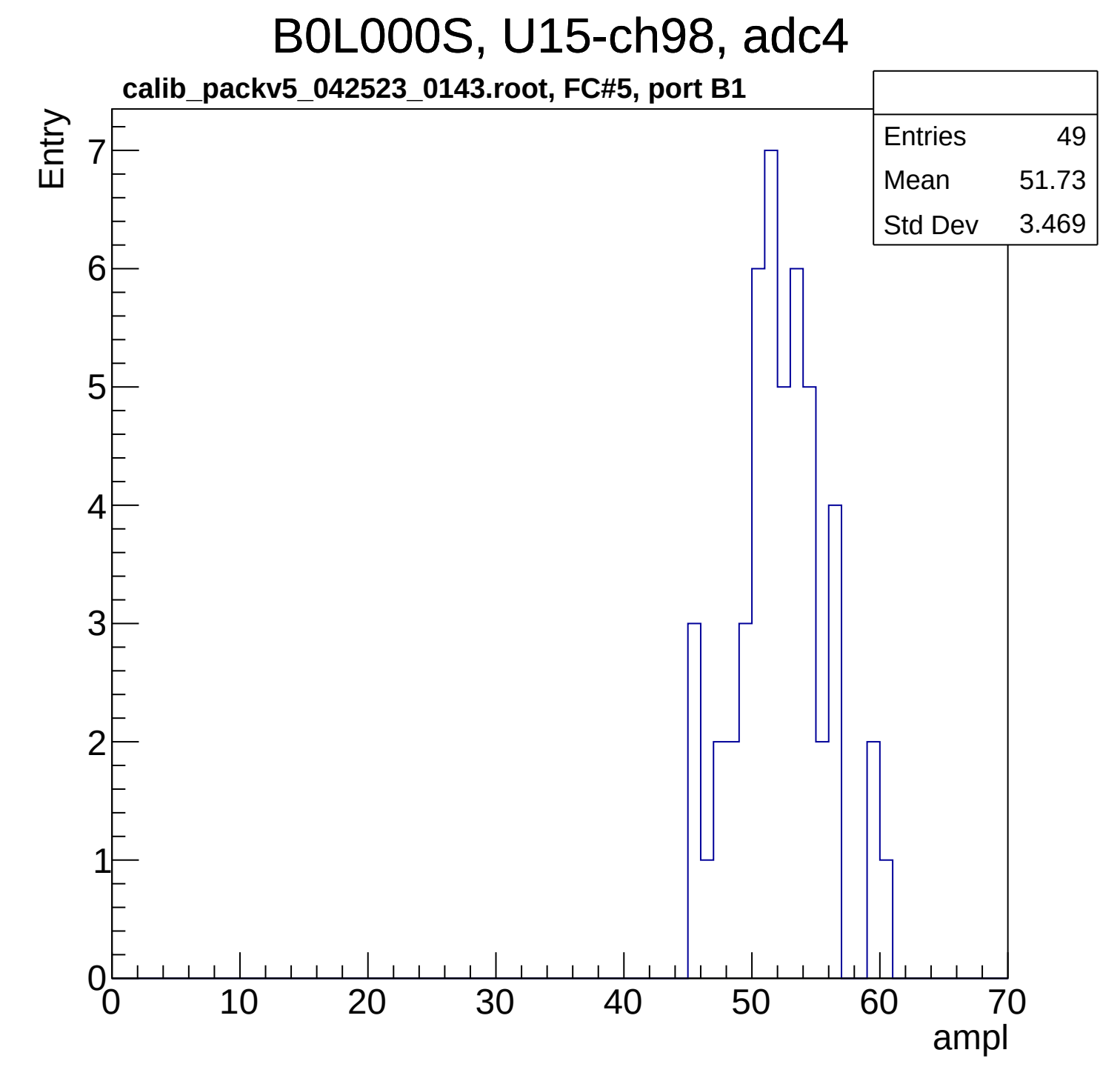
B0L000S, U15-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 51.73
Std Dev 3.469
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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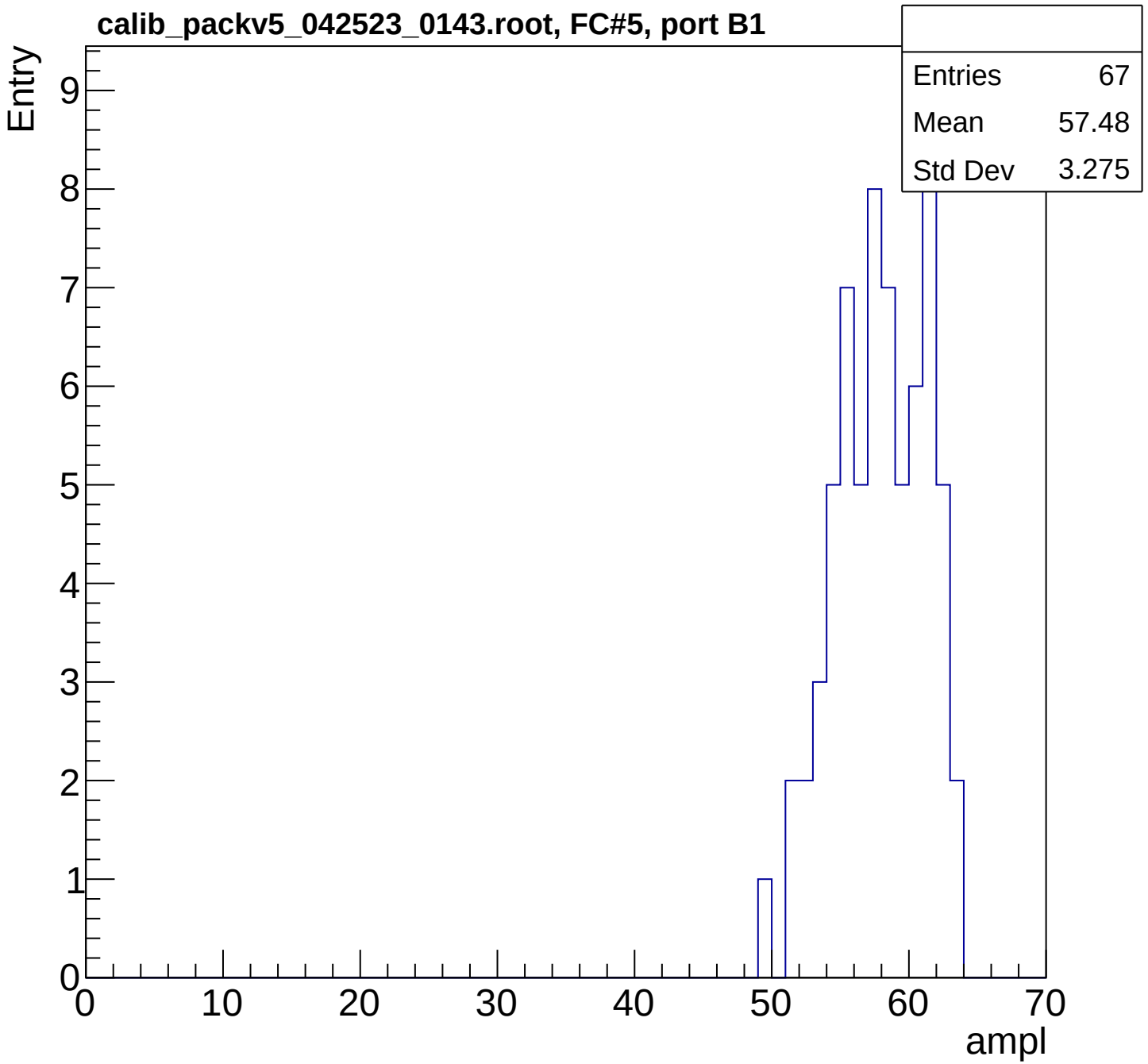
B0L000S, U15-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 57.48
Std Dev 3.275
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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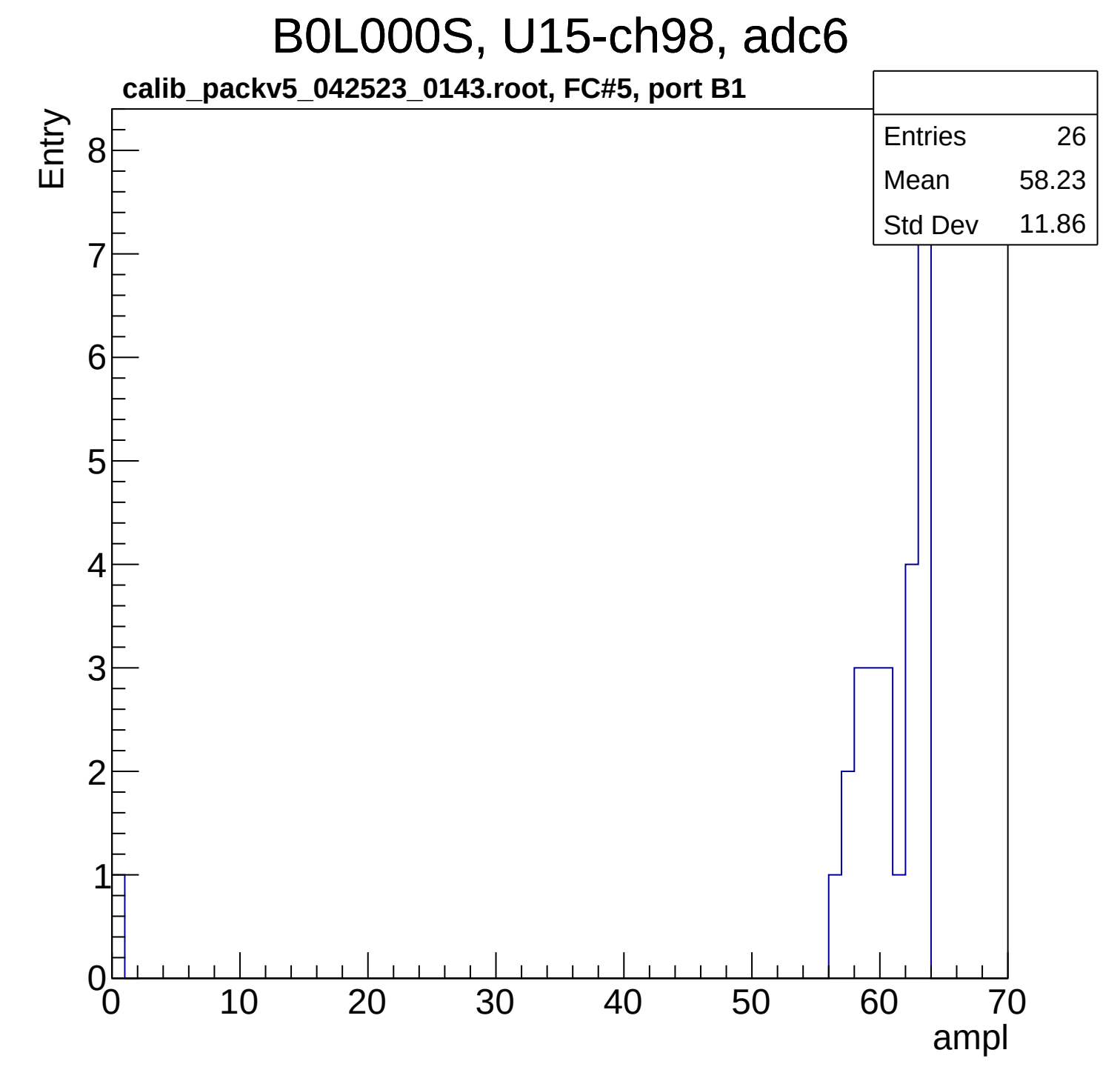
B0L000S, U15-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 58.23
Std Dev 11.86
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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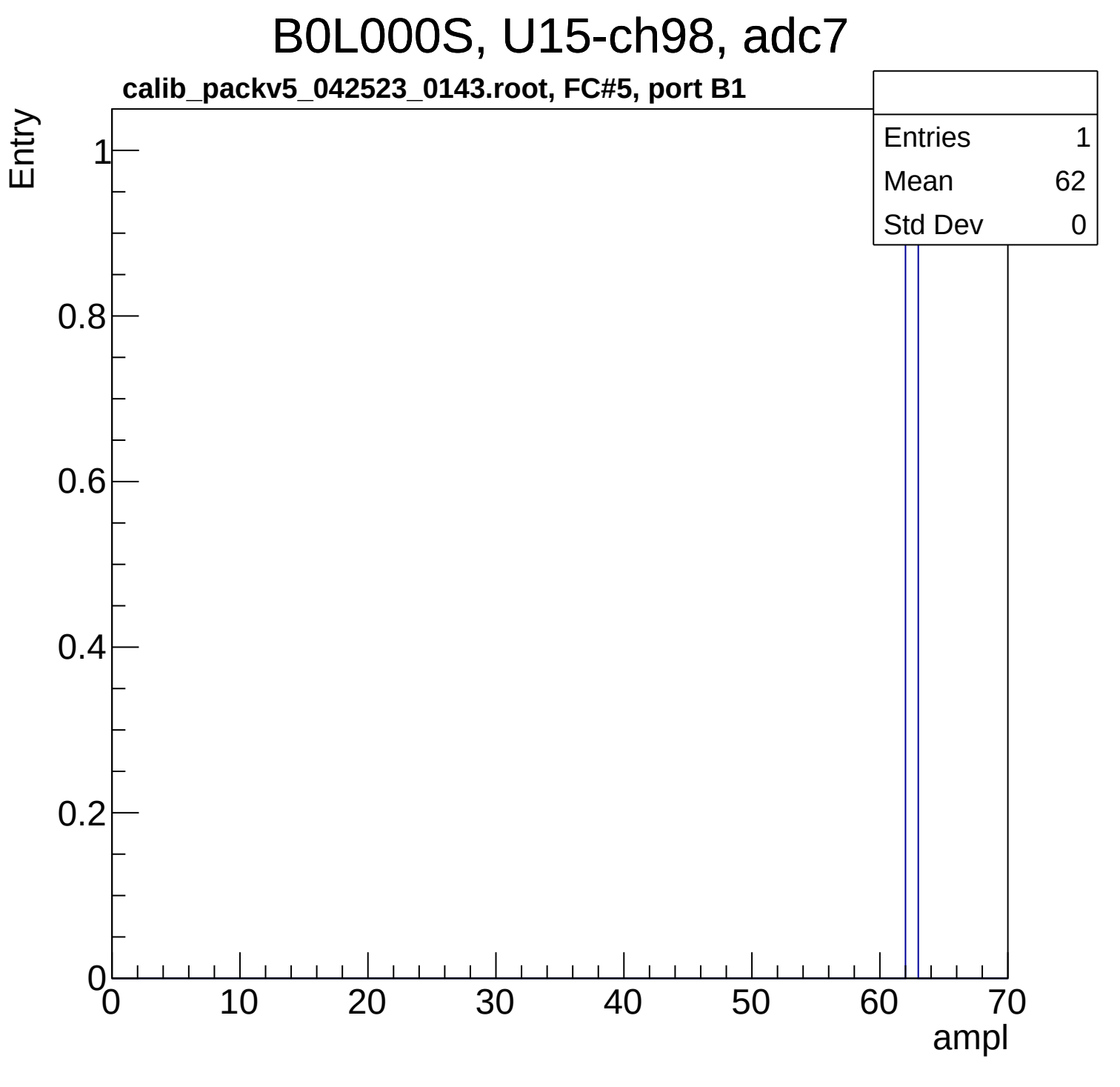
B0L000S, U15-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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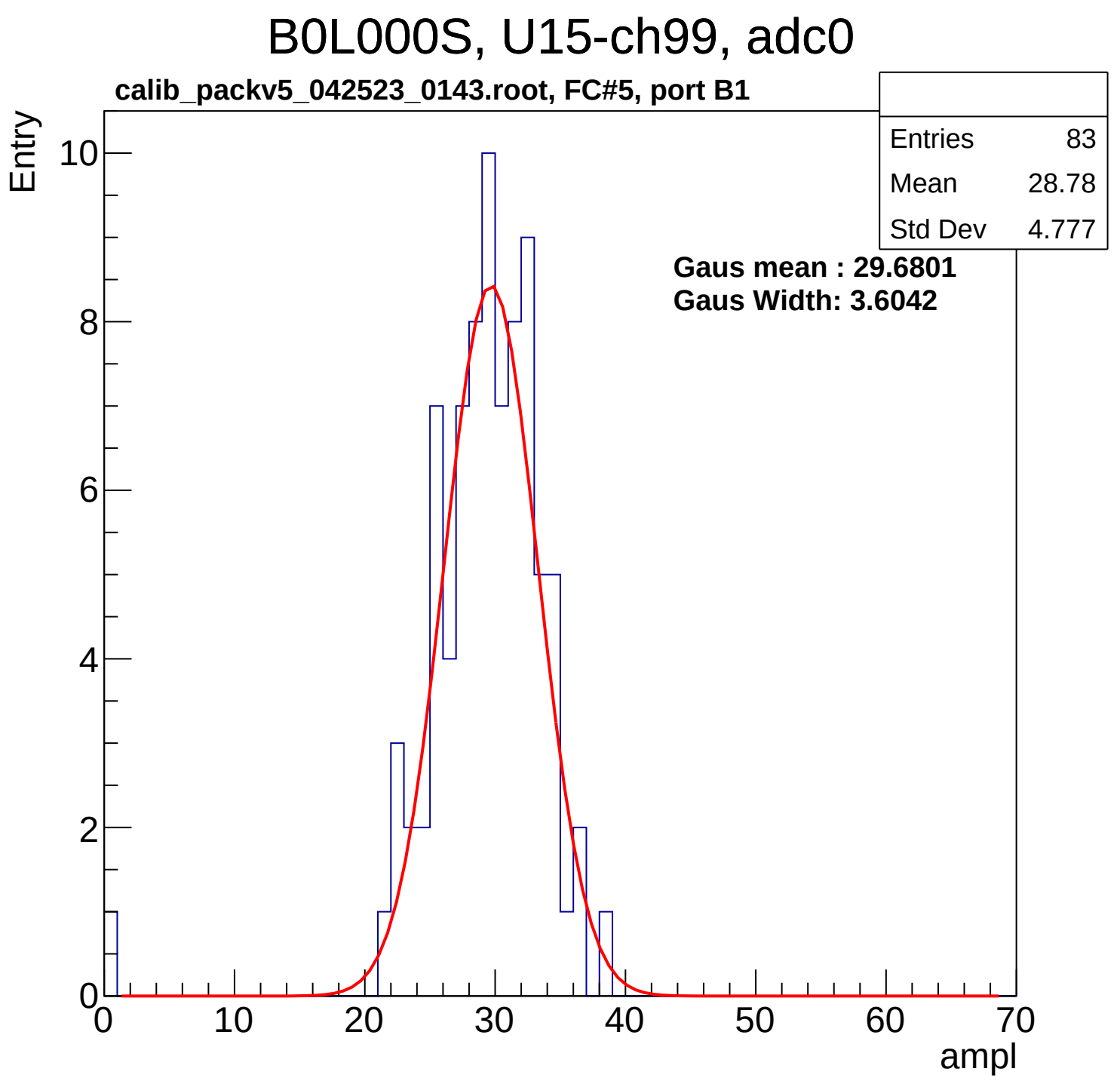
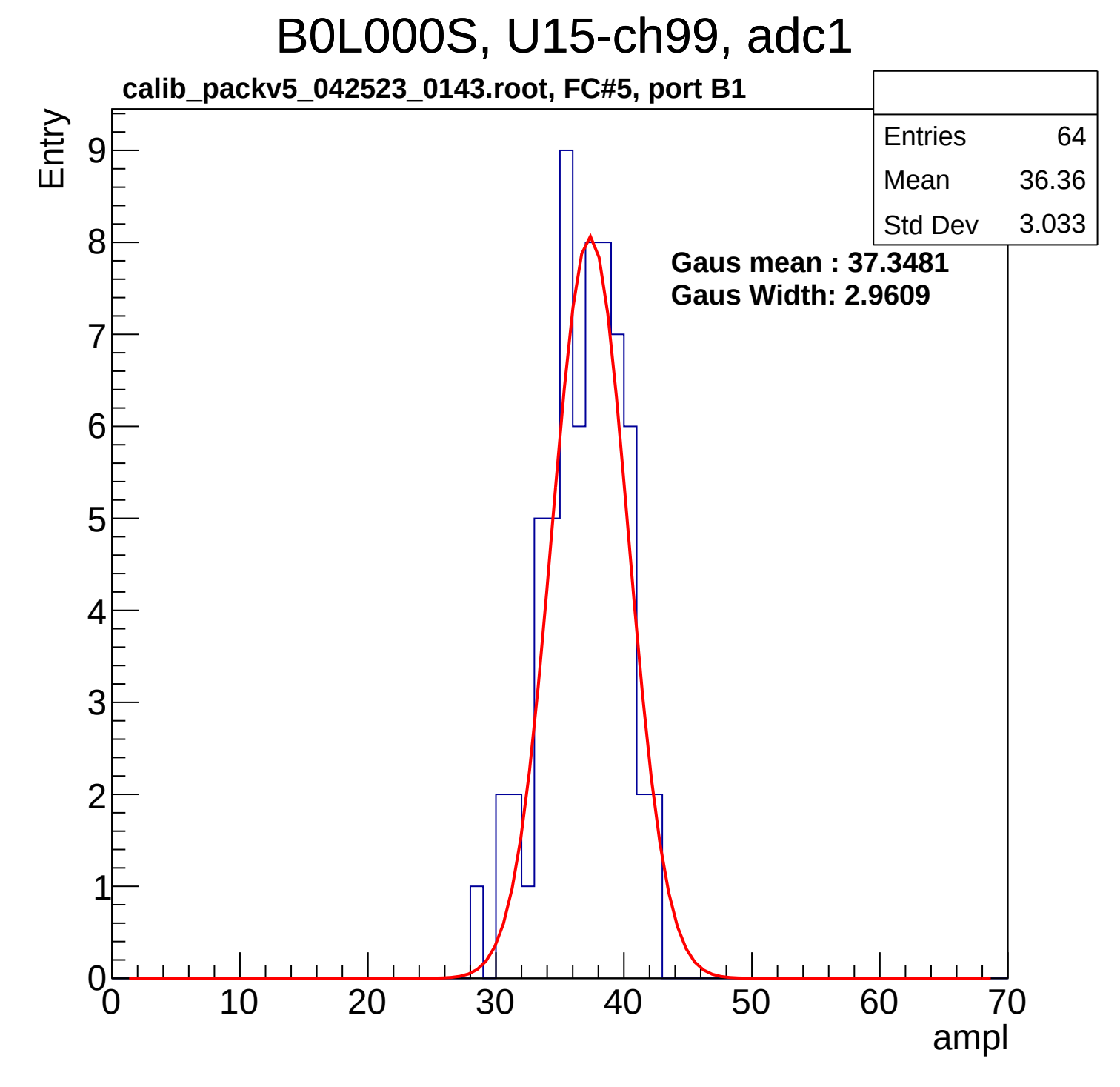
50
60
70
ampl


B0L000S, U15-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 28.78
Std Dev 4.777
Gaus mean : 29.6801
Gaus Width: 3.6042
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 36.36
Std Dev 3.033
Gaus mean : 37.3481
Gaus Width: 2.9609
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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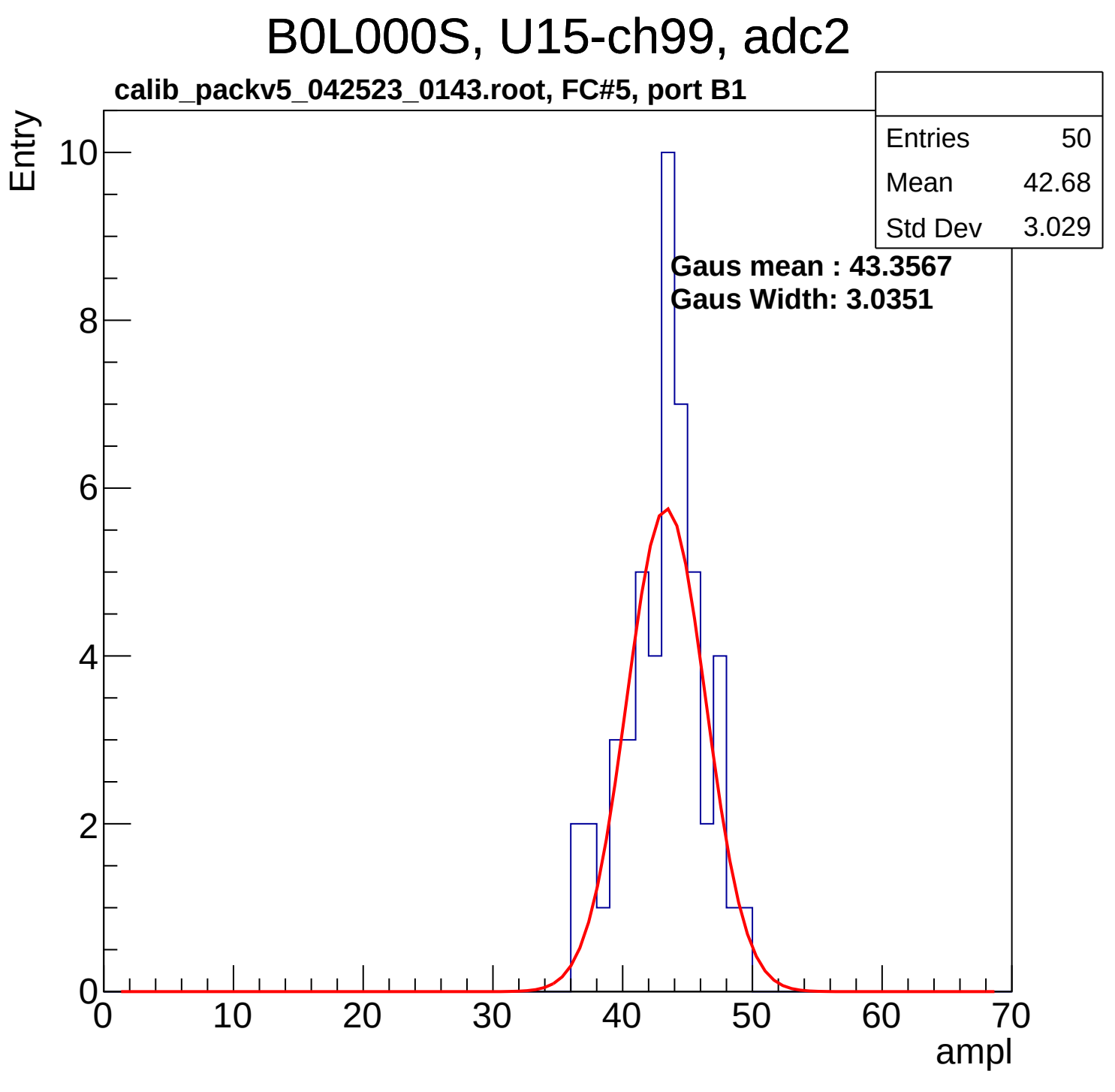
B0L000S, U15-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.68
Std Dev 3.029
Gaus mean : 43.3567
Gaus Width: 3.0351
Entry
ampl
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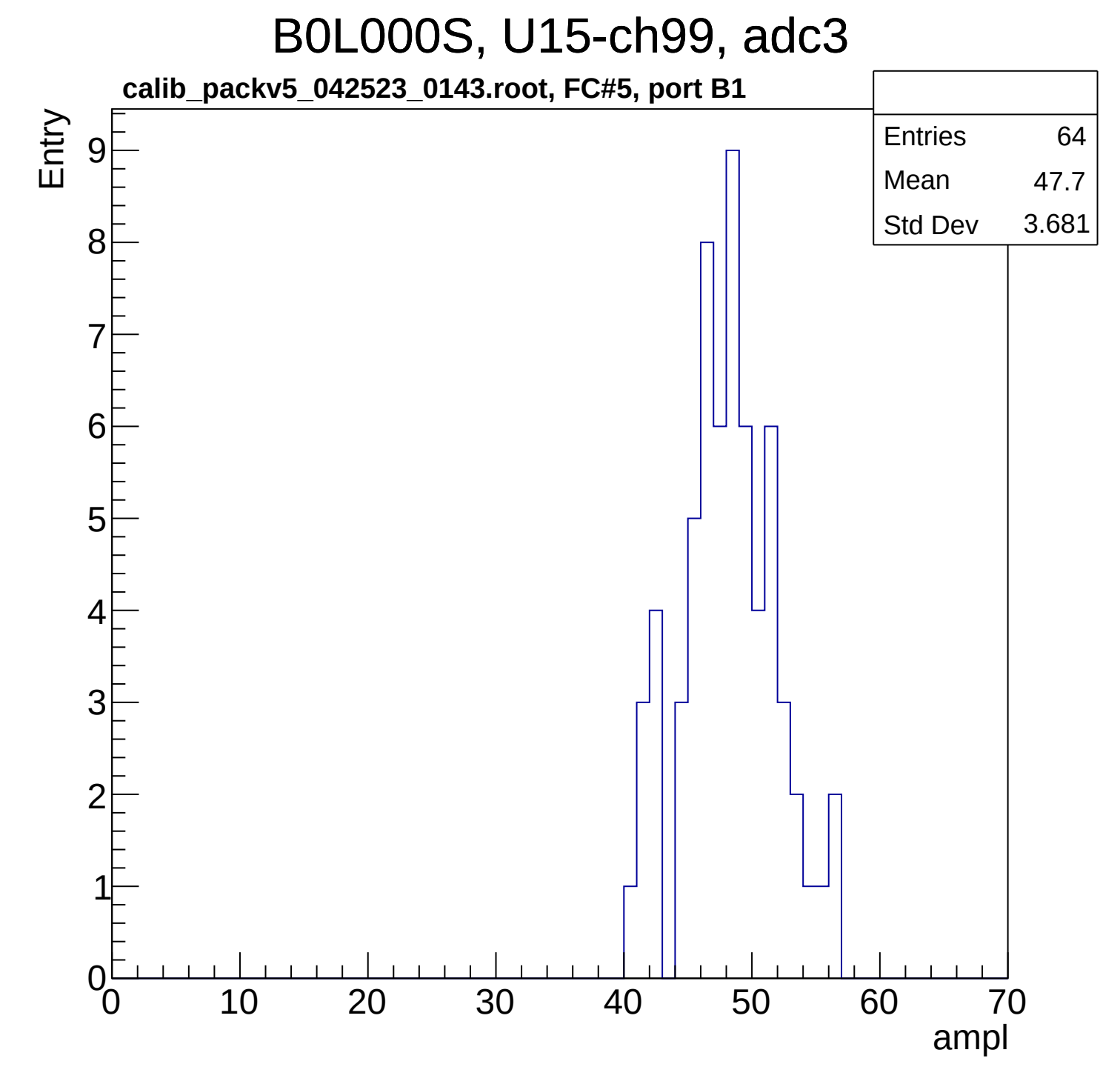
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.7
Std Dev 3.681
Entry
ampl
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4
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6
7
8
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0
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60
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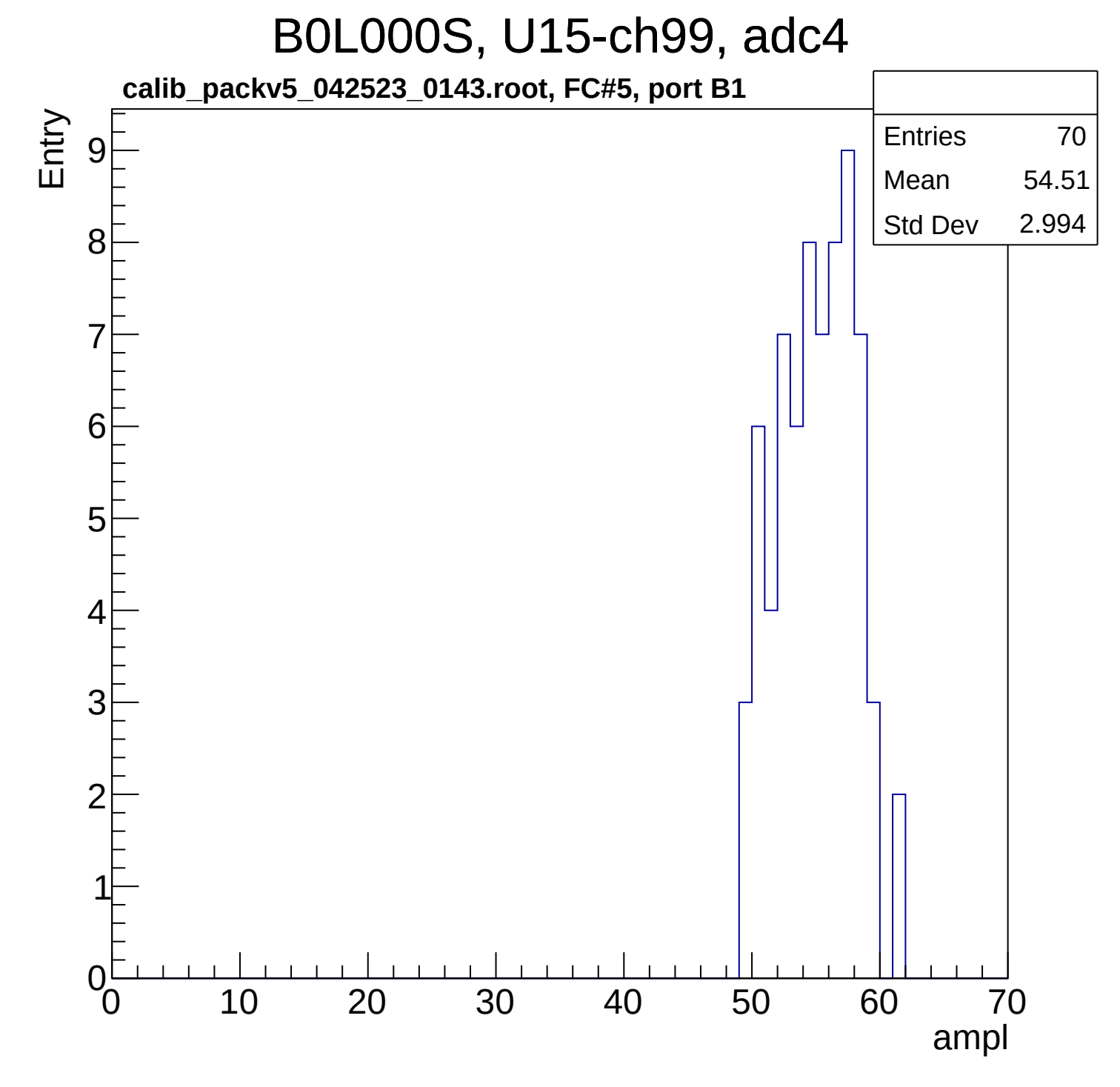
B0L000S, U15-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 54.51
Std Dev 2.994
Entry
ampl
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4
5
6
7
8
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0
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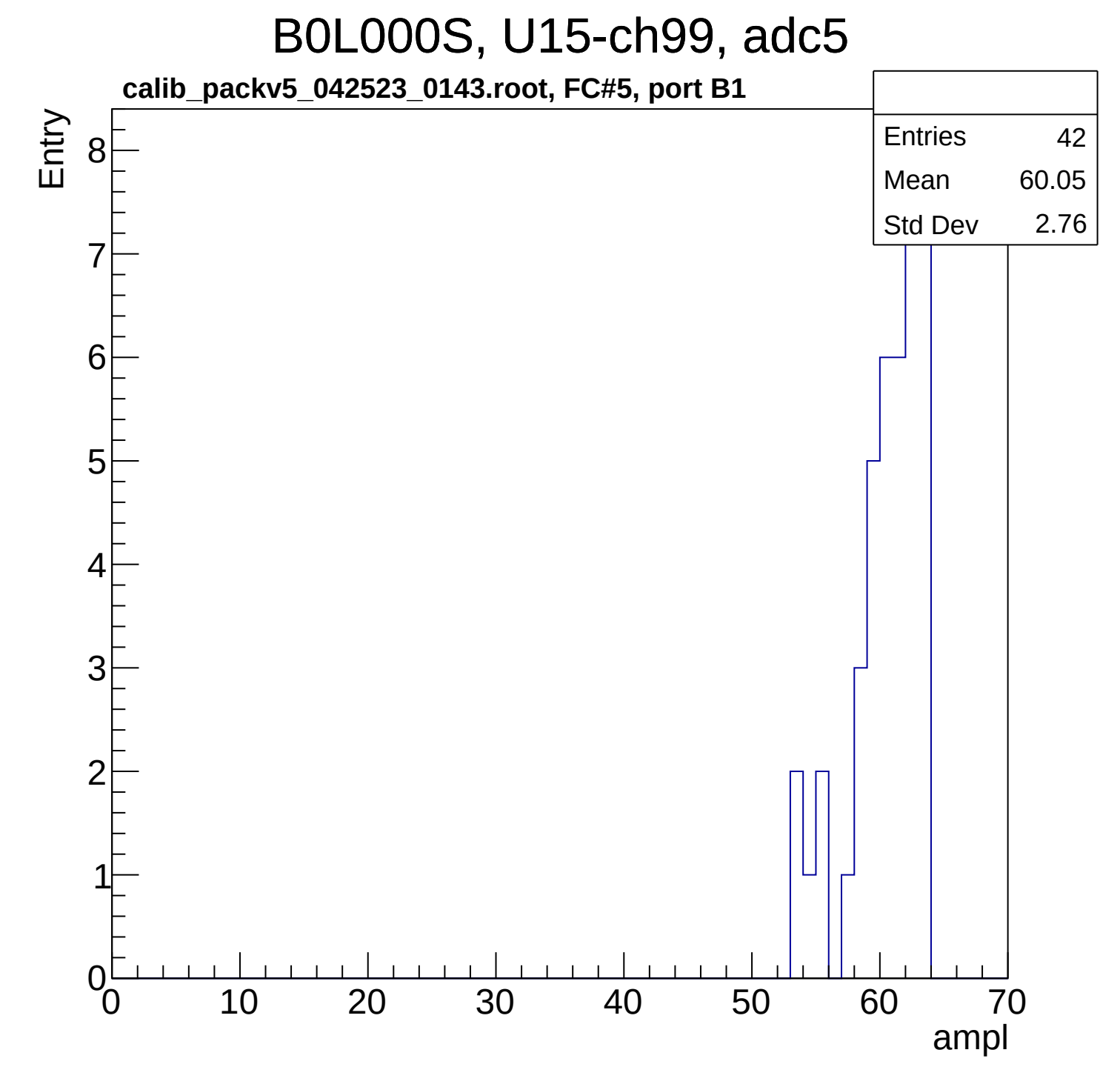
B0L000S, U15-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 60.05
Std Dev 2.76
Entry
ampl
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2
3
4
5
6
7
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0
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60
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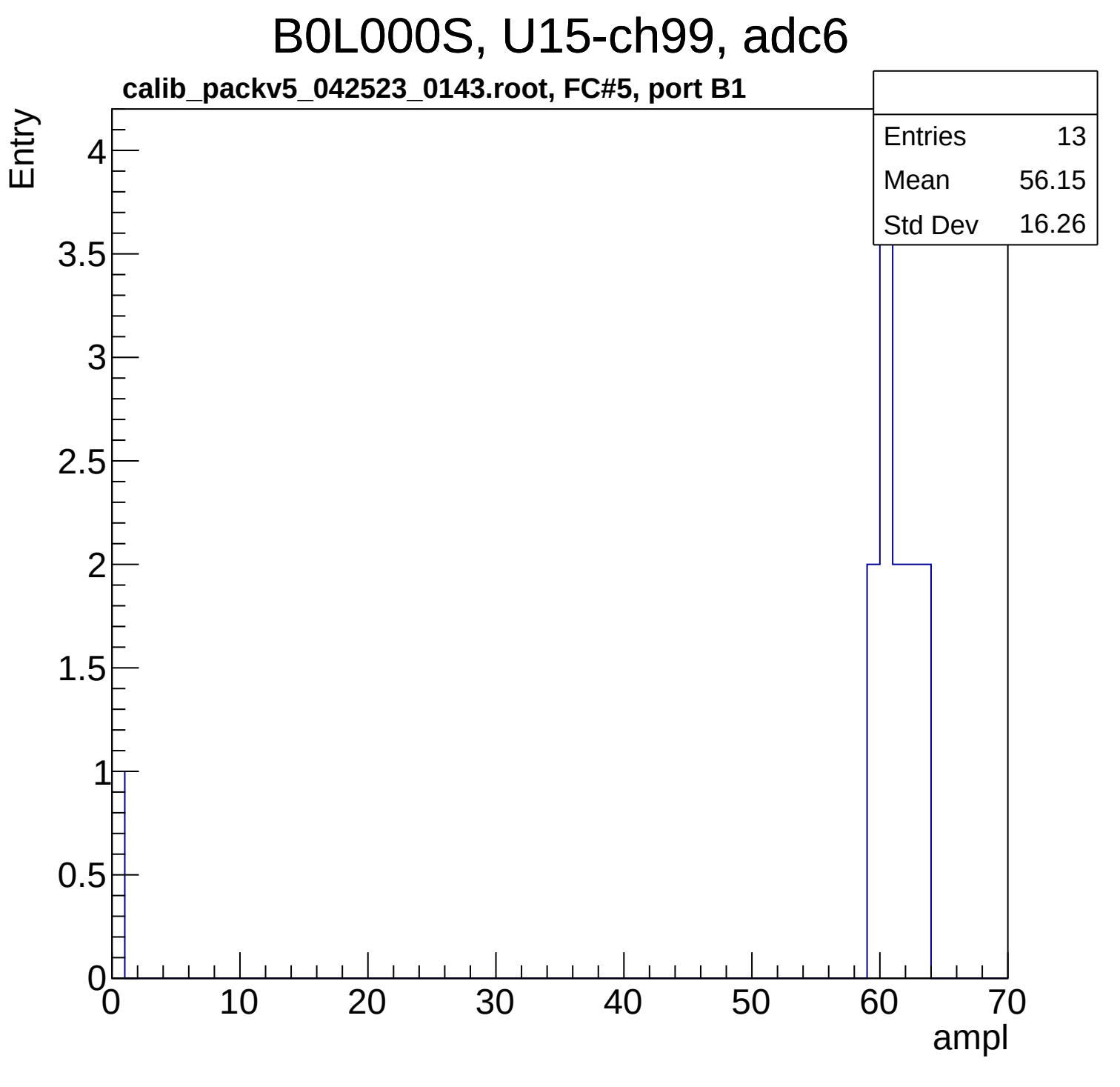
B0L000S, U15-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.15
Std Dev 16.26
Entry
ampl
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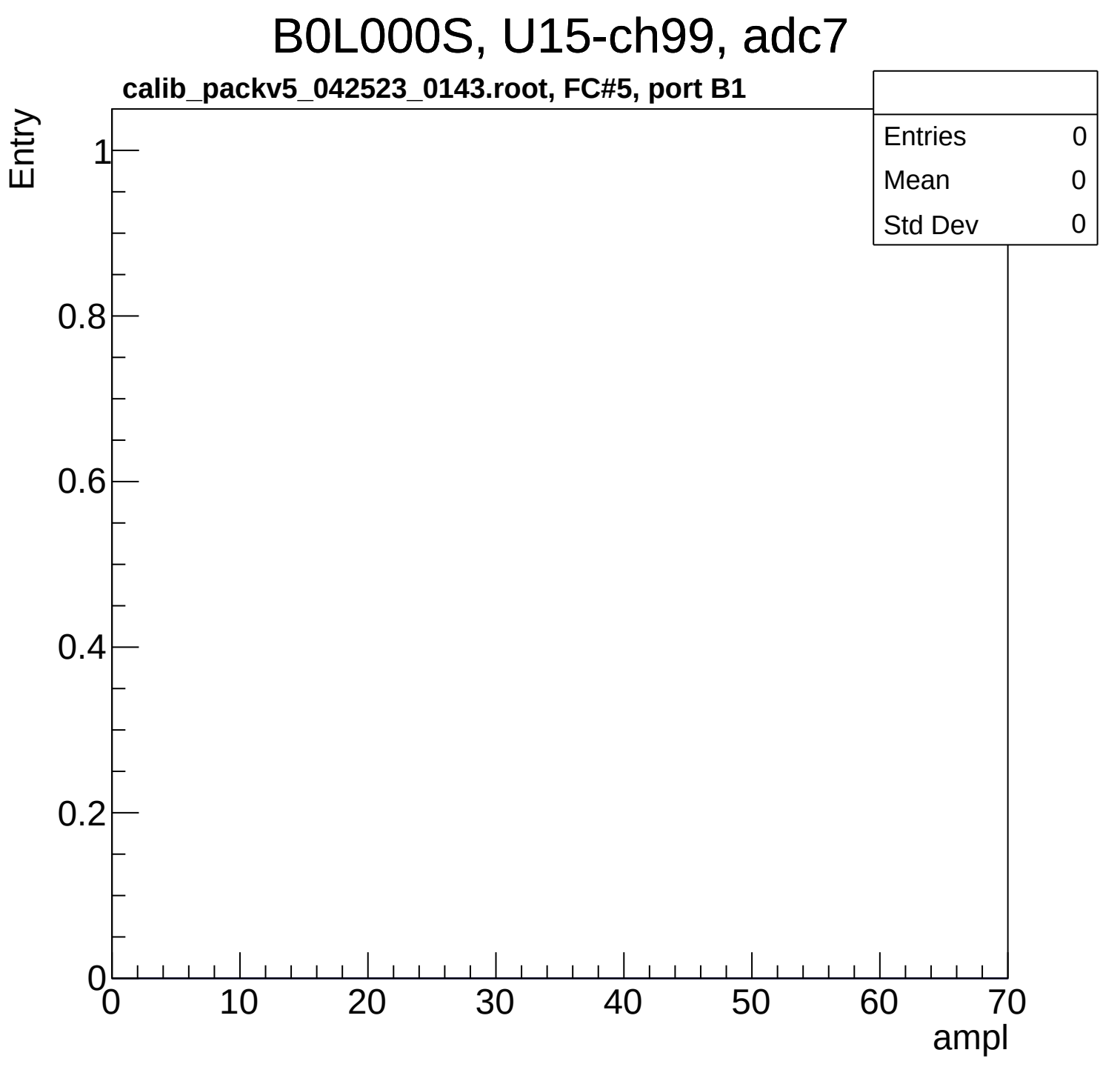
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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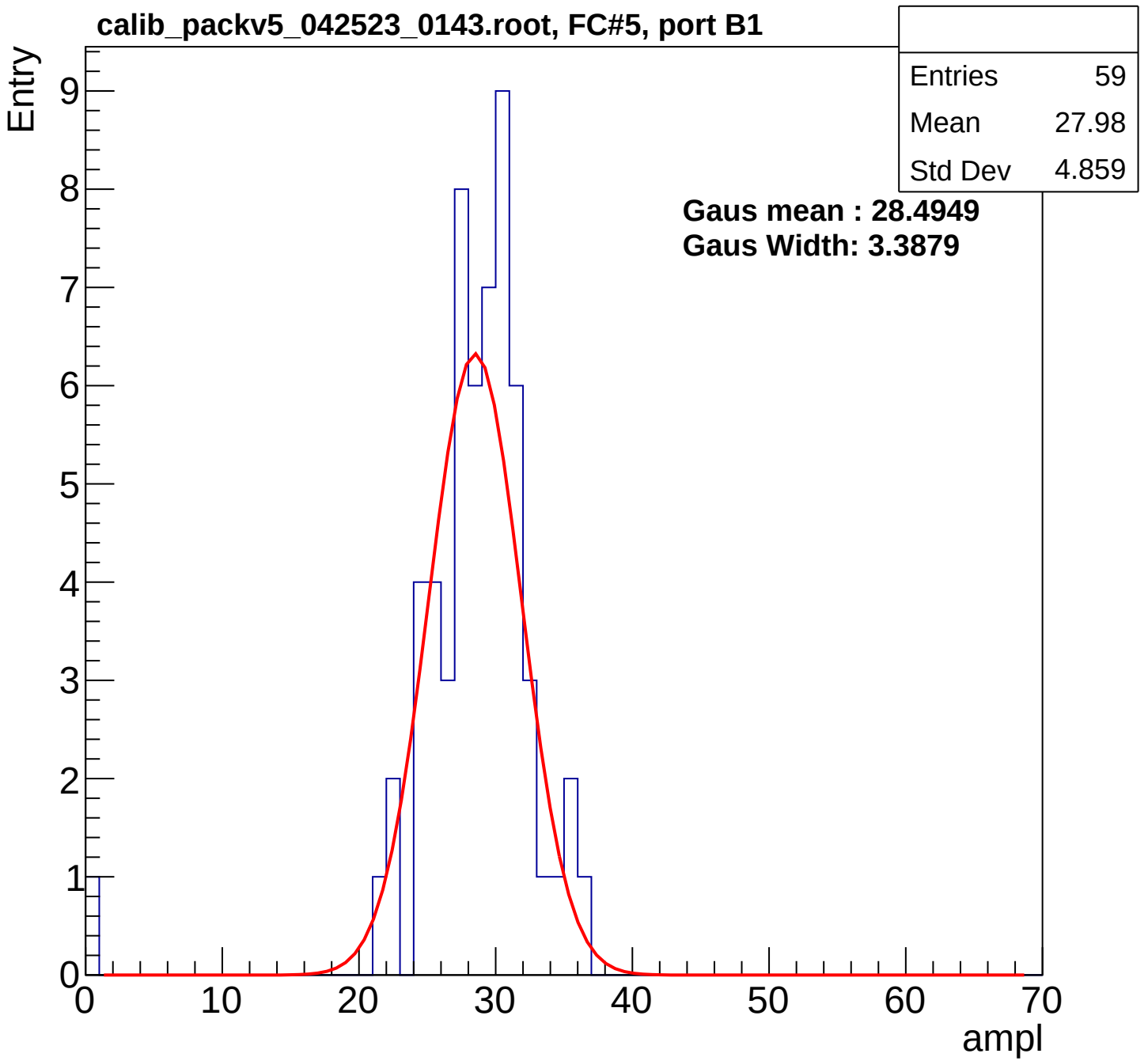
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.98
Std Dev 4.859
Gaus mean : 28.4949
Gaus Width: 3.3879
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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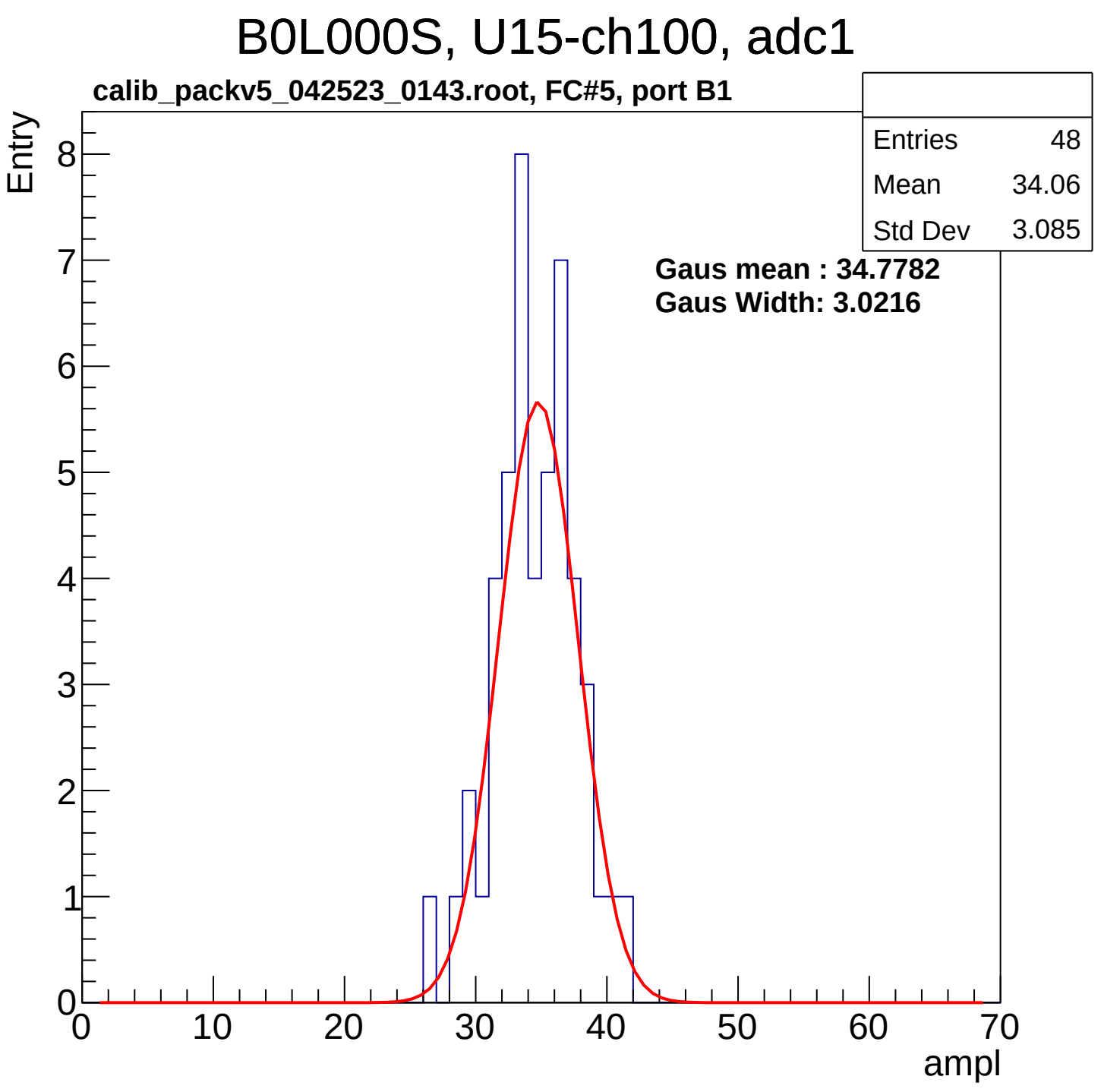
B0L000S, U15-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 34.06
Std Dev 3.085
Gaus mean : 34.7782
Gaus Width: 3.0216
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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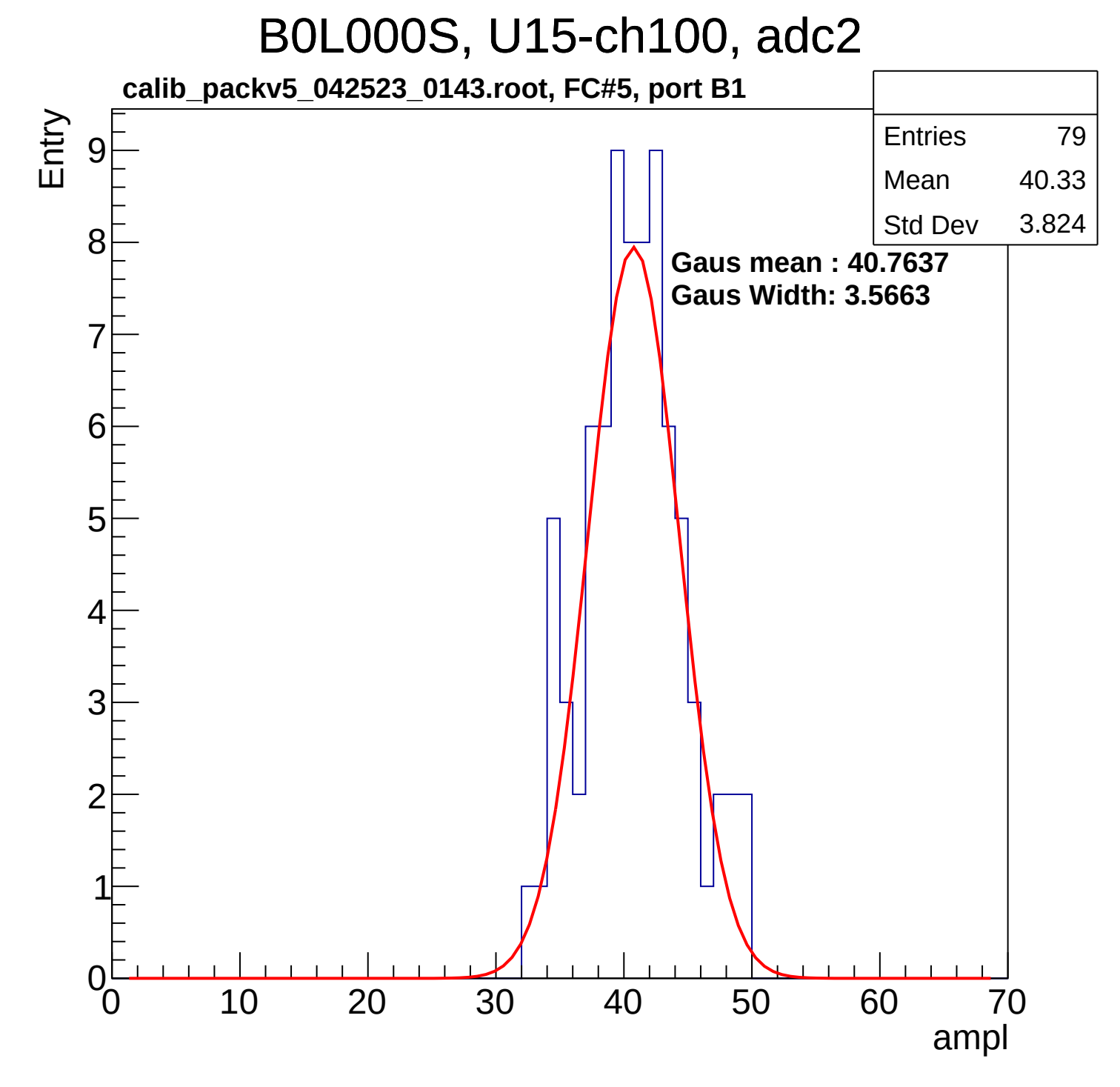
B0L000S, U15-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 40.33
Std Dev 3.824
Gaus mean : 40.7637
Gaus Width: 3.5663
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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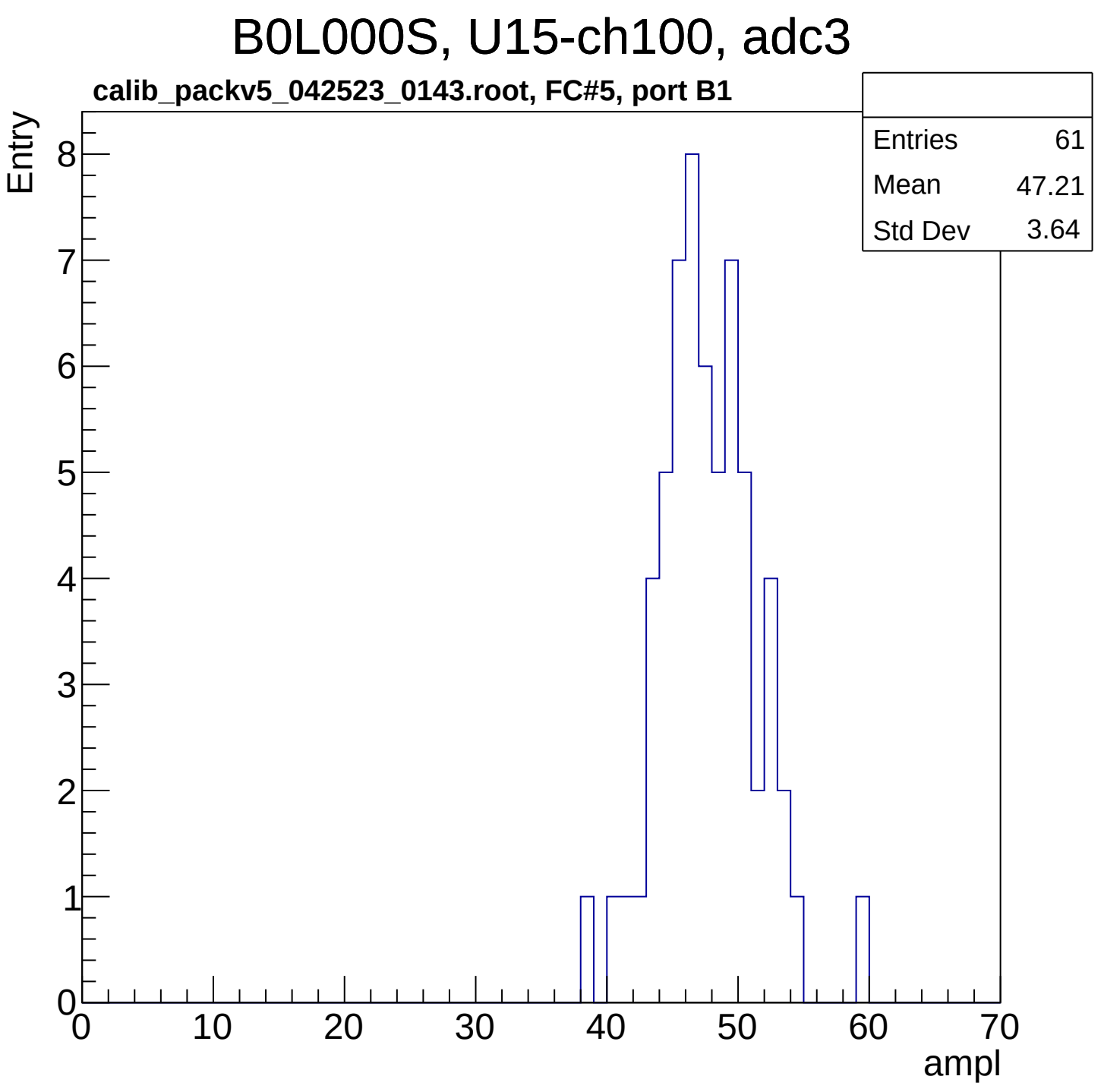
B0L000S, U15-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.21
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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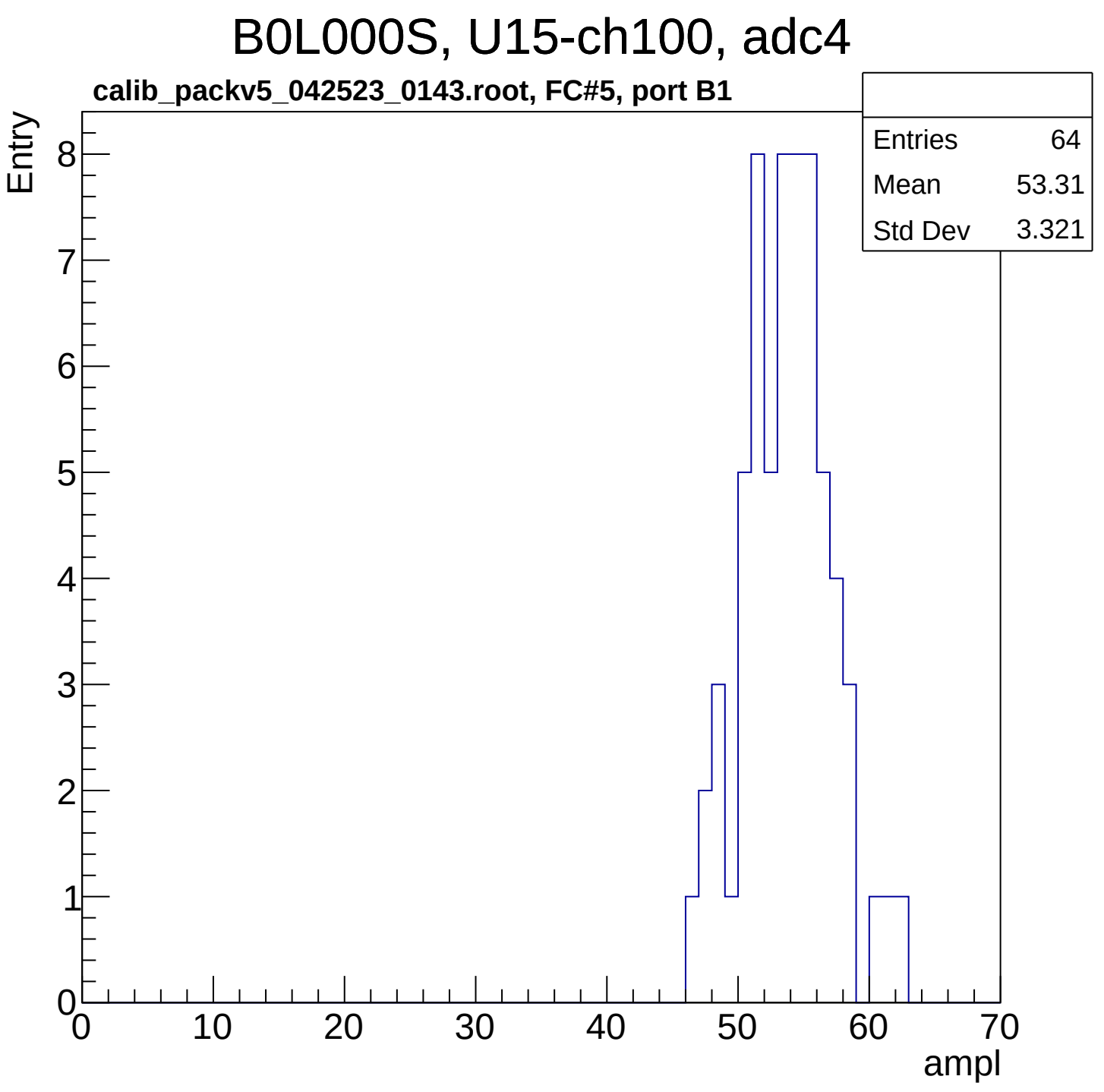
B0L000S, U15-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 53.31
Std Dev 3.321
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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40
50
60
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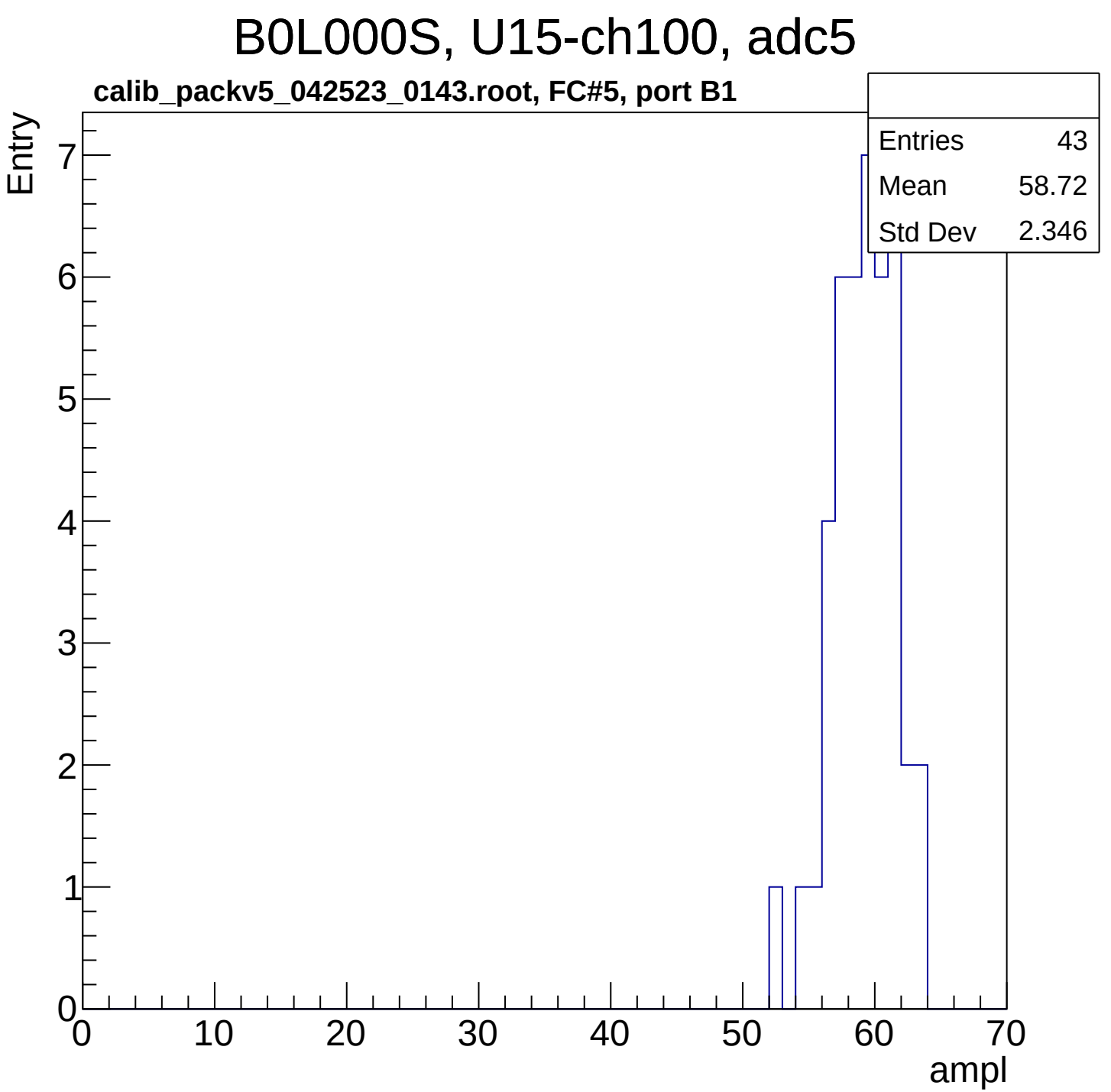
B0L000S, U15-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.72
Std Dev 2.346
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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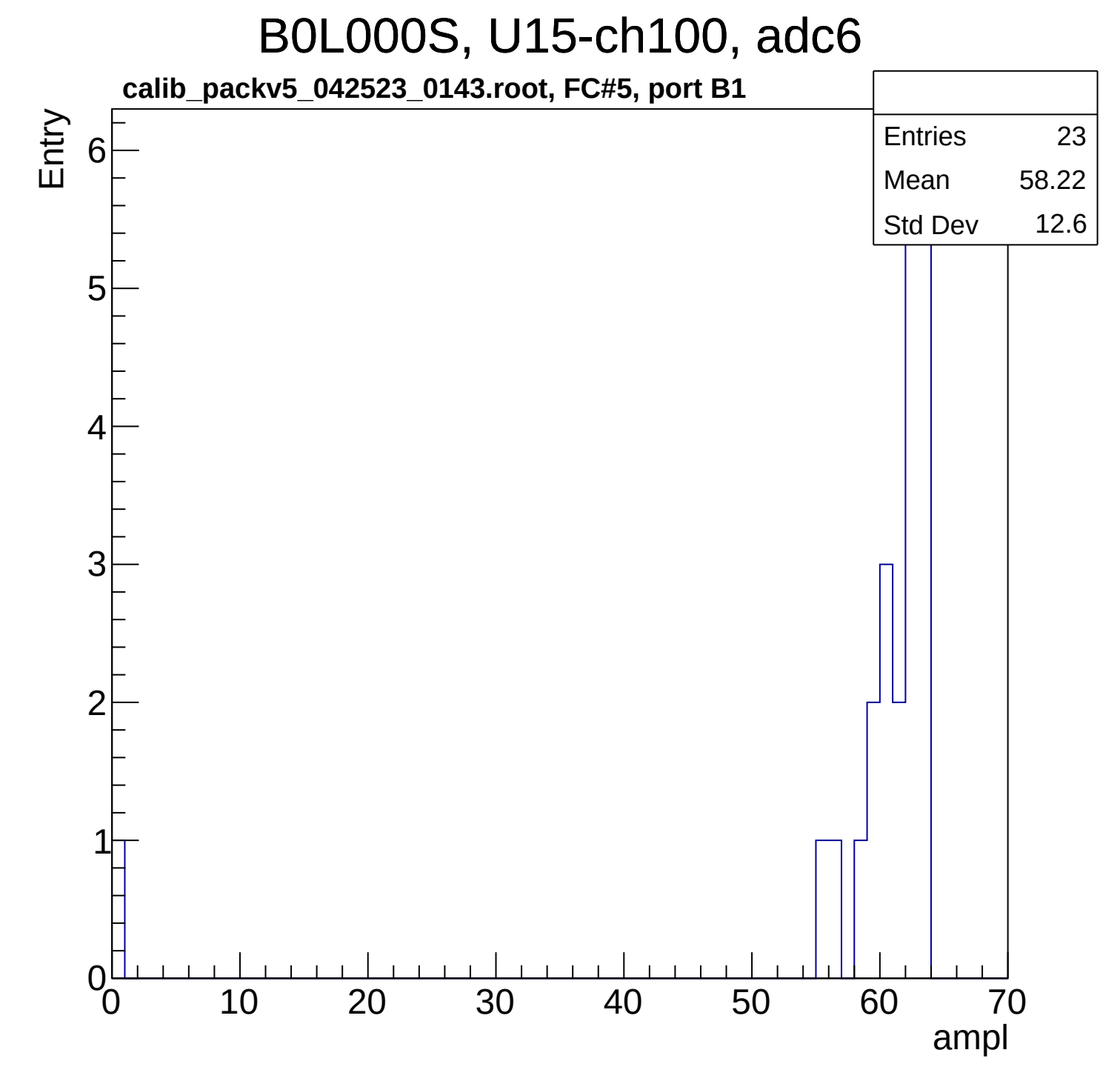
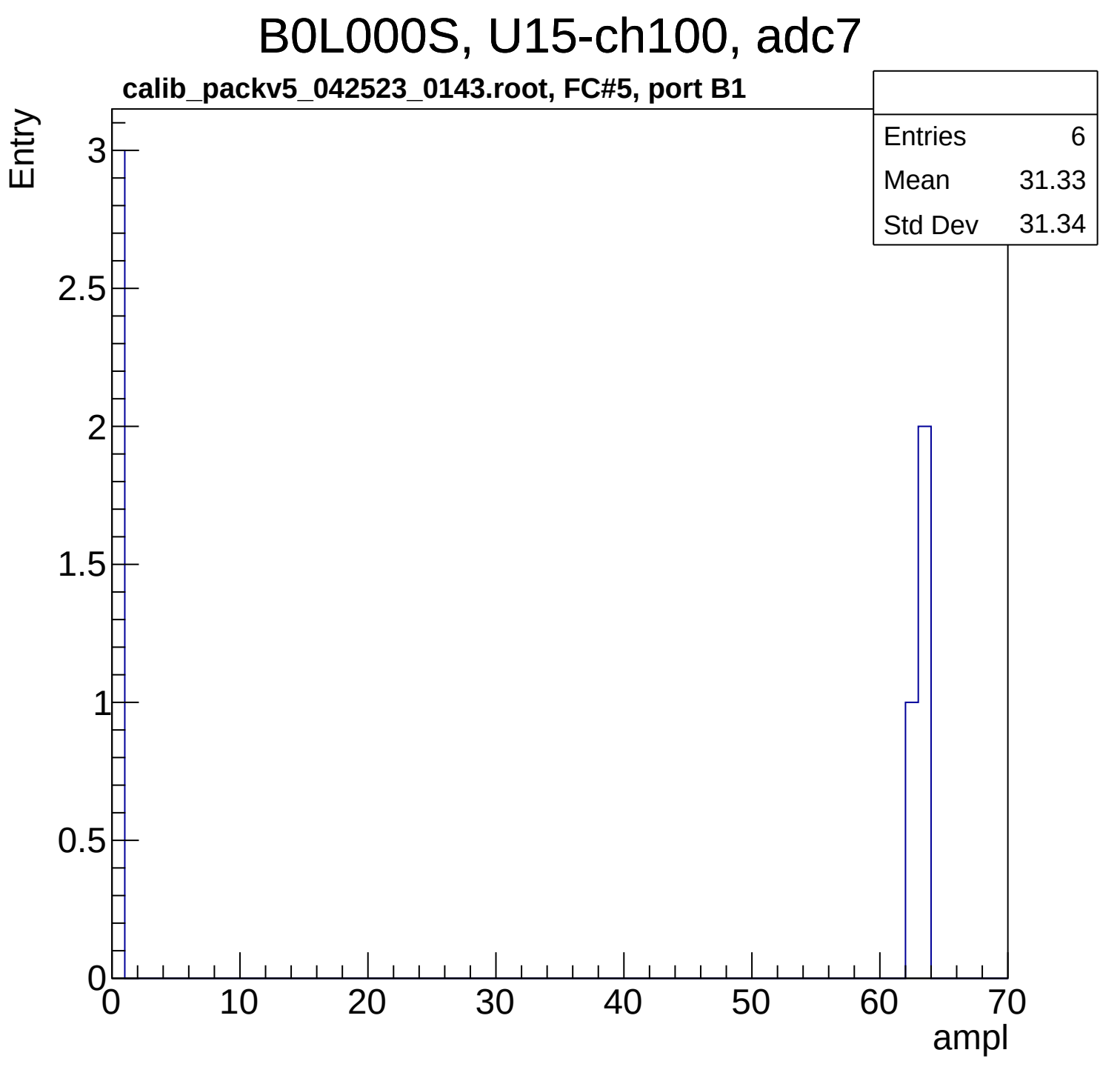

B0L000S, U15-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.22
Std Dev 12.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 31.33
Std Dev 31.34
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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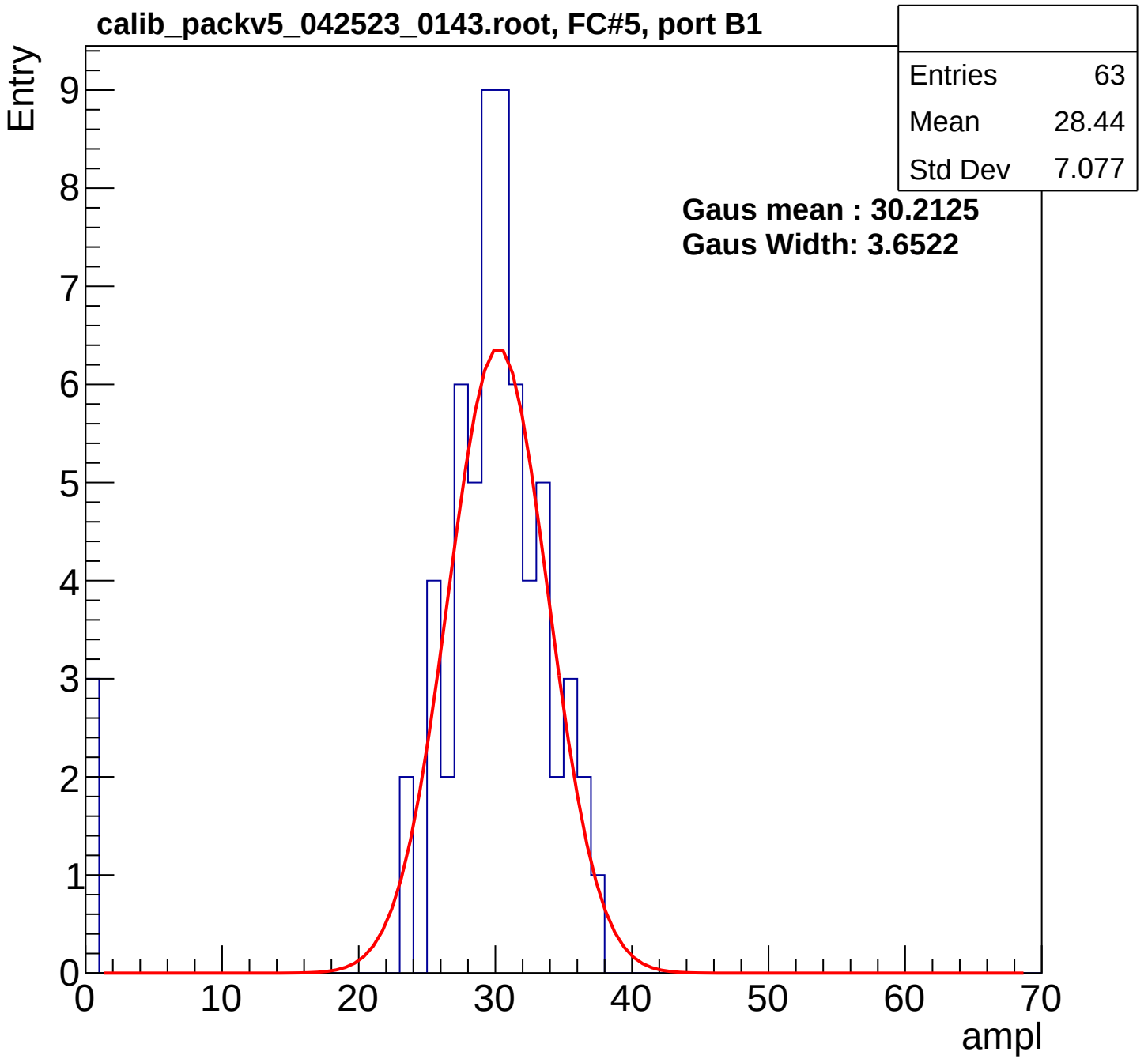
B0L000S, U15-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.44
Std Dev 7.077
Gaus mean : 30.2125
Gaus Width: 3.6522
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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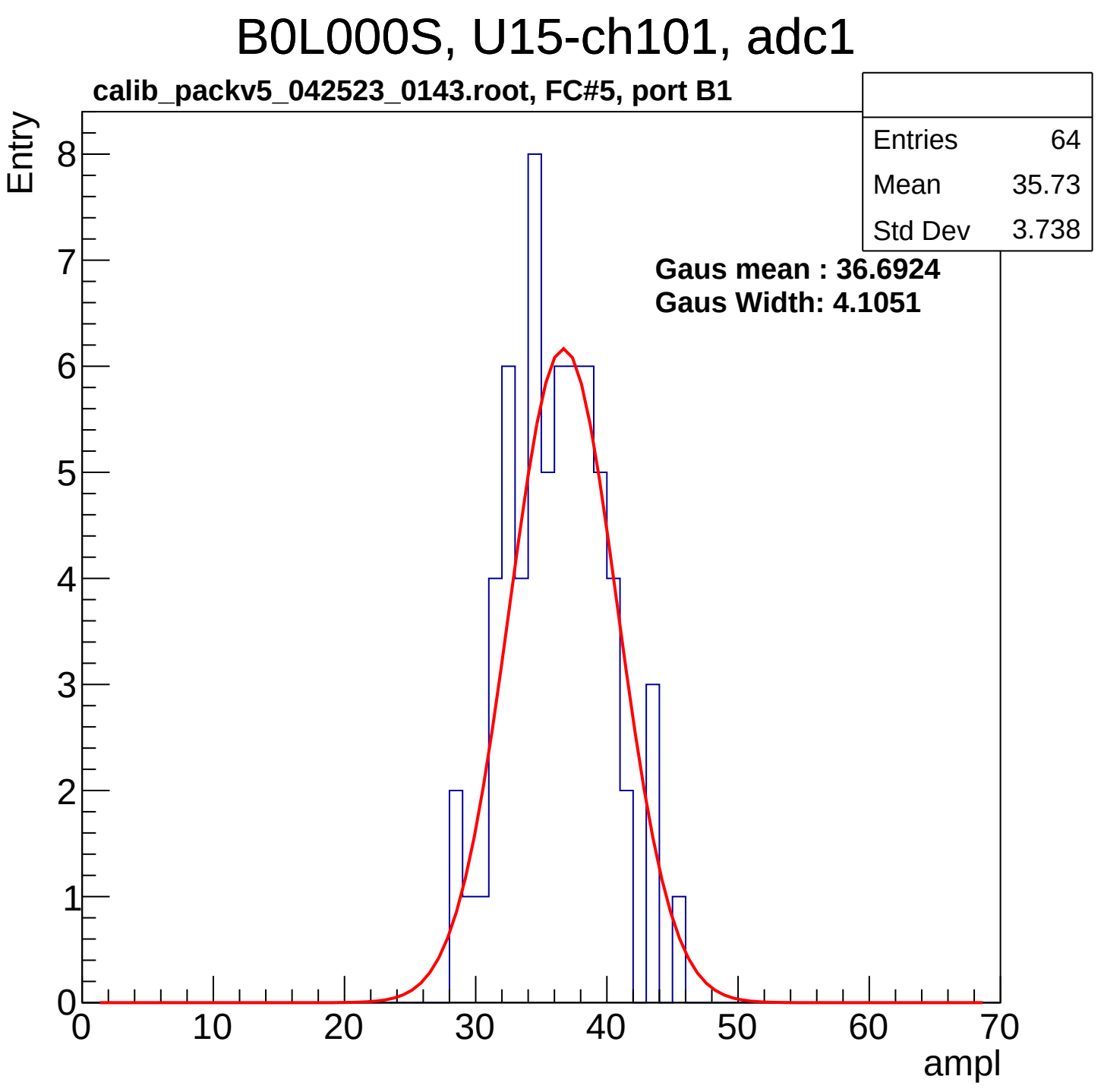
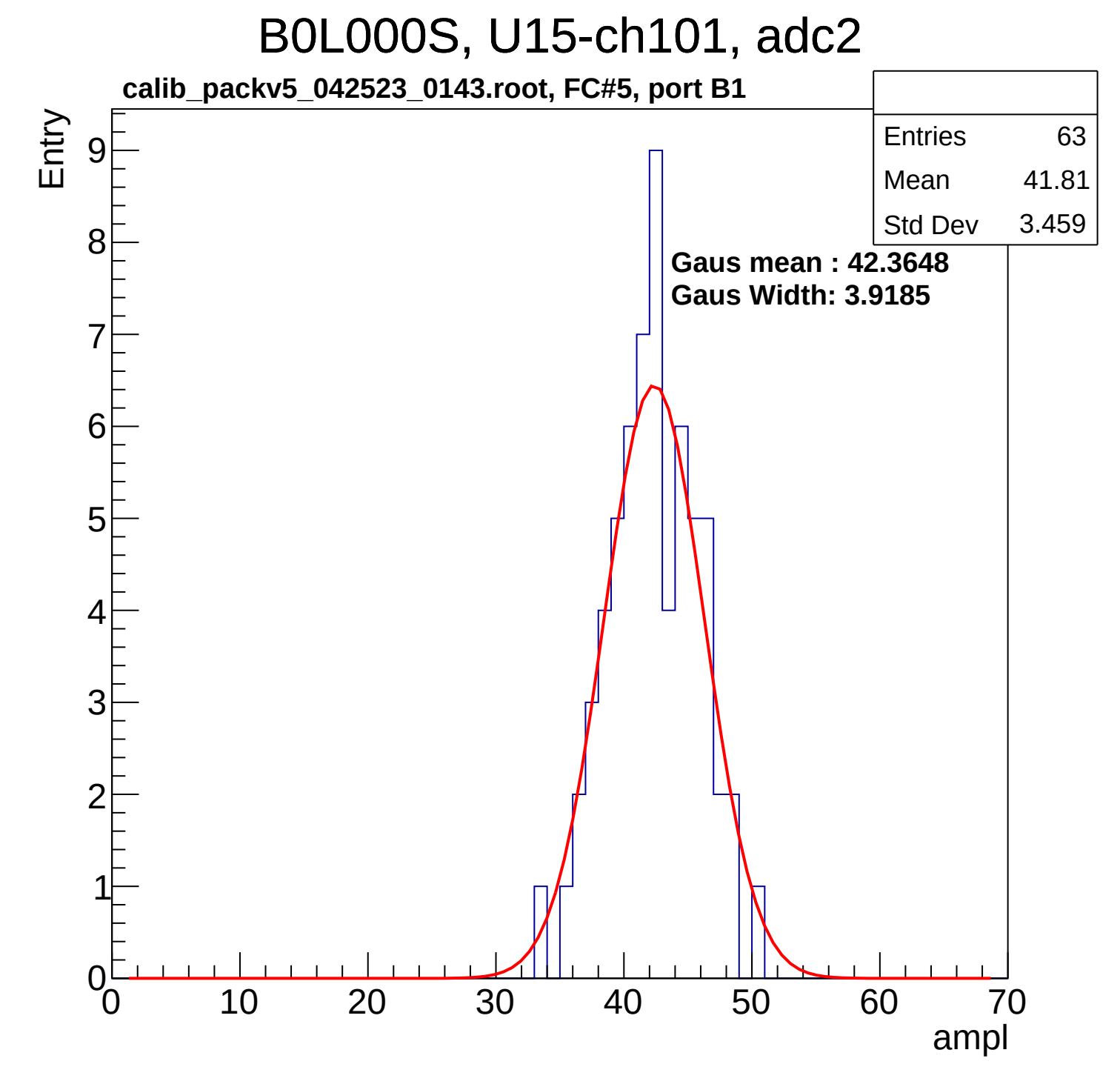
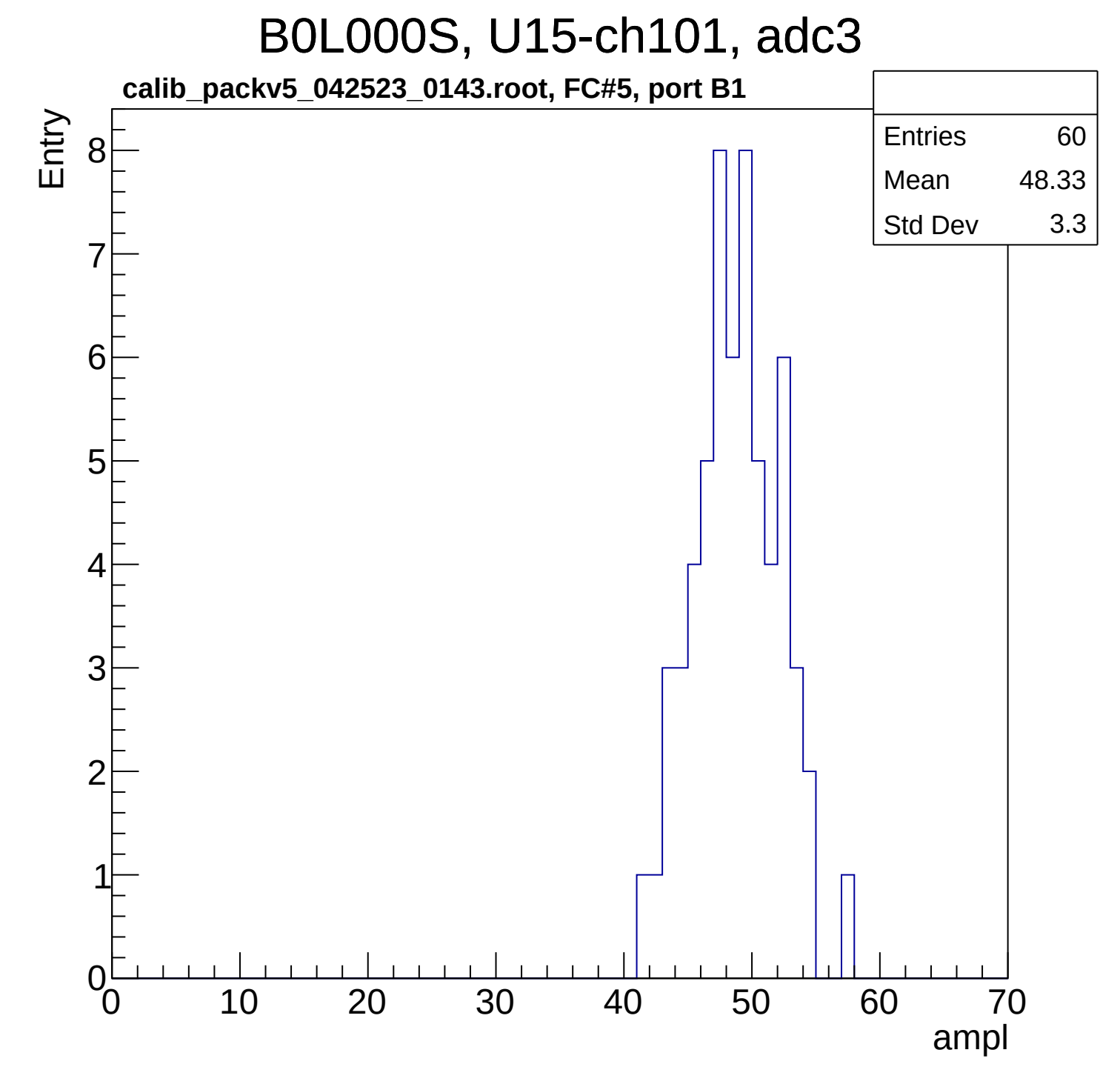

B0L000S, U15-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.73
Std Dev 3.738
Gaus mean : 36.6924
Gaus Width: 4.1051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.81
Std Dev 3.459
Gaus mean : 42.3648
Gaus Width: 3.9185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.33
Std Dev 3.3
Entry
ampl
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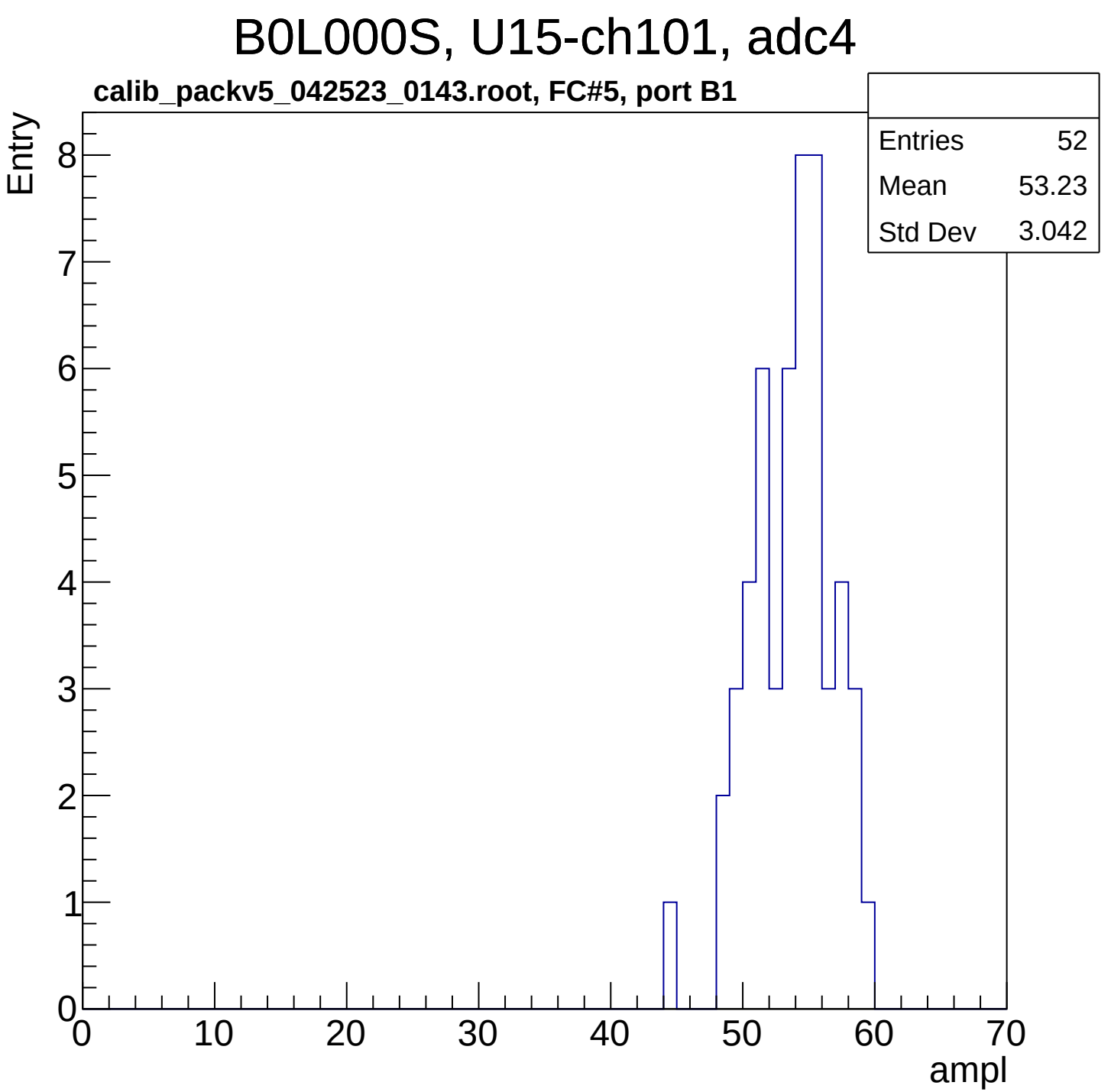
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.23
Std Dev 3.042
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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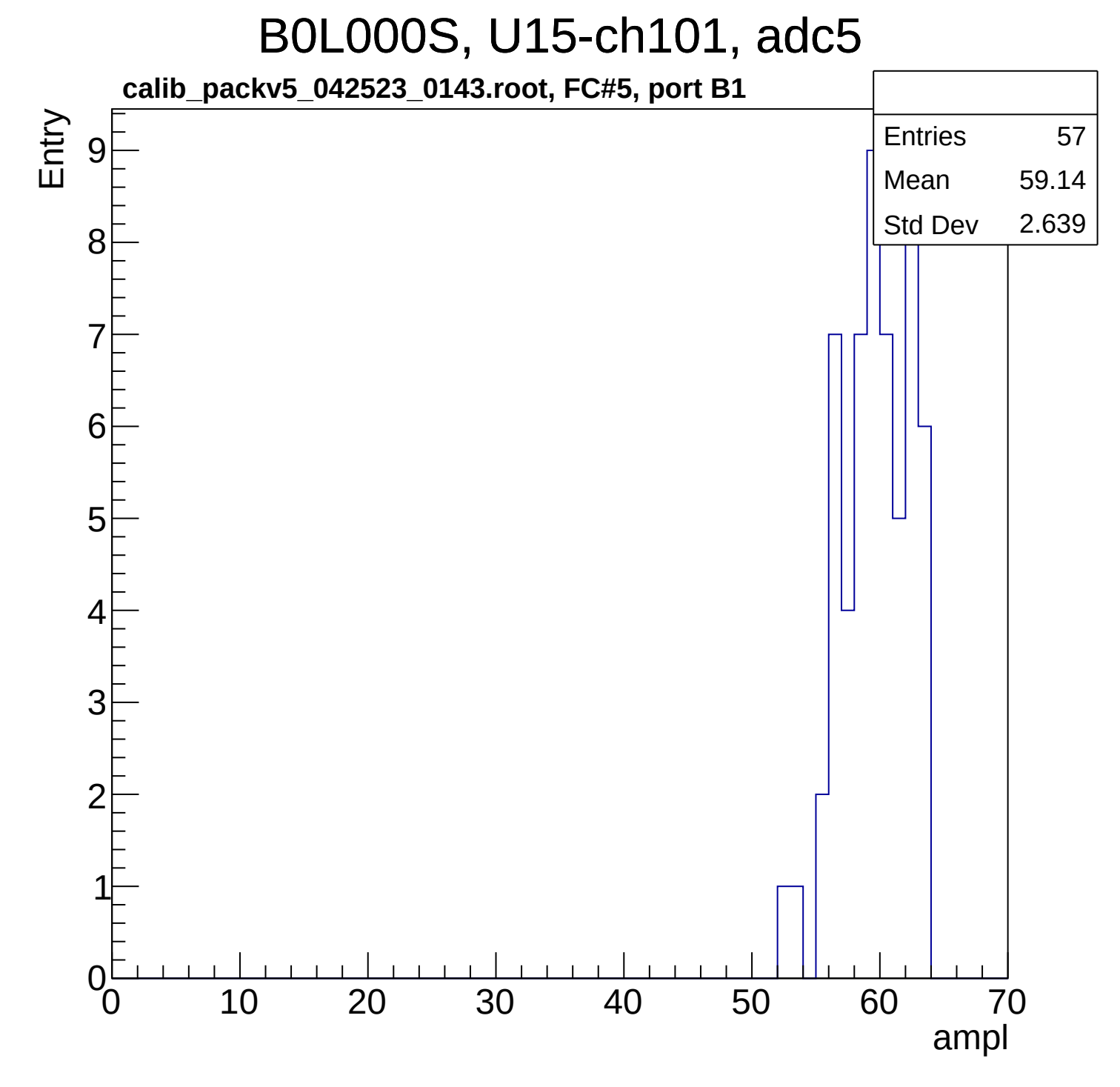
B0L000S, U15-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 59.14
Std Dev 2.639
Entry
ampl
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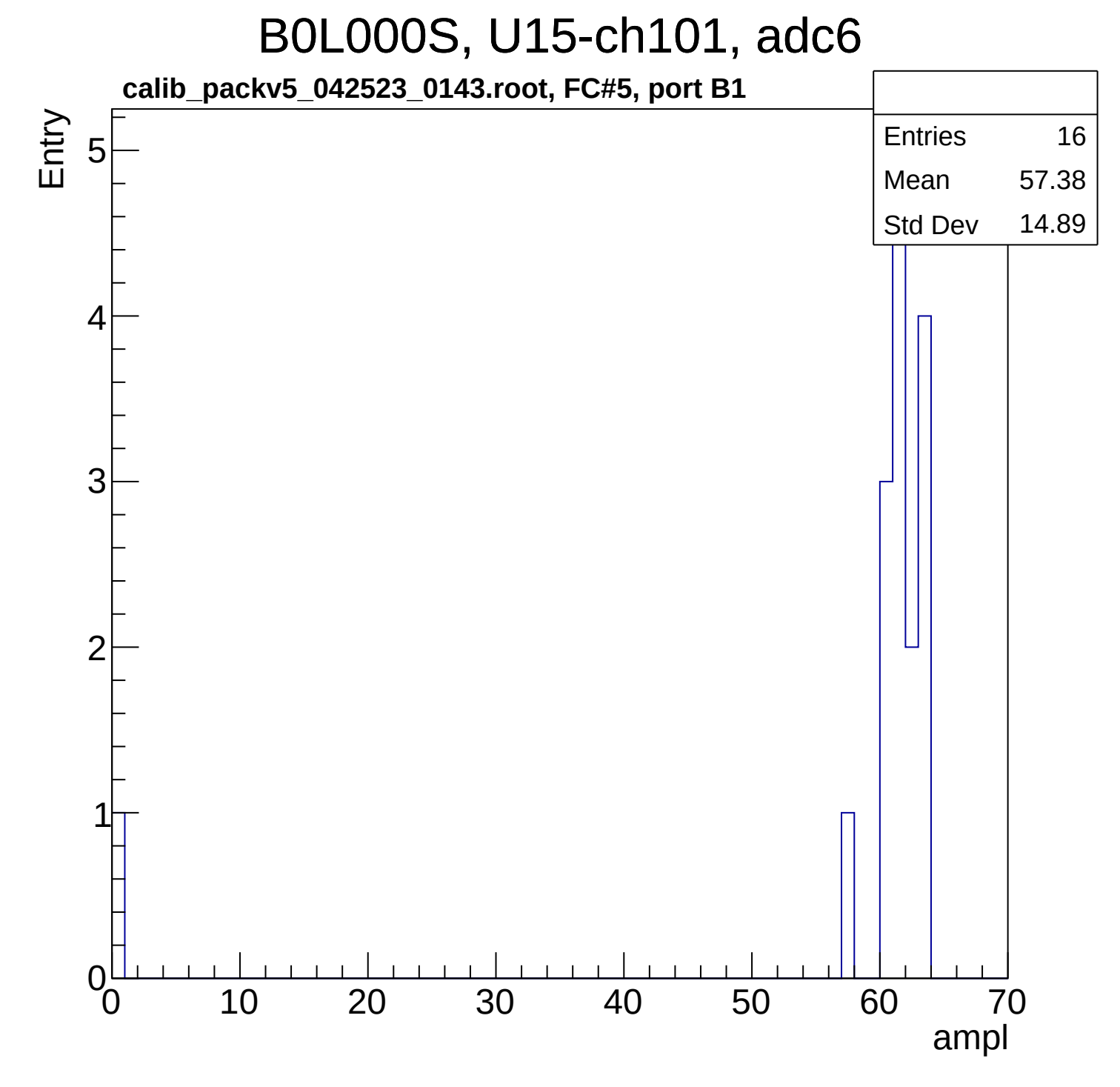
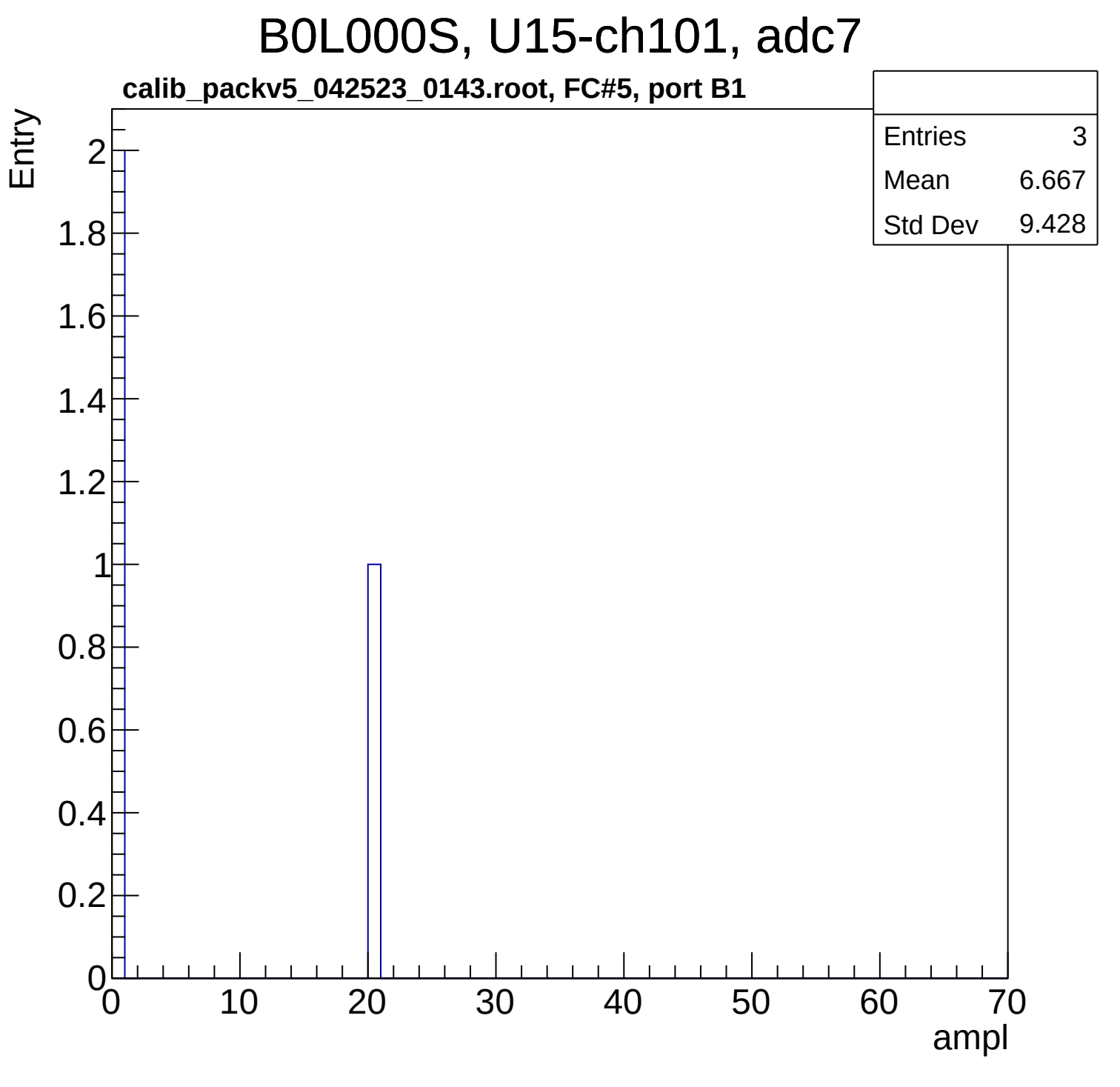
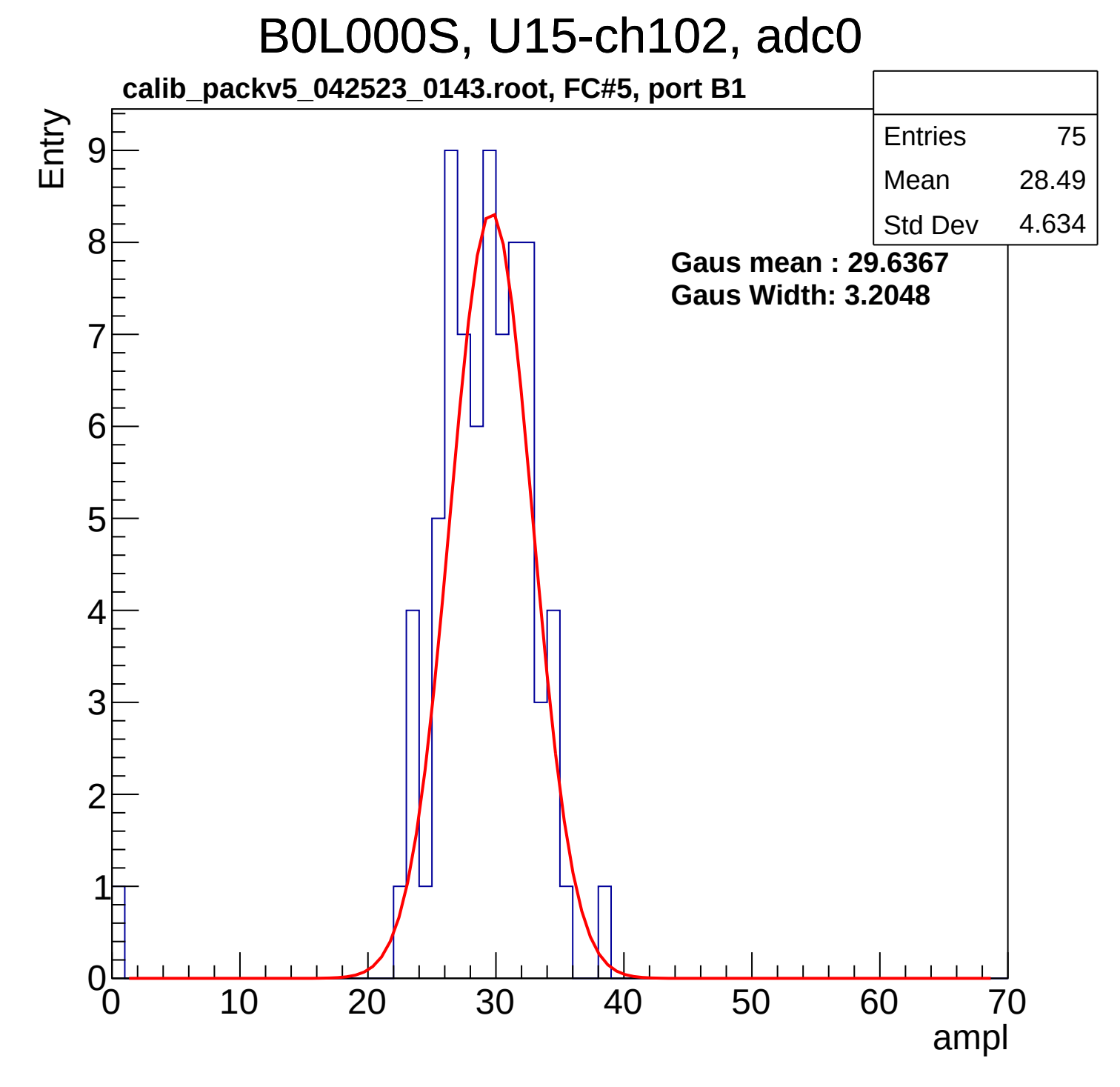
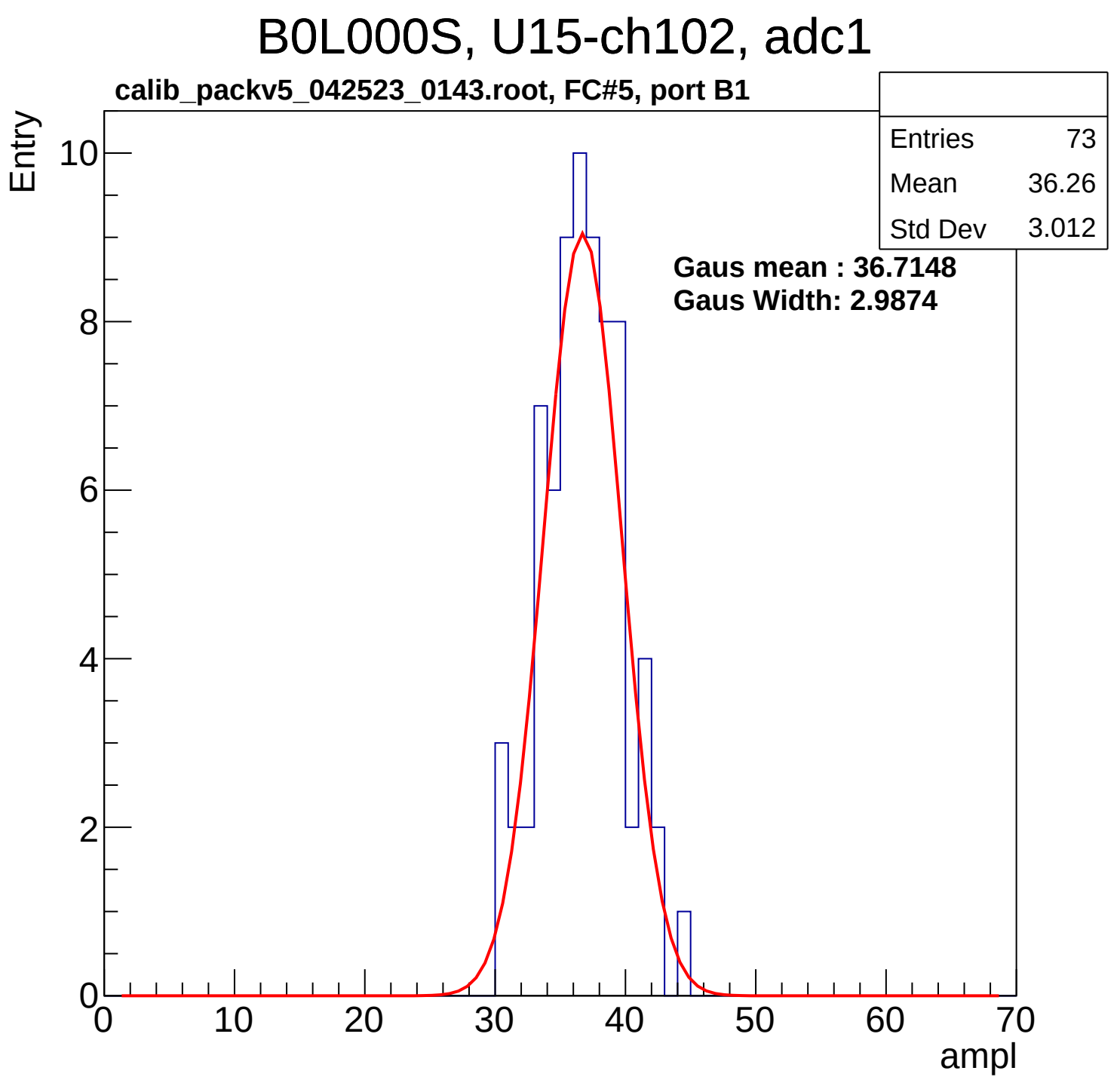
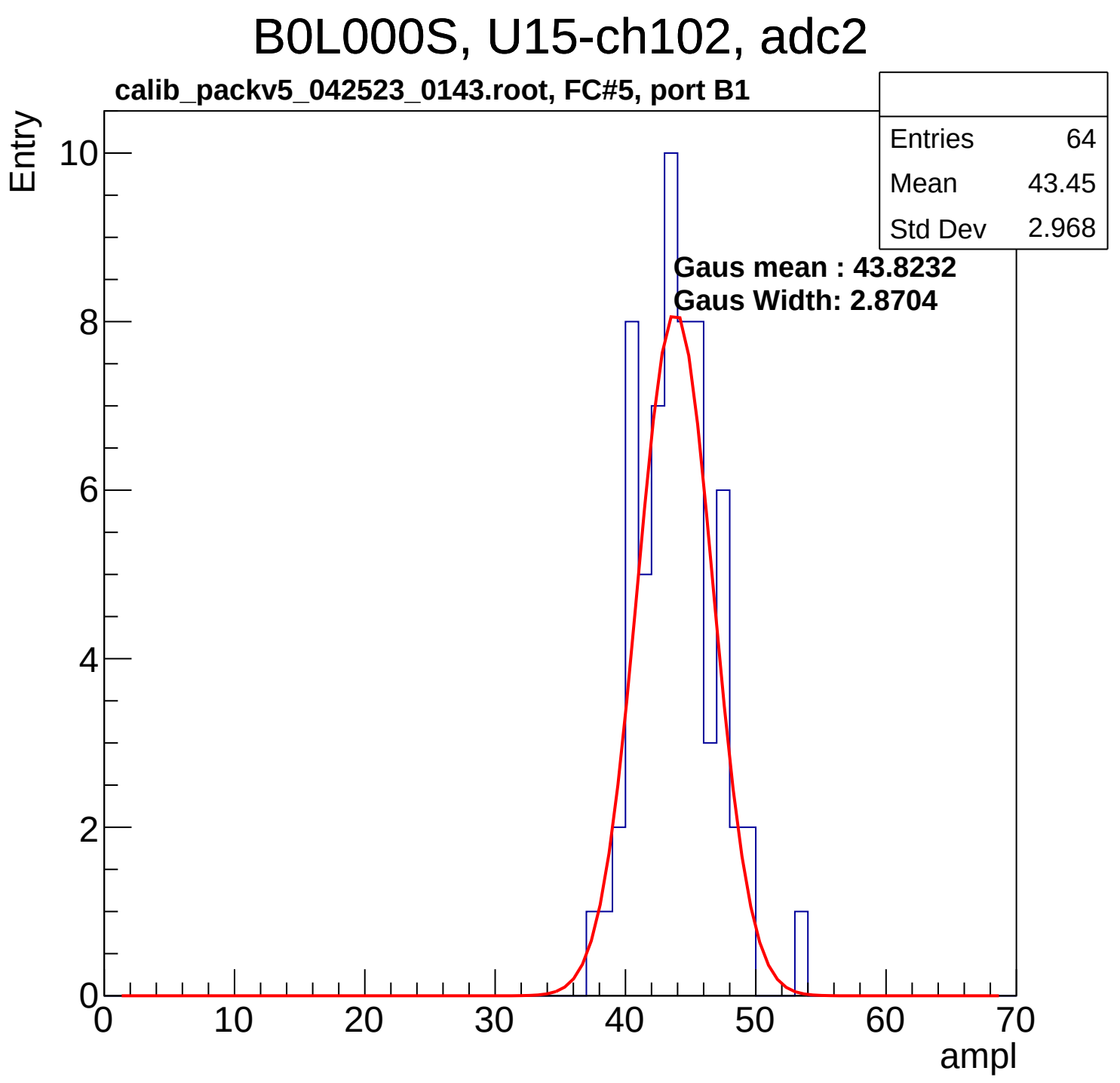
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

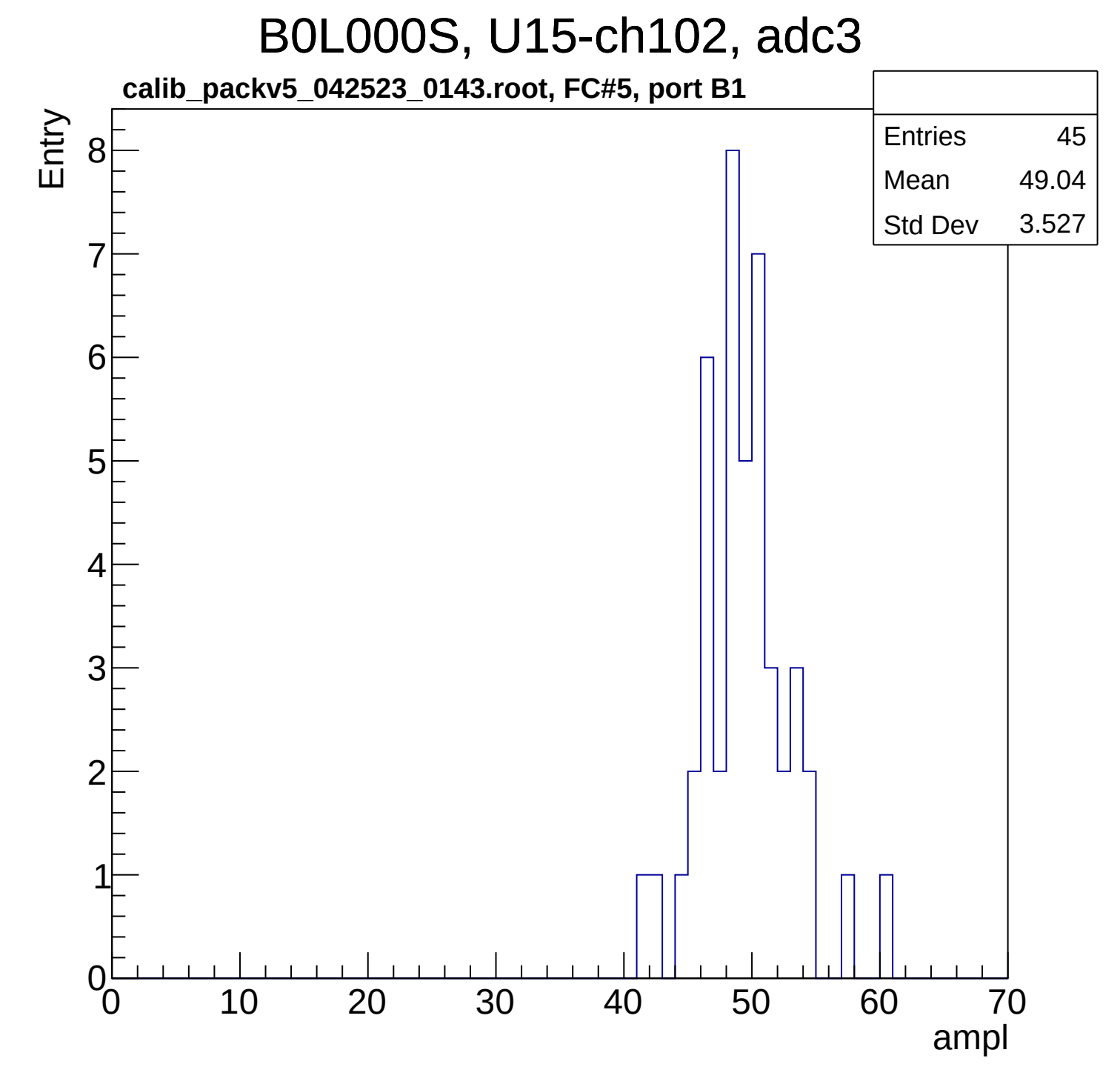

B0L000S, U15-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.38
Std Dev 14.89
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.49
Std Dev 4.634
Gaus mean : 29.6367
Gaus Width: 3.2048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.26
Std Dev 3.012
Gaus mean : 36.7148
Gaus Width: 2.9874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.45
Std Dev 2.968
Gaus mean : 43.8232
Gaus Width: 2.8704
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 49.04
Std Dev 3.527
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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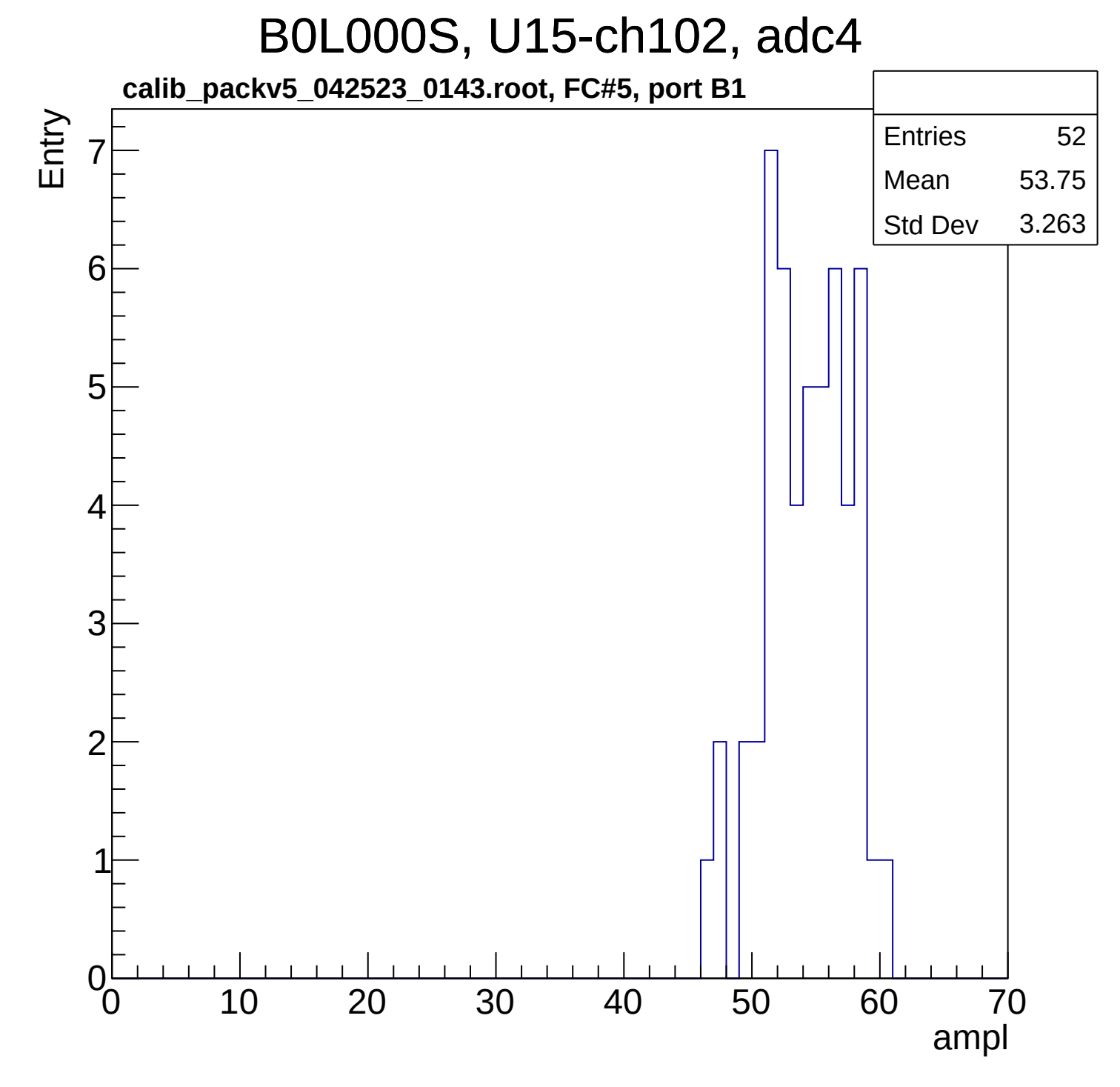
B0L000S, U15-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.75
Std Dev 3.263
Entry
ampl
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2
3
4
5
6
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0
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20
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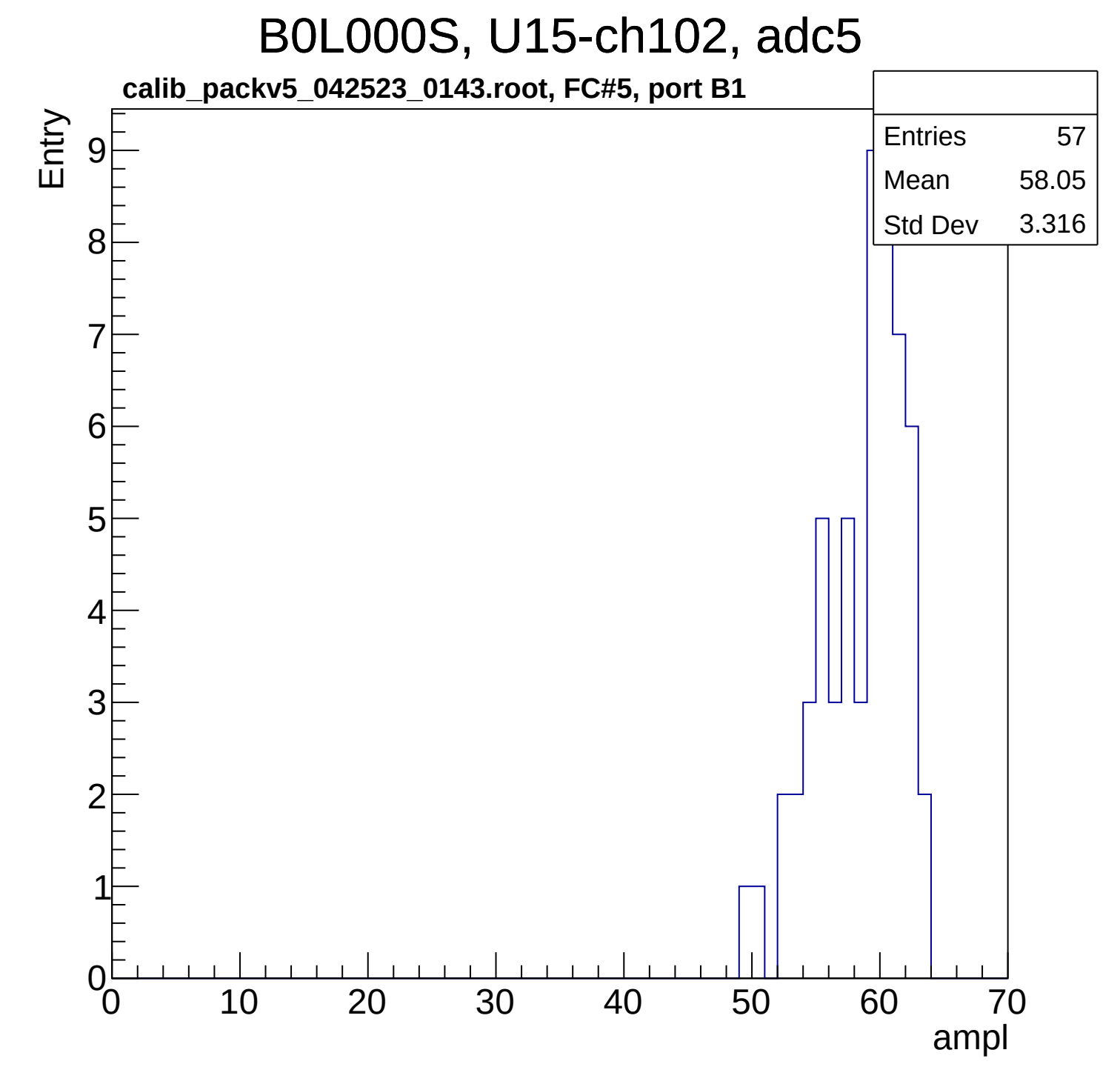
40
50
60
70


B0L000S, U15-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.05
Std Dev 3.316
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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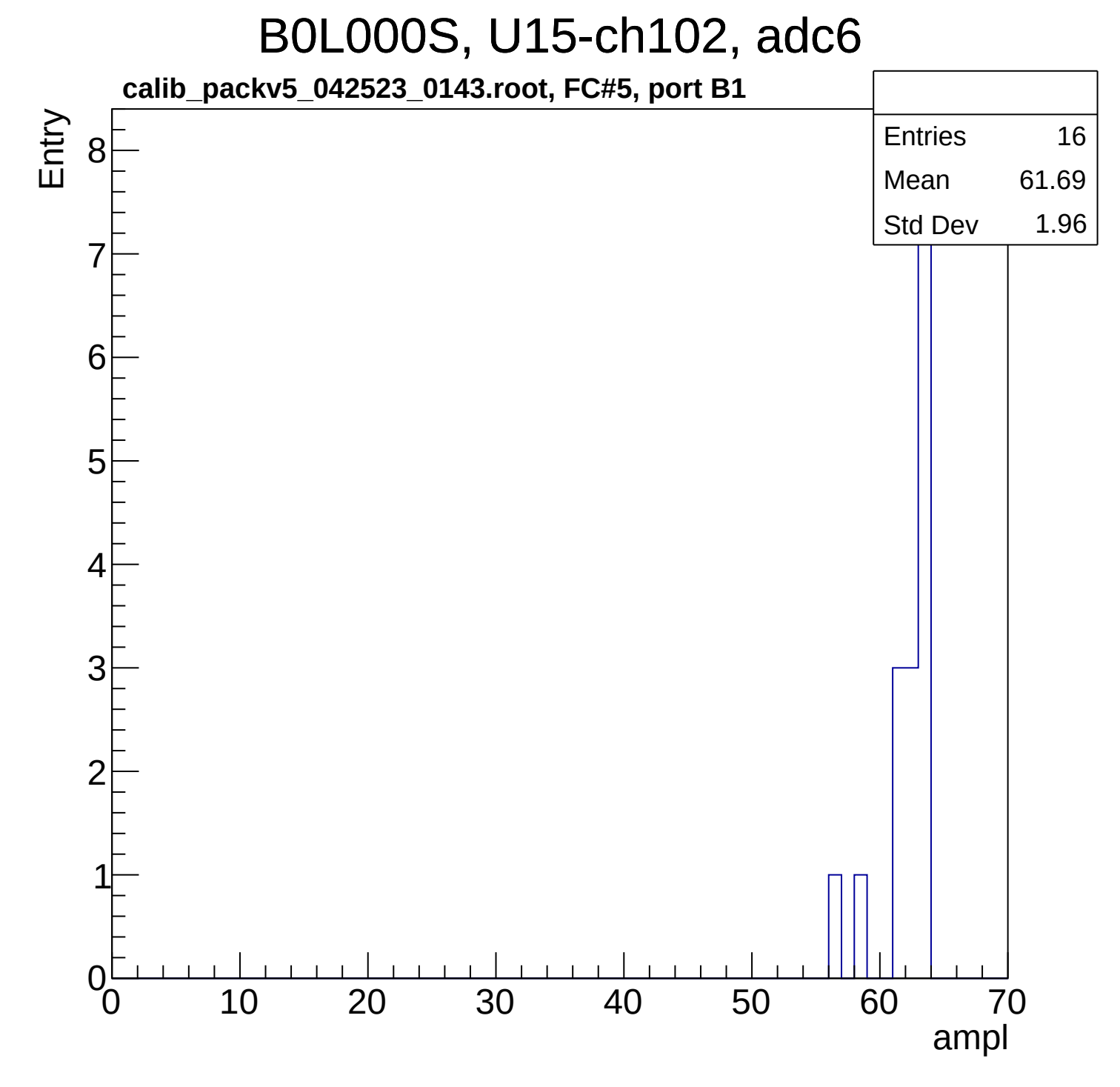
B0L000S, U15-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.69
Std Dev 1.96
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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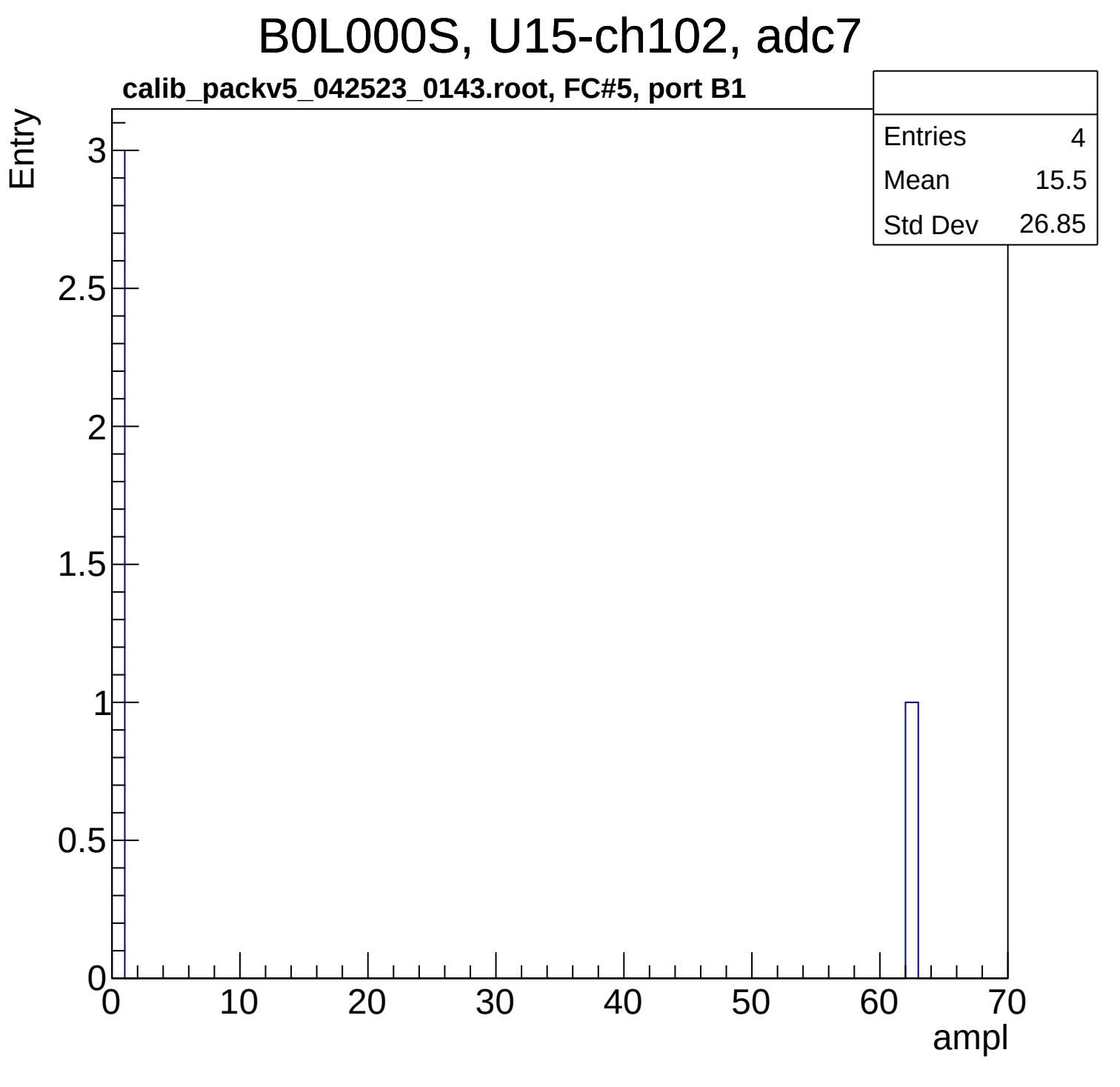
B0L000S, U15-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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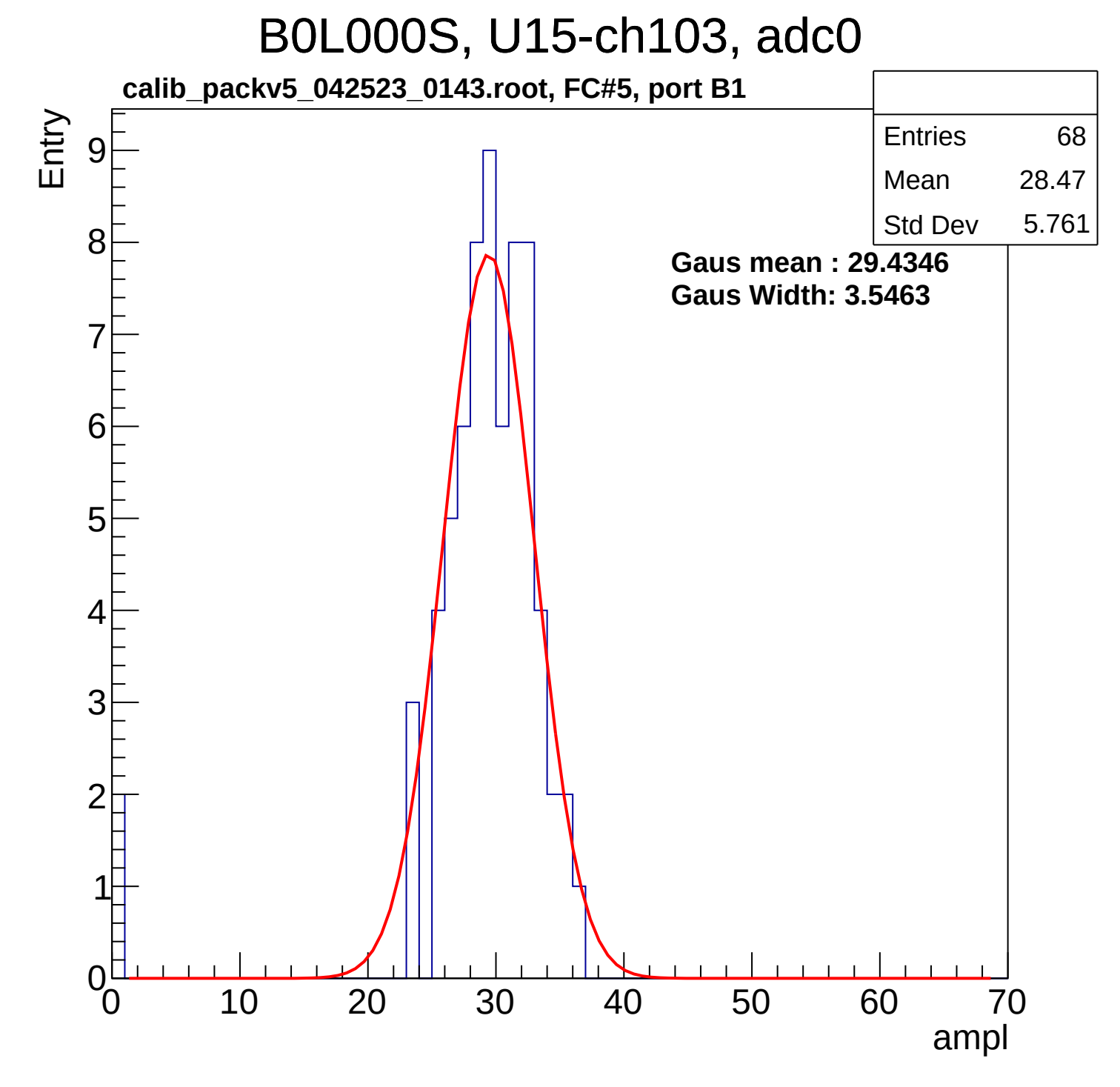
40
50
60
70


B0L000S, U15-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.47
Std Dev 5.761
Gaus mean : 29.4346
Gaus Width: 3.5463
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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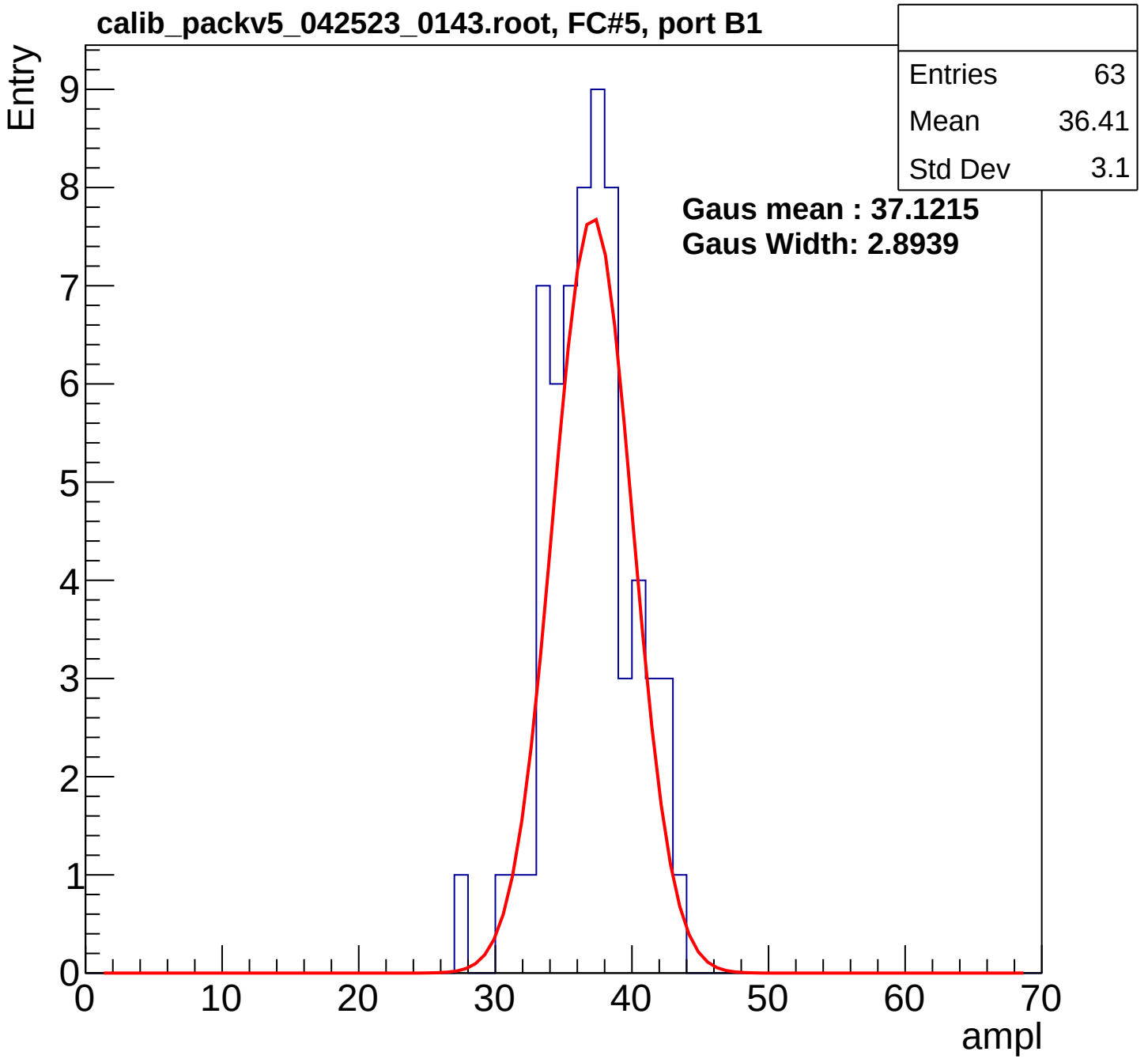
60
70


B0L000S, U15-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.41
Std Dev 3.1
Gaus mean : 37.1215
Gaus Width: 2.8939
Entry
ampl
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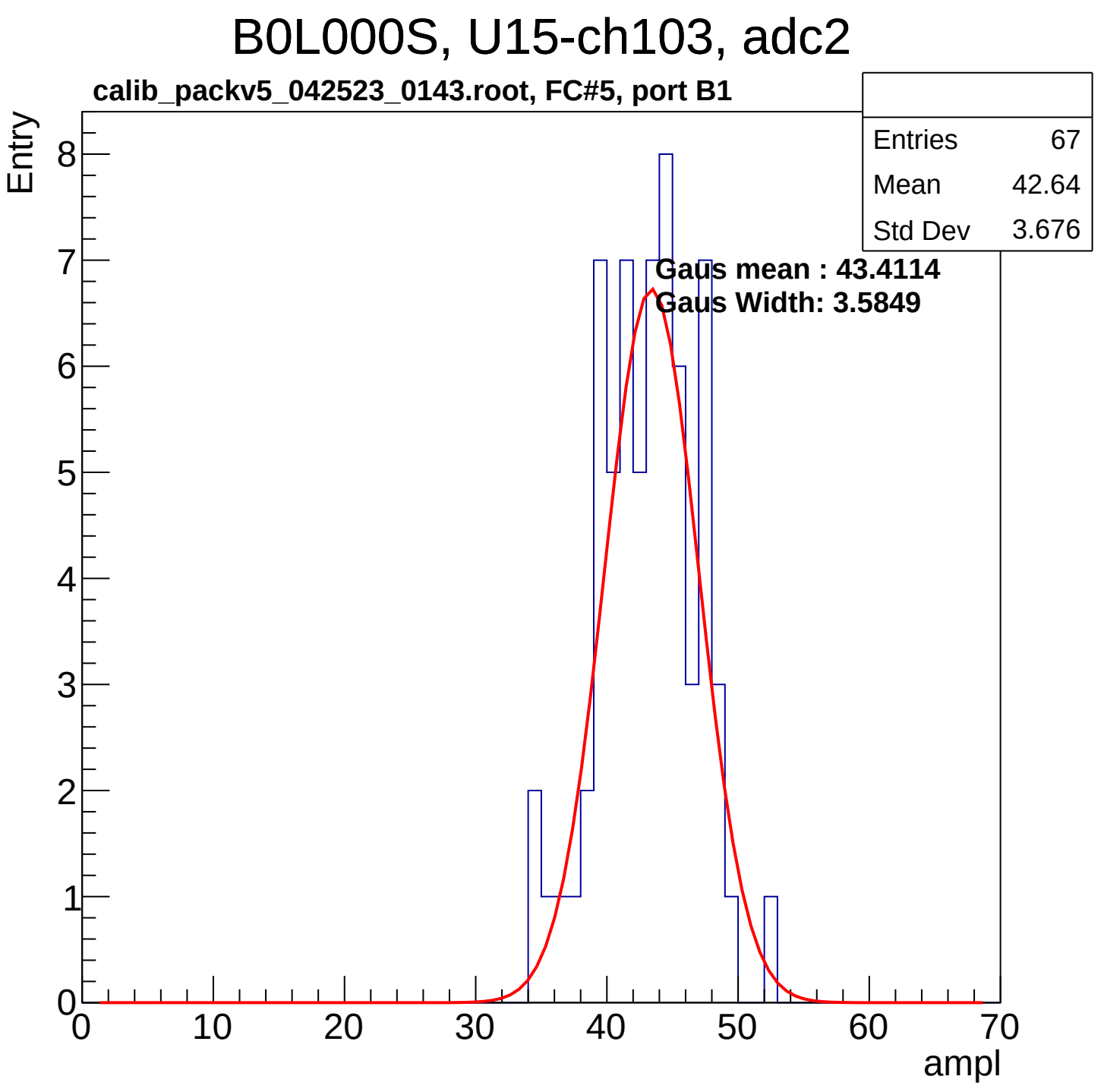
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.64
Std Dev 3.676
Gaus mean : 43.4114
Gaus Width: 3.5849
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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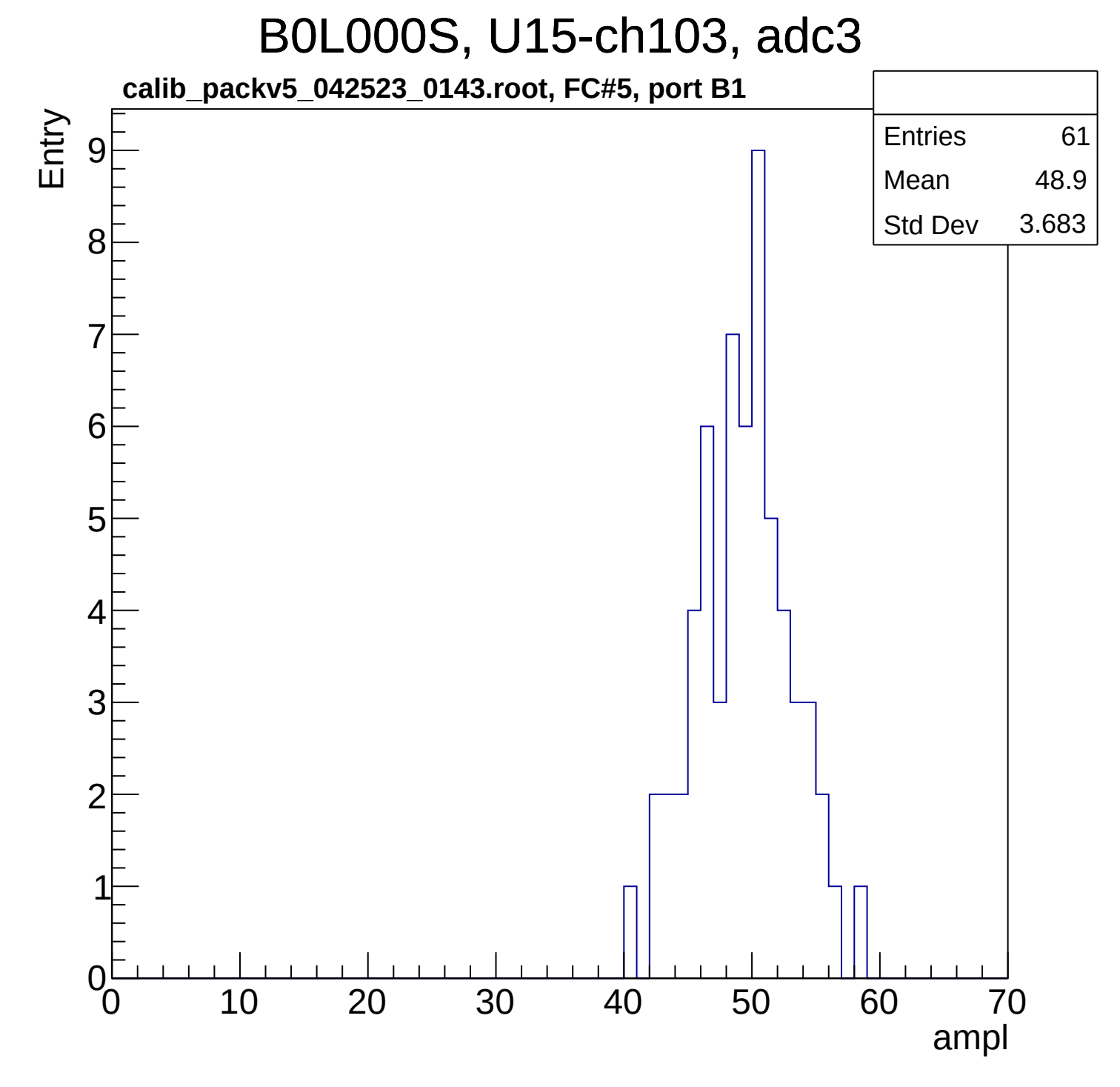
B0L000S, U15-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.9
Std Dev 3.683
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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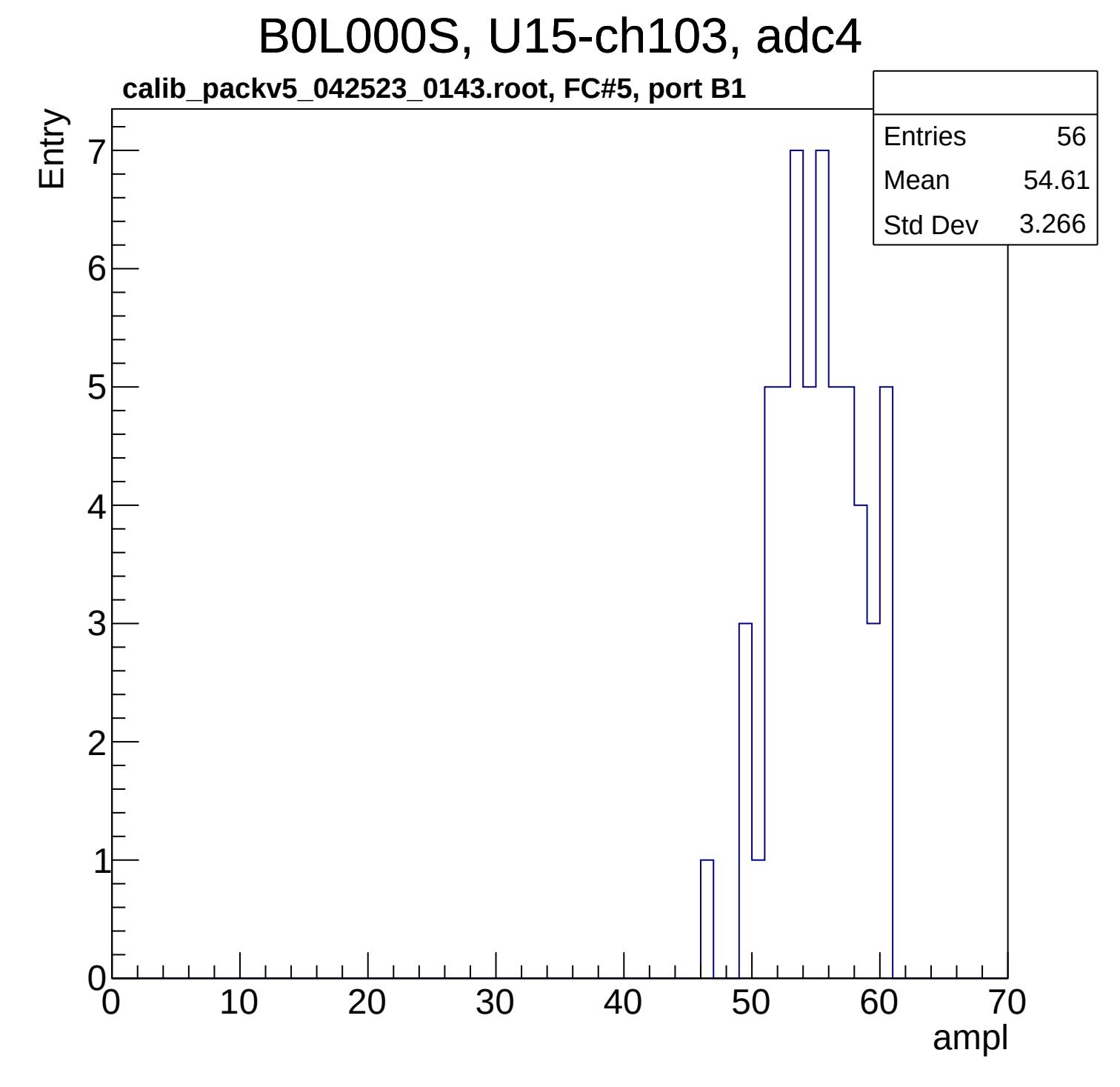
B0L000S, U15-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.61
Std Dev 3.266
Entry
ampl
0
1
2
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4
5
6
7
0
10
20
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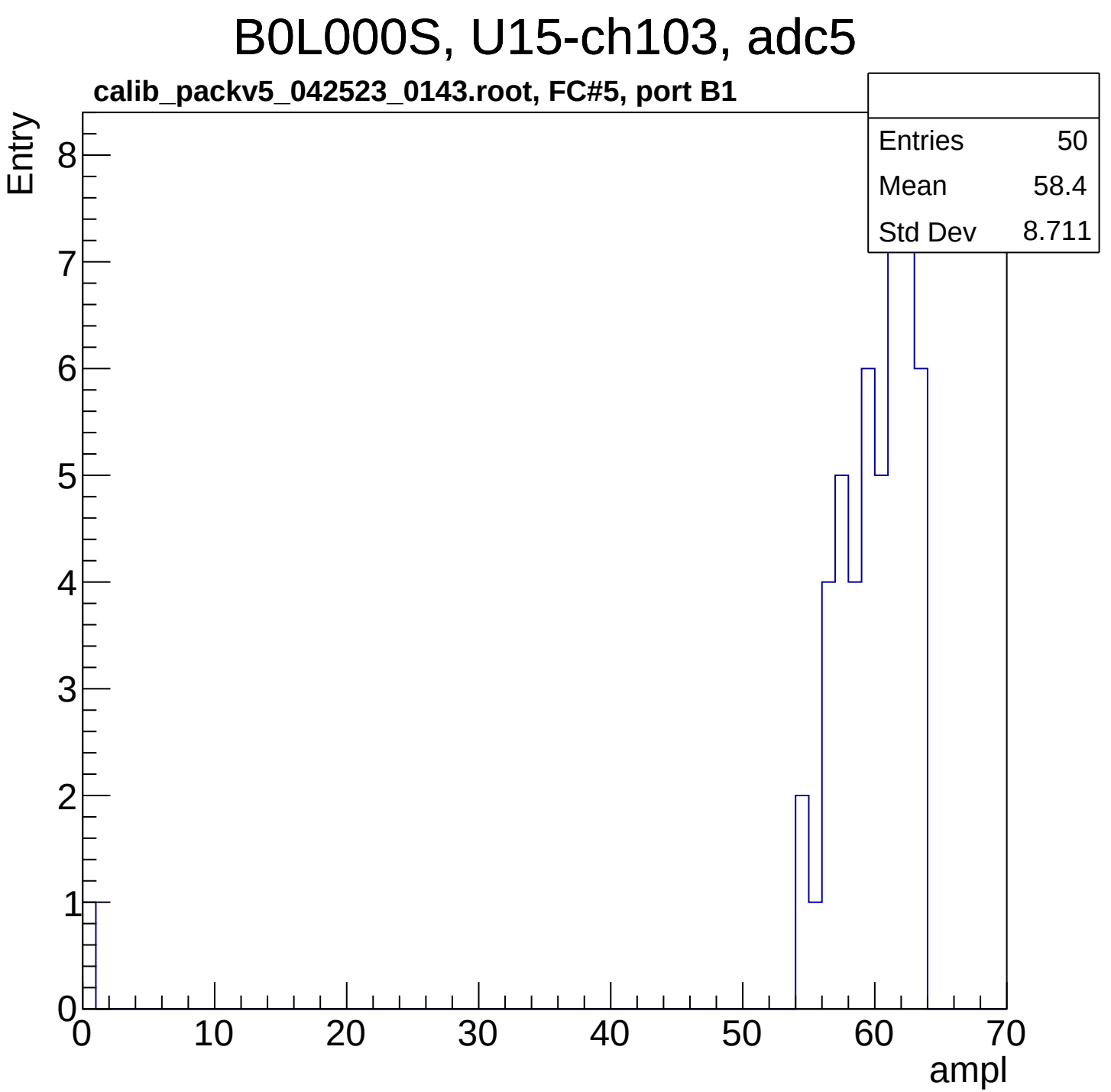
40
50
60
70


B0L000S, U15-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.4
Std Dev 8.711
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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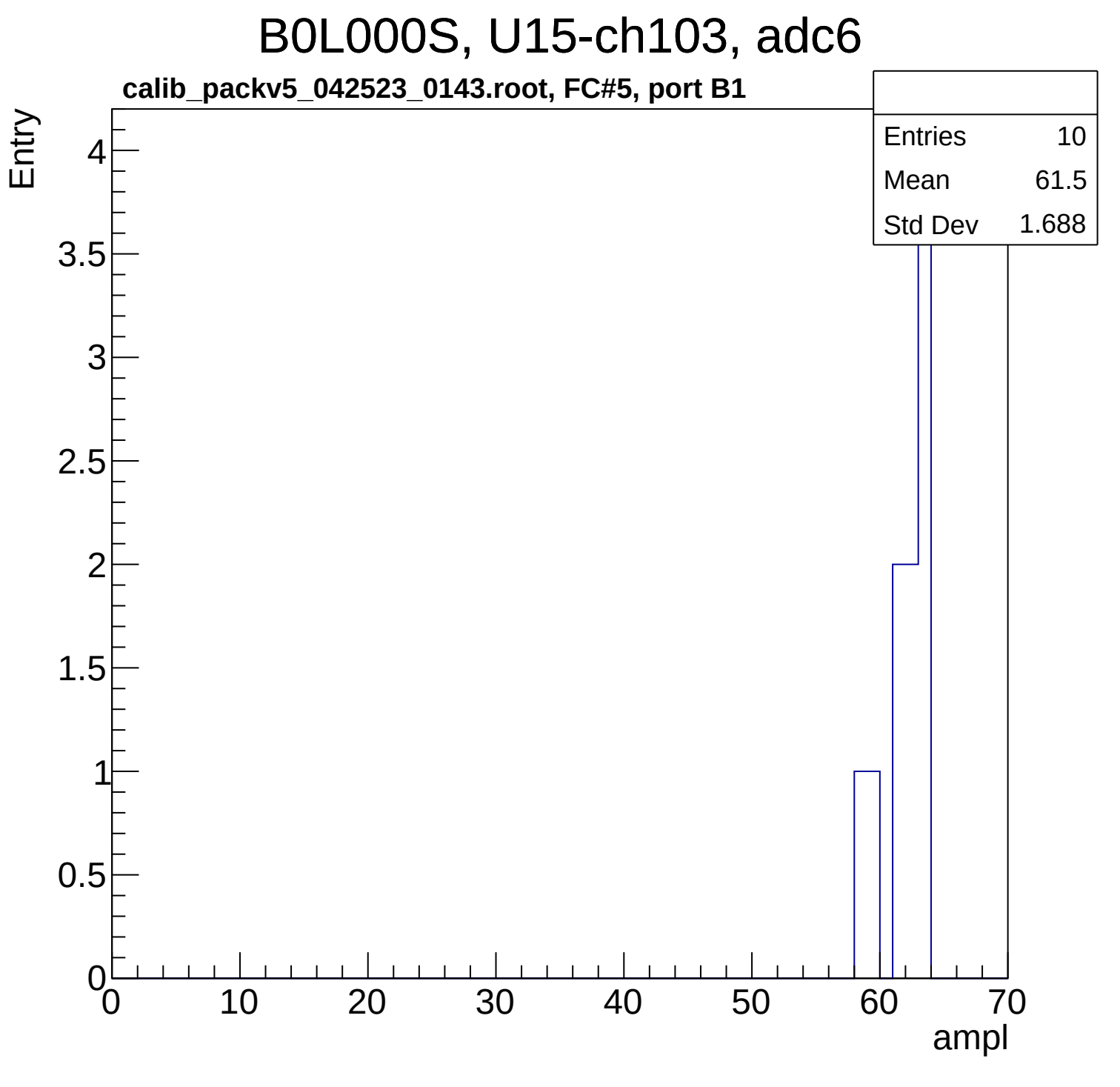
B0L000S, U15-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 1.688
Entry
ampl
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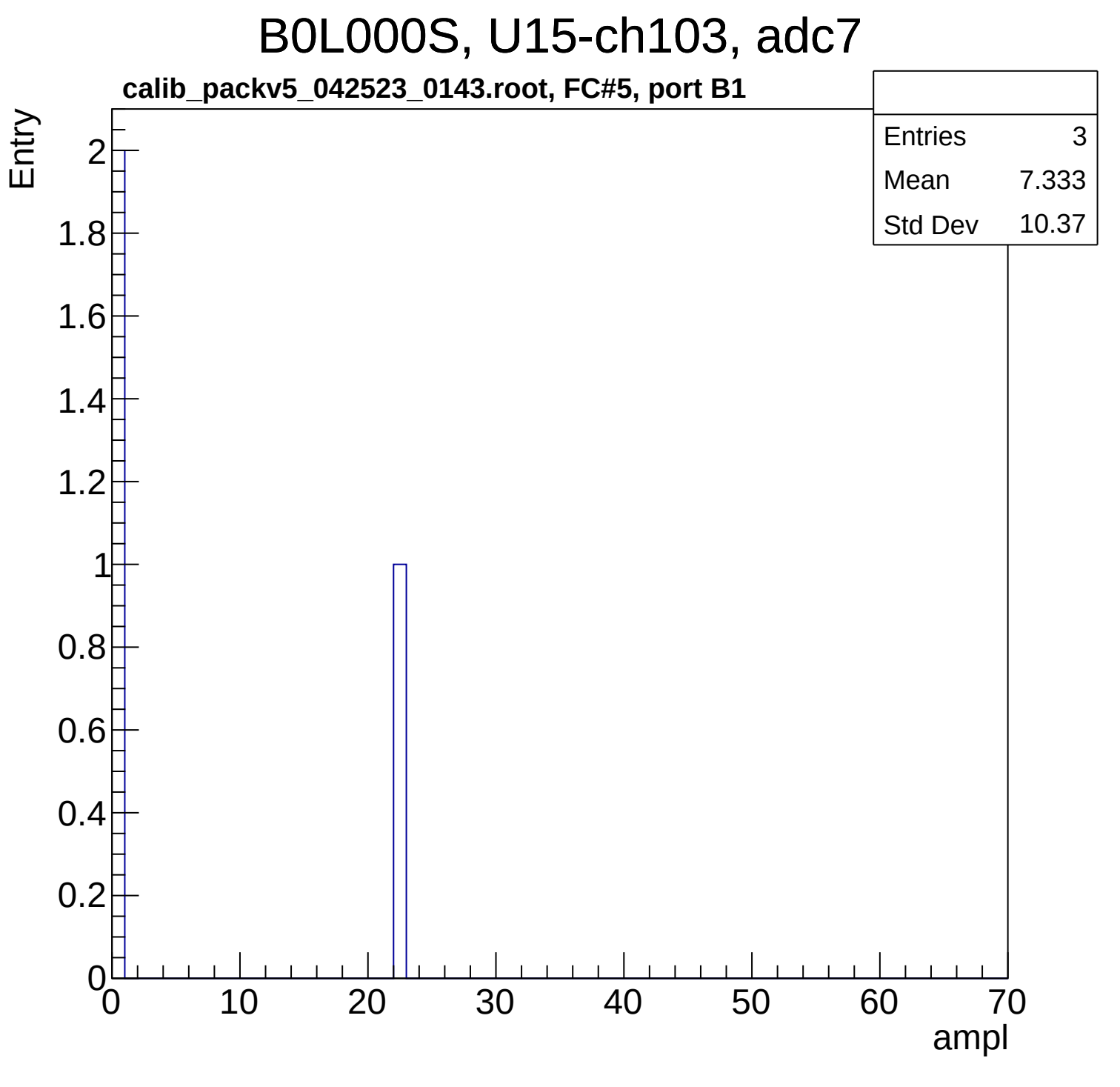
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U15-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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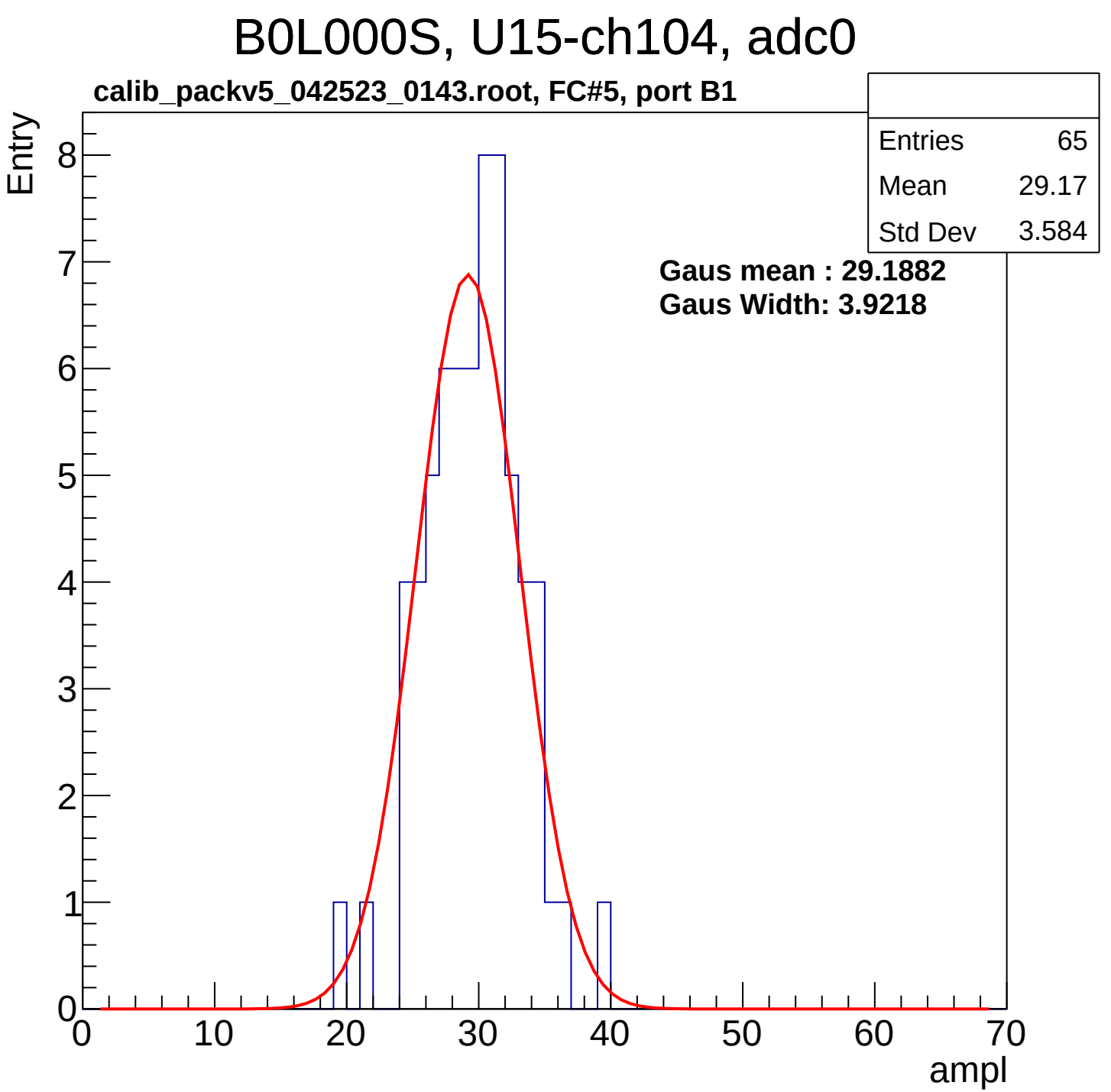
B0L000S, U15-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.17
Std Dev 3.584
Gaus mean : 29.1882
Gaus Width: 3.9218
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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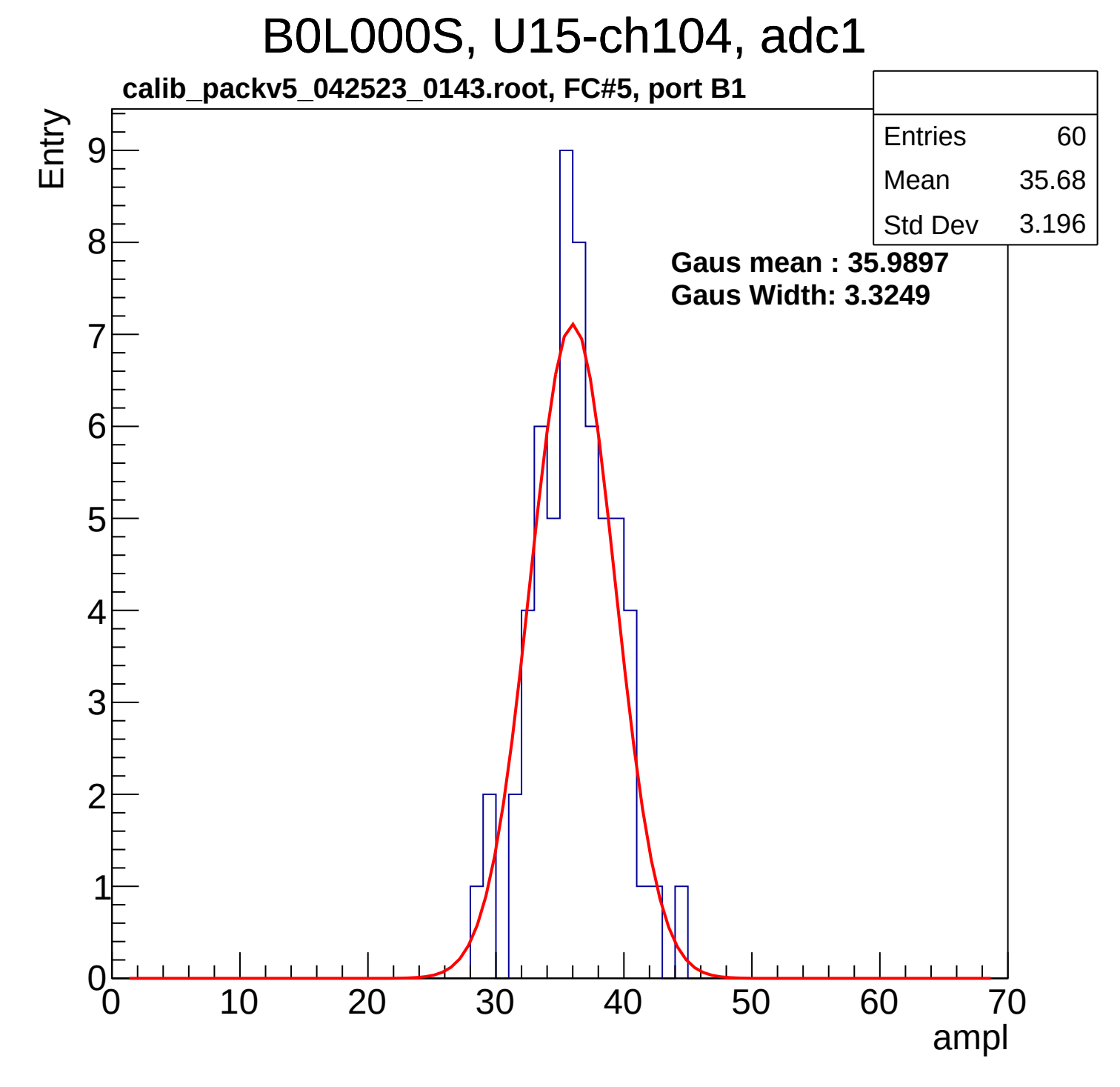
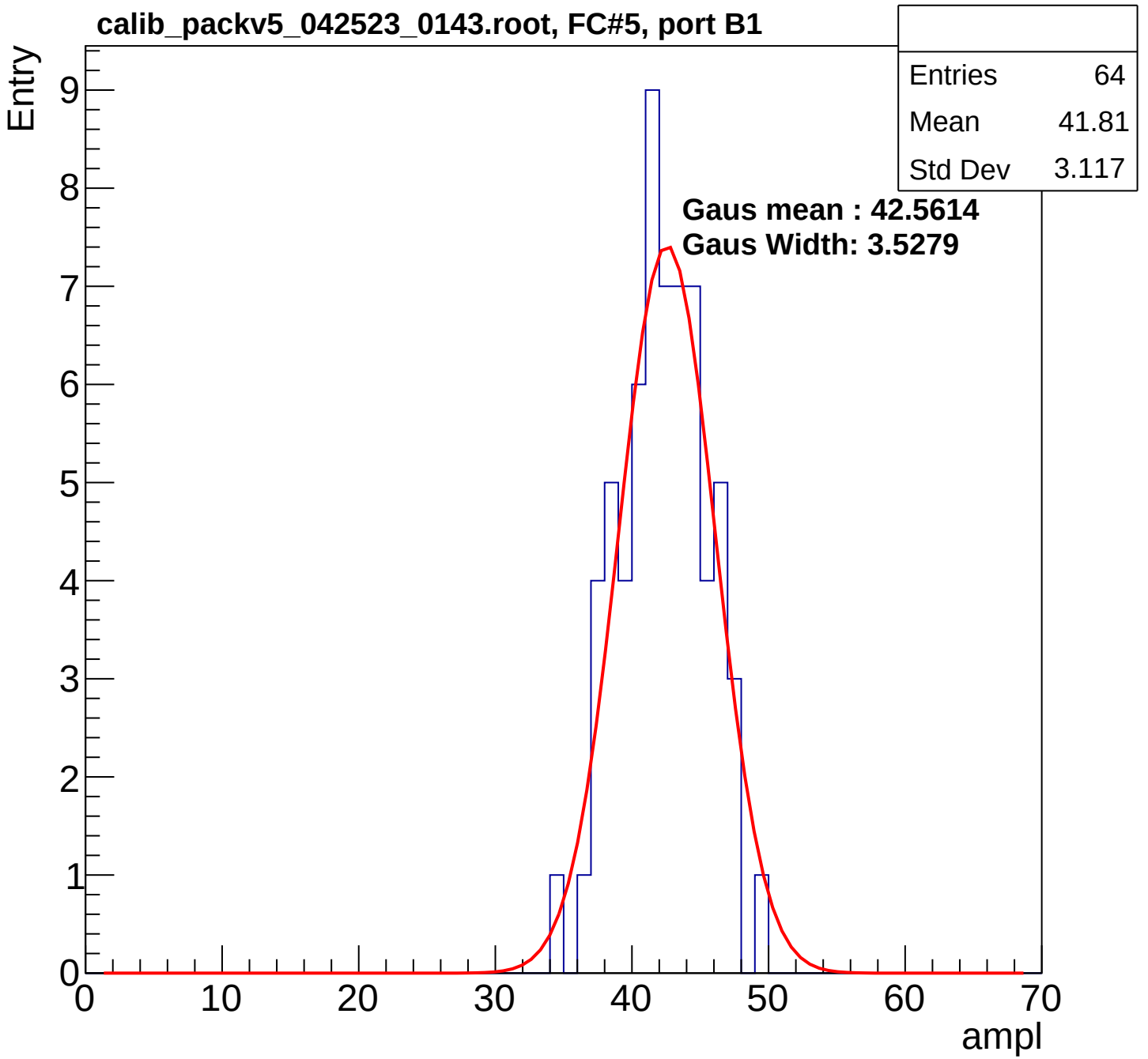

B0L000S, U15-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.68
Std Dev 3.196
Gaus mean : 35.9897
Gaus Width: 3.3249
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 41.81
Std Dev 3.117
Gaus mean : 42.5614
Gaus Width: 3.5279
Entry
ampl
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2
3
4
5
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0
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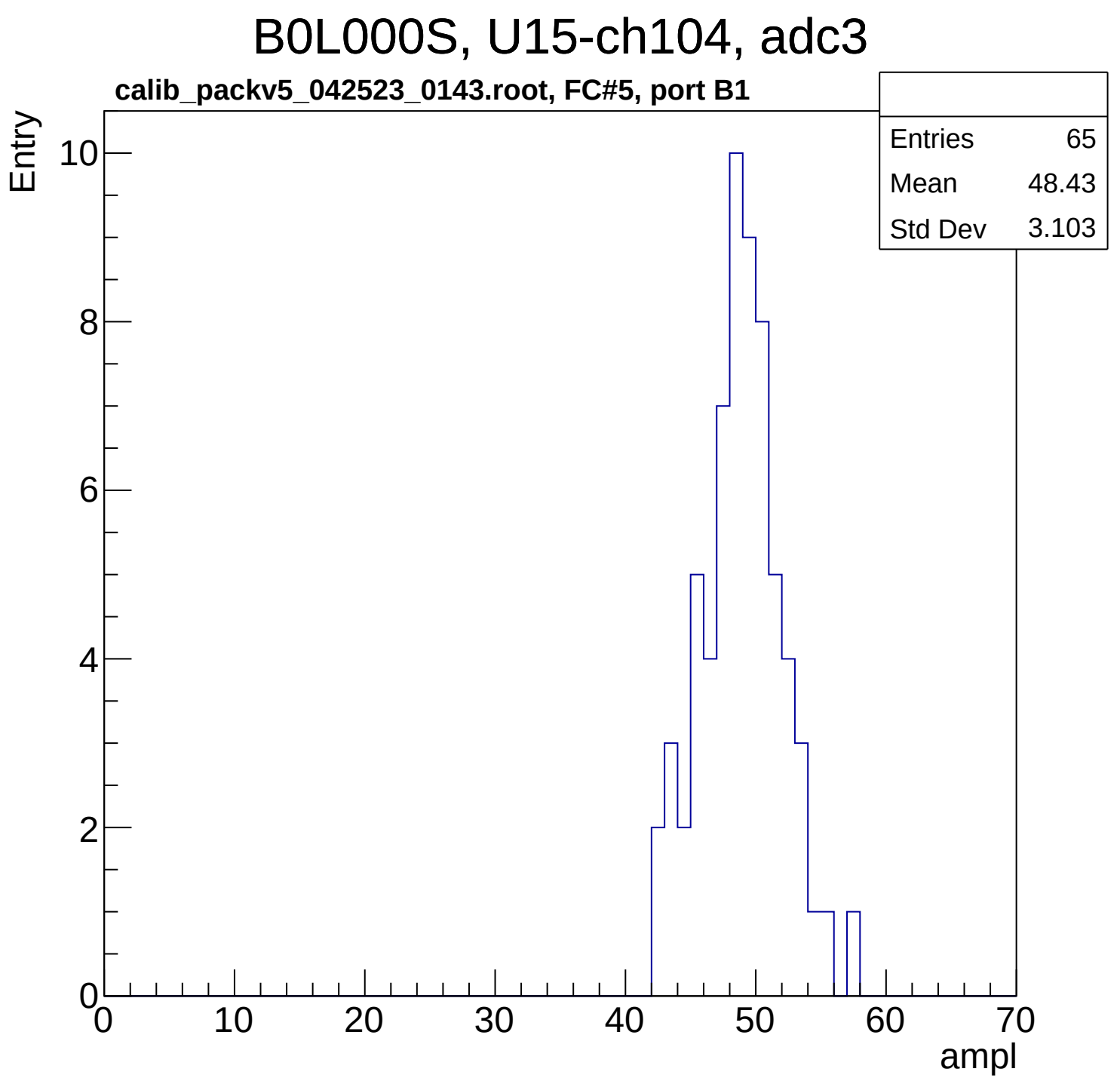
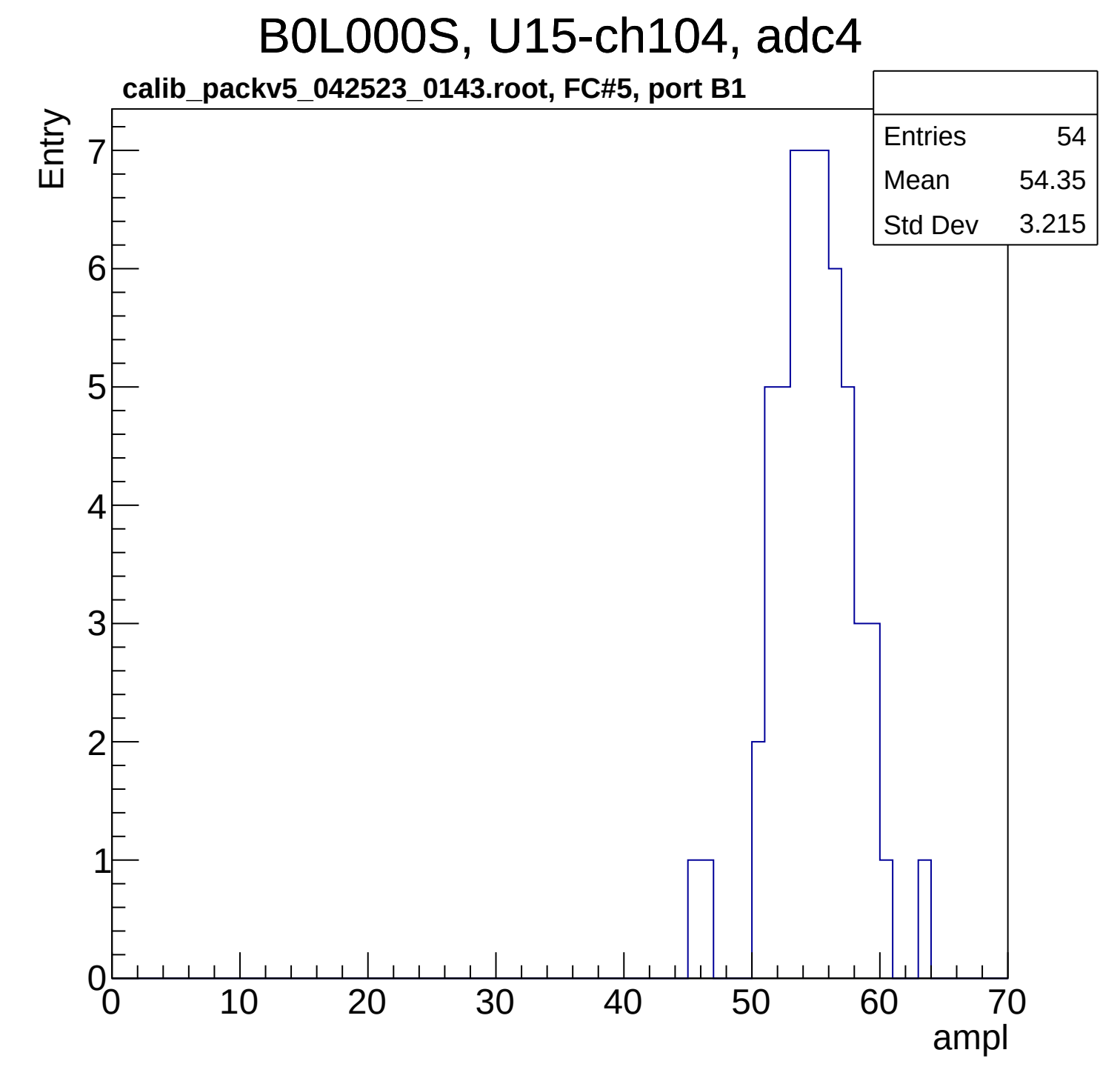
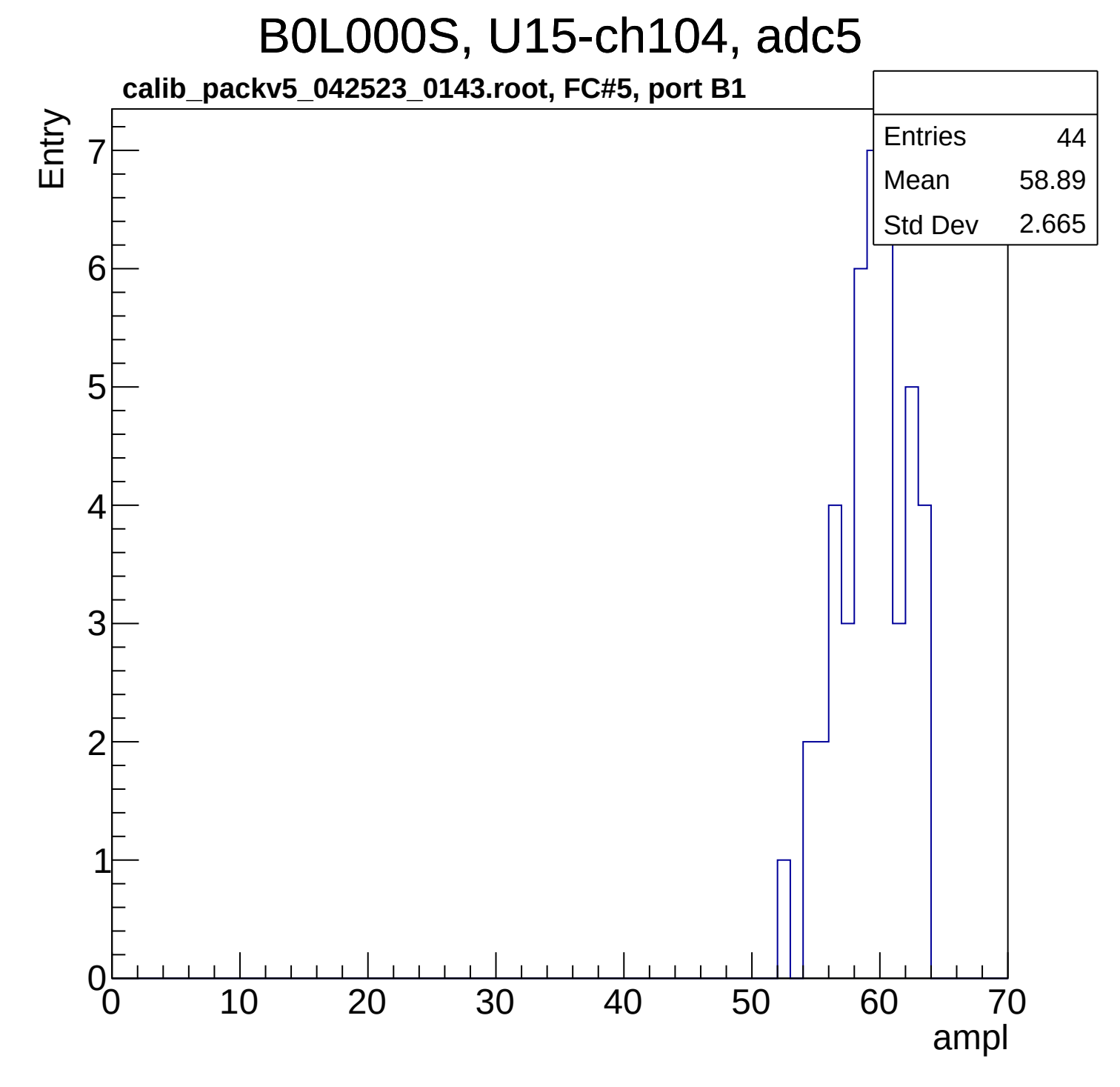

B0L000S, U15-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 48.43
Std Dev 3.103
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.35
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.89
Std Dev 2.665
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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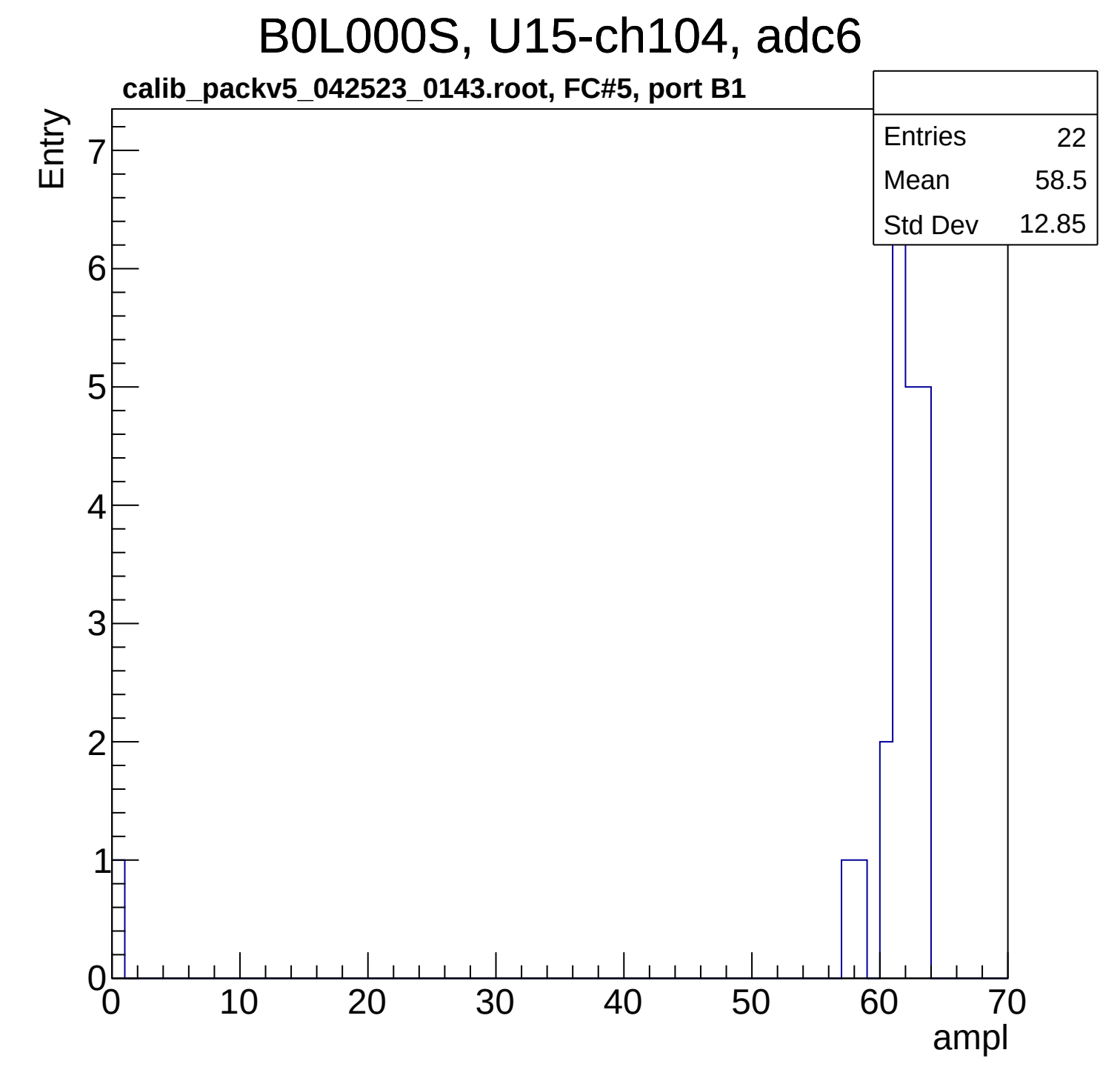
B0L000S, U15-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.5
Std Dev 12.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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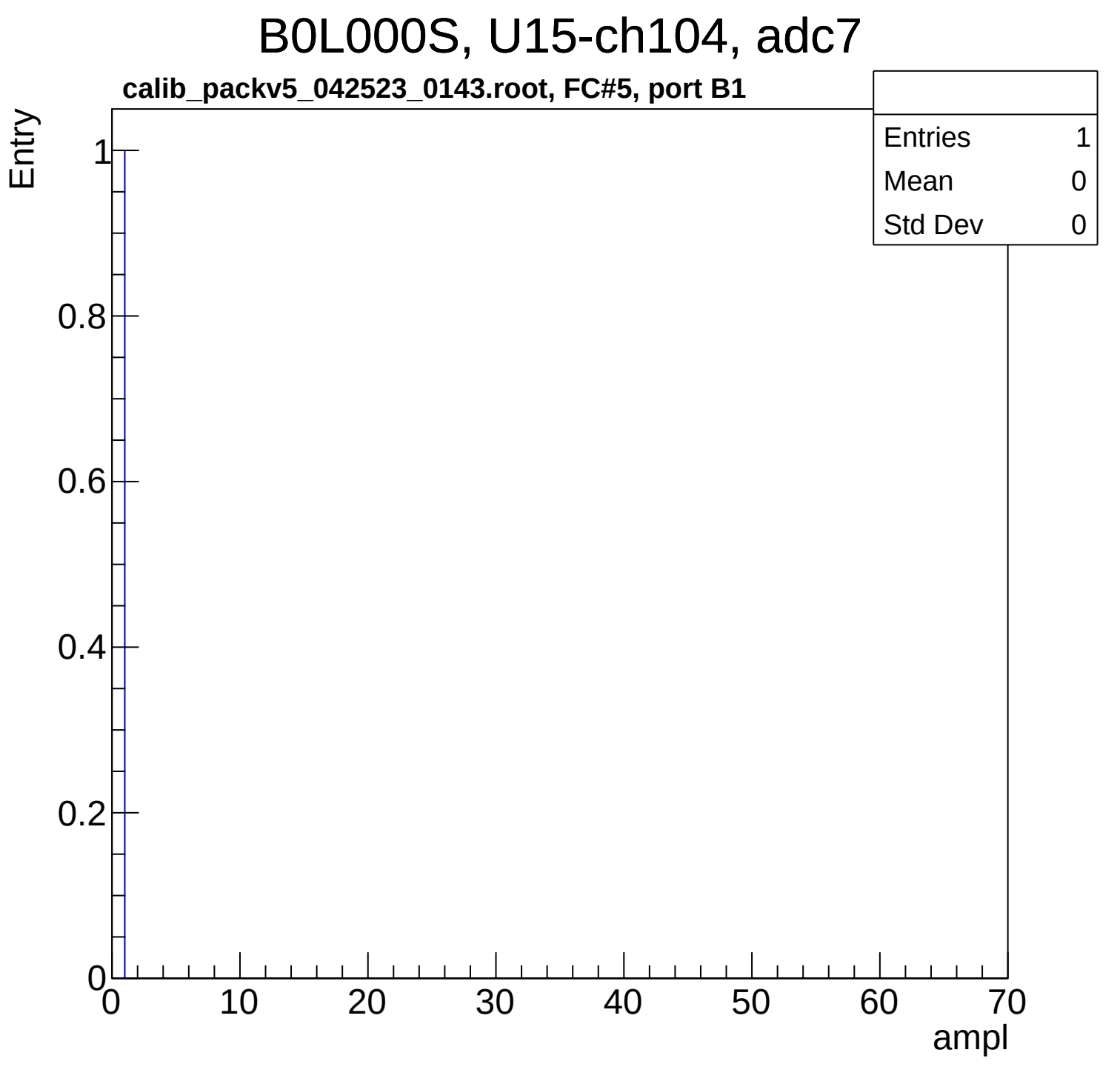
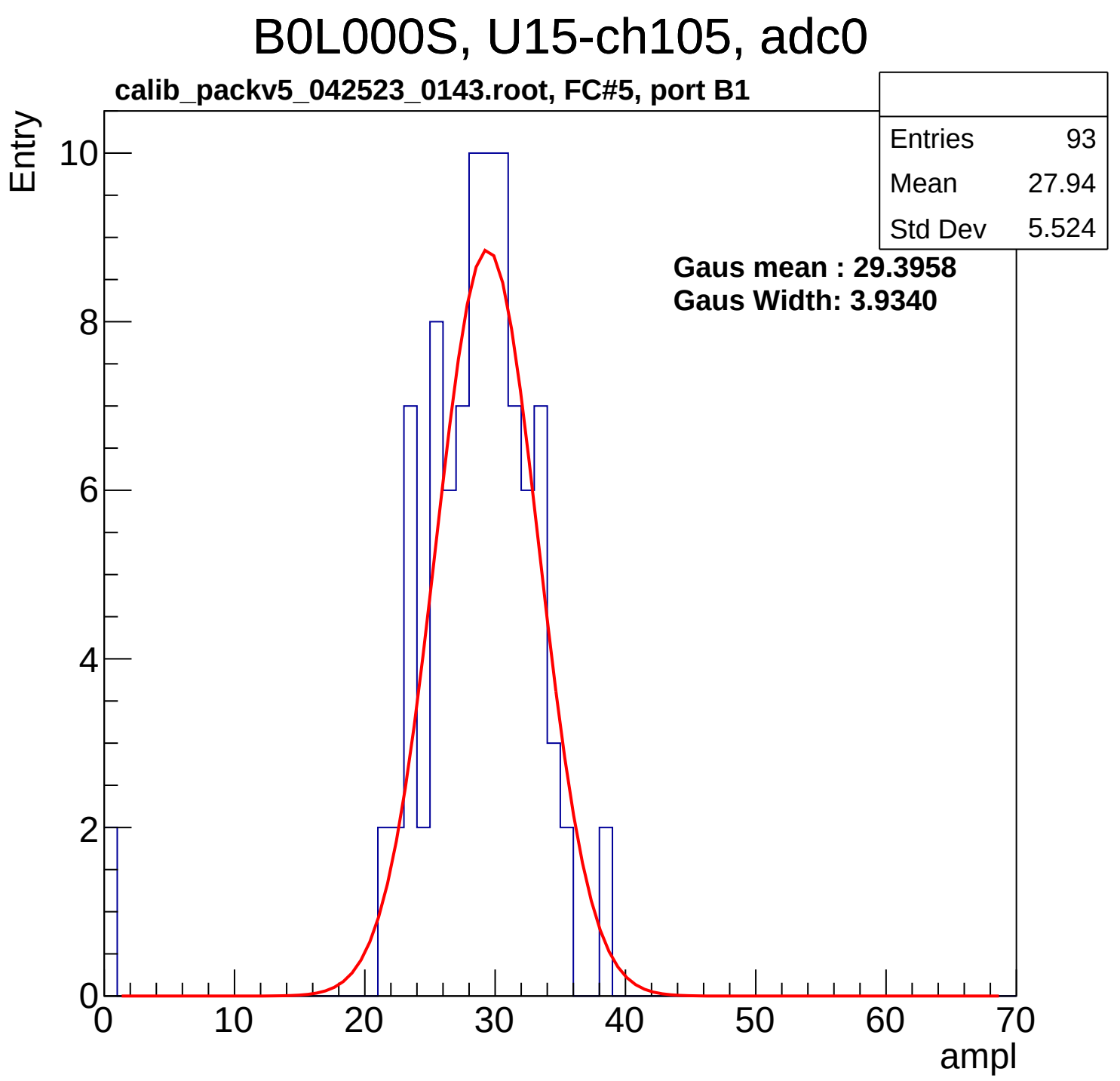
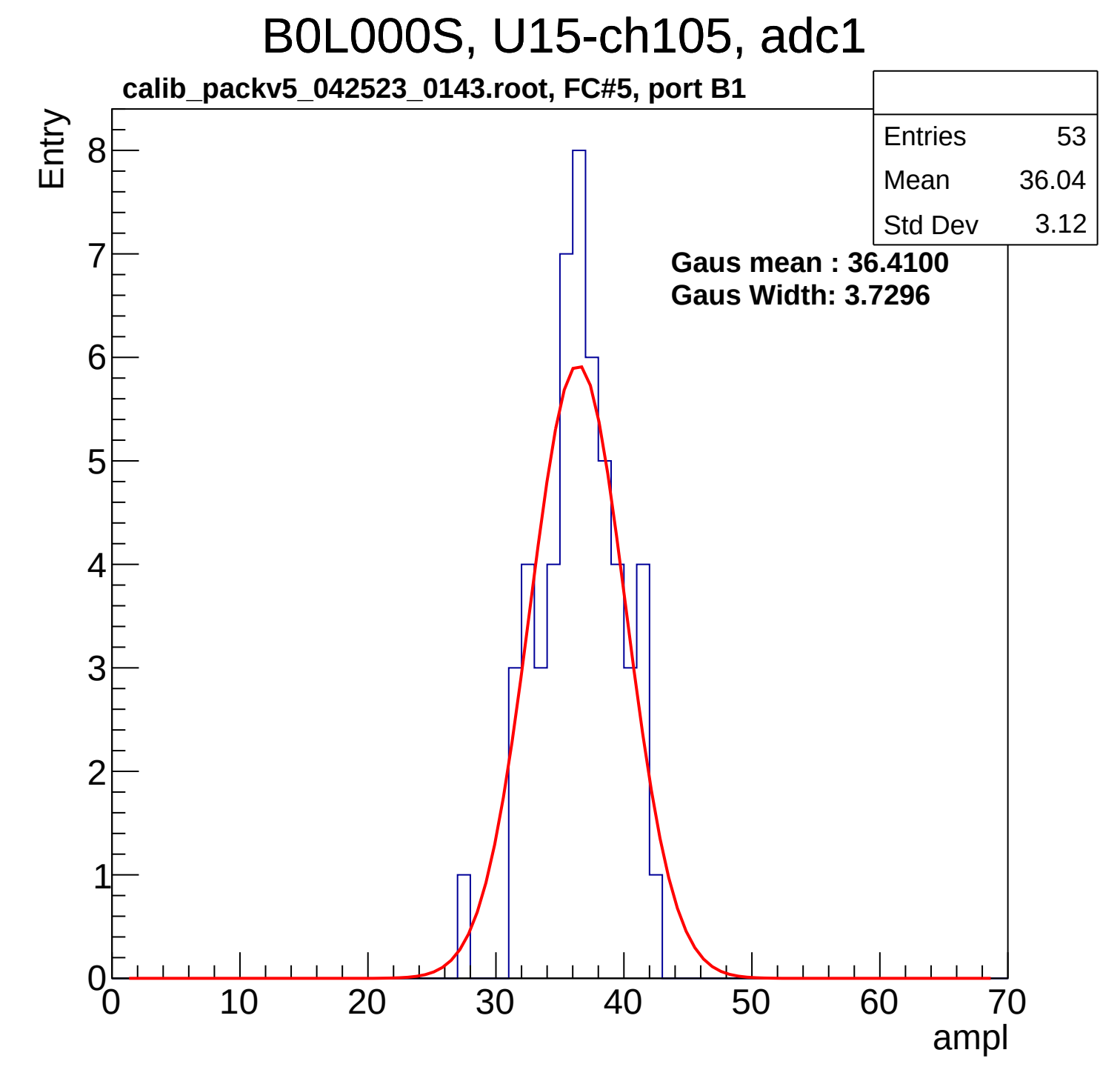

B0L000S, U15-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 93
Mean 27.94
Std Dev 5.524
Gaus mean : 29.3958
Gaus Width: 3.9340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 36.04
Std Dev 3.12
Gaus mean : 36.4100
Gaus Width: 3.7296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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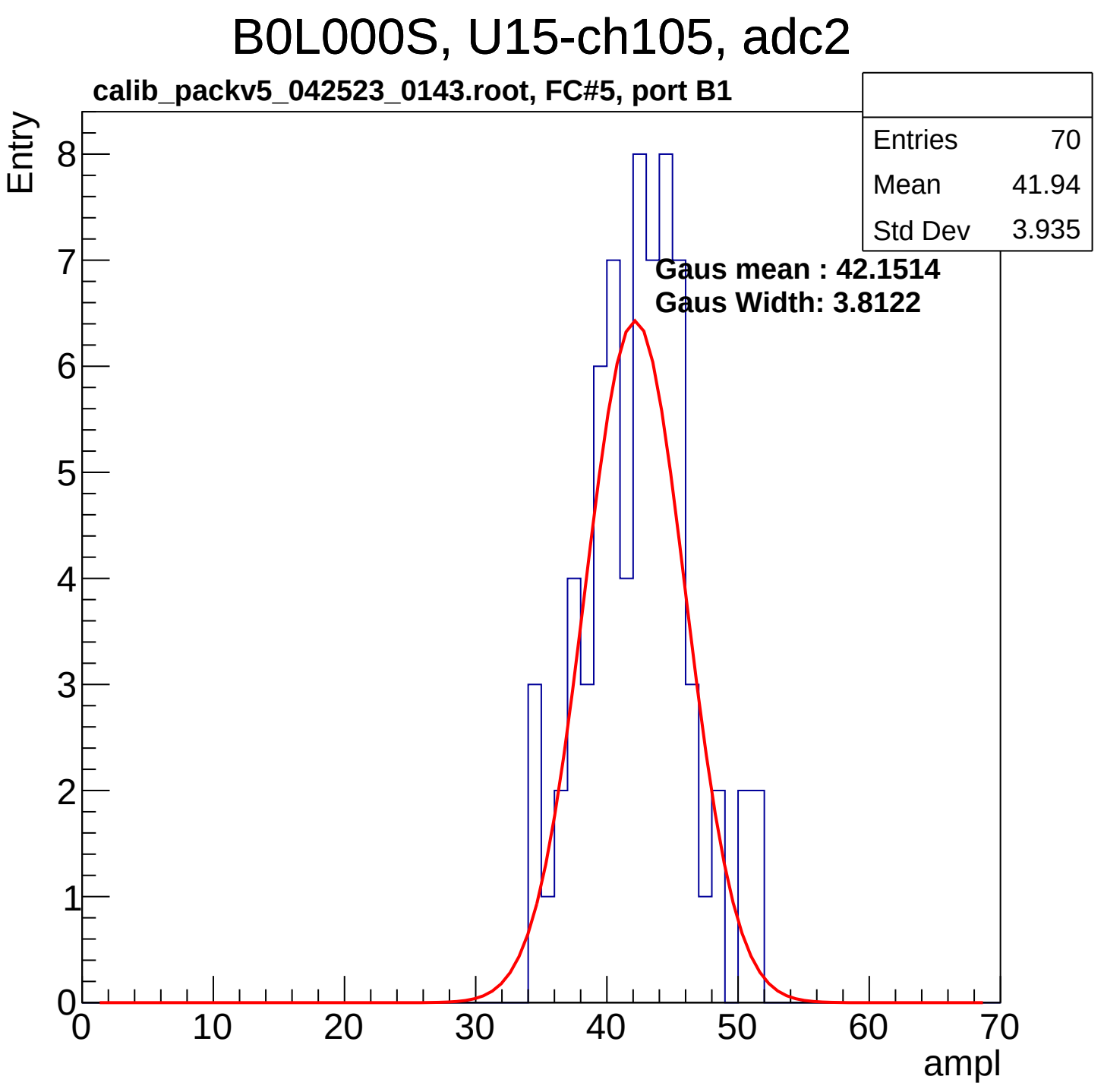
B0L000S, U15-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 41.94
Std Dev 3.935
Gaus mean : 42.1514
Gaus Width: 3.8122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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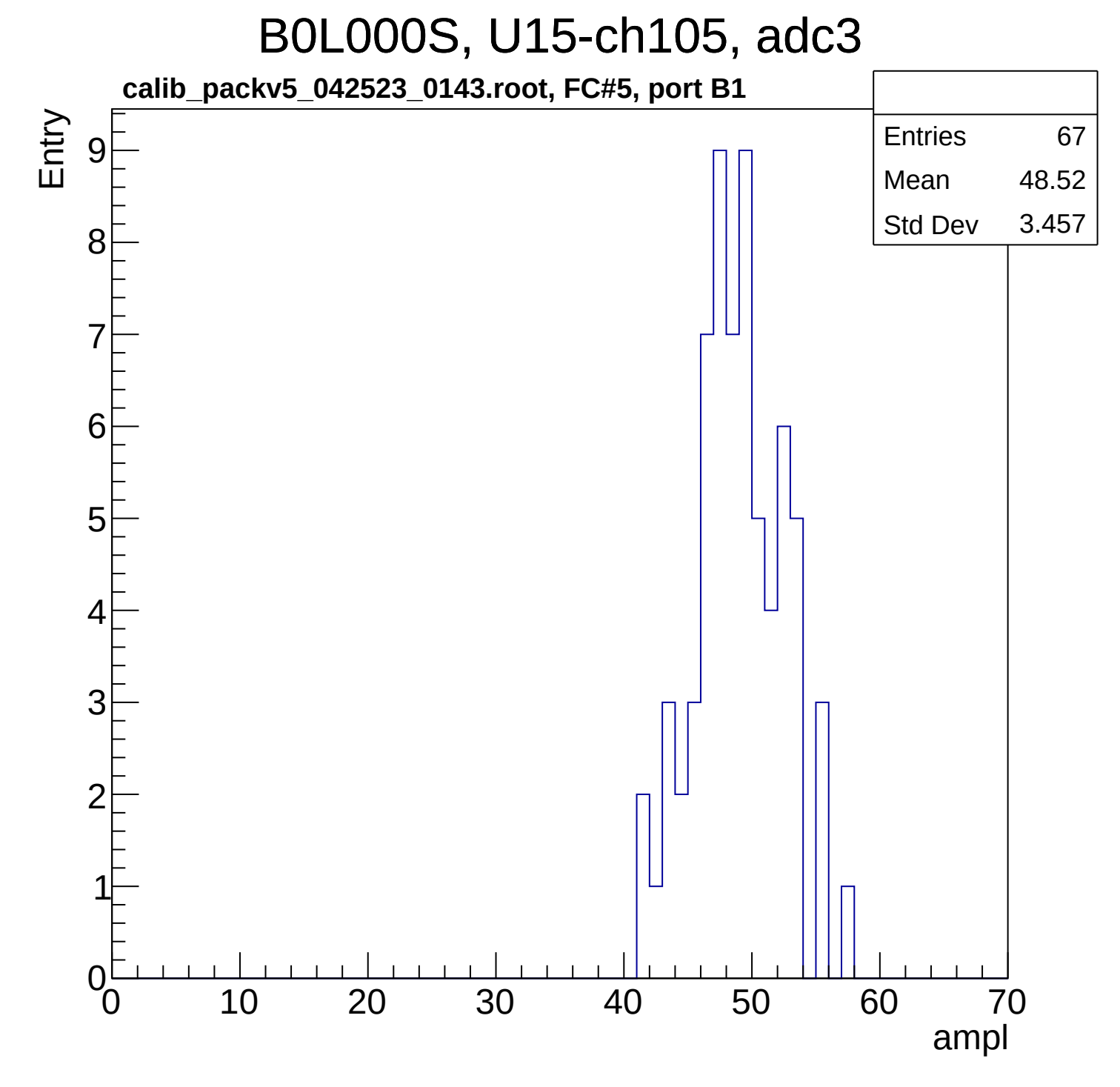
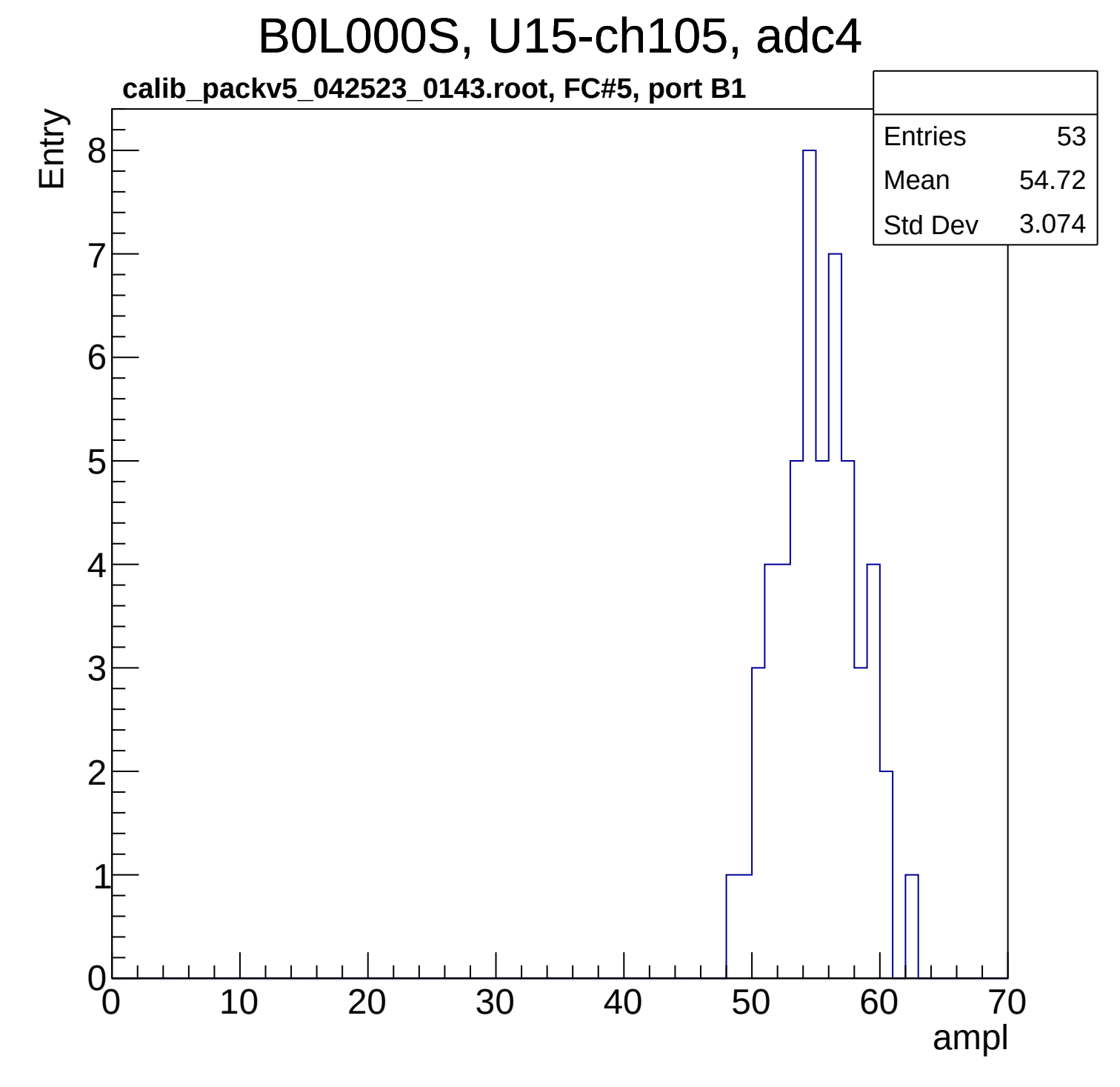
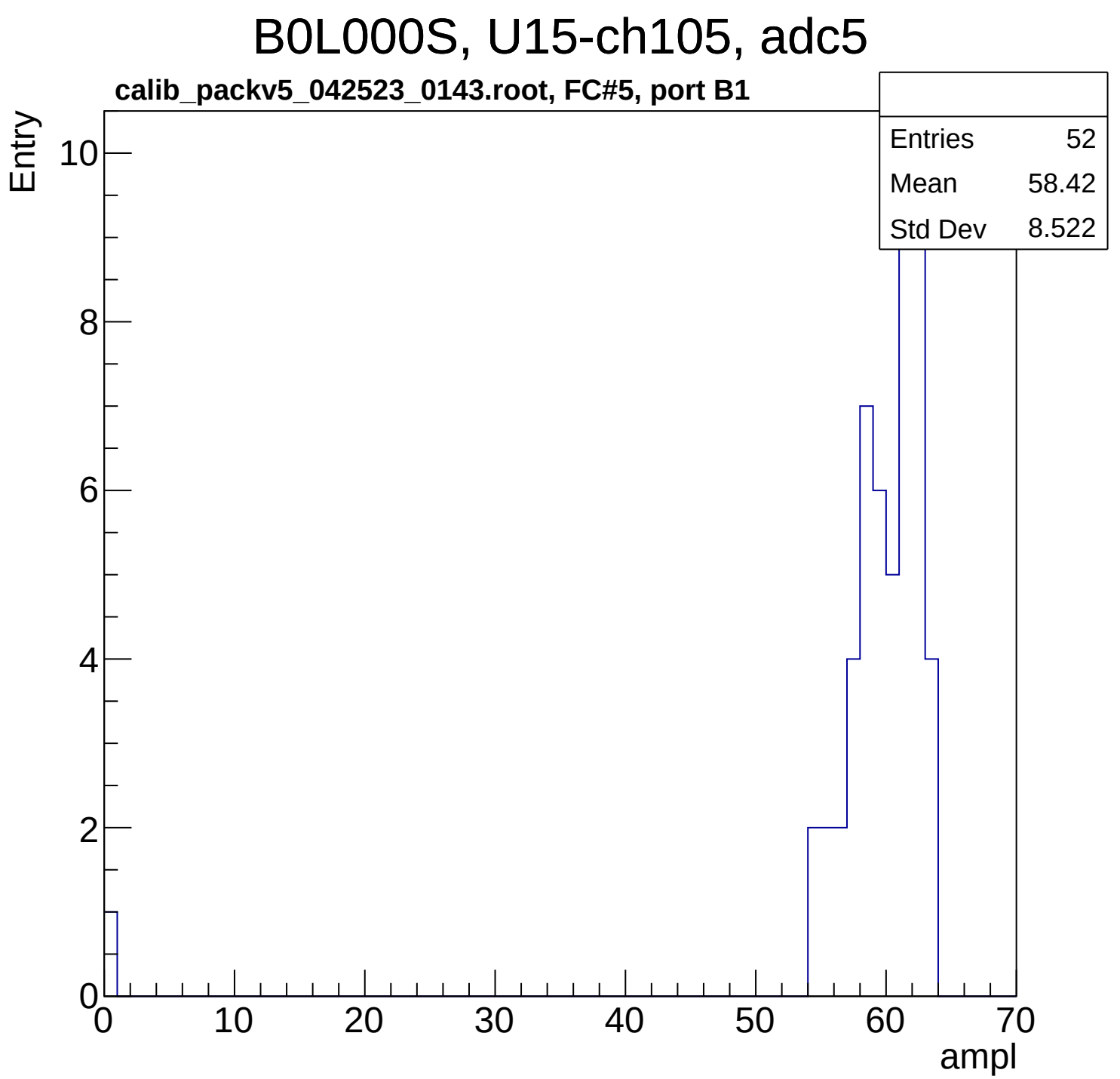

B0L000S, U15-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.52
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.72
Std Dev 3.074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.42
Std Dev 8.522
Entry
ampl
0
2
4
6
8
10
0
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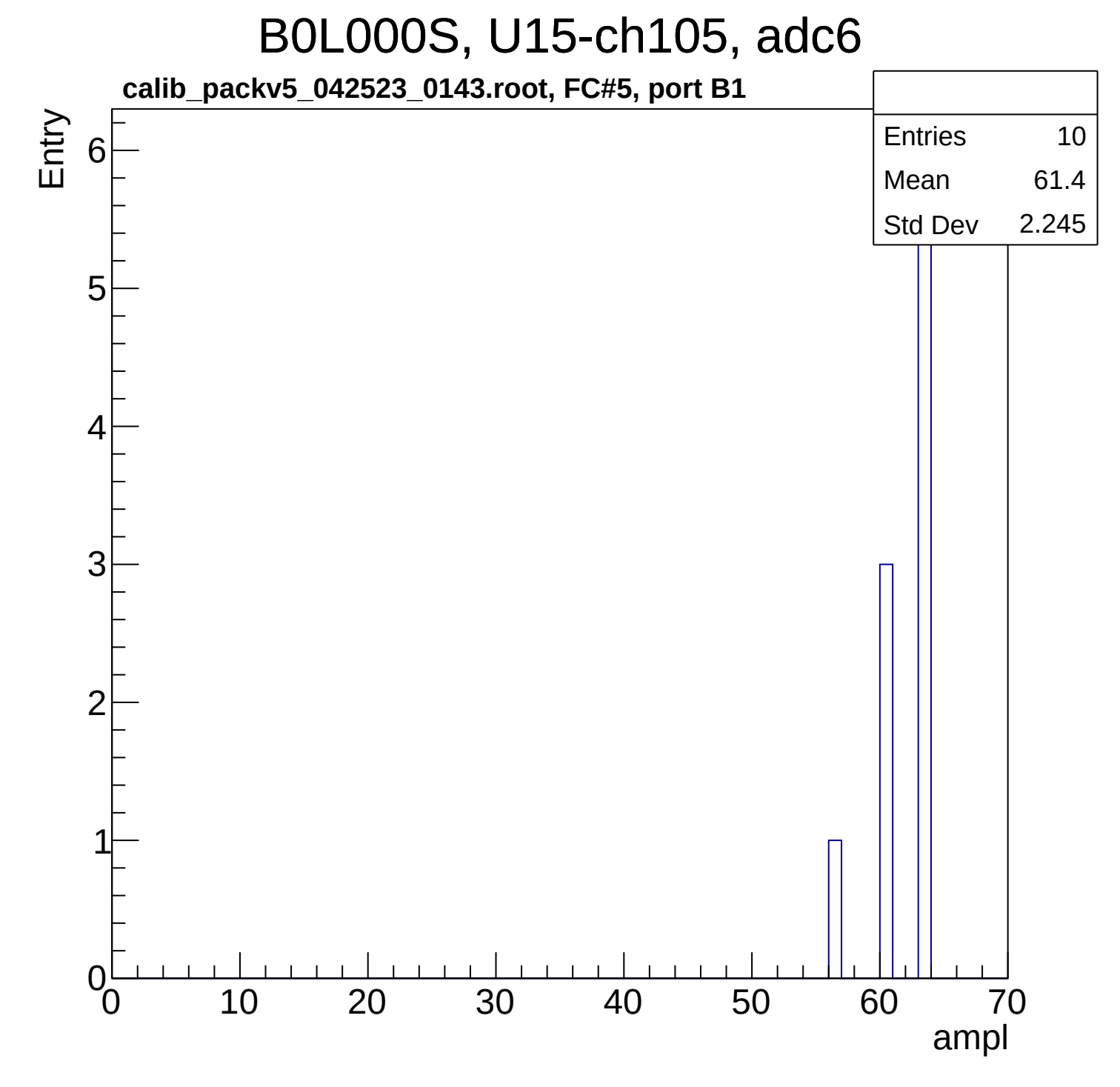
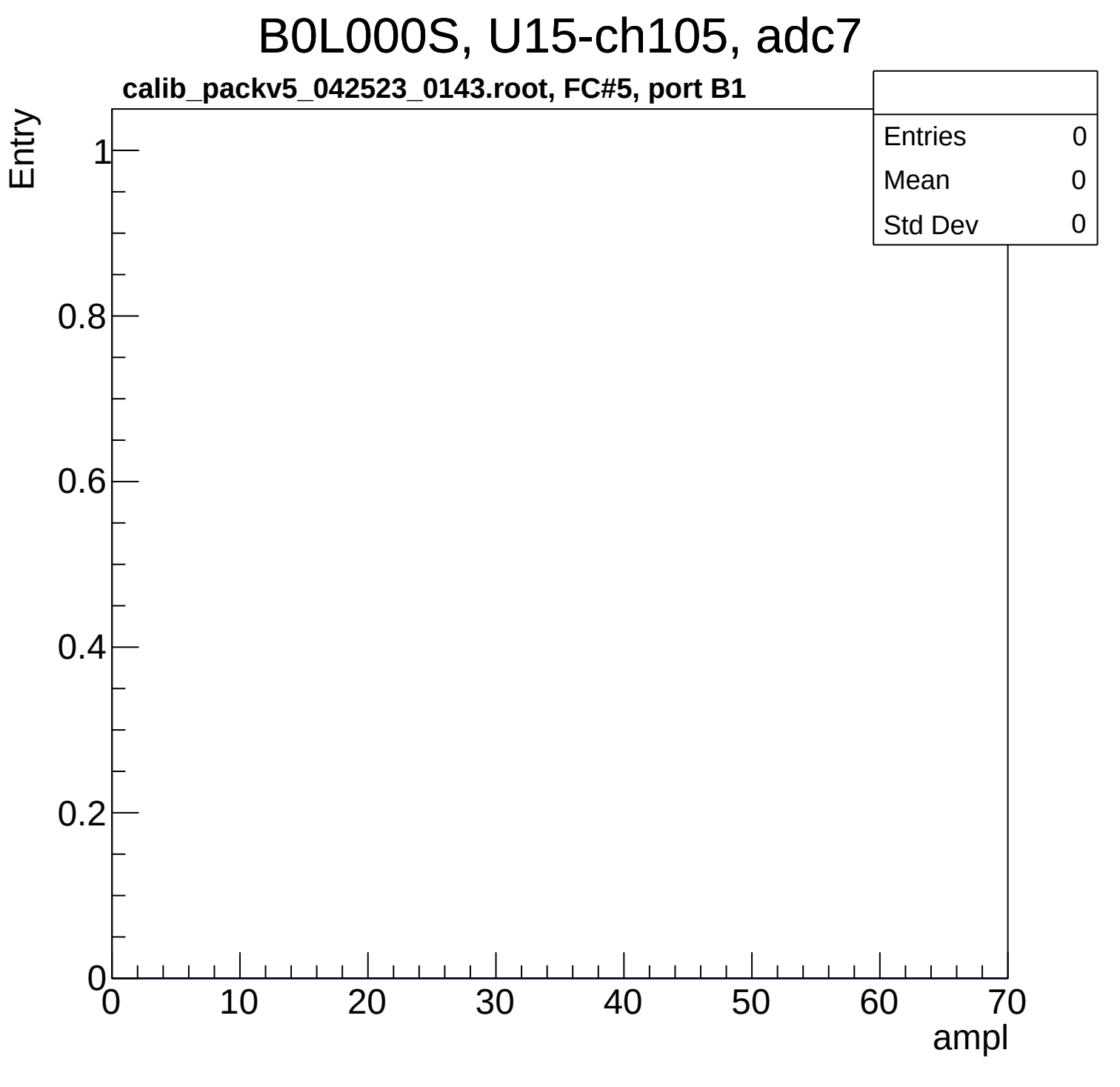
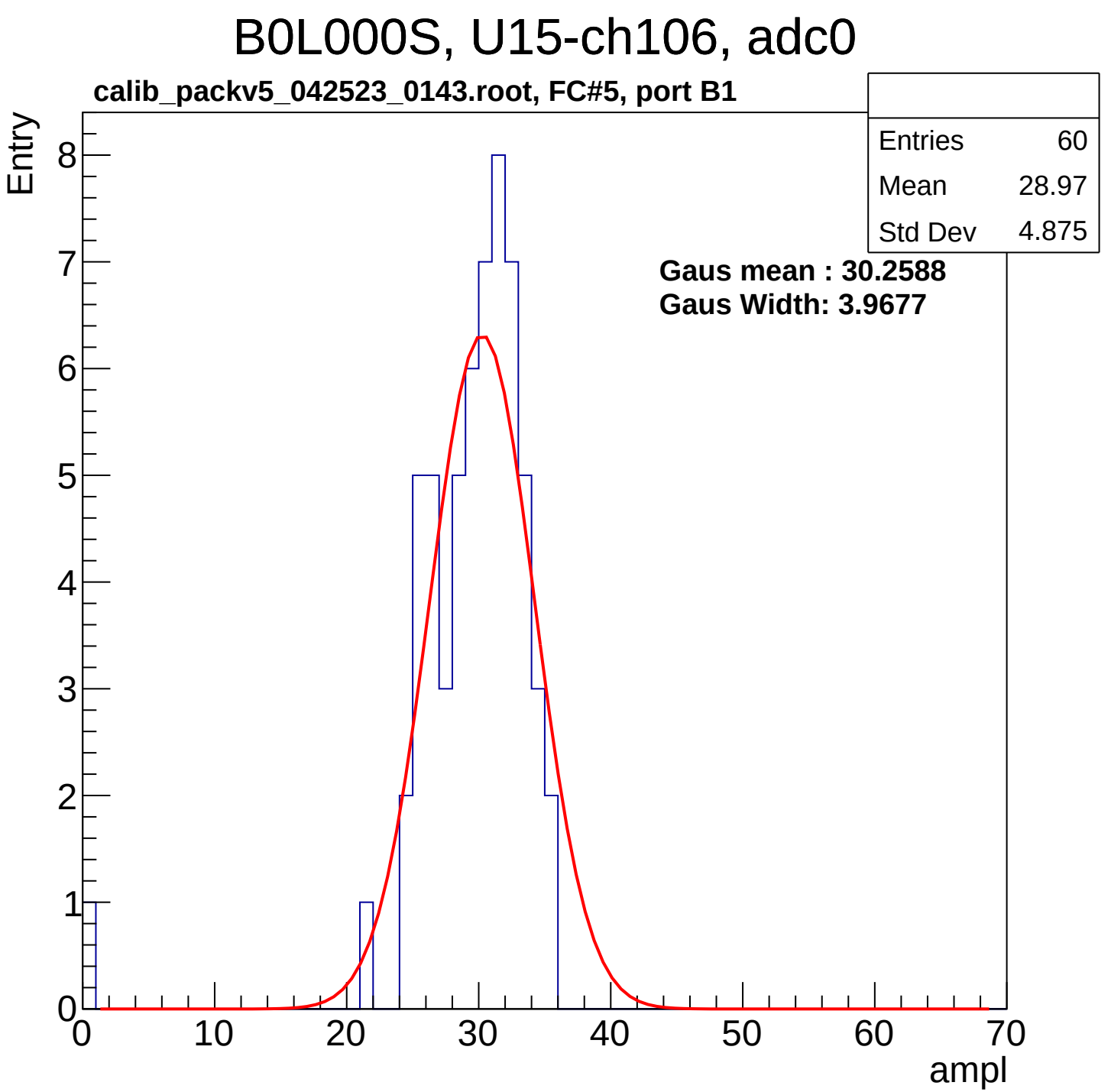
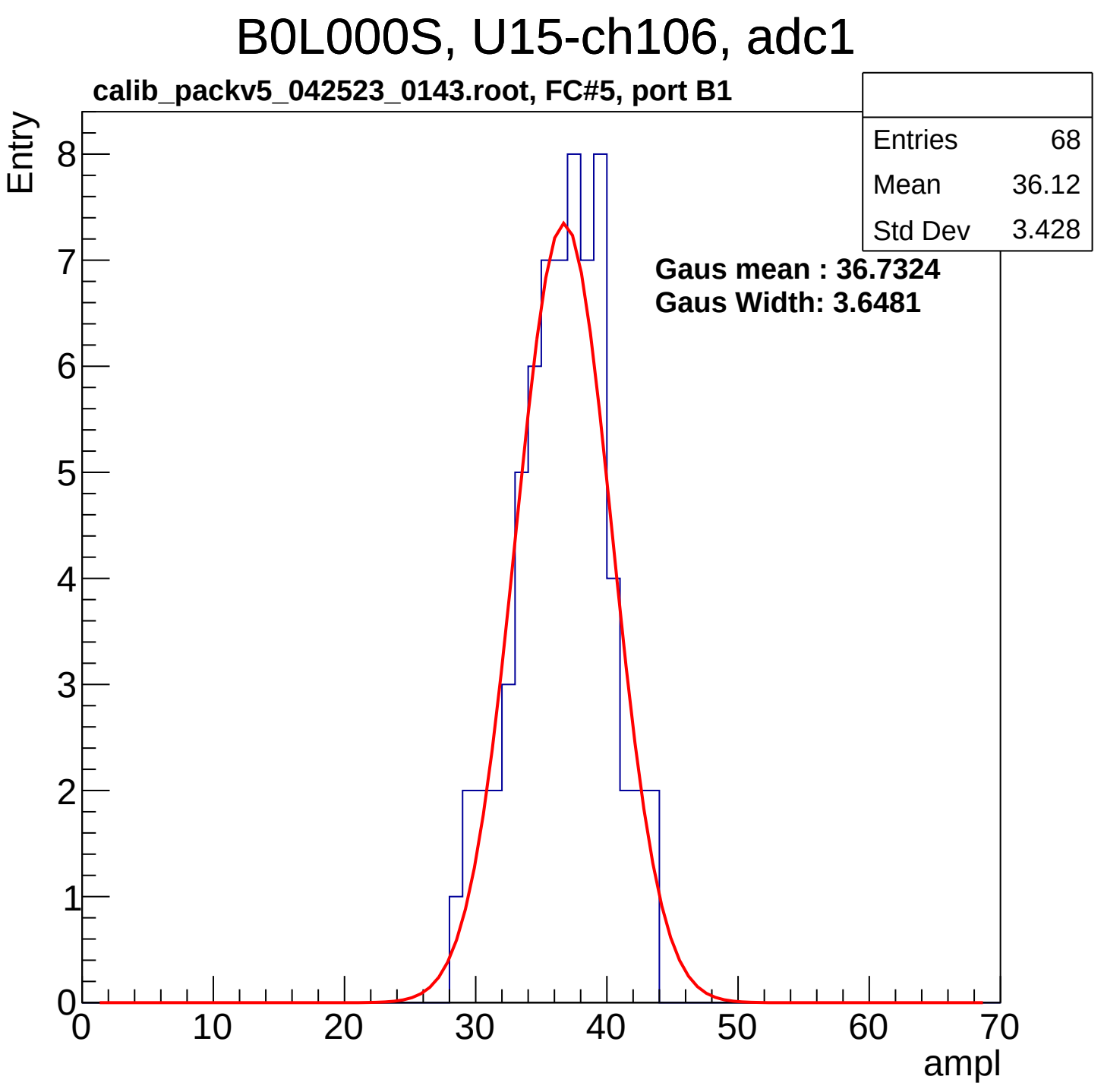
20
30
40
50
60
70


B0L000S, U15-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.4
Std Dev 2.245
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.97
Std Dev 4.875
Gaus mean : 30.2588
Gaus Width: 3.9677
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.12
Std Dev 3.428
Gaus mean : 36.7324
Gaus Width: 3.6481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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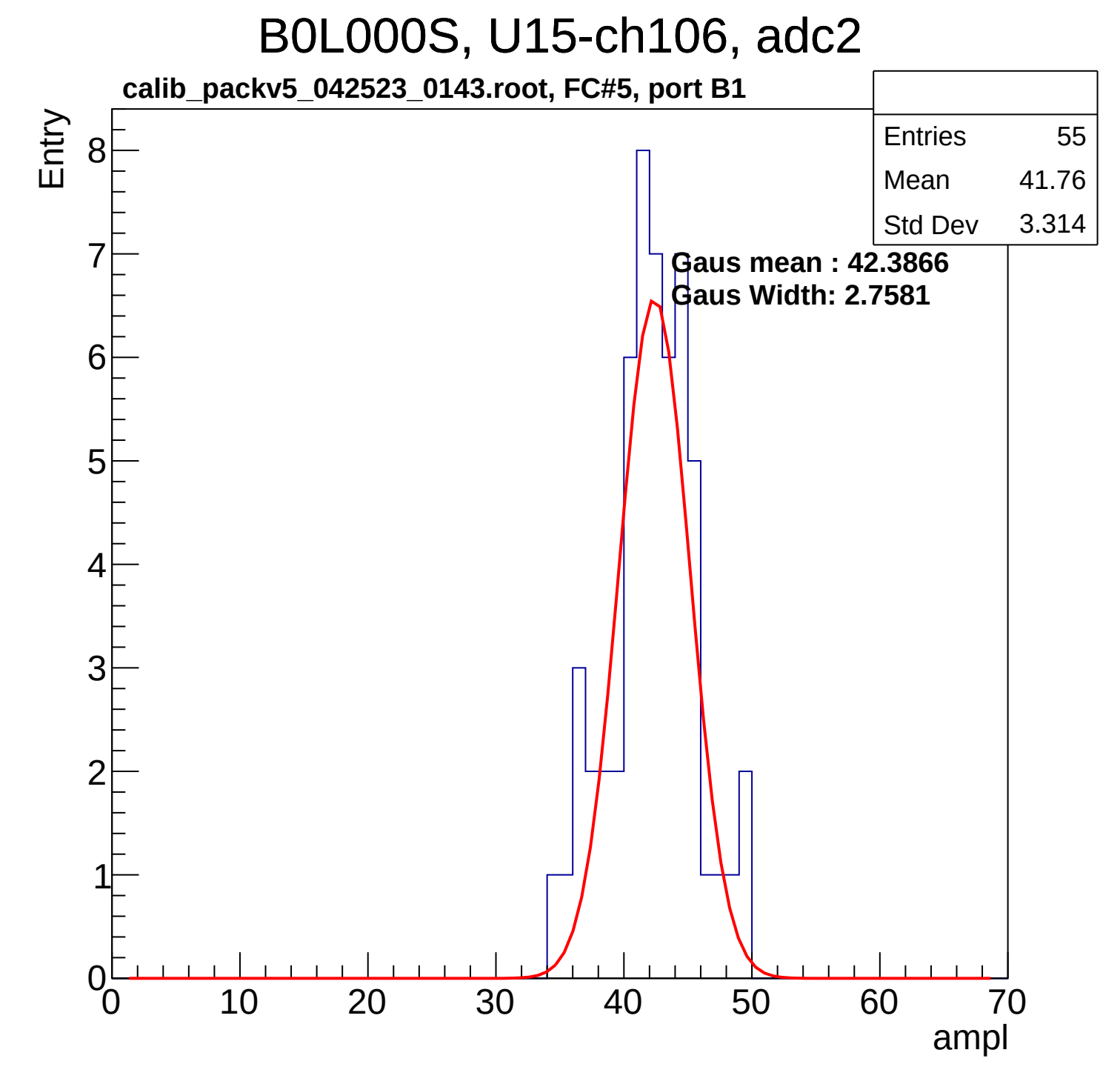
B0L000S, U15-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.76
Std Dev 3.314
Gaus mean : 42.3866
Gaus Width: 2.7581
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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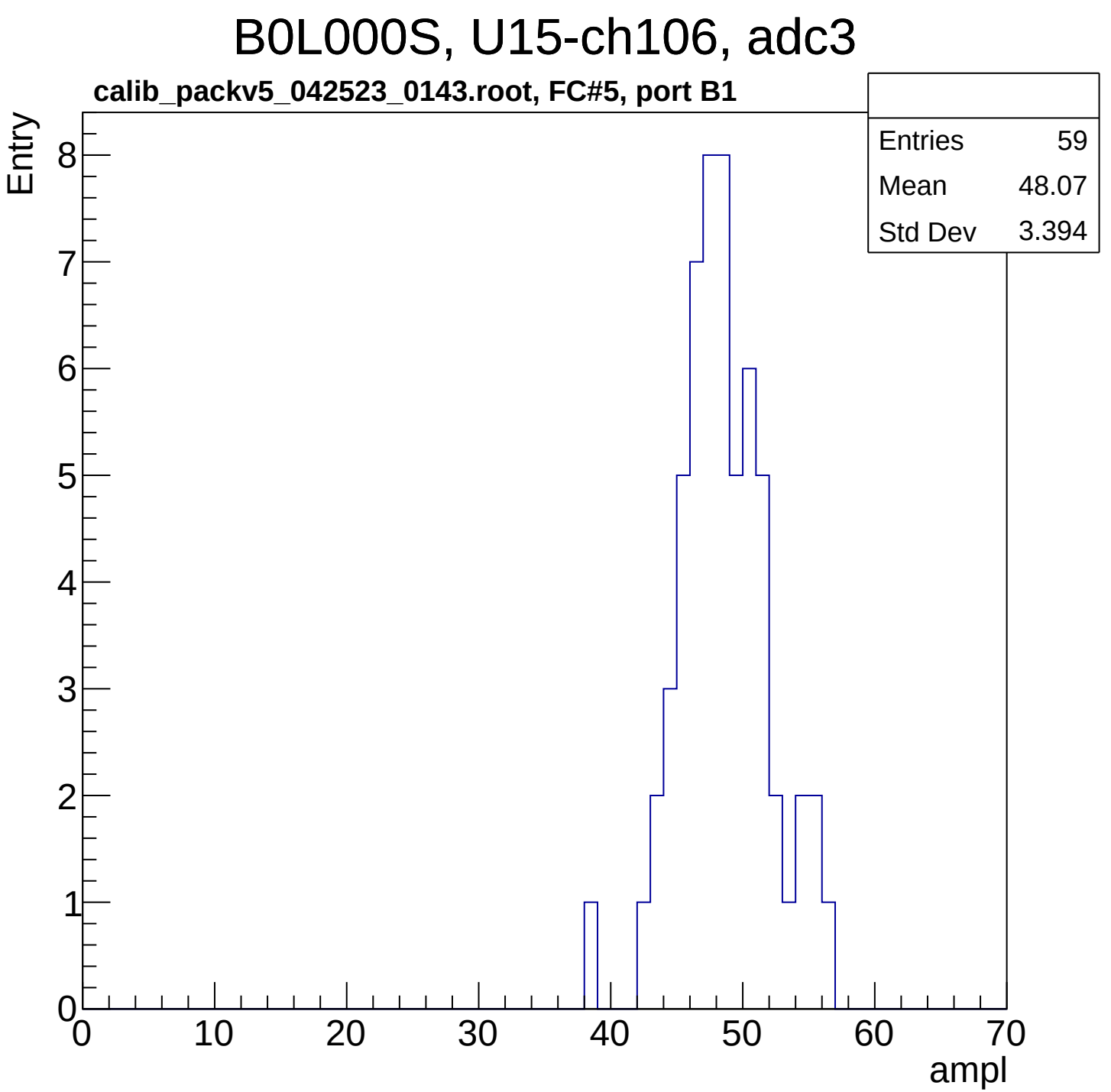
B0L000S, U15-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.07
Std Dev 3.394
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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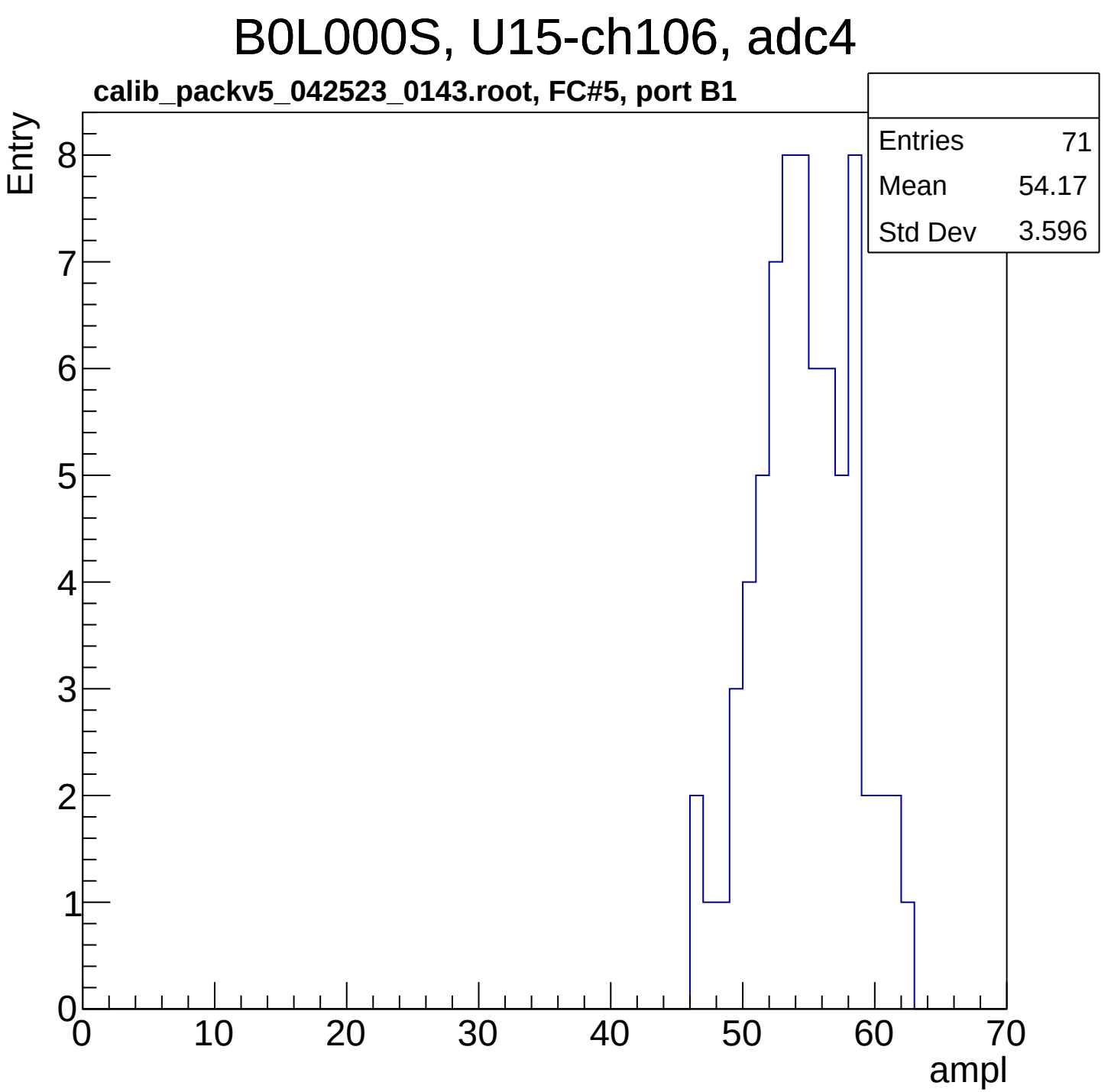
B0L000S, U15-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.17
Std Dev 3.596
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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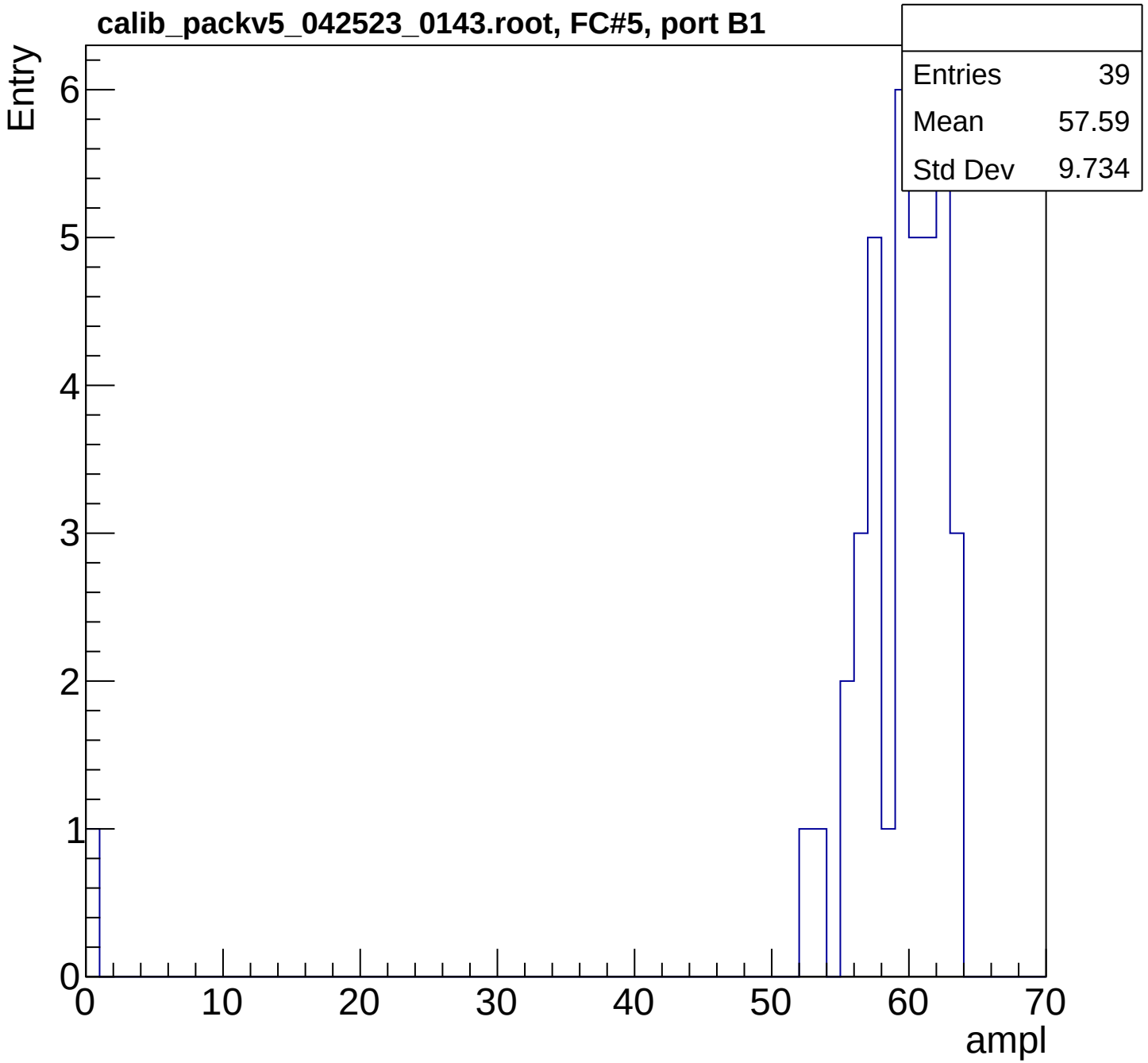
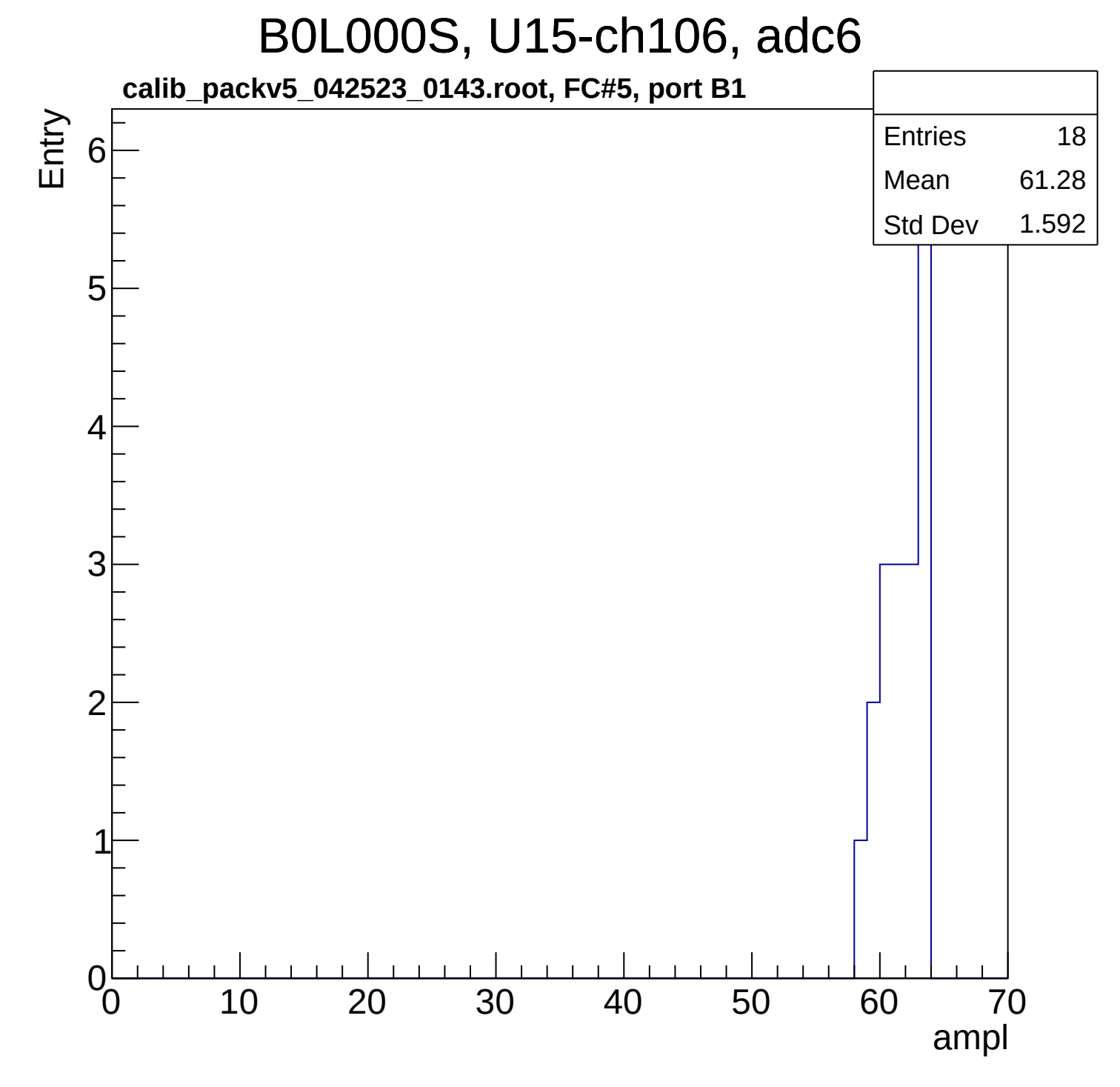
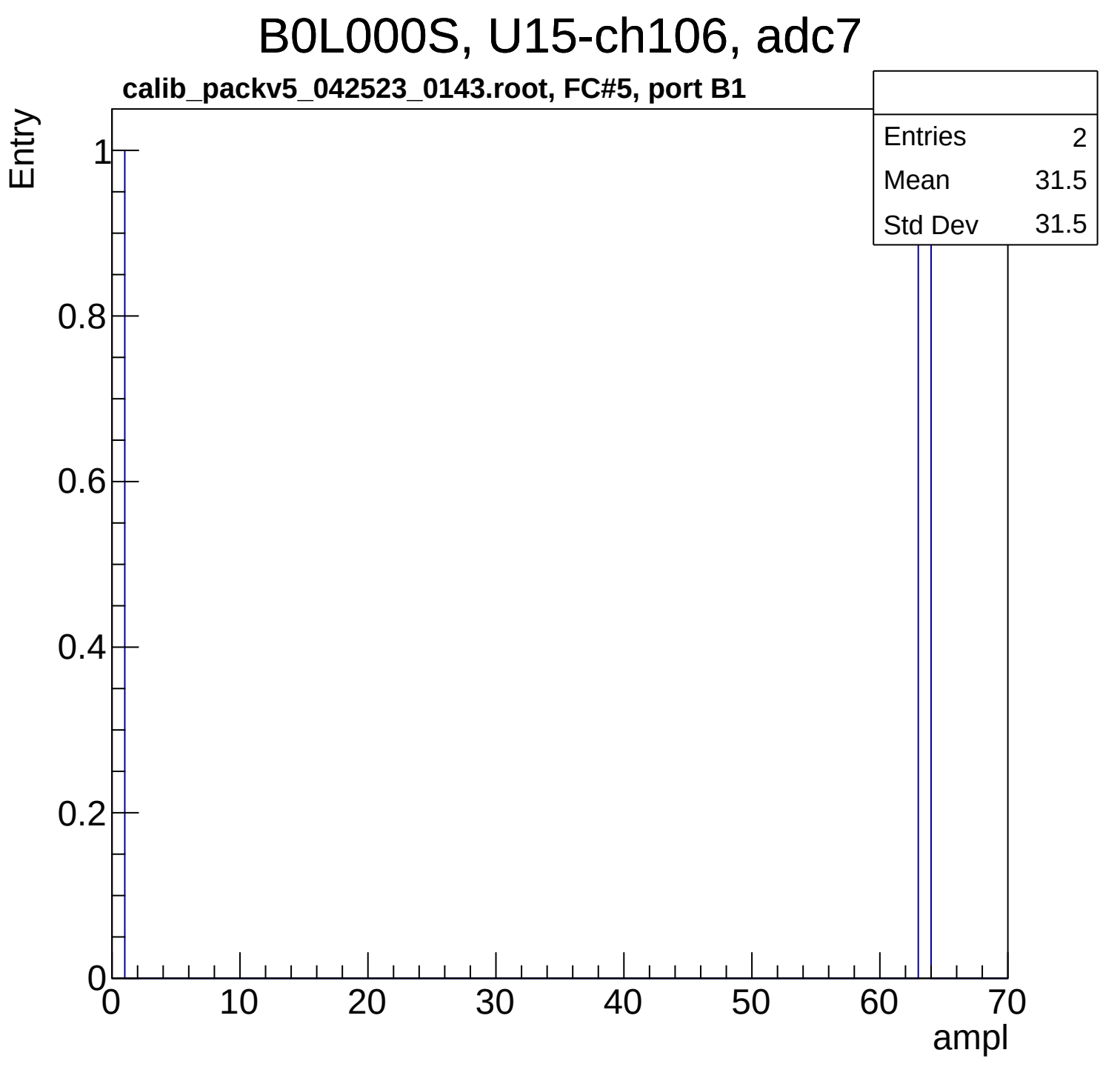

B0L000S, U15-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 57.59
Std Dev 9.734
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.28
Std Dev 1.592
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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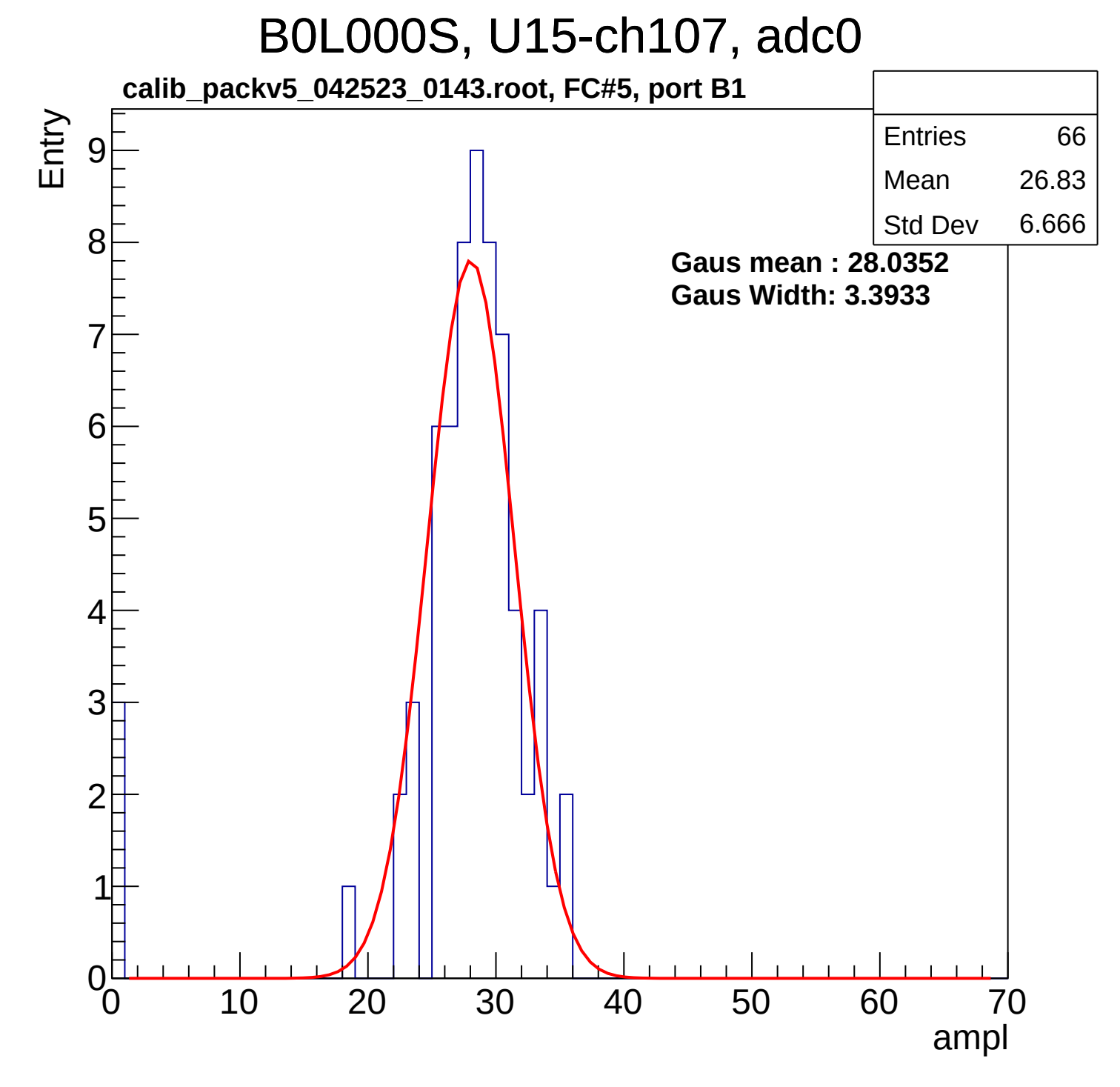
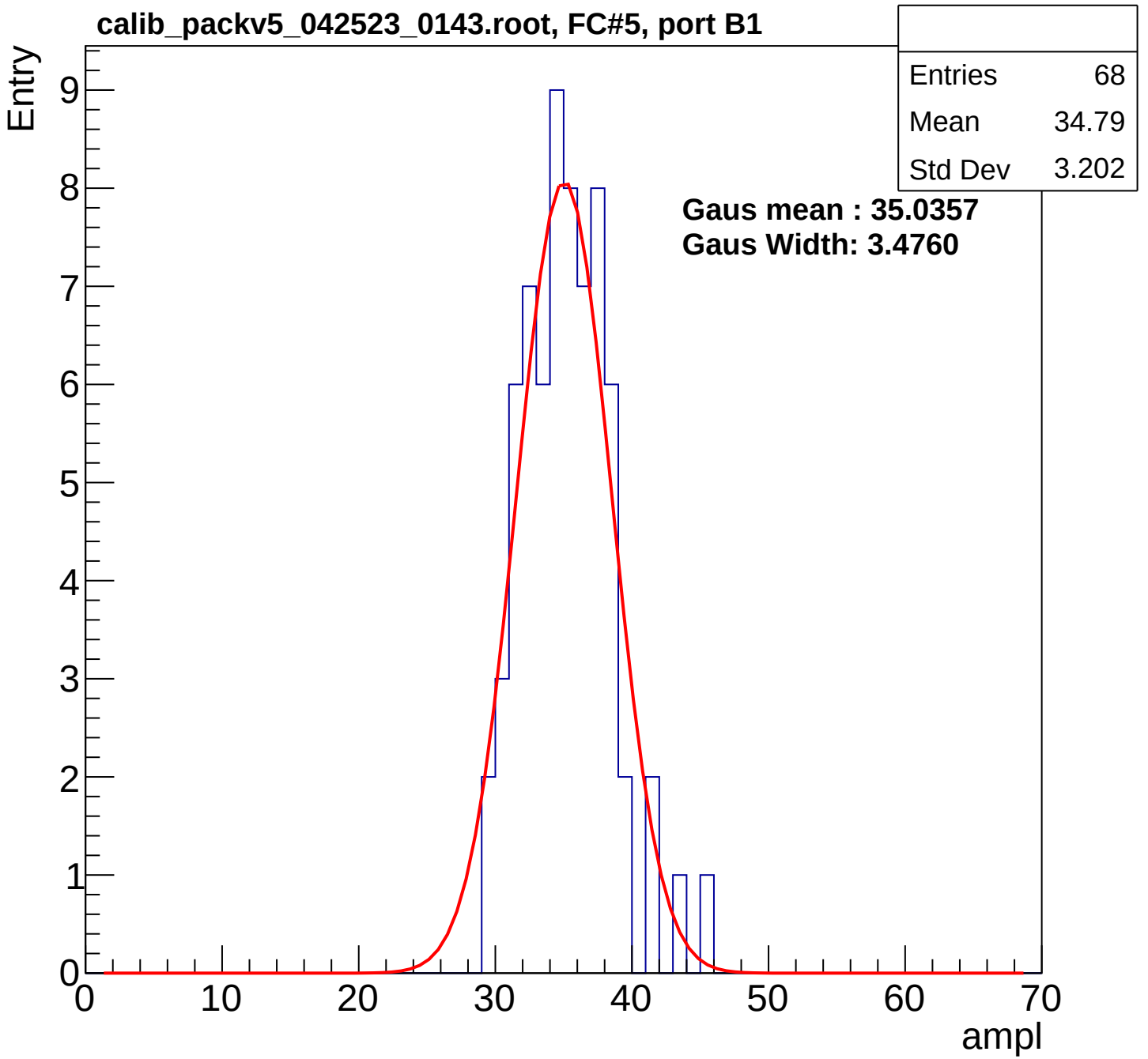
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 26.83
Std Dev 6.666
Gaus mean : 28.0352
Gaus Width: 3.3933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 34.79
Std Dev 3.202
Gaus mean : 35.0357
Gaus Width: 3.4760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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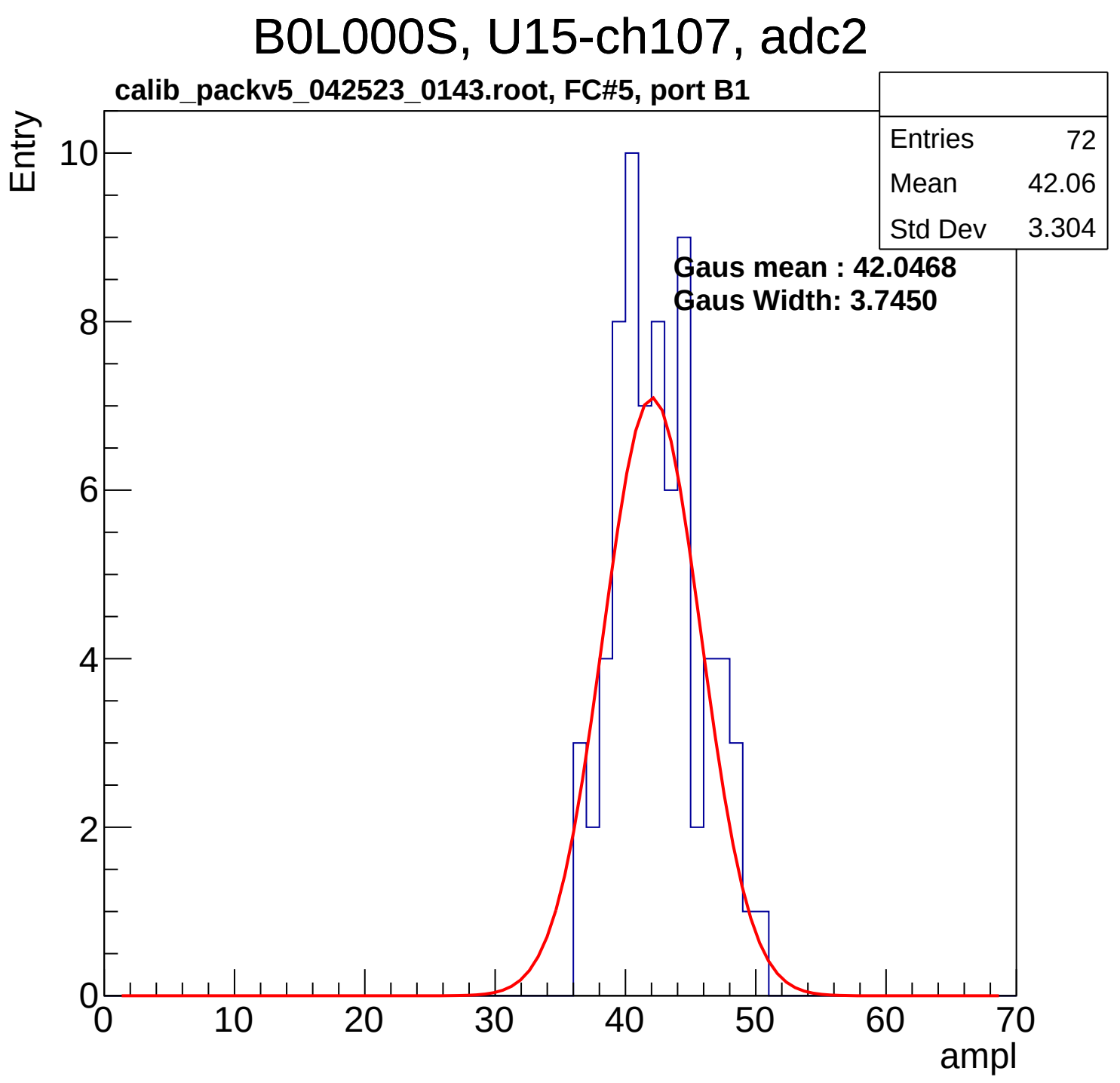
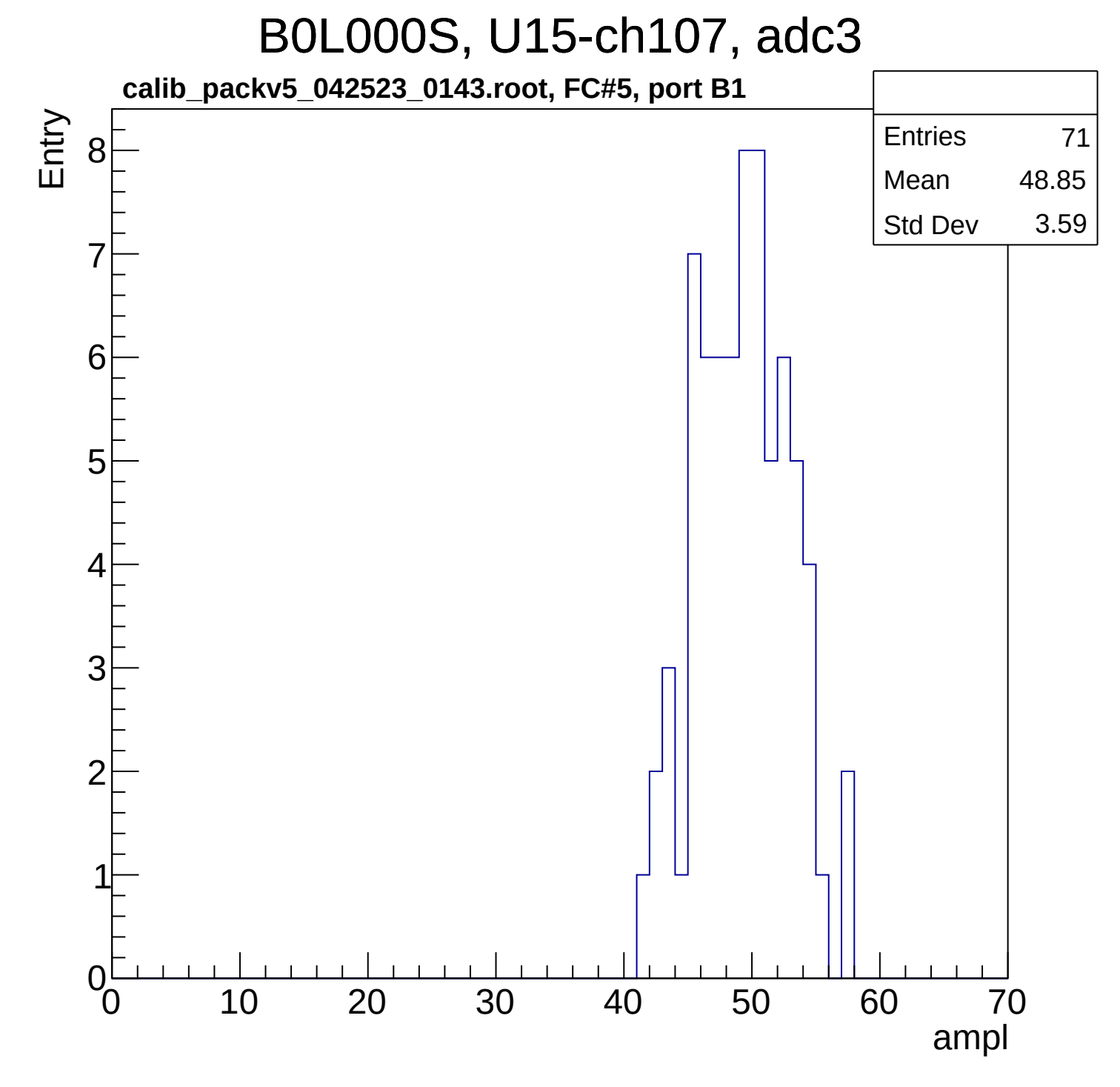

B0L000S, U15-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.06
Std Dev 3.304
Gaus mean : 42.0468
Gaus Width: 3.7450
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.85
Std Dev 3.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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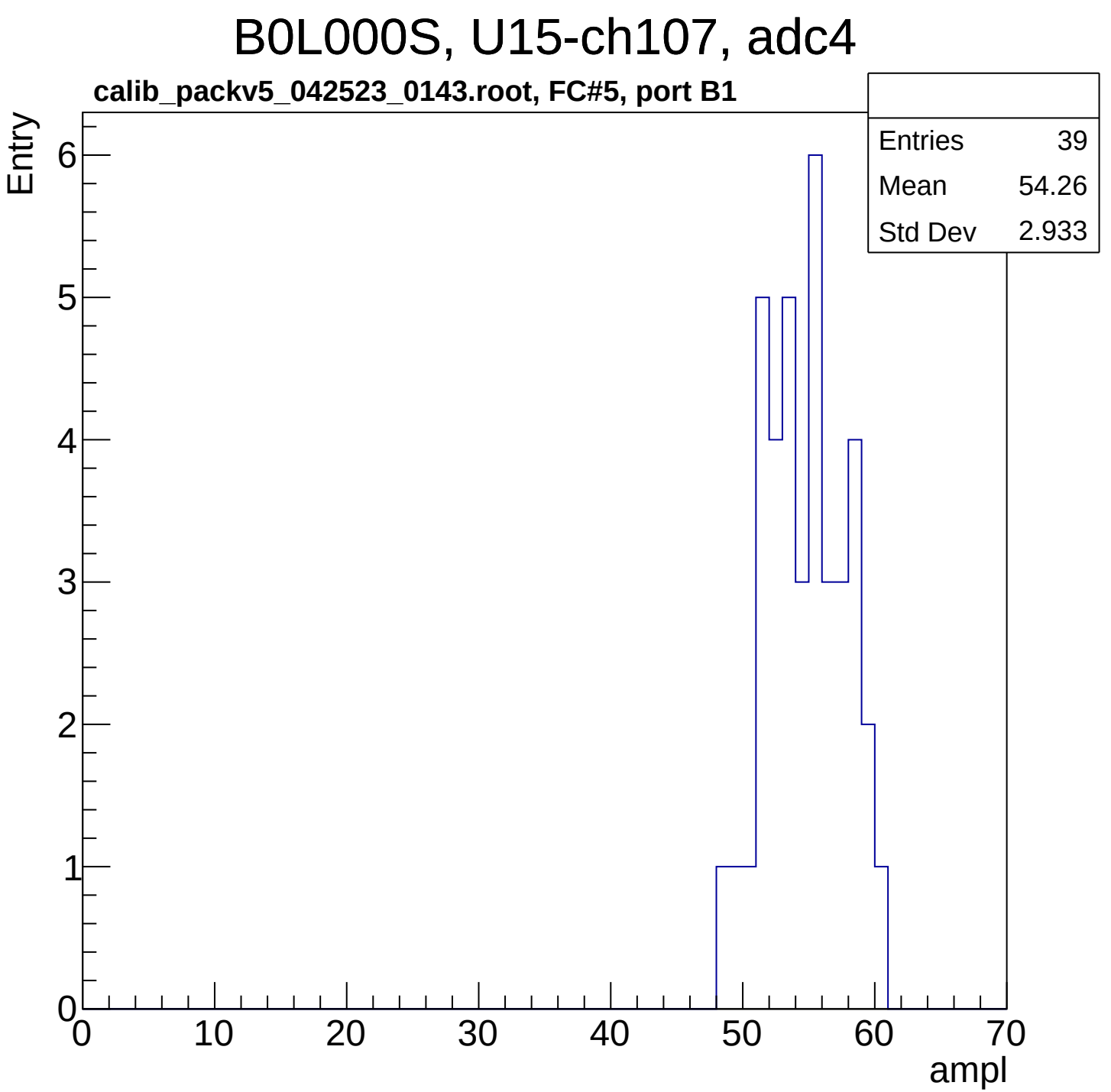
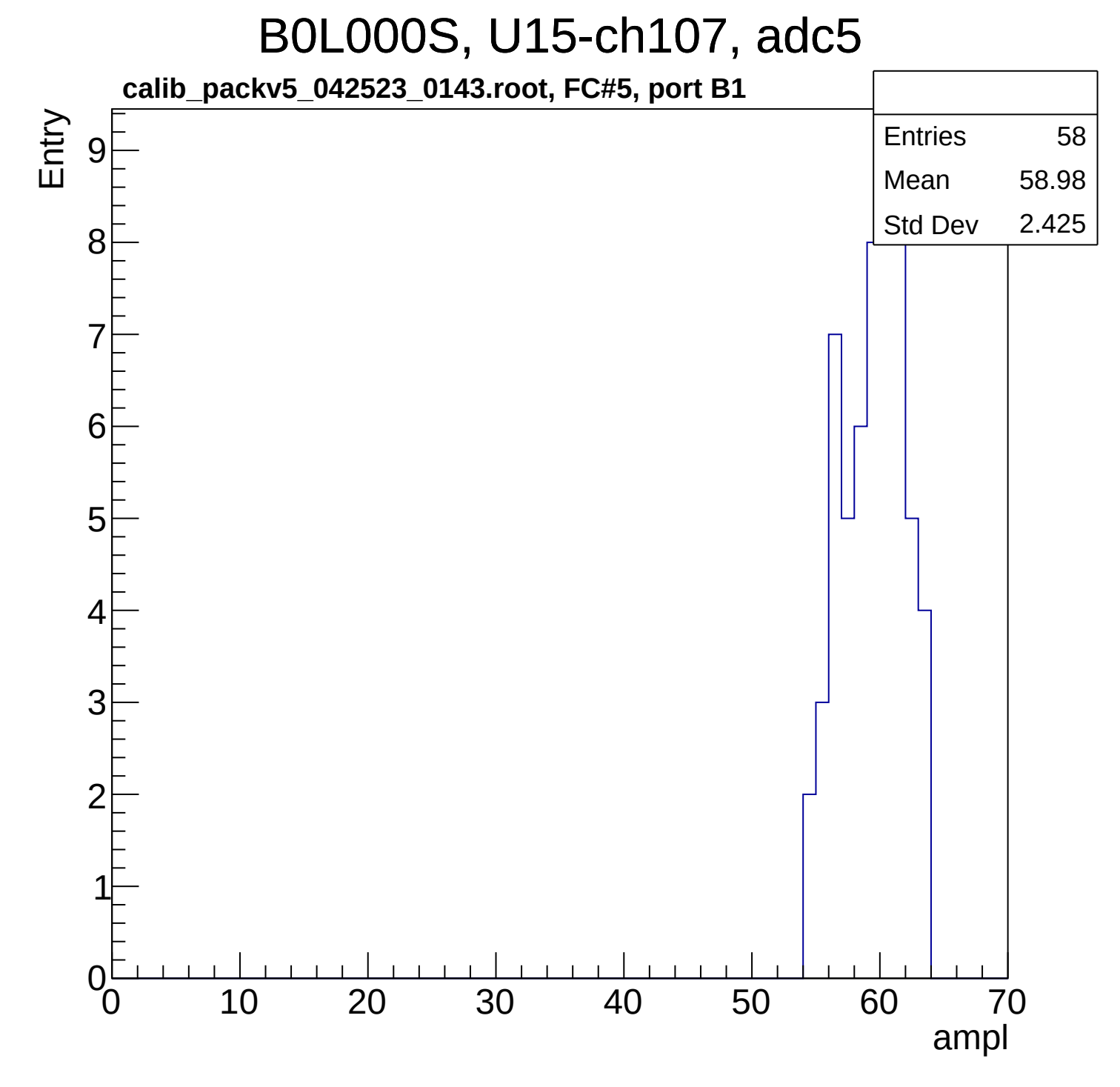

B0L000S, U15-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 54.26
Std Dev 2.933
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.98
Std Dev 2.425
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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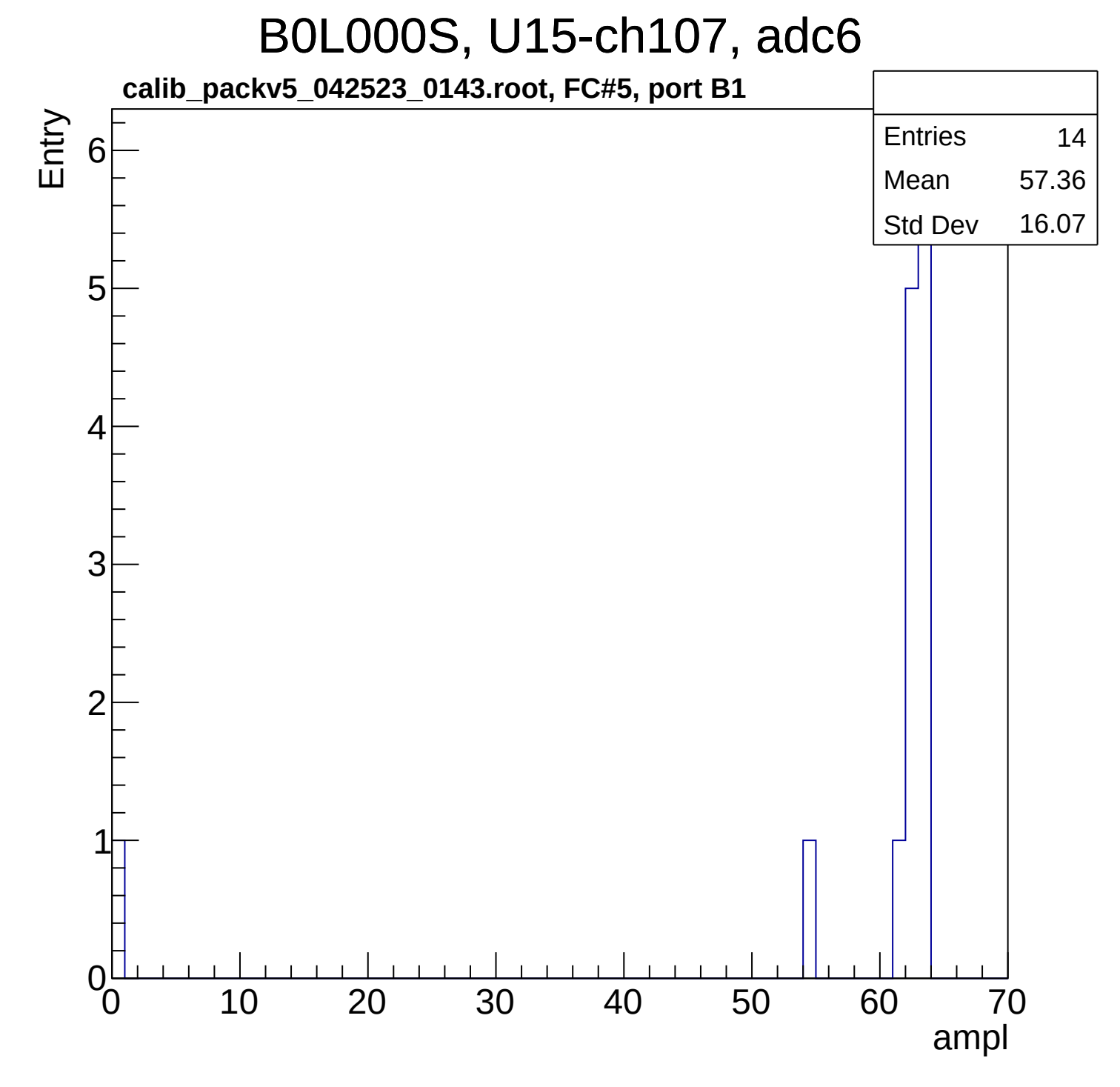
B0L000S, U15-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.36
Std Dev 16.07
Entry
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5
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10
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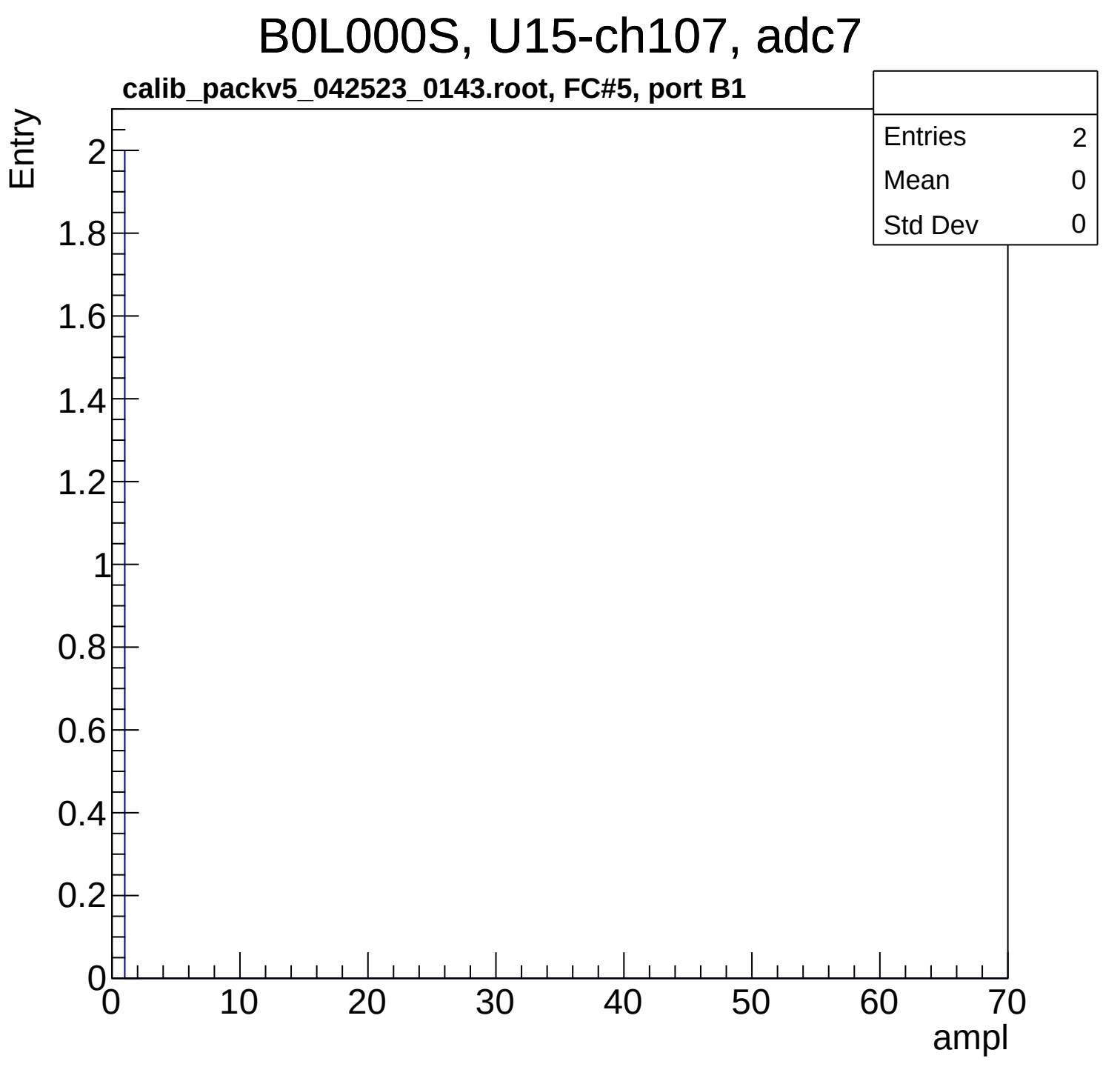
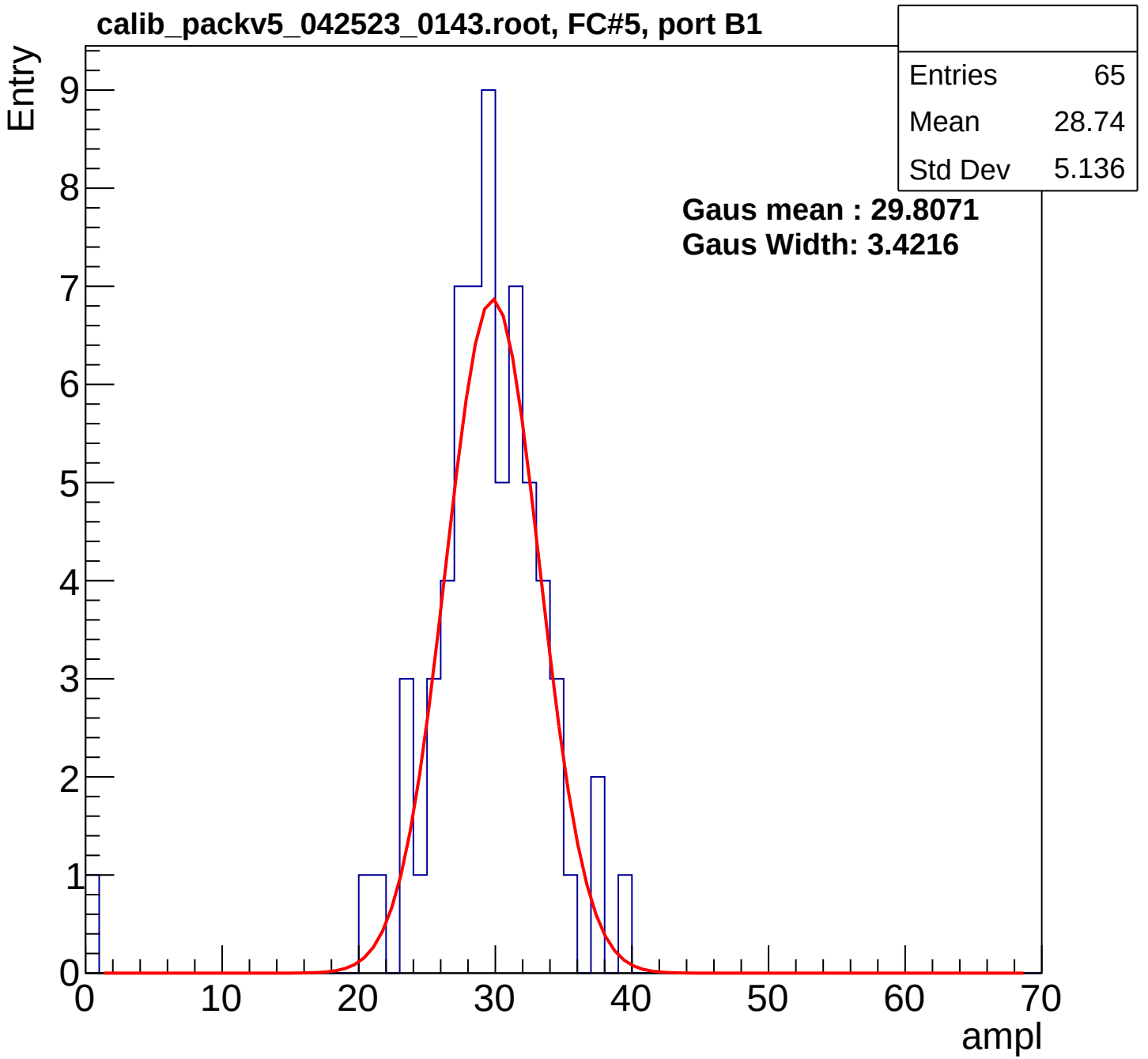
50
60
70
ampl


B0L000S, U15-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.74
Std Dev 5.136
Gaus mean : 29.8071
Gaus Width: 3.4216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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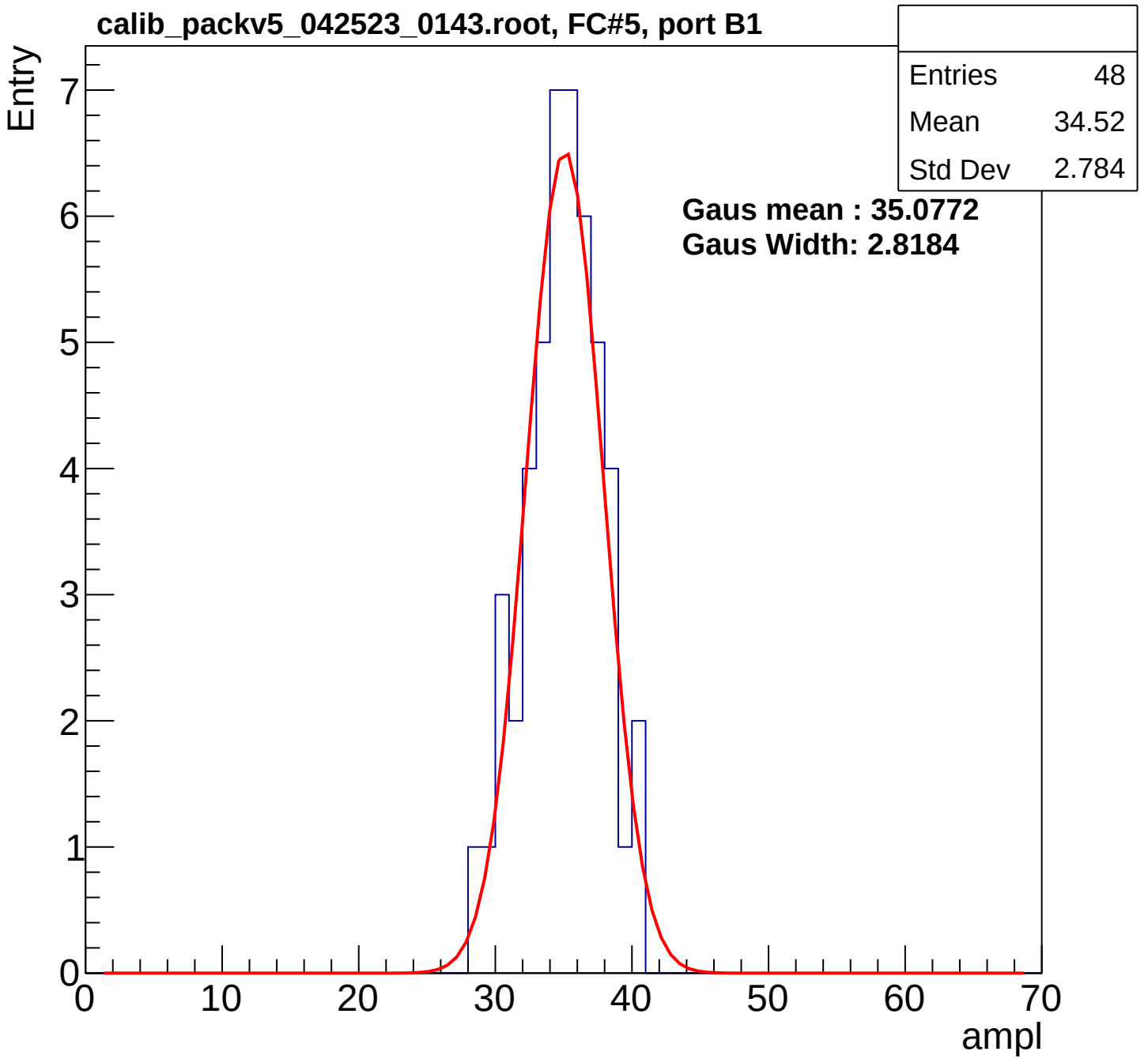
40
50
60
70


B0L000S, U15-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 34.52
Std Dev 2.784
Gaus mean : 35.0772
Gaus Width: 2.8184
Entry
ampl
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1
2
3
4
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6
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0
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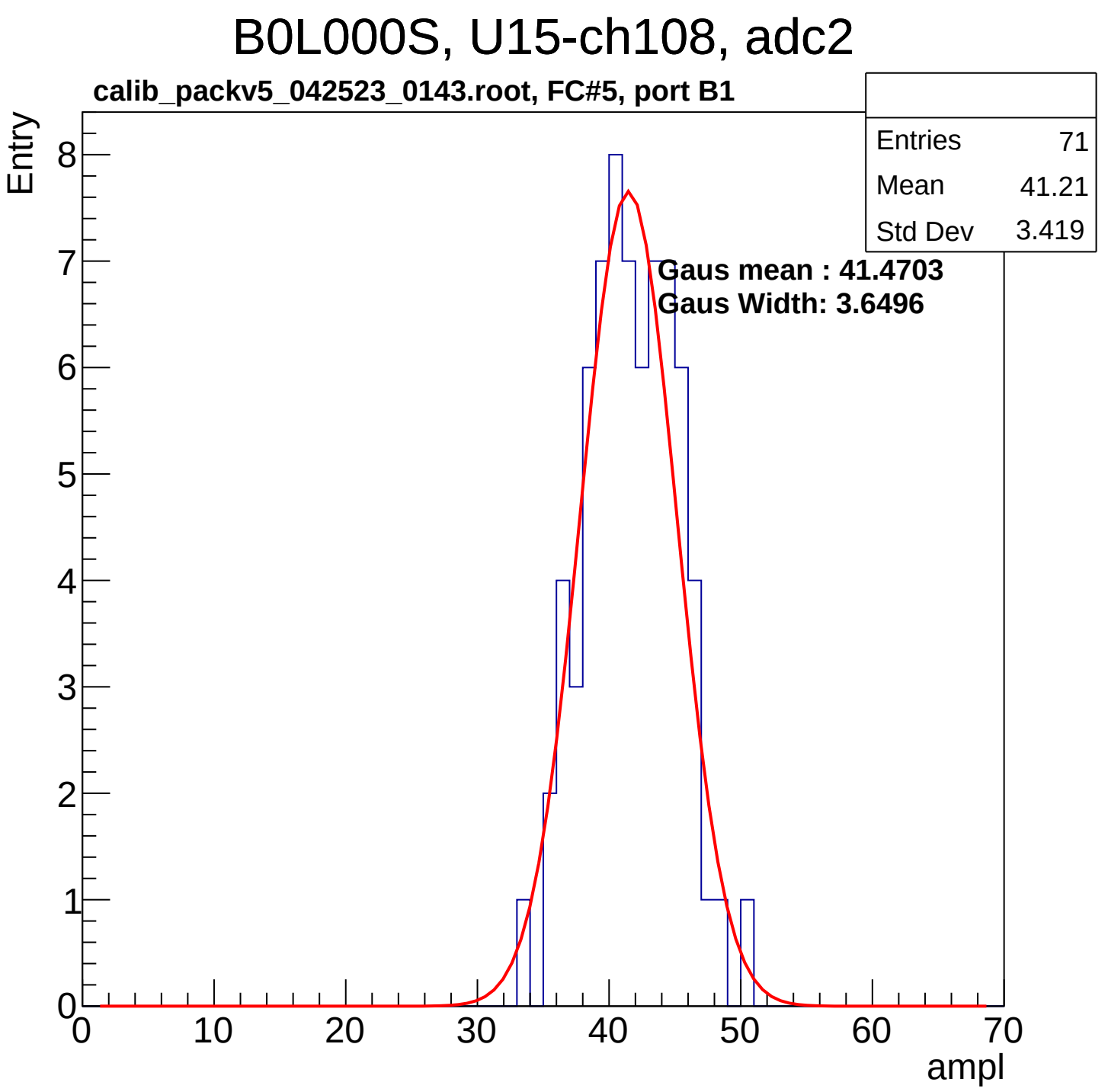
B0L000S, U15-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 41.21
Std Dev 3.419
Gaus mean : 41.4703
Gaus Width: 3.6496
Entry
ampl
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1
2
3
4
5
6
7
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0
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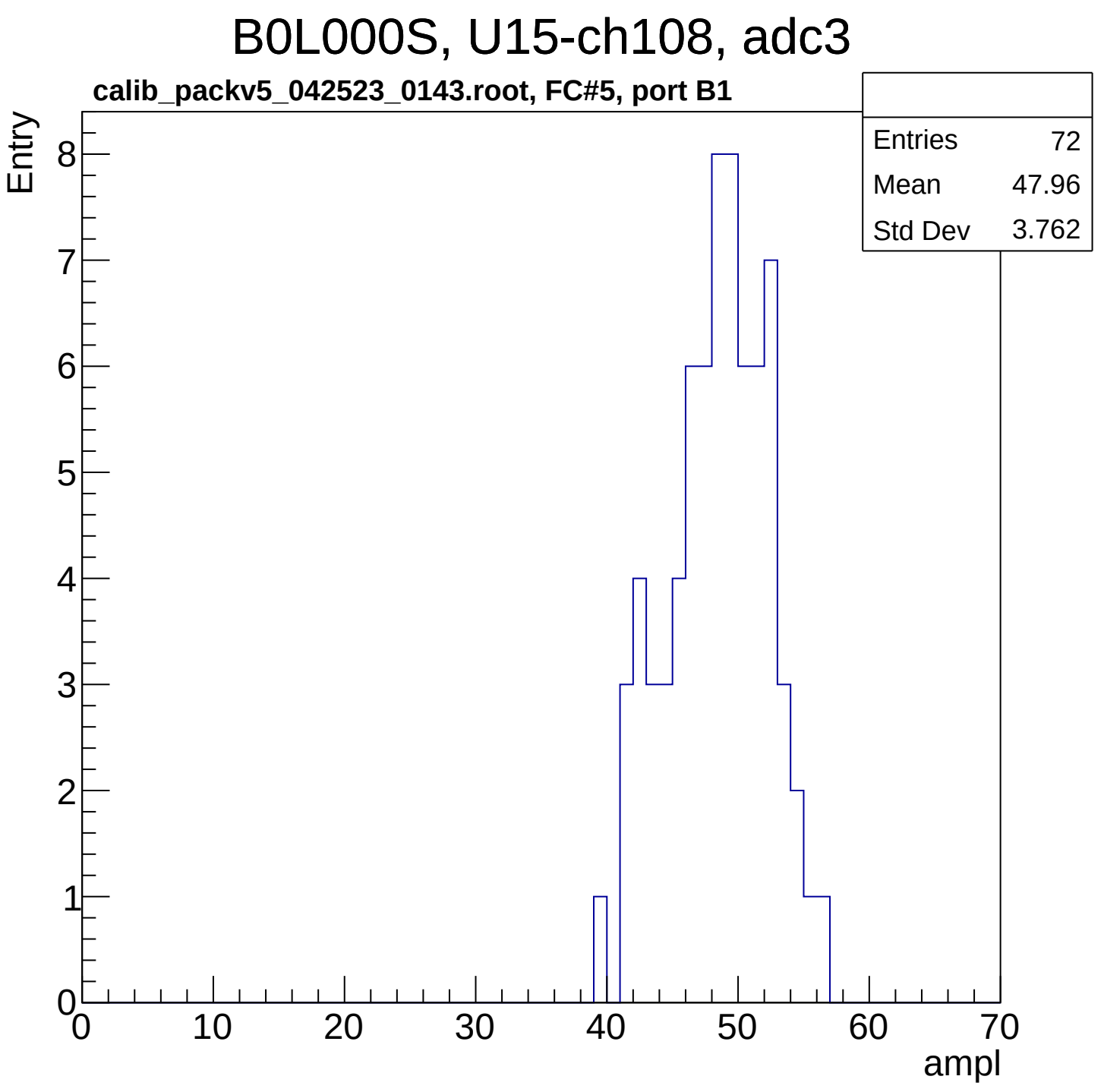
B0L000S, U15-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 47.96
Std Dev 3.762
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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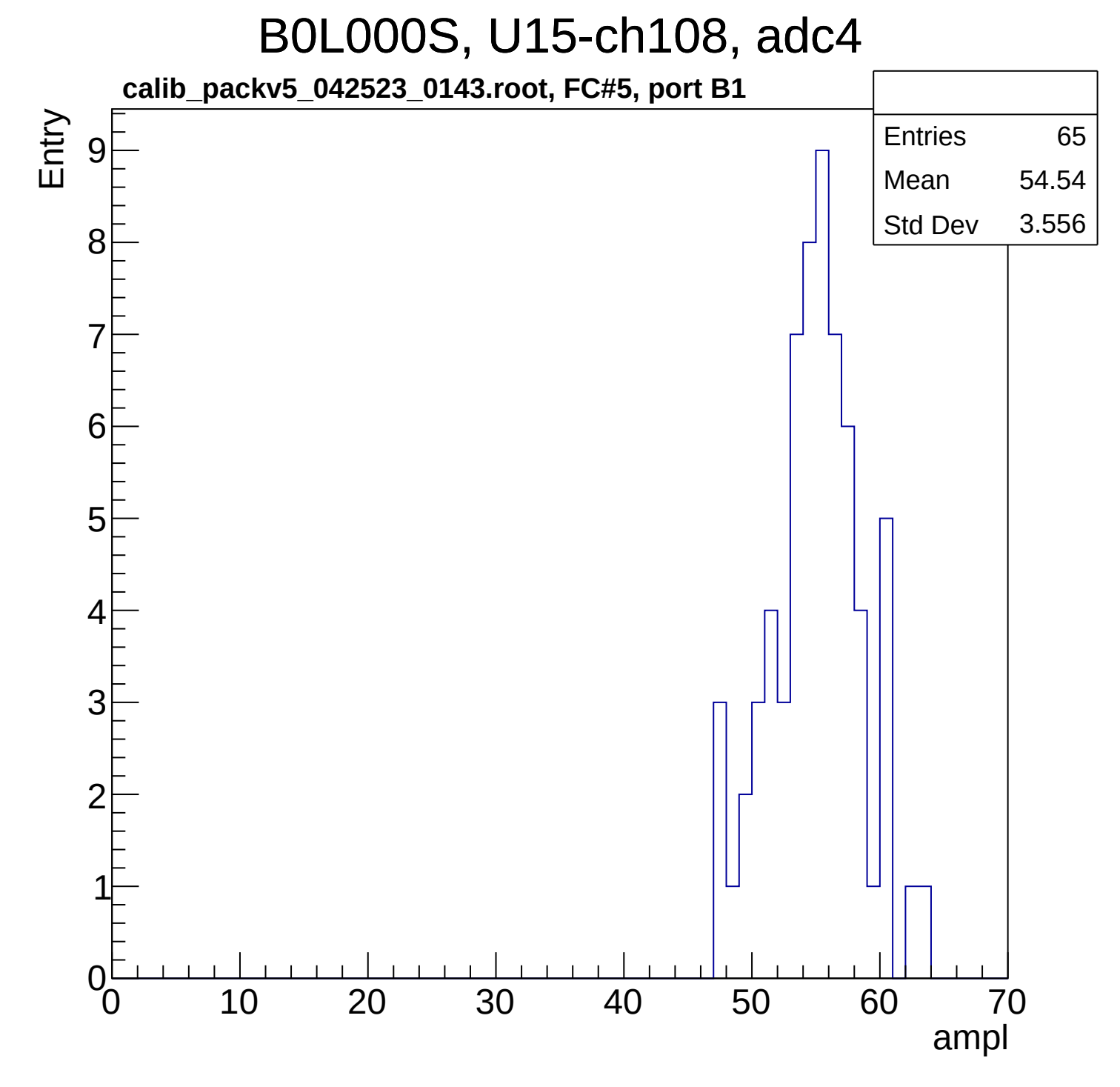
B0L000S, U15-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 54.54
Std Dev 3.556
Entry
ampl
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6
7
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0
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60
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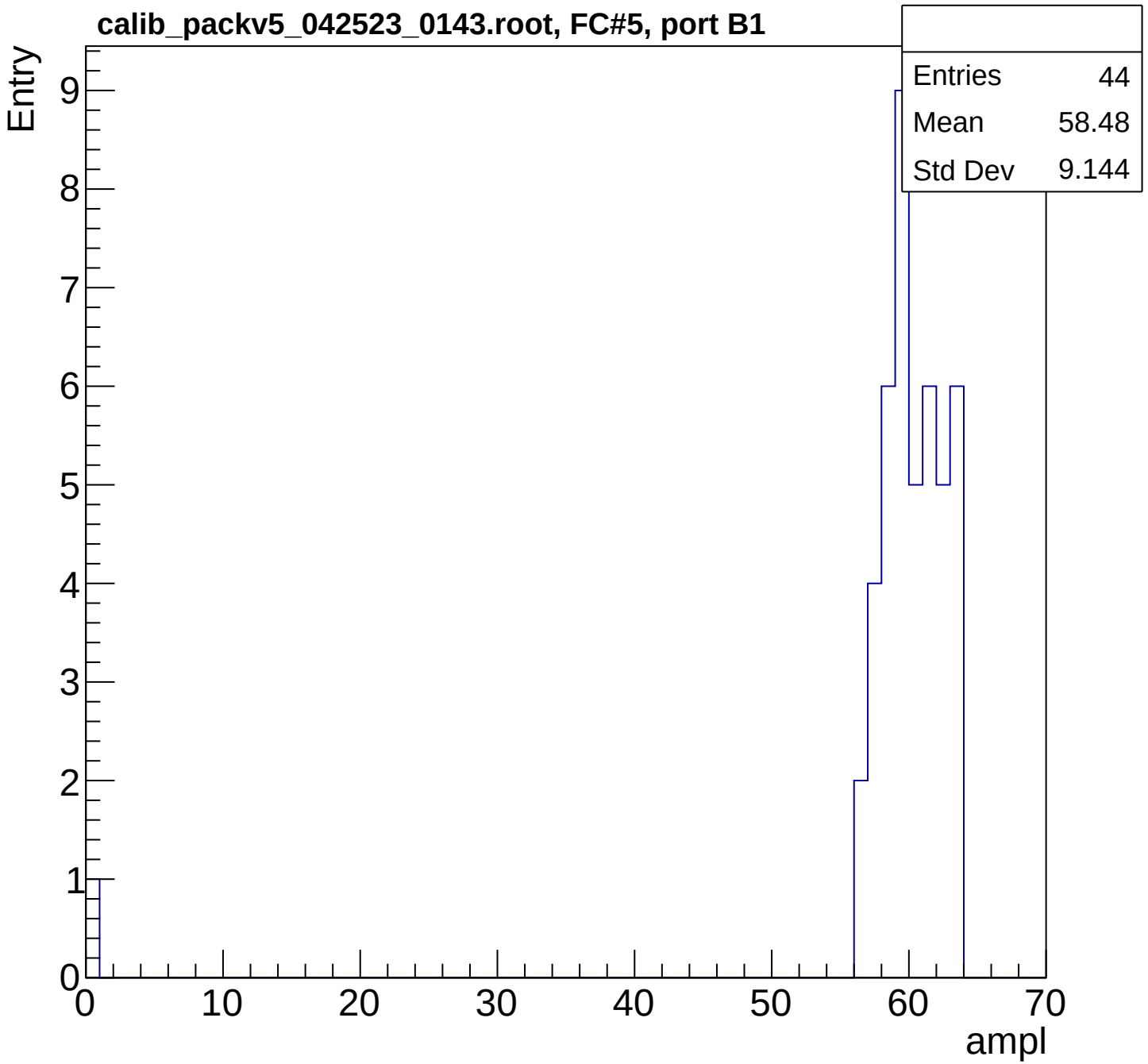
B0L000S, U15-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.48
Std Dev 9.144
Entry
ampl
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6
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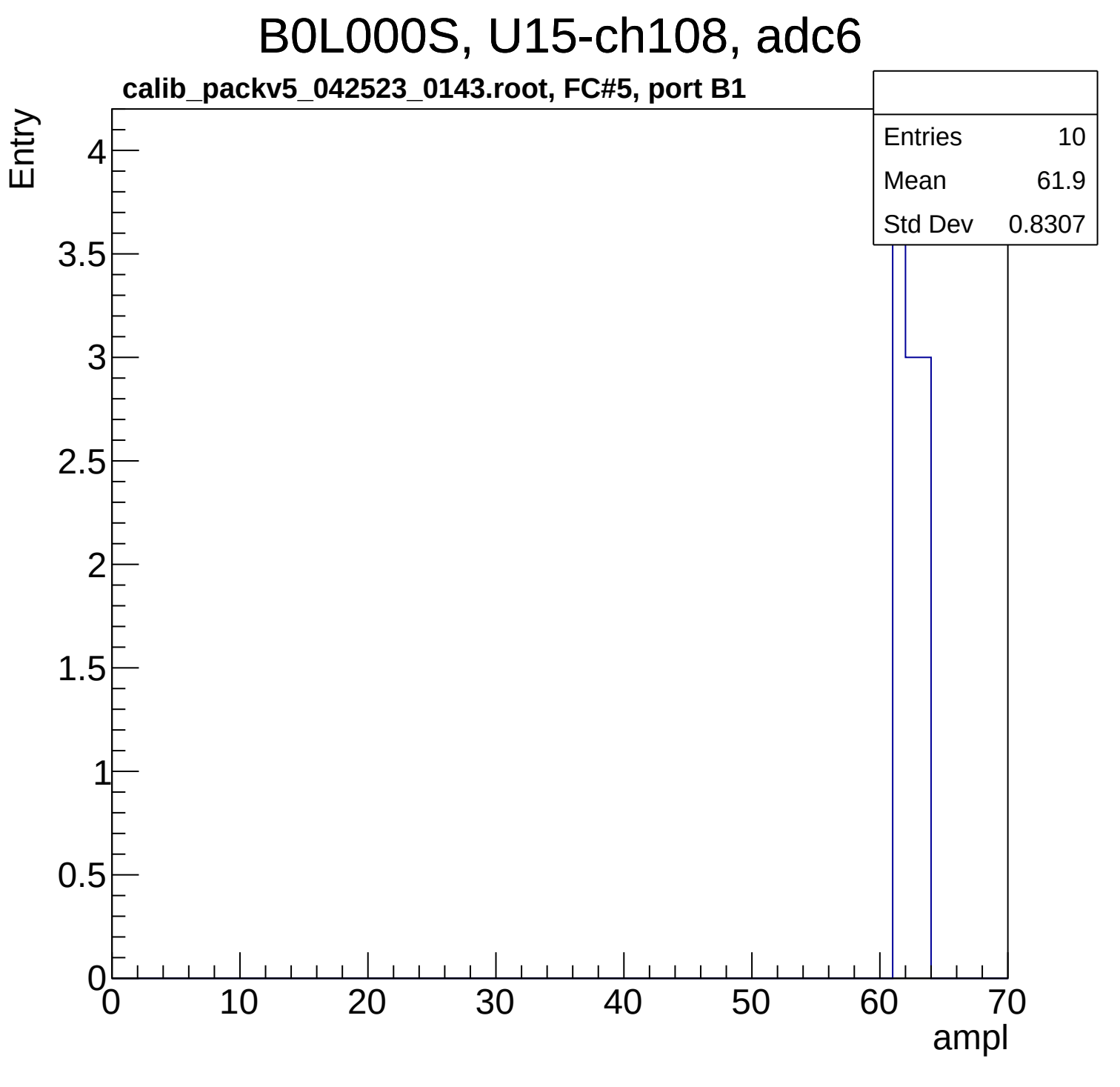
B0L000S, U15-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.9
Std Dev 0.8307
Entry
ampl
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1.5
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2.5
3
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0
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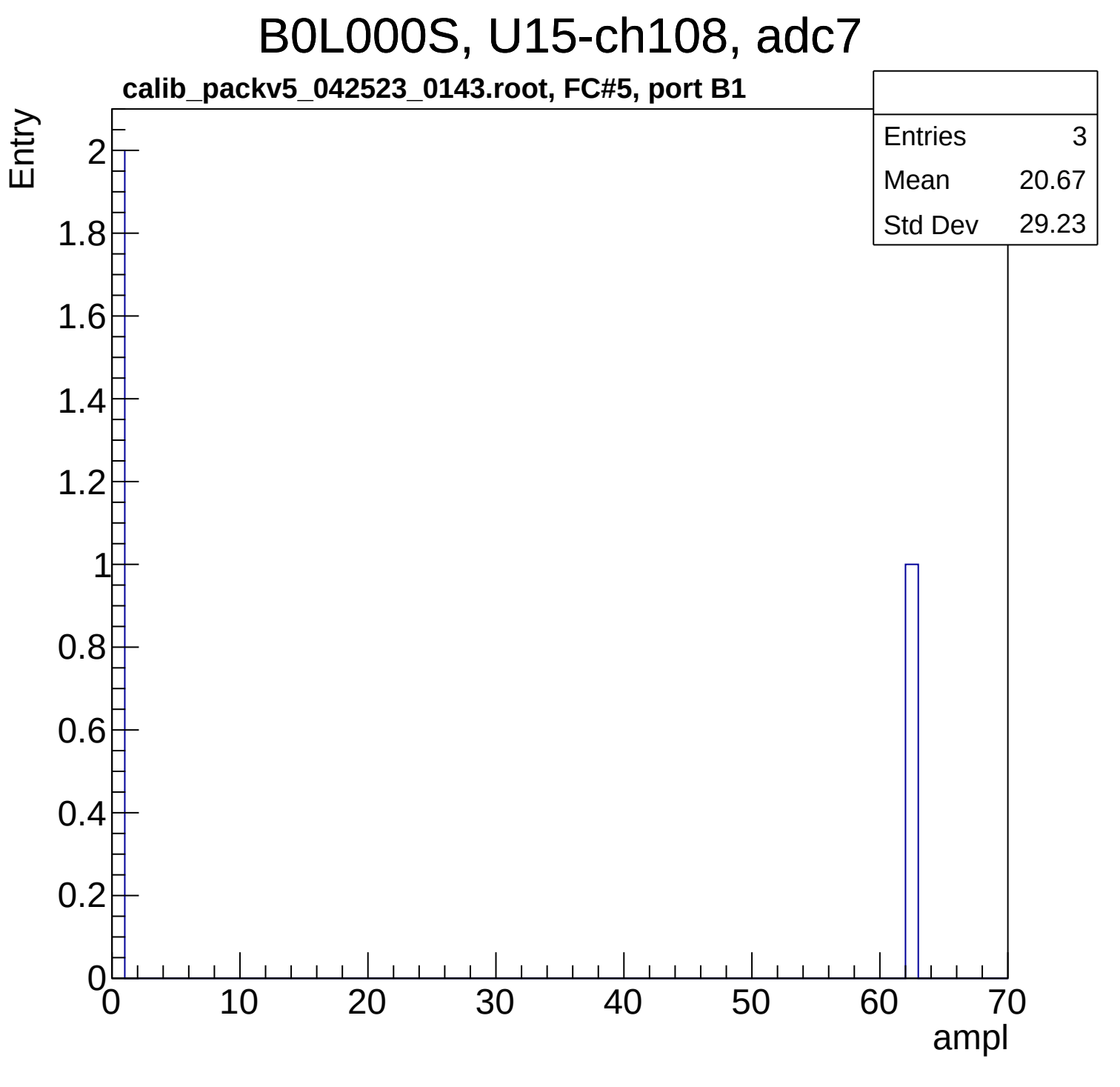
B0L000S, U15-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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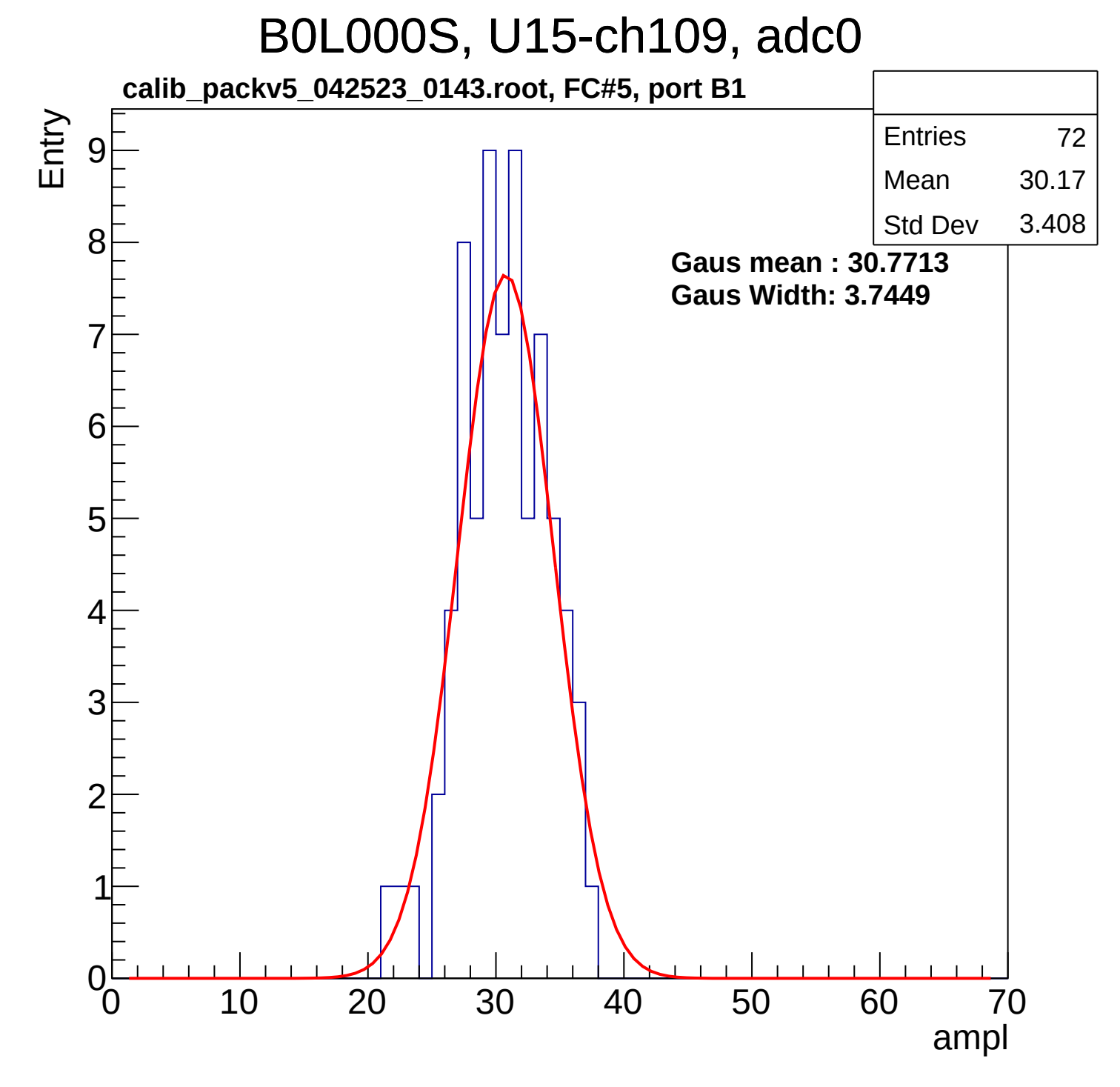
B0L000S, U15-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.17
Std Dev 3.408
Gaus mean : 30.7713
Gaus Width: 3.7449
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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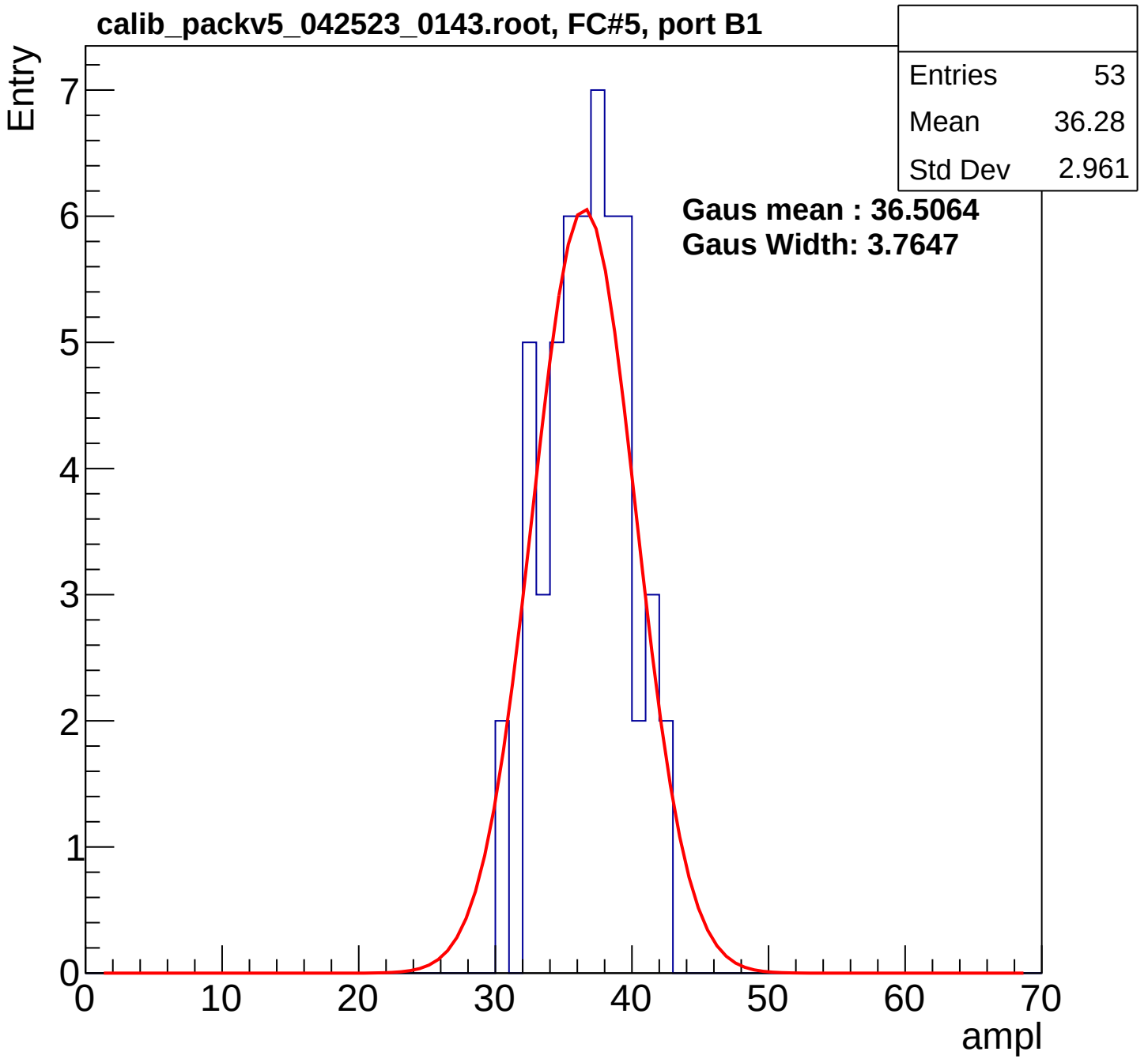
B0L000S, U15-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 36.28
Std Dev 2.961
Gaus mean : 36.5064
Gaus Width: 3.7647
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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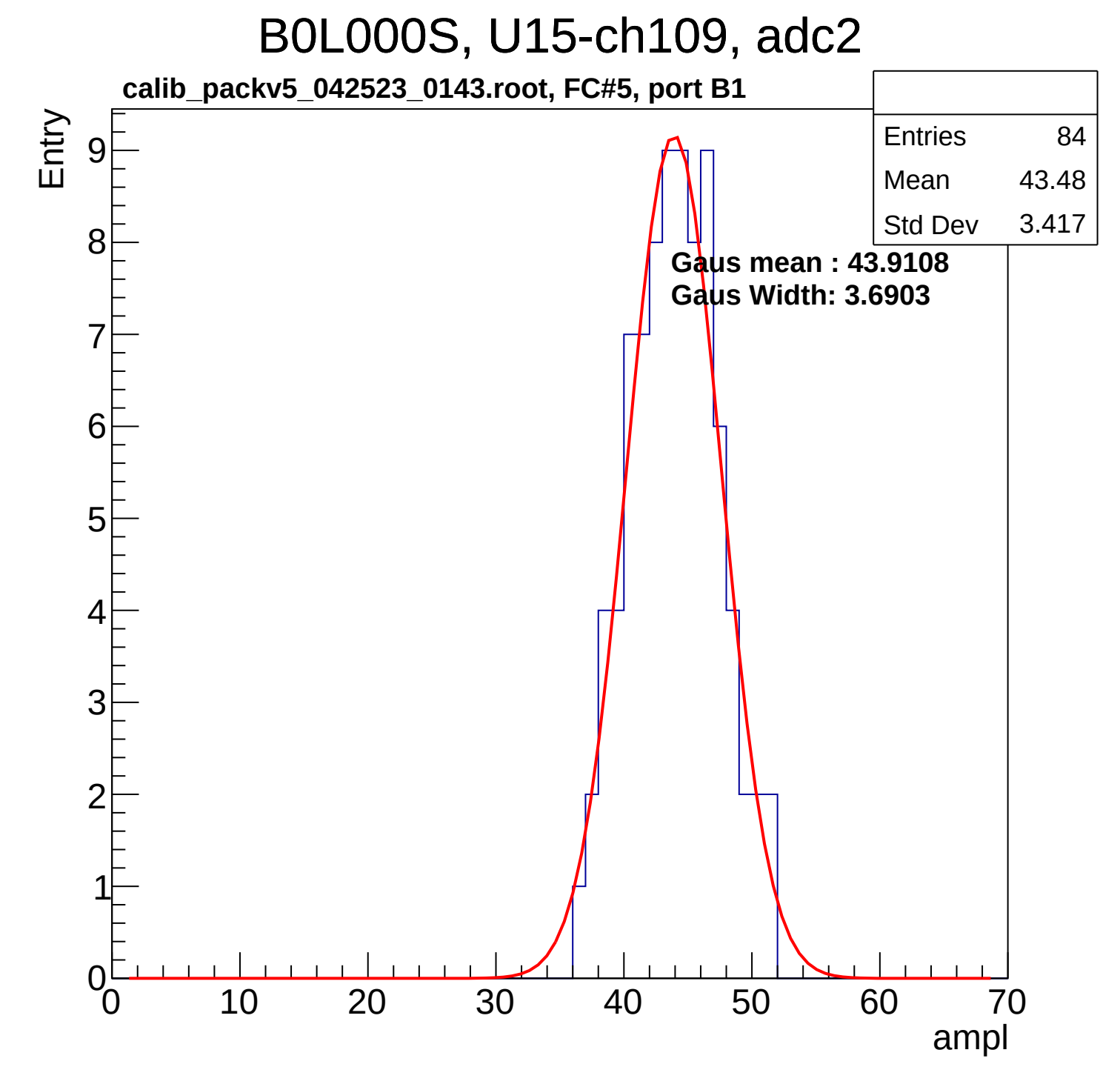
B0L000S, U15-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 43.48
Std Dev 3.417
Gaus mean : 43.9108
Gaus Width: 3.6903
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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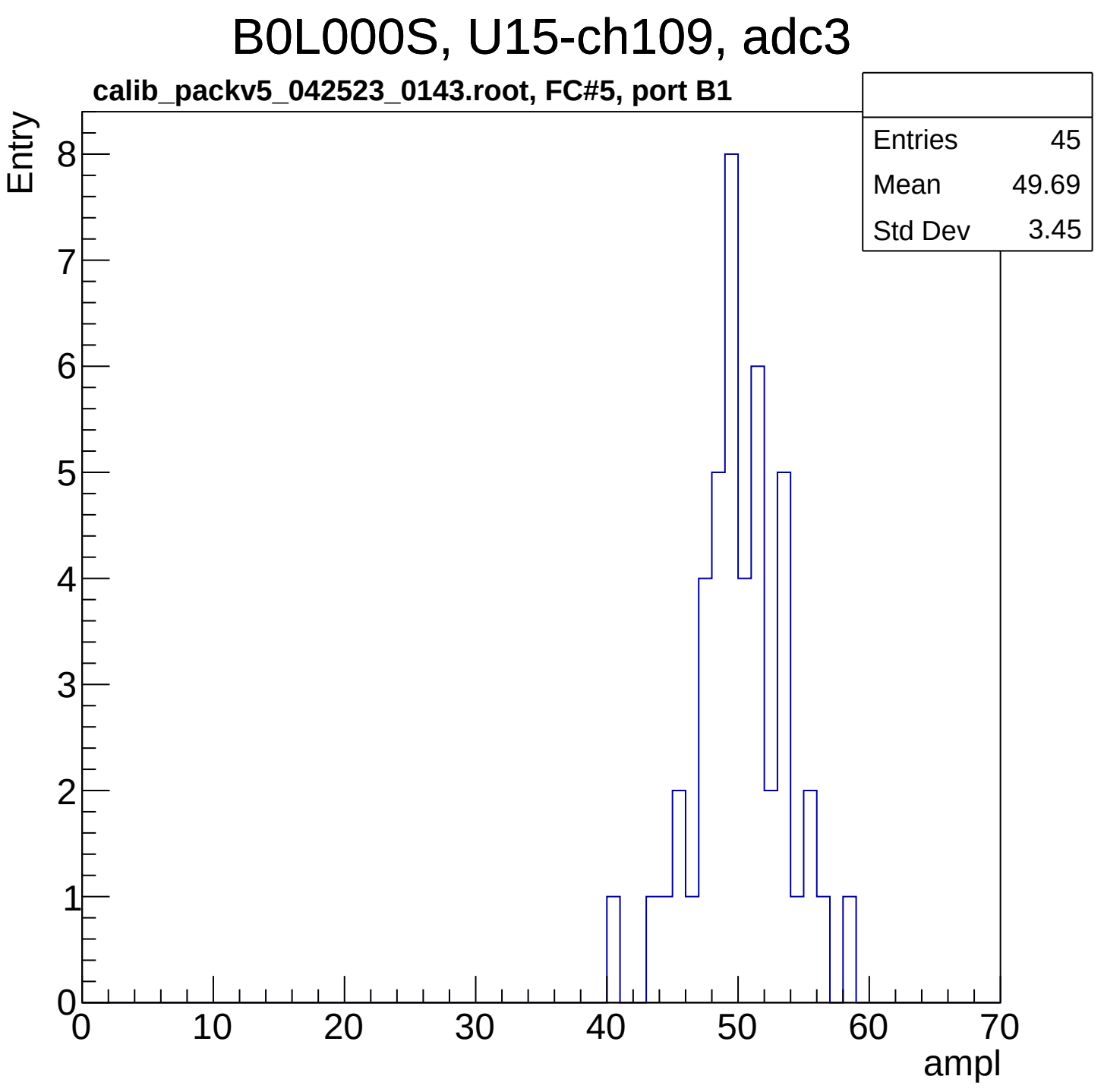
B0L000S, U15-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 49.69
Std Dev 3.45
Entry
ampl
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2
3
4
5
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7
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0
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60
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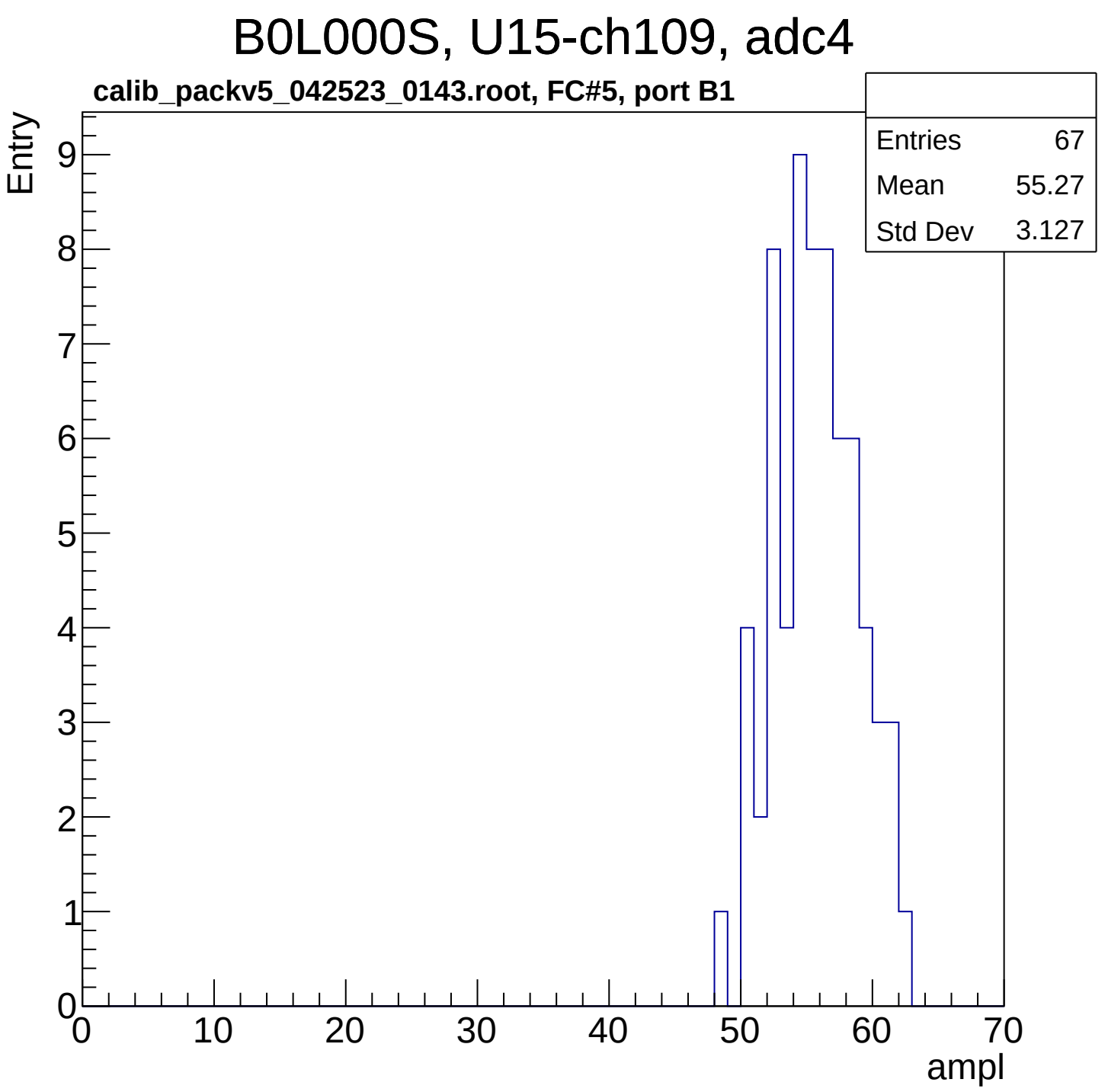
B0L000S, U15-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 55.27
Std Dev 3.127
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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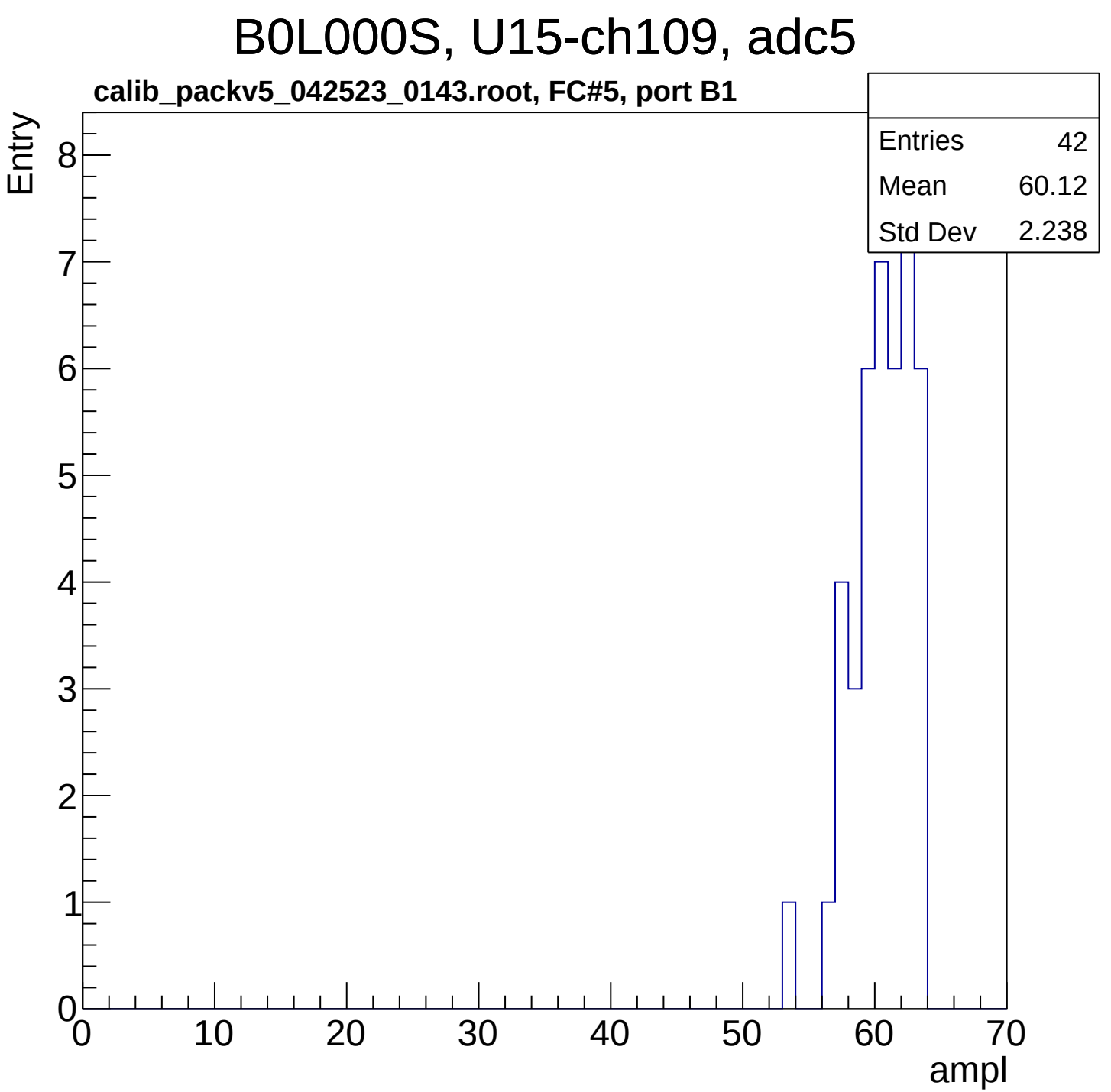
B0L000S, U15-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 60.12
Std Dev 2.238
Entry
ampl
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2
3
4
5
6
7
8
0
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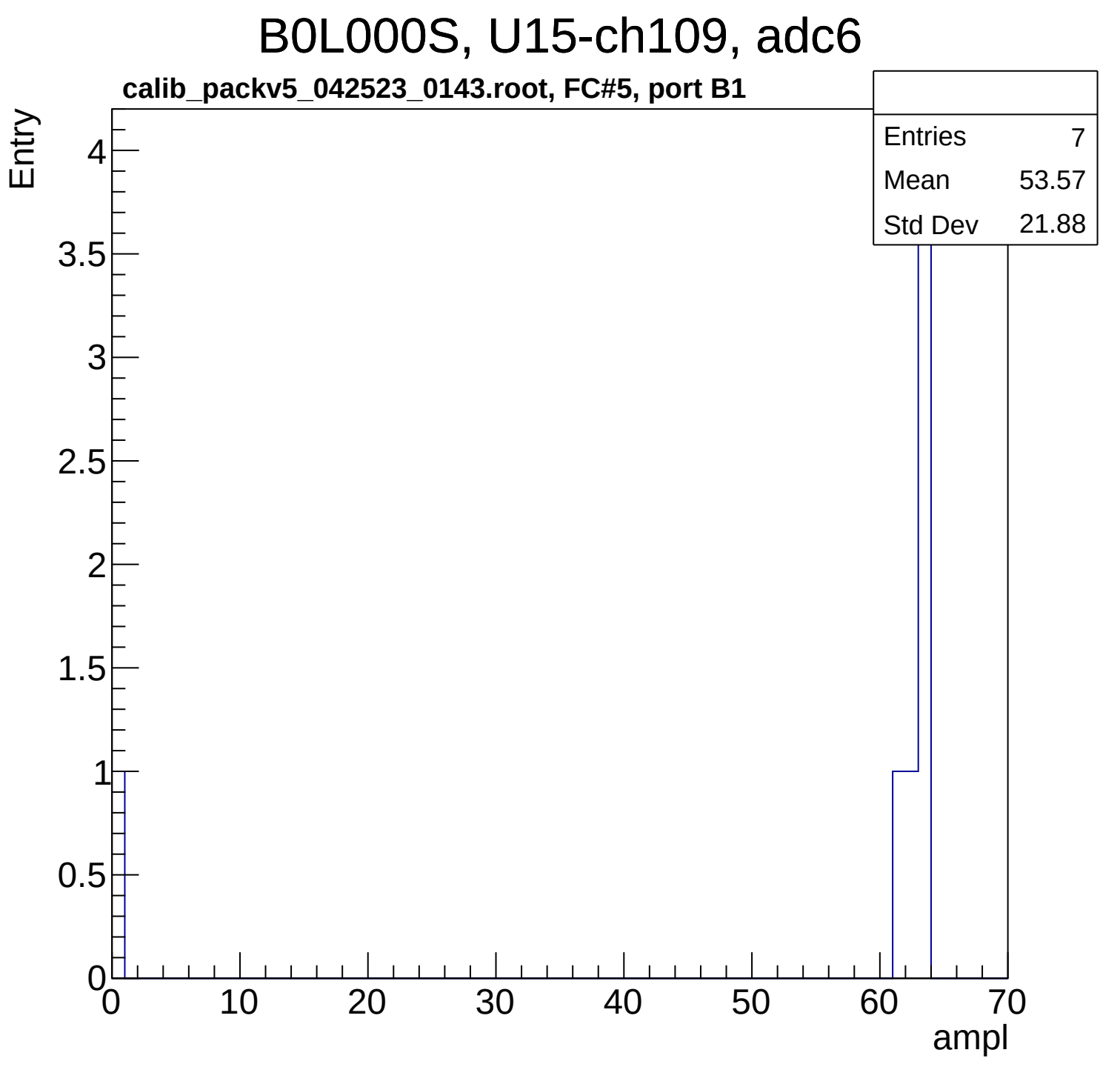
B0L000S, U15-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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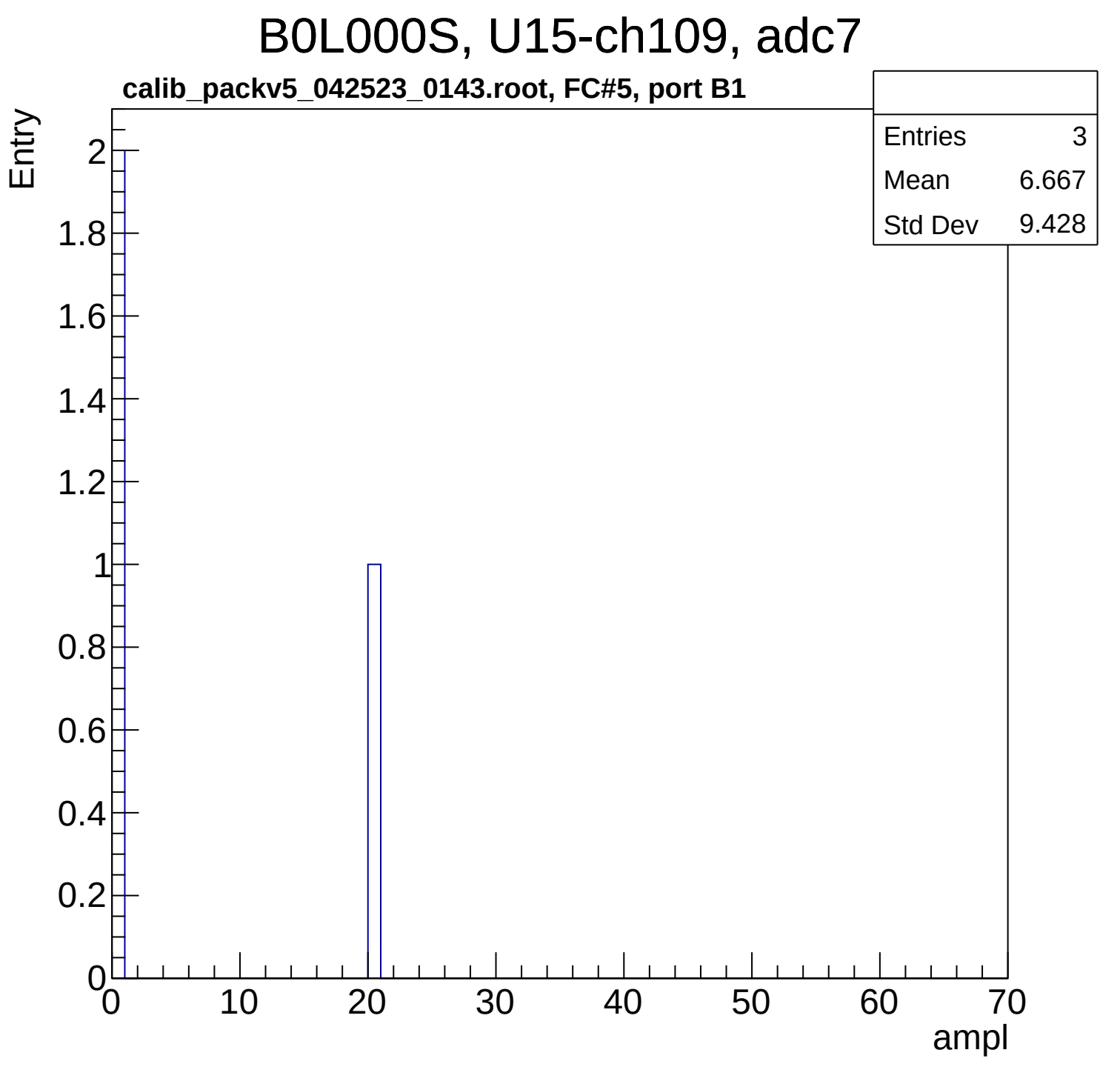
B0L000S, U15-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
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0
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60
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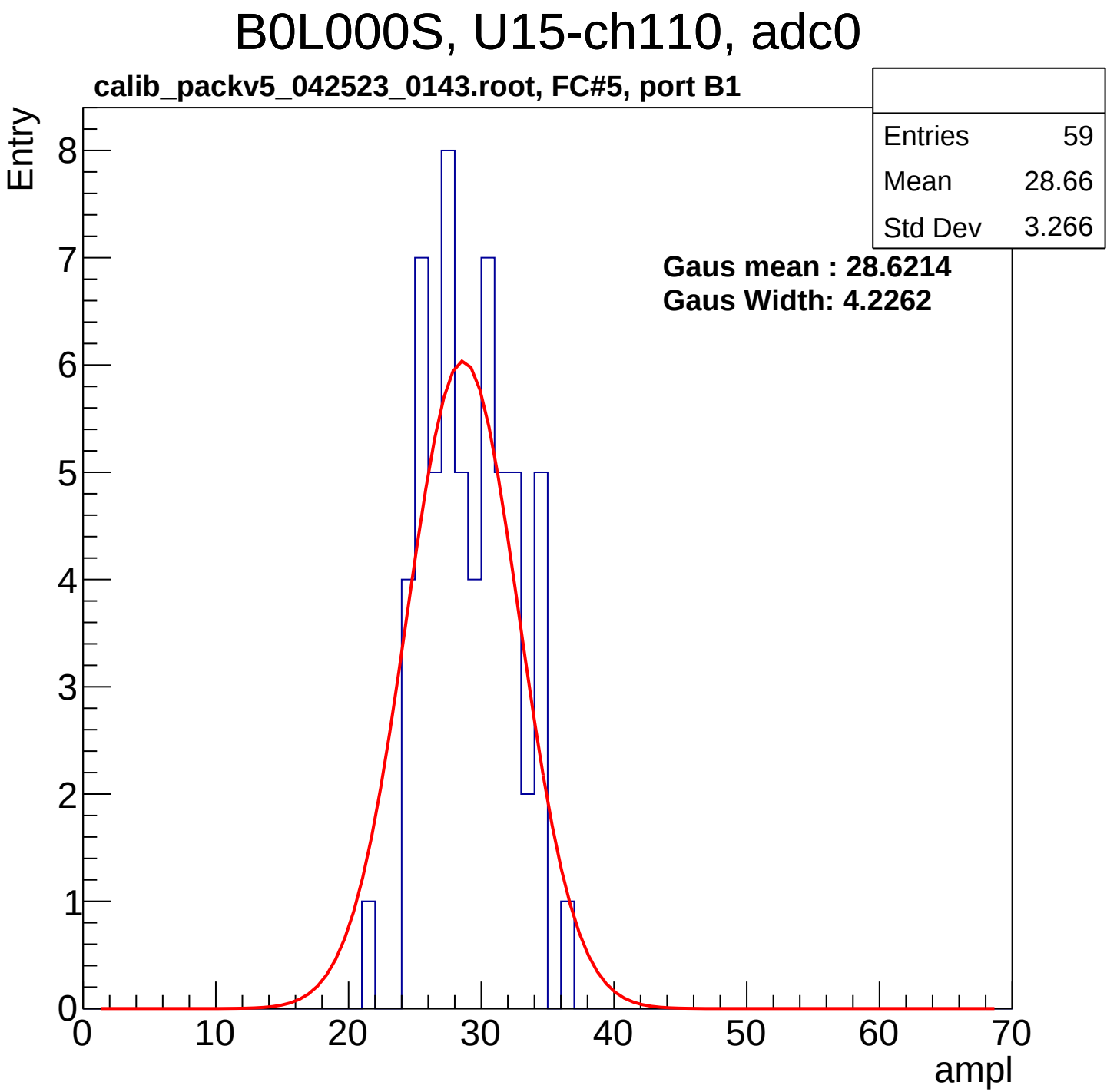
B0L000S, U15-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 28.66
Std Dev 3.266
Gaus mean : 28.6214
Gaus Width: 4.2262
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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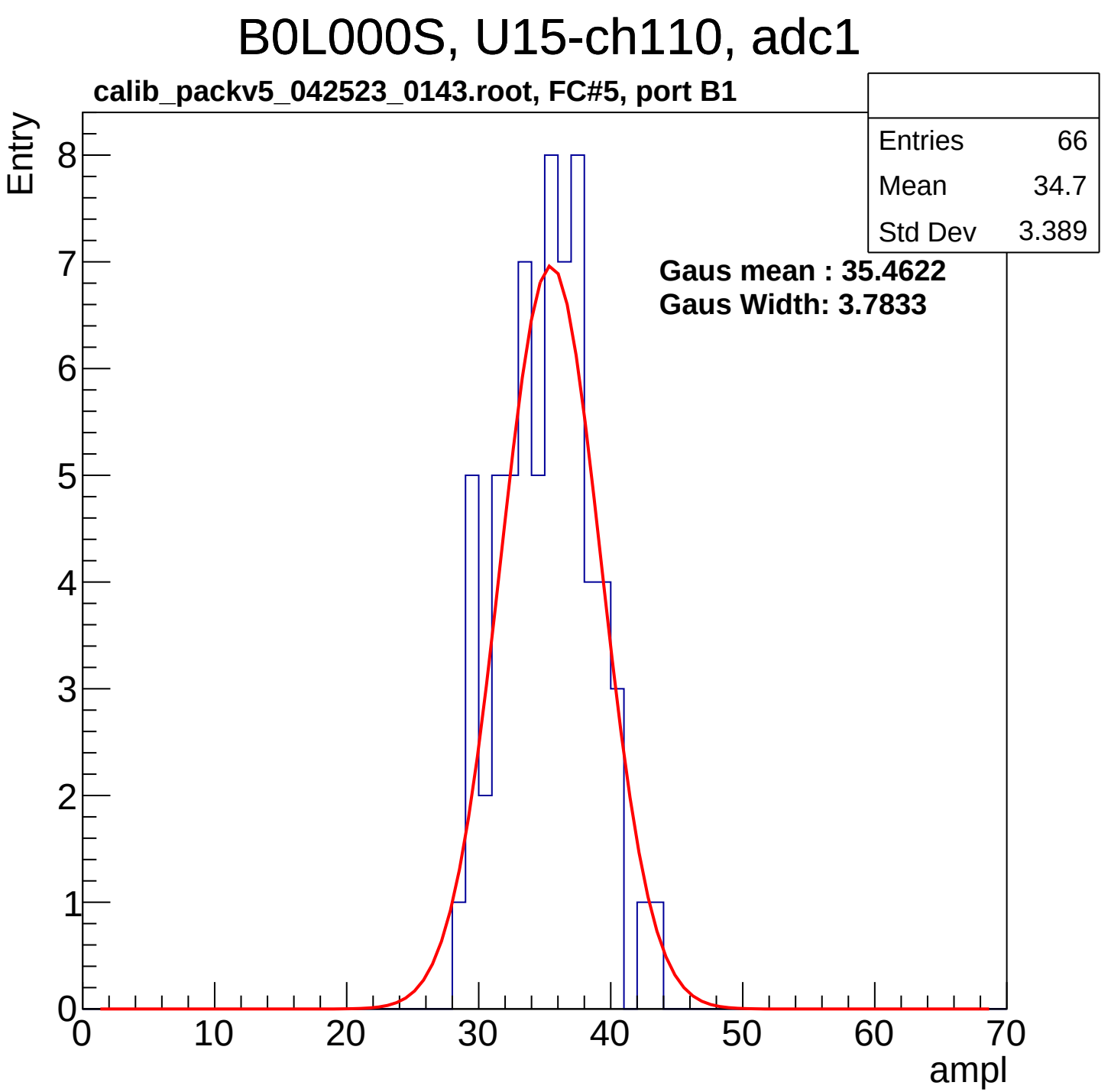
B0L000S, U15-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 34.7
Std Dev 3.389
Gaus mean : 35.4622
Gaus Width: 3.7833
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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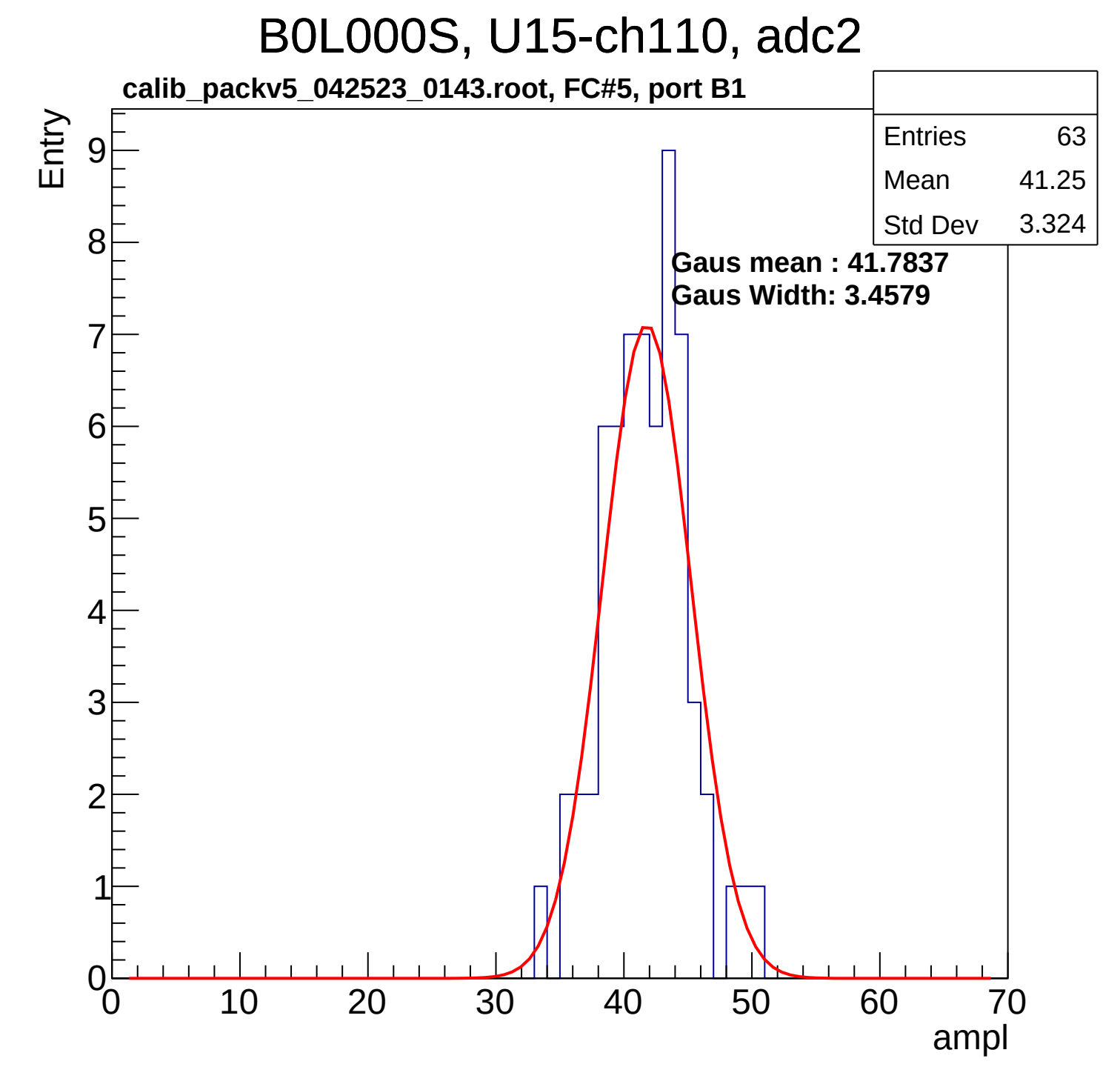
B0L000S, U15-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.25
Std Dev 3.324
Gaus mean : 41.7837
Gaus Width: 3.4579
Entry
ampl
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2
3
4
5
6
7
8
9
0
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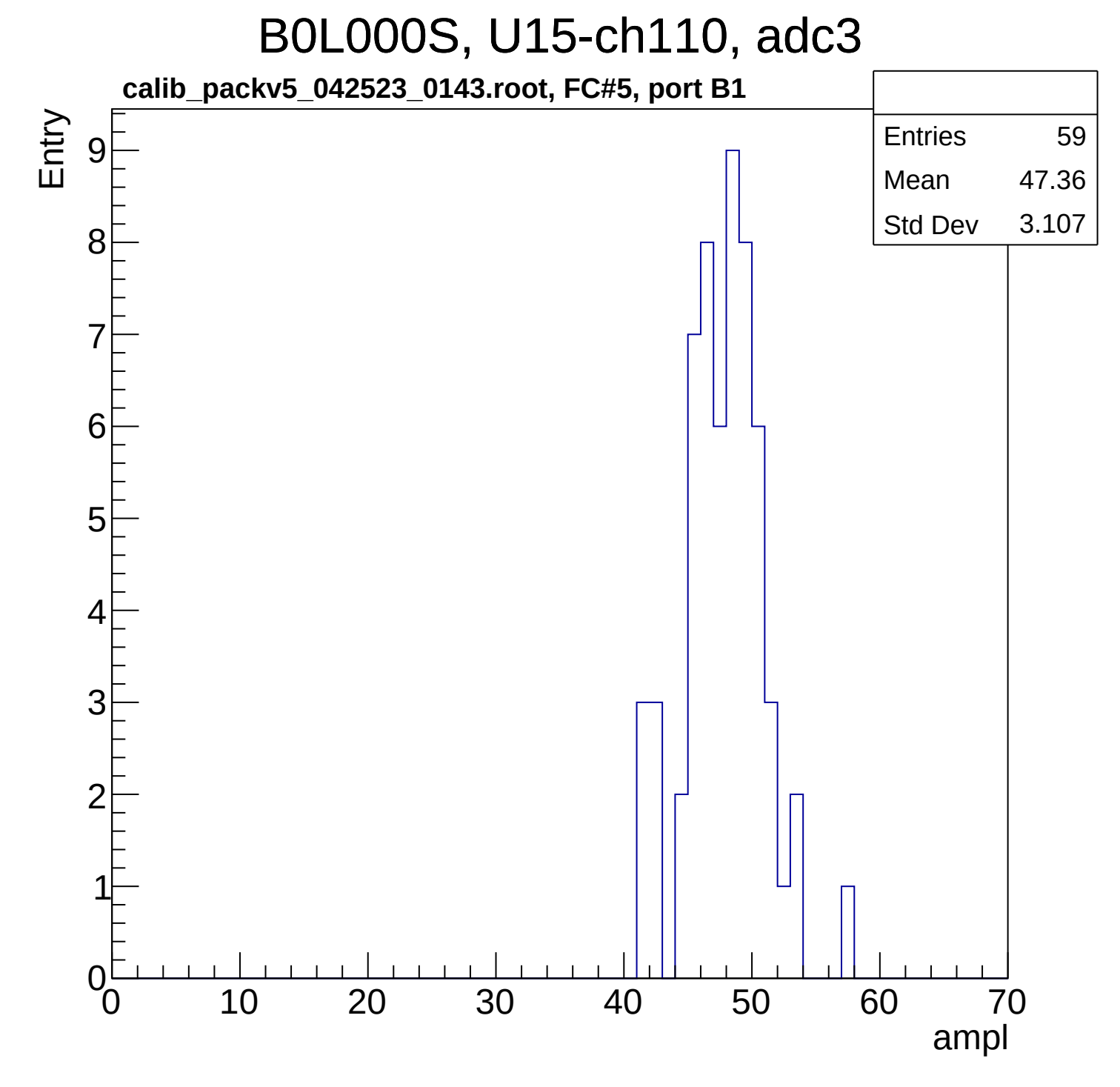
B0L000S, U15-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 47.36
Std Dev 3.107
Entry
ampl
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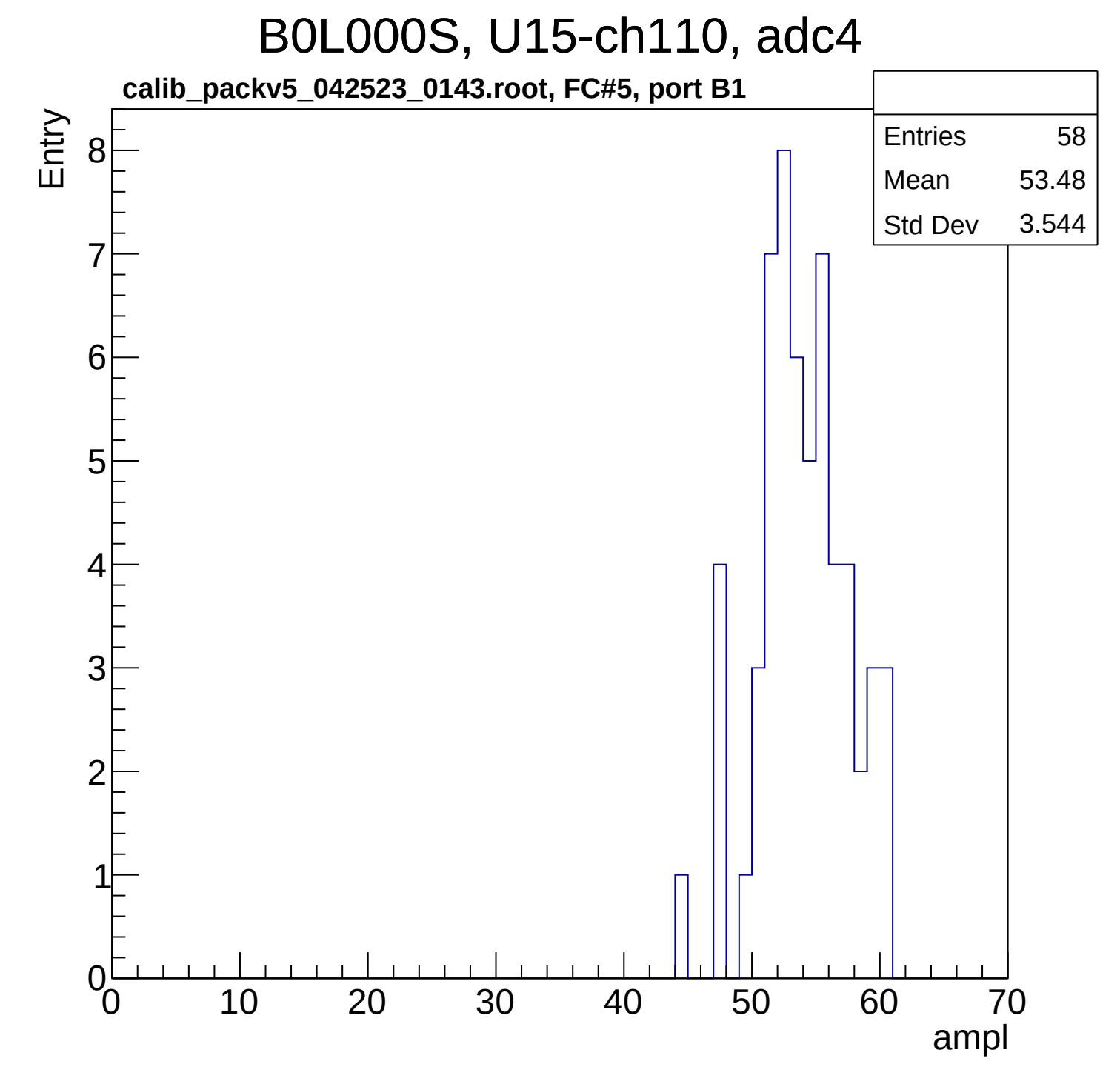
B0L000S, U15-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.48
Std Dev 3.544
Entry
ampl
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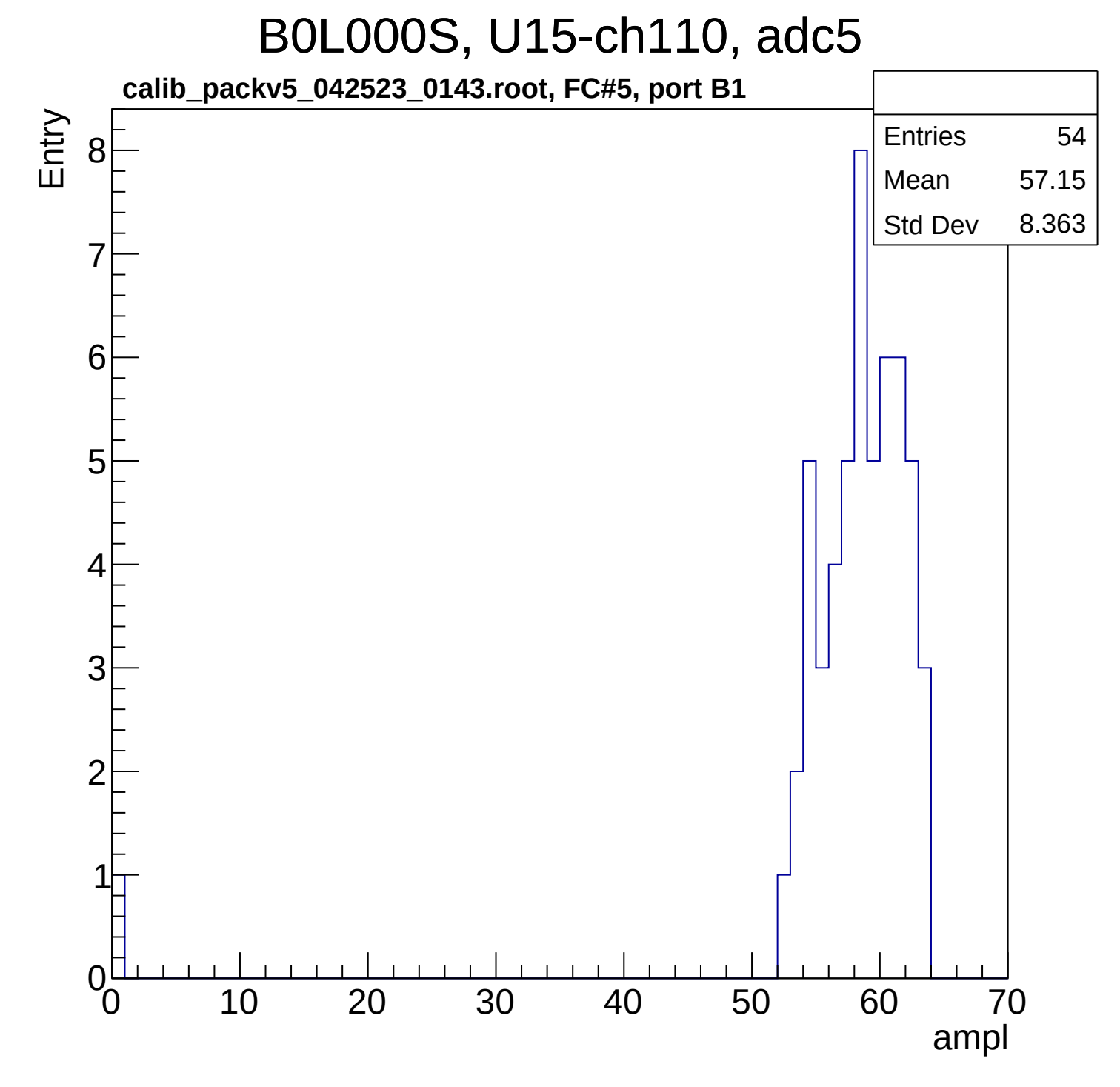
B0L000S, U15-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 57.15
Std Dev 8.363
Entry
ampl
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5
6
7
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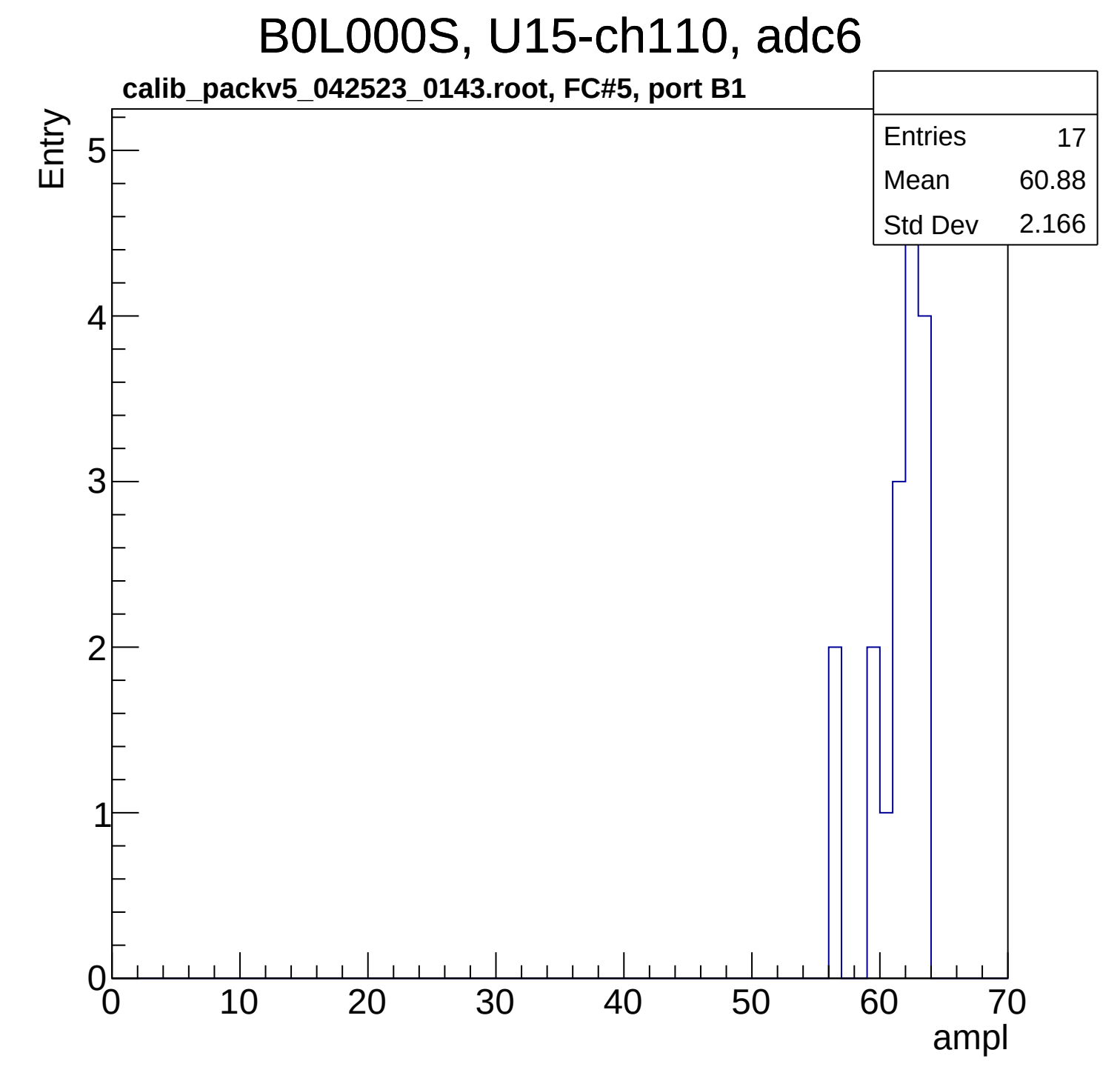
B0L000S, U15-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 60.88
Std Dev 2.166
Entry
ampl
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2
3
4
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20
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60
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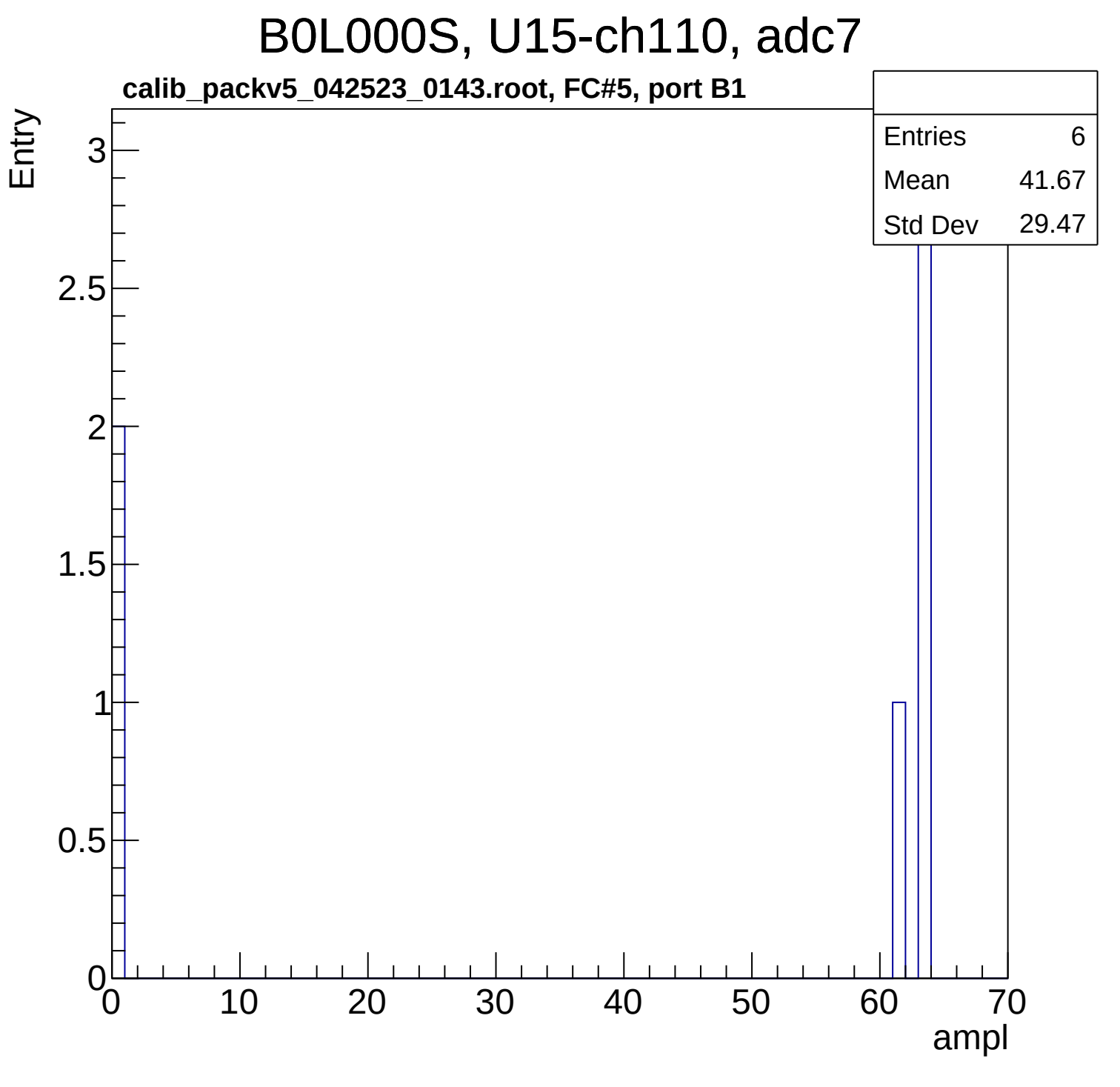
B0L000S, U15-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 41.67
Std Dev 29.47
Entry
ampl
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60
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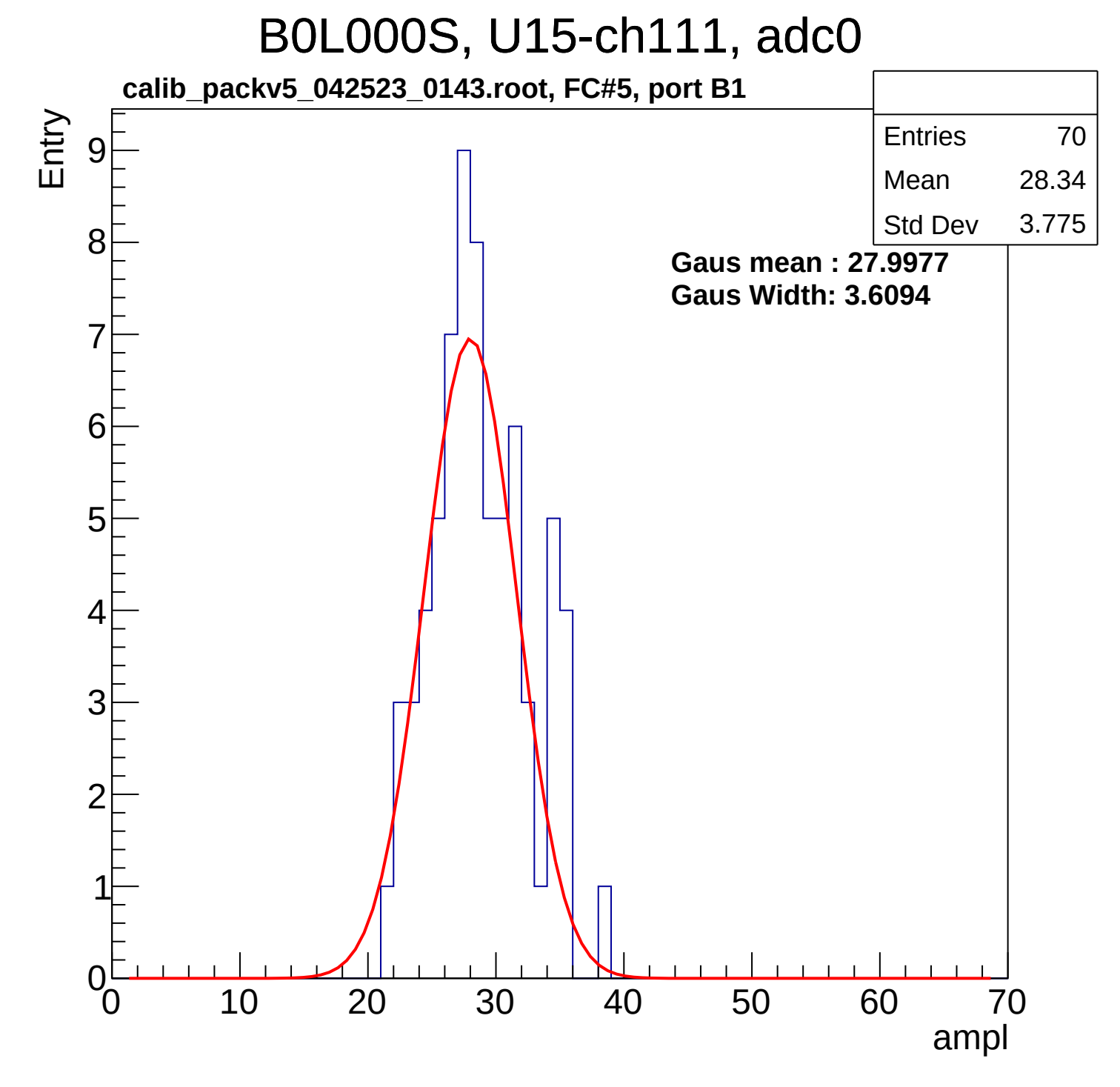
B0L000S, U15-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.34
Std Dev 3.775
Gaus mean : 27.9977
Gaus Width: 3.6094
Entry
ampl
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2
3
4
5
6
7
8
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0
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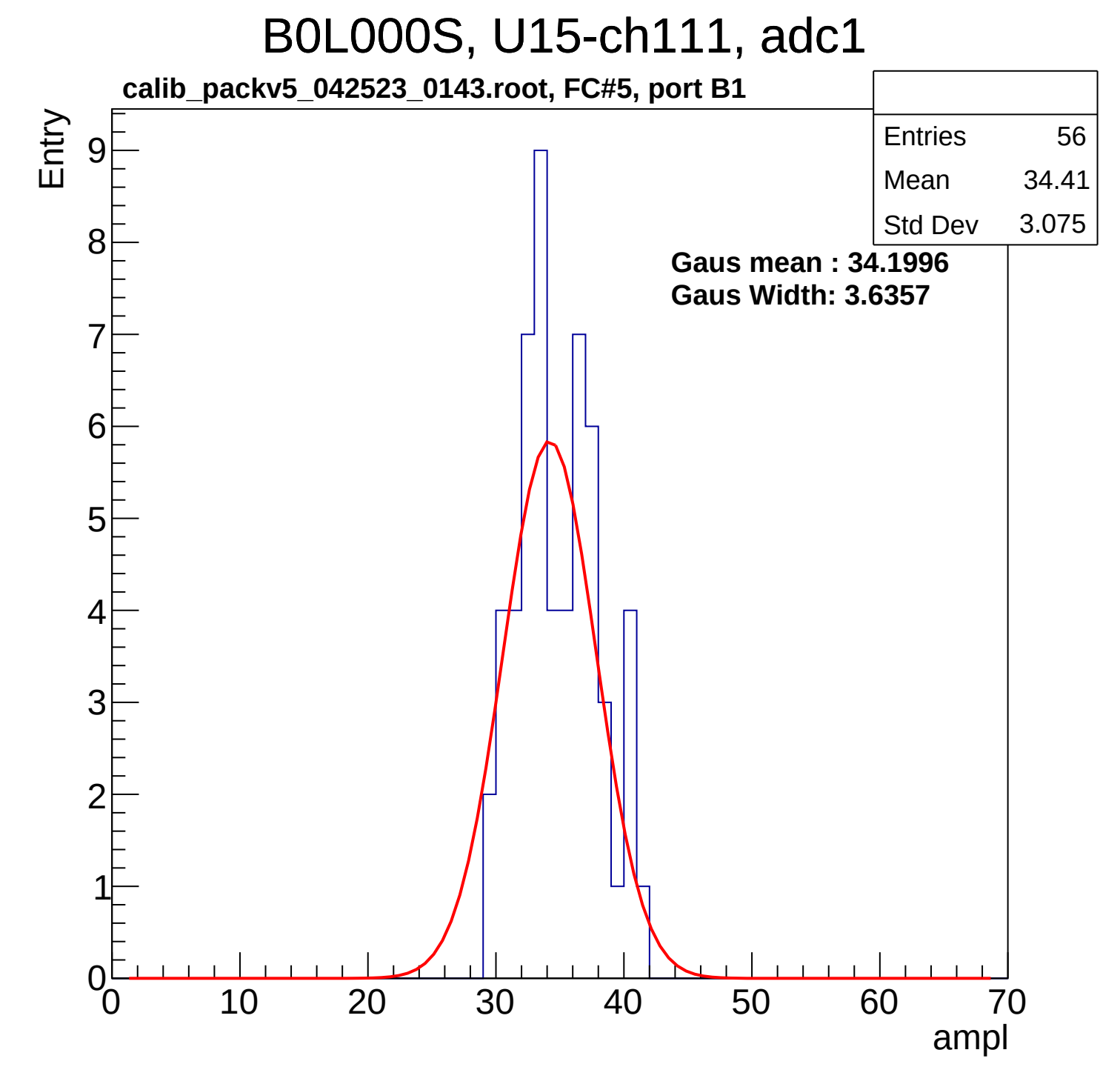
B0L000S, U15-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 34.41
Std Dev 3.075
Gaus mean : 34.1996
Gaus Width: 3.6357
Entry
ampl
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2
3
4
5
6
7
8
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0
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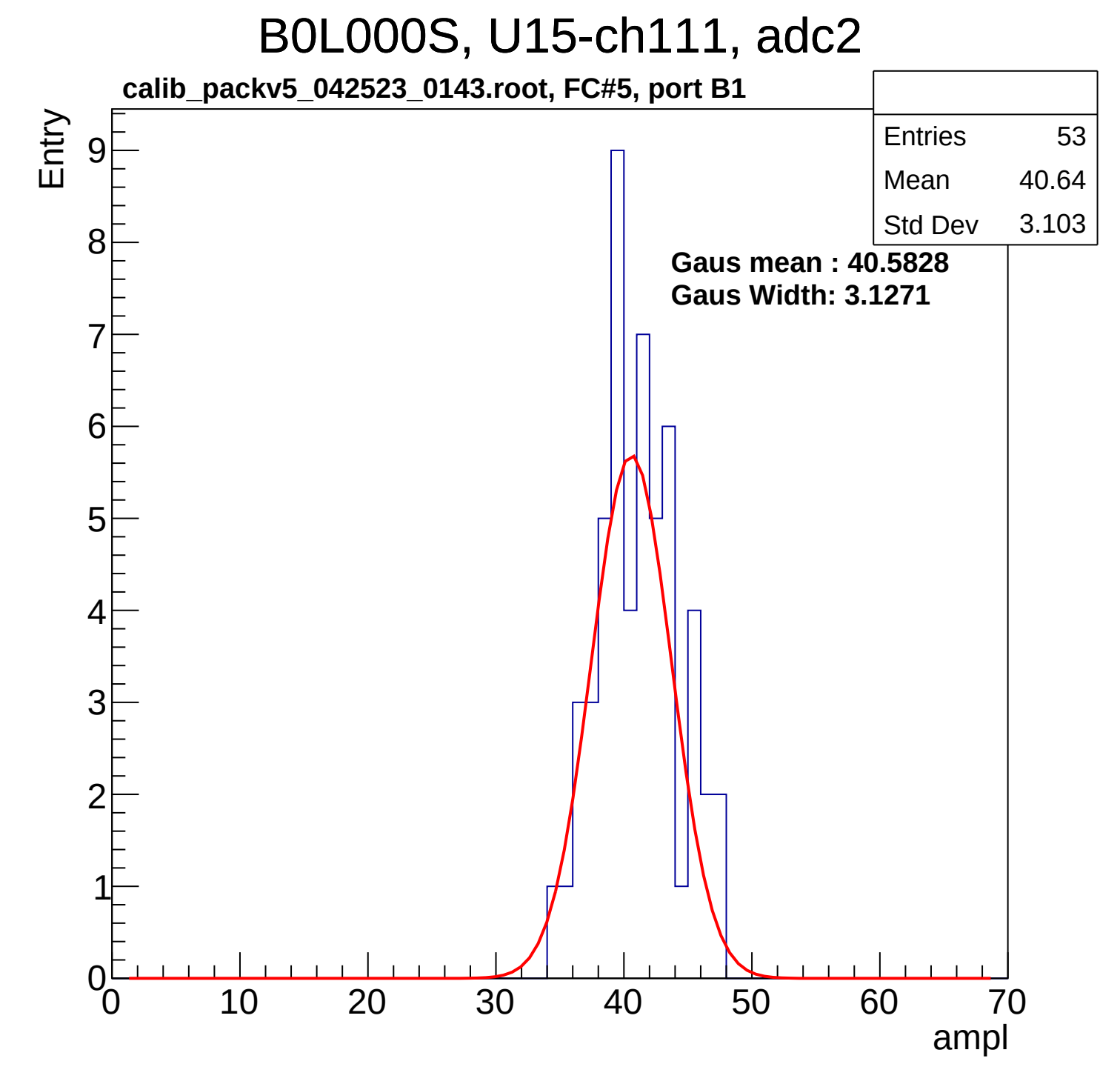
B0L000S, U15-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 40.64
Std Dev 3.103
Gaus mean : 40.5828
Gaus Width: 3.1271
Entry
ampl
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2
3
4
5
6
7
8
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0
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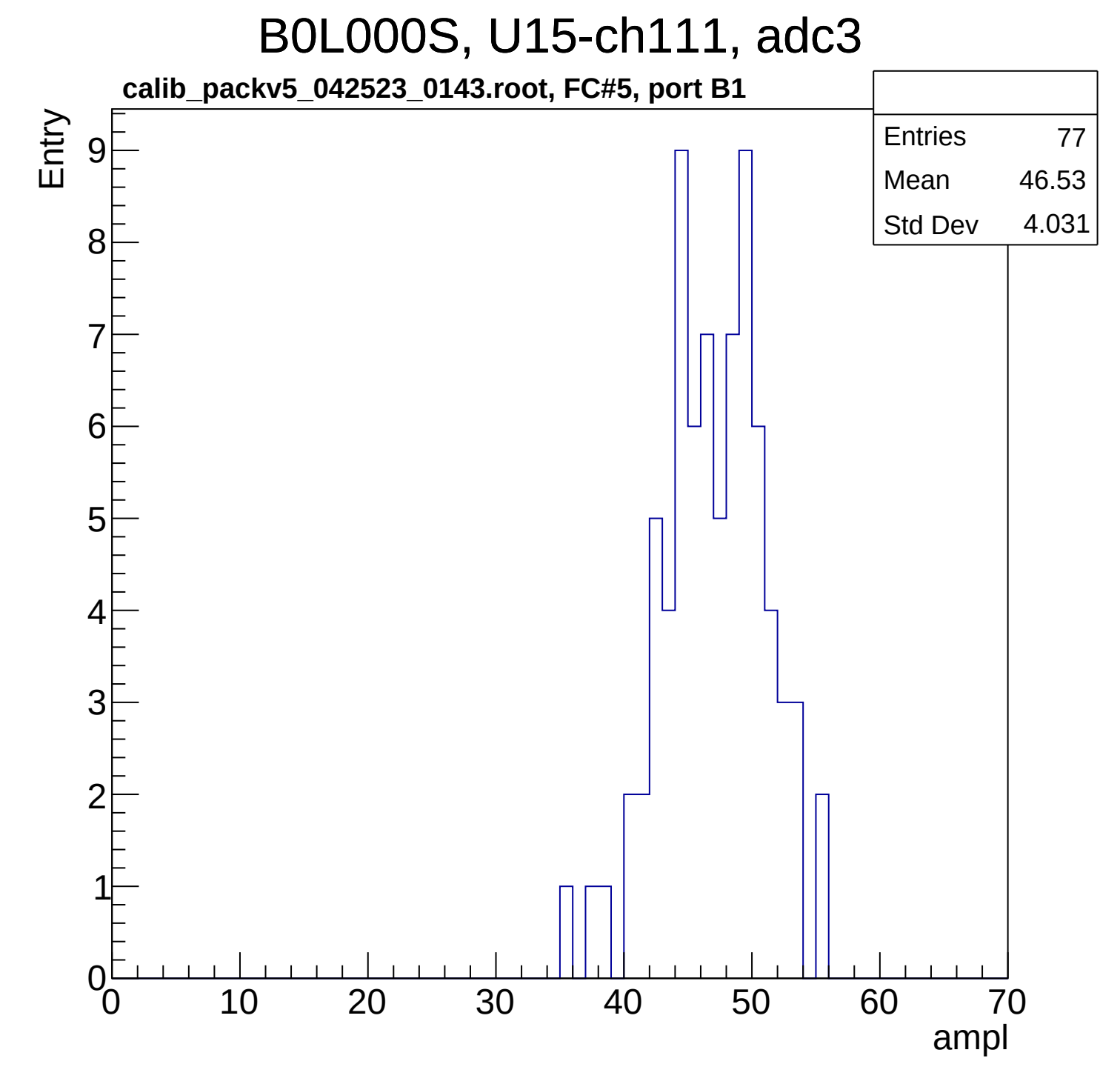
B0L000S, U15-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 46.53
Std Dev 4.031
Entry
ampl
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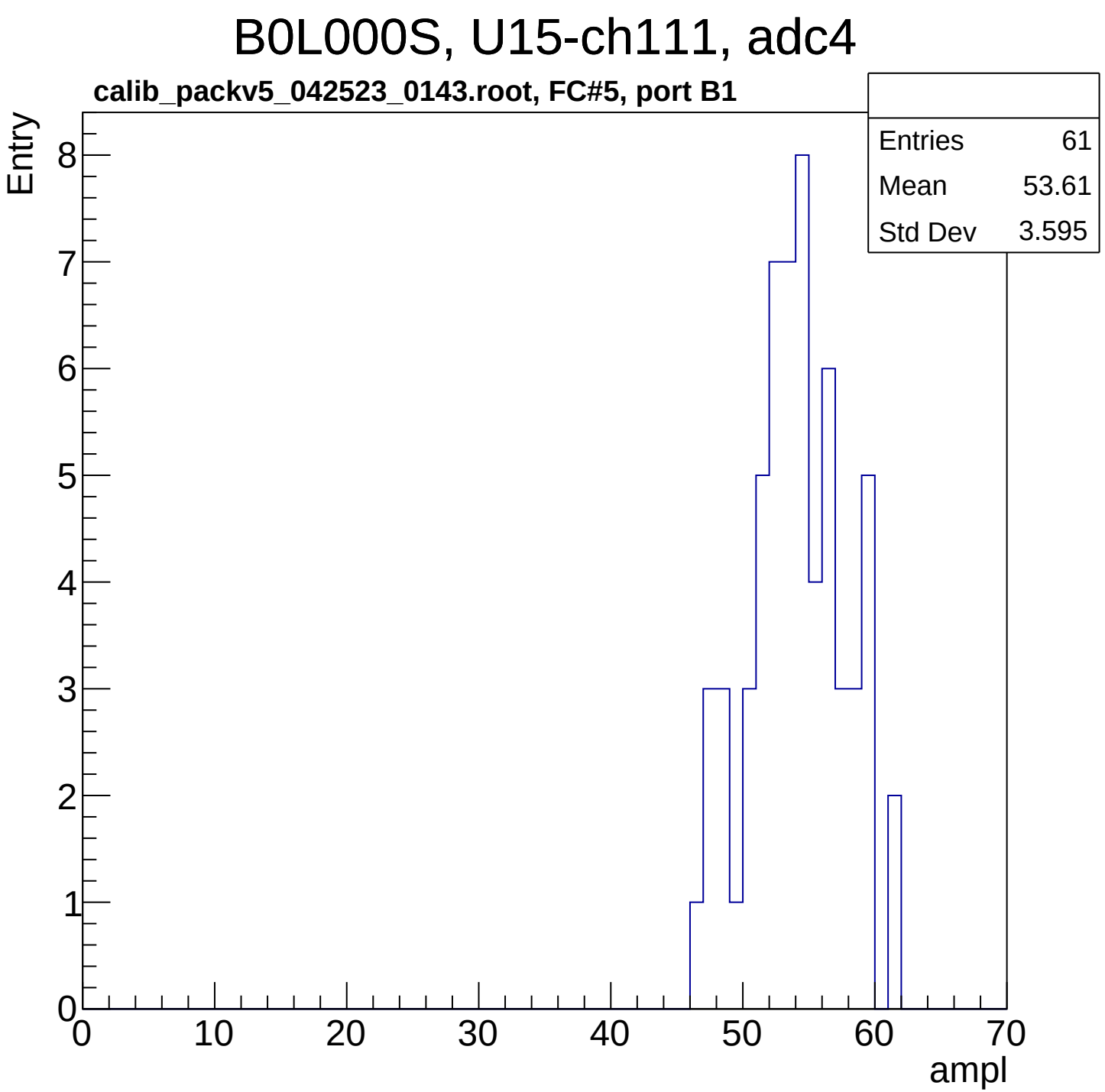
B0L000S, U15-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.61
Std Dev 3.595
Entry
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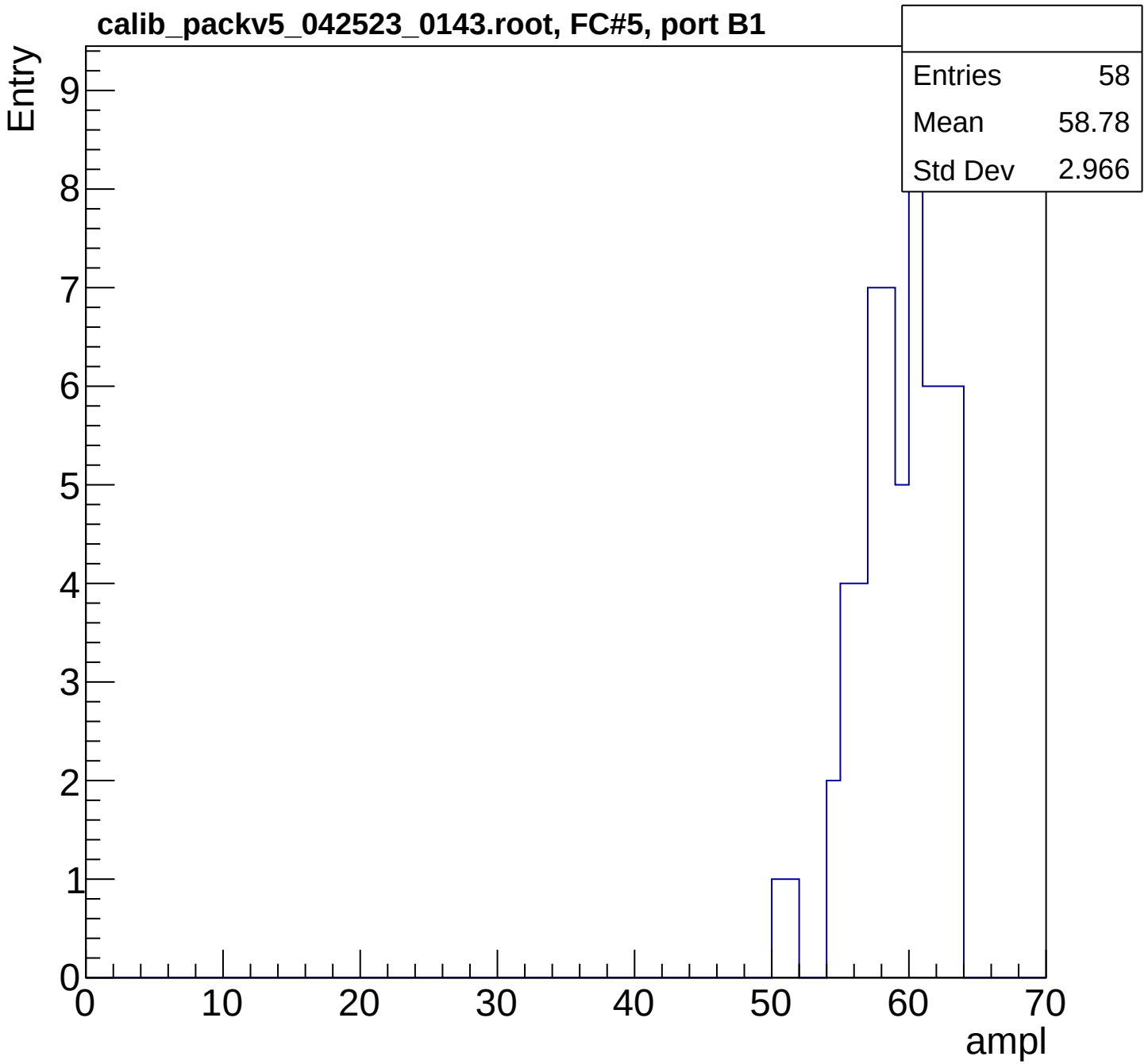
B0L000S, U15-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.78
Std Dev 2.966
Entry
ampl
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6
7
8
9
0
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20
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60
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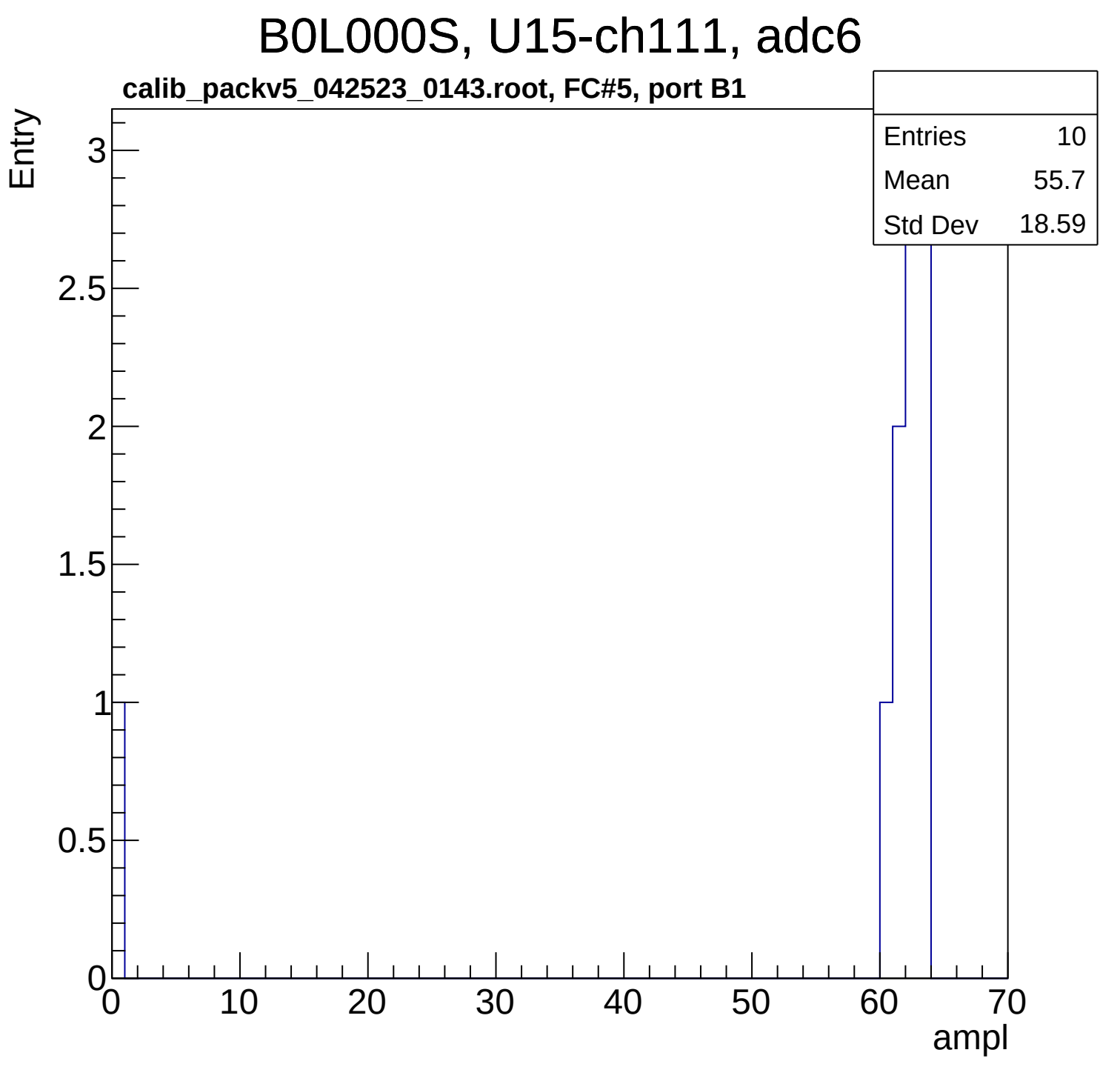
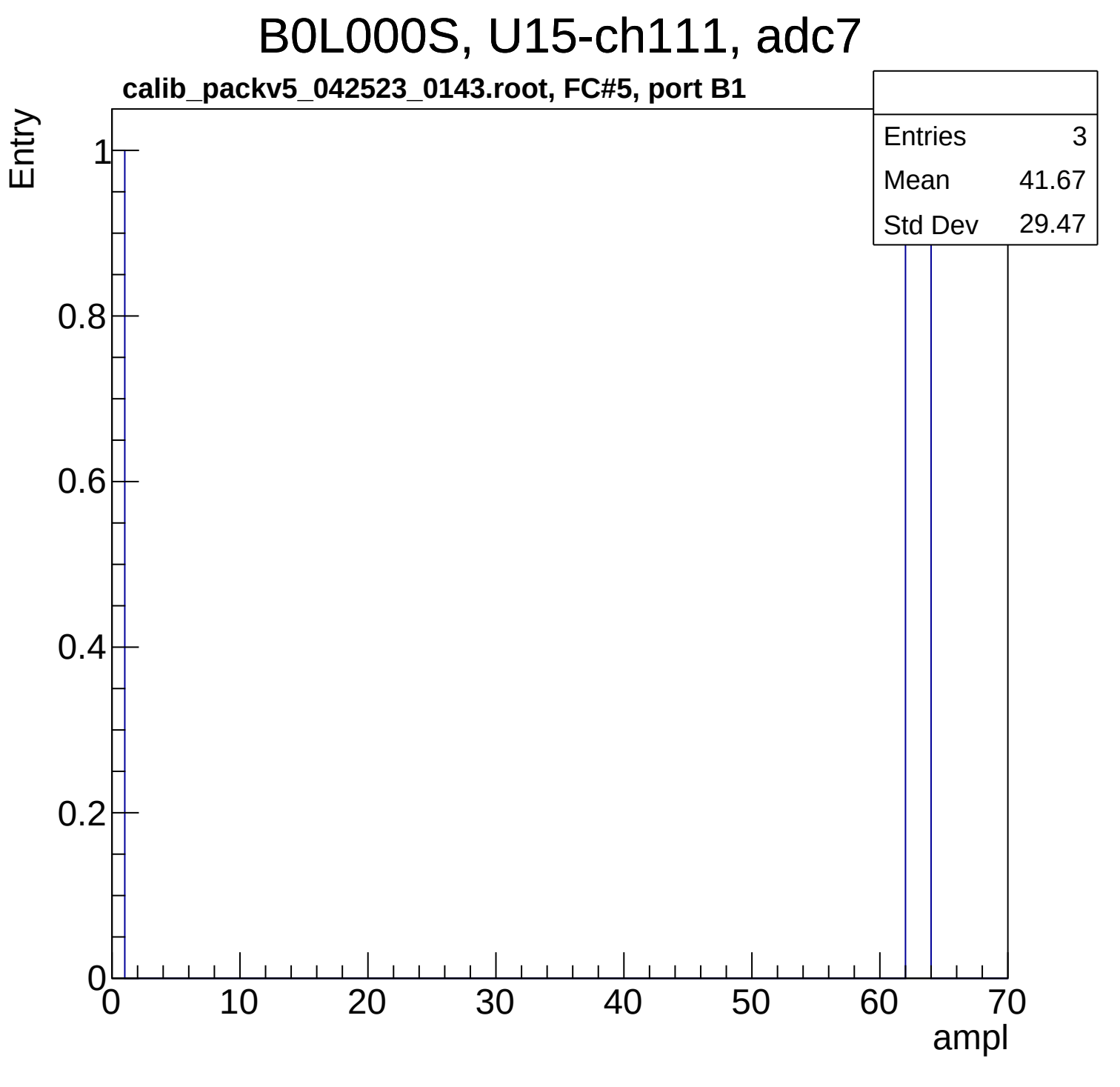

B0L000S, U15-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.7
Std Dev 18.59
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.2
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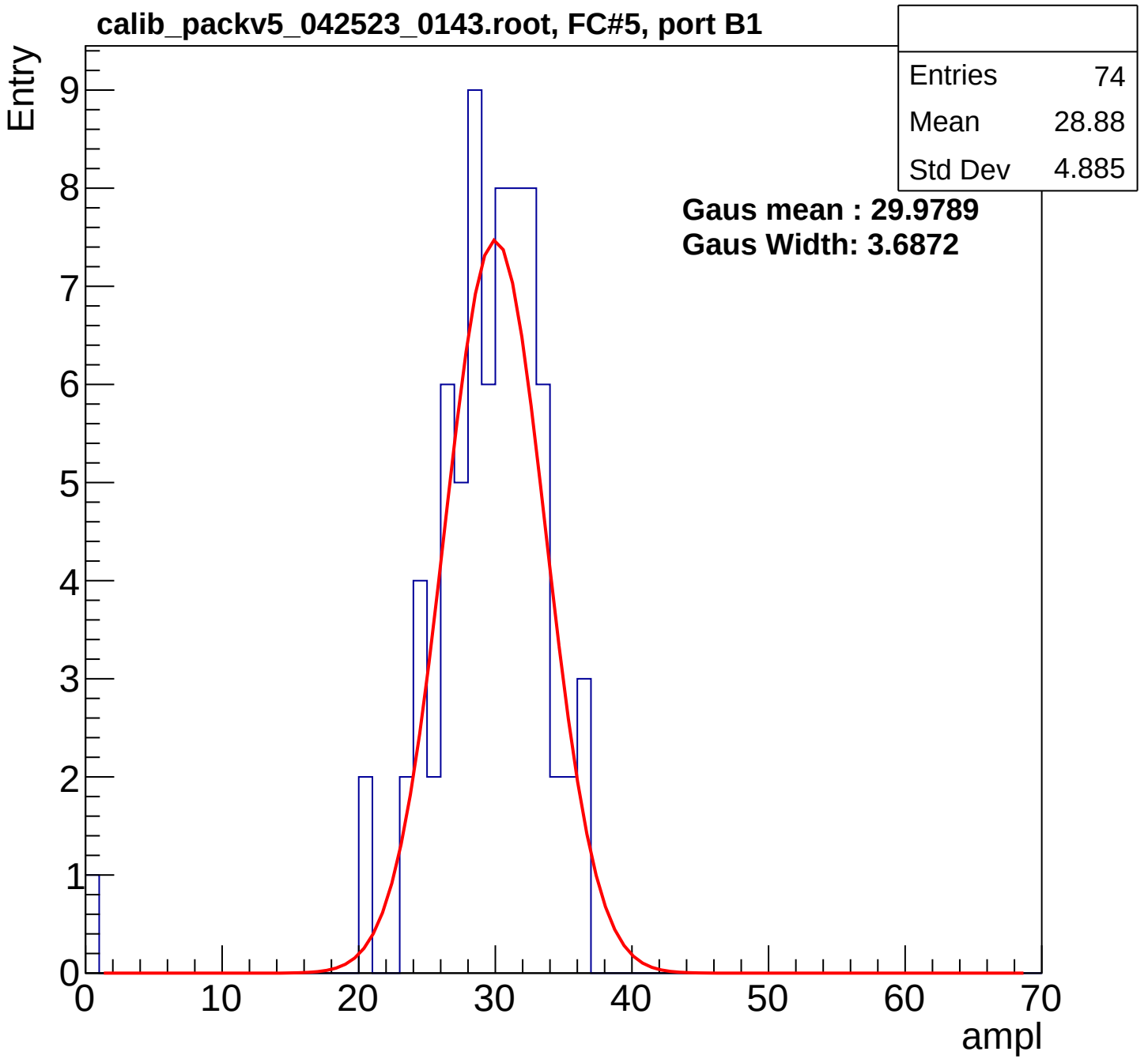
ampl


B0L000S, U15-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.88
Std Dev 4.885
Gaus mean : 29.9789
Gaus Width: 3.6872
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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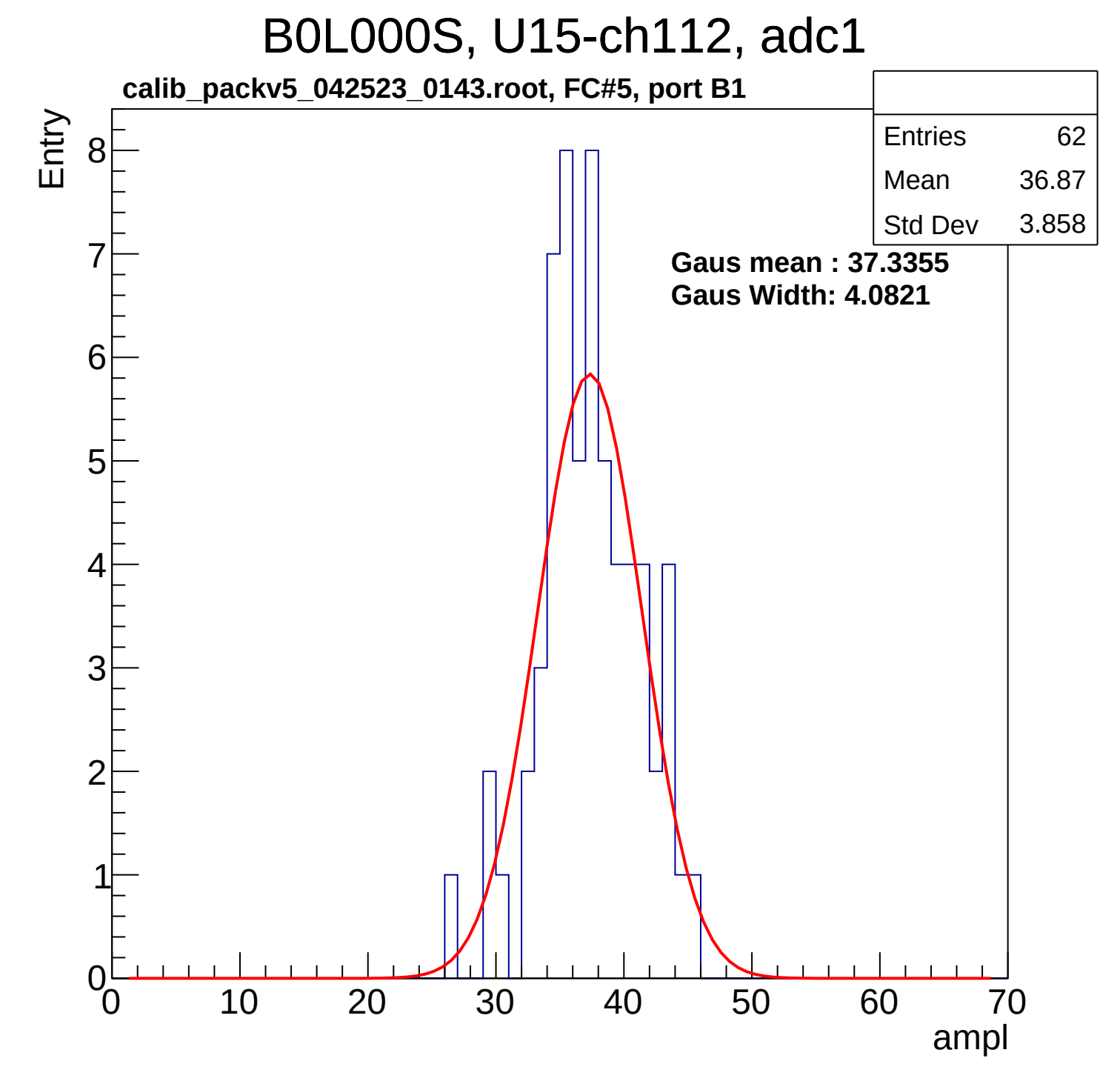
B0L000S, U15-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.87
Std Dev 3.858
Gaus mean : 37.3355
Gaus Width: 4.0821
Entry
ampl
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1
2
3
4
5
6
7
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0
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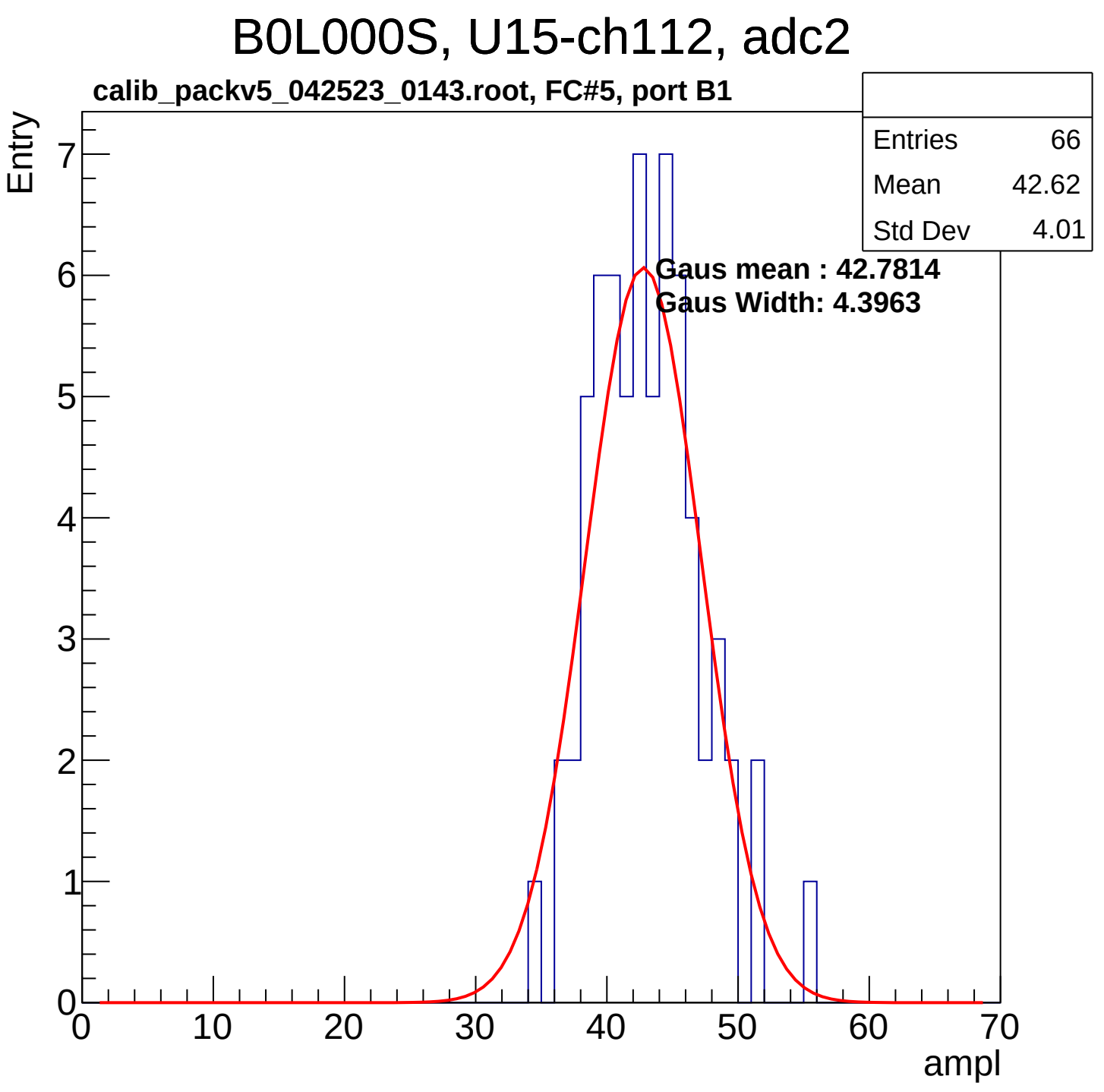
B0L000S, U15-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.62
Std Dev 4.01
Gaus mean : 42.7814
Gaus Width: 4.3963
Entry
ampl
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1
2
3
4
5
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0
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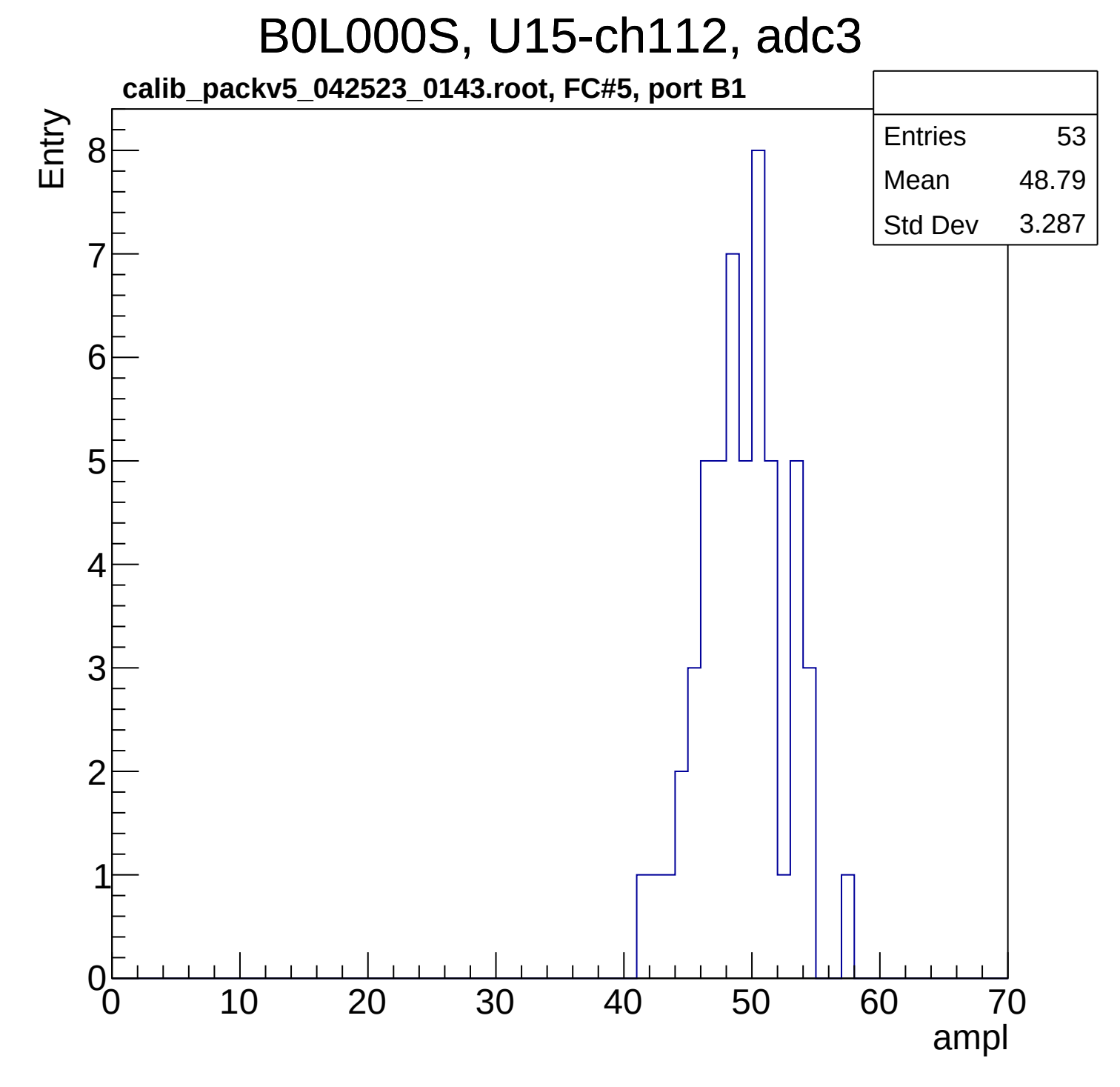
B0L000S, U15-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 48.79
Std Dev 3.287
Entry
ampl
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2
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4
5
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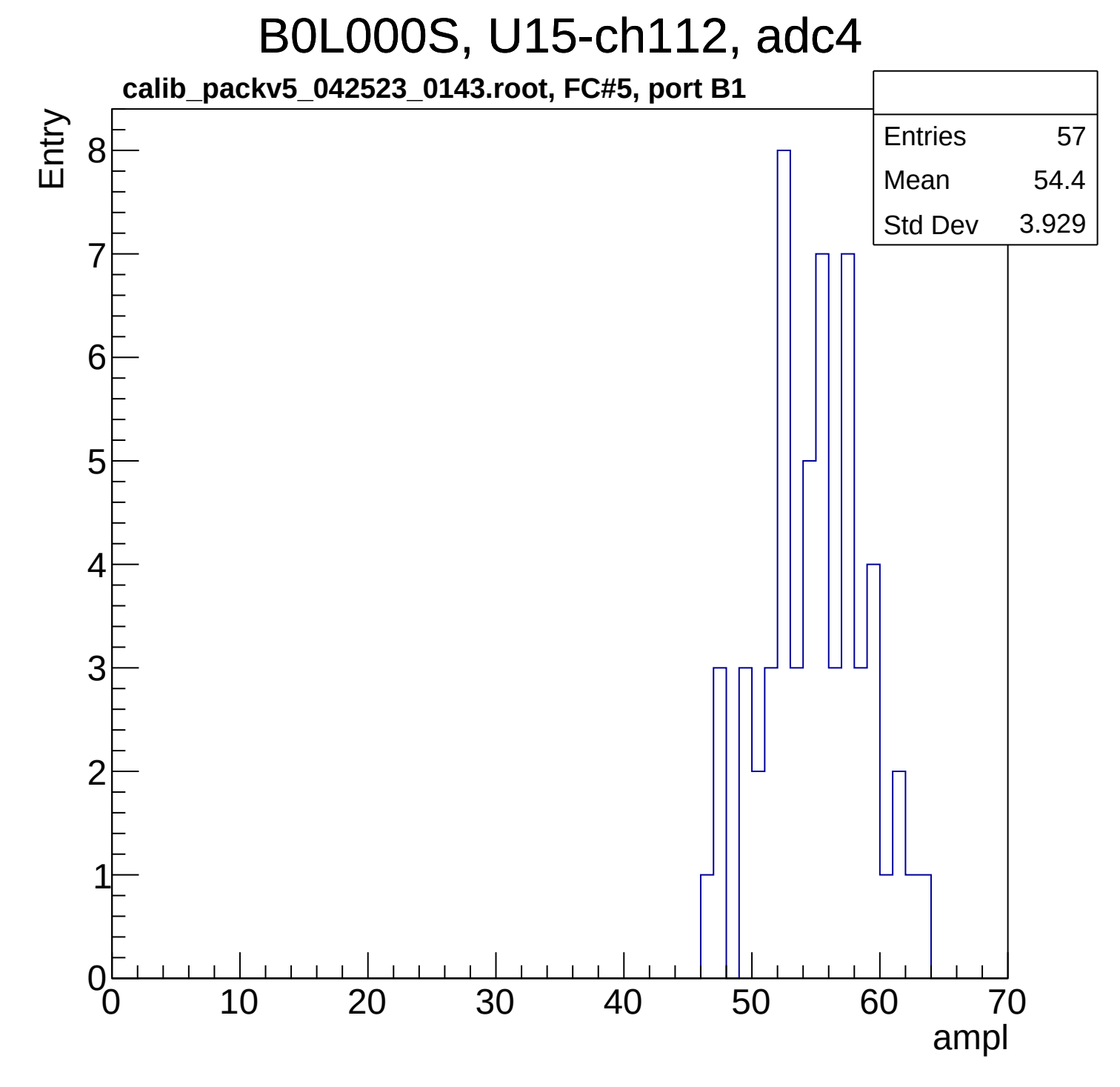
B0L000S, U15-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.4
Std Dev 3.929
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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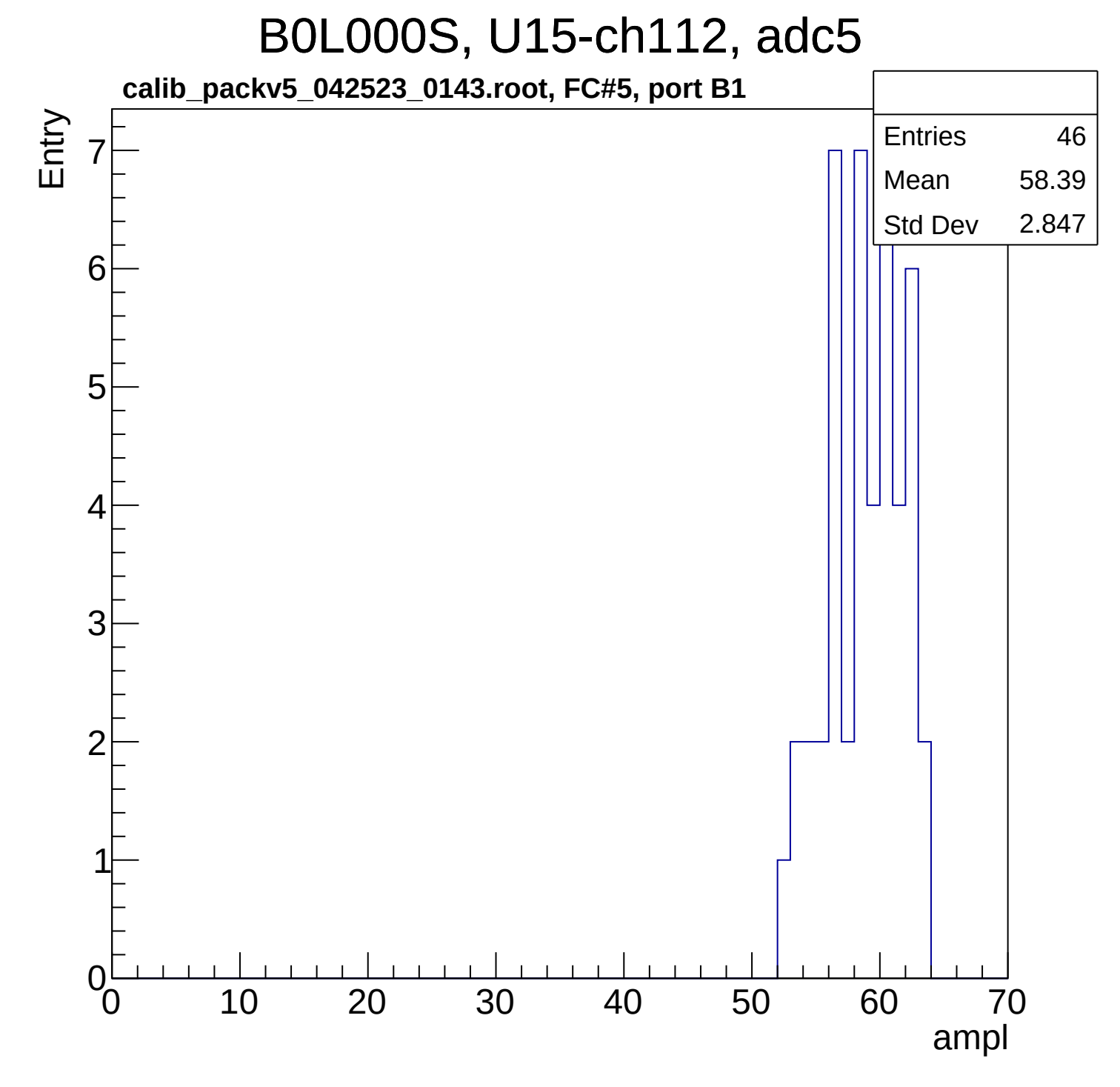
B0L000S, U15-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.39
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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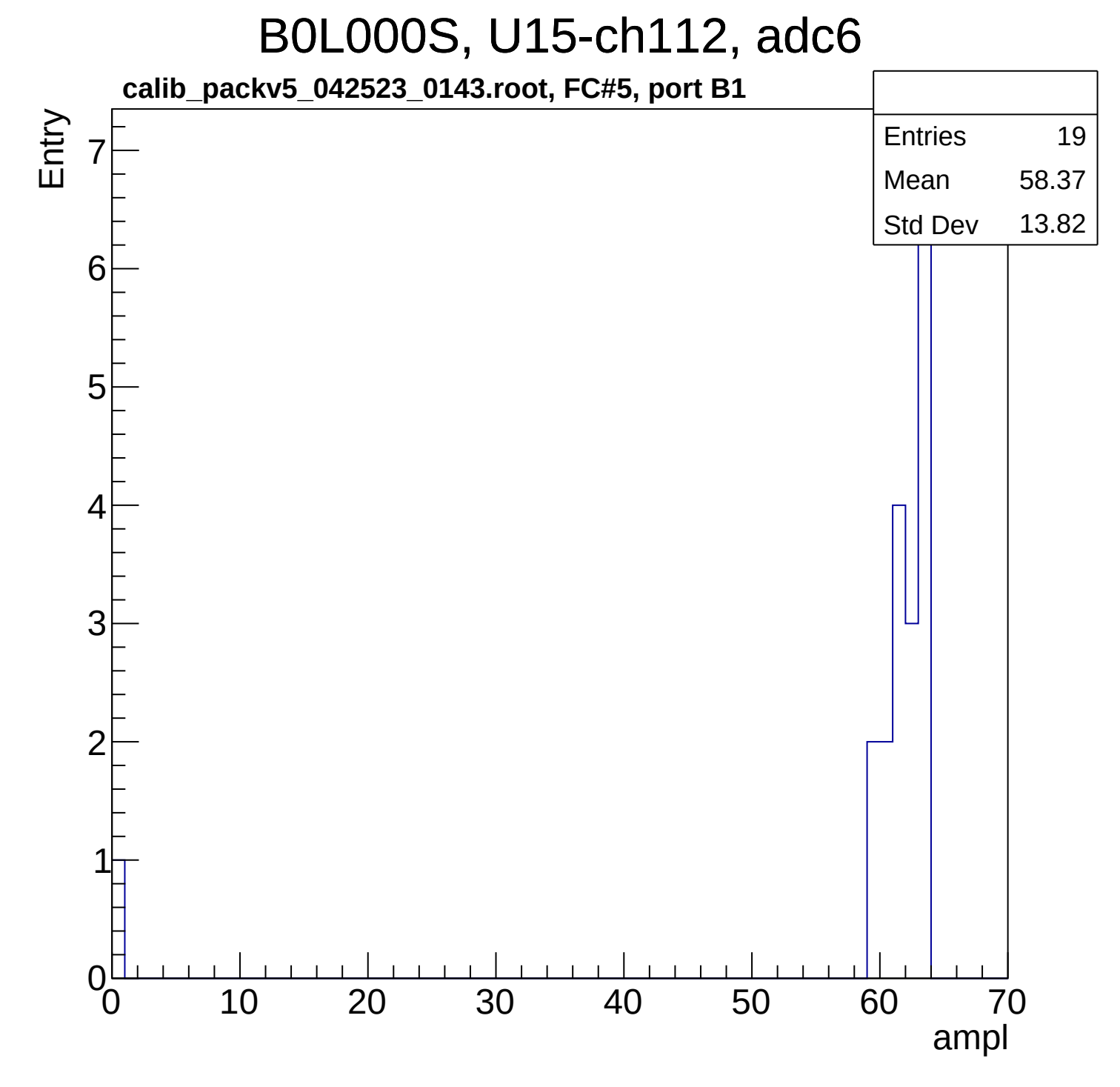
B0L000S, U15-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.37
Std Dev 13.82
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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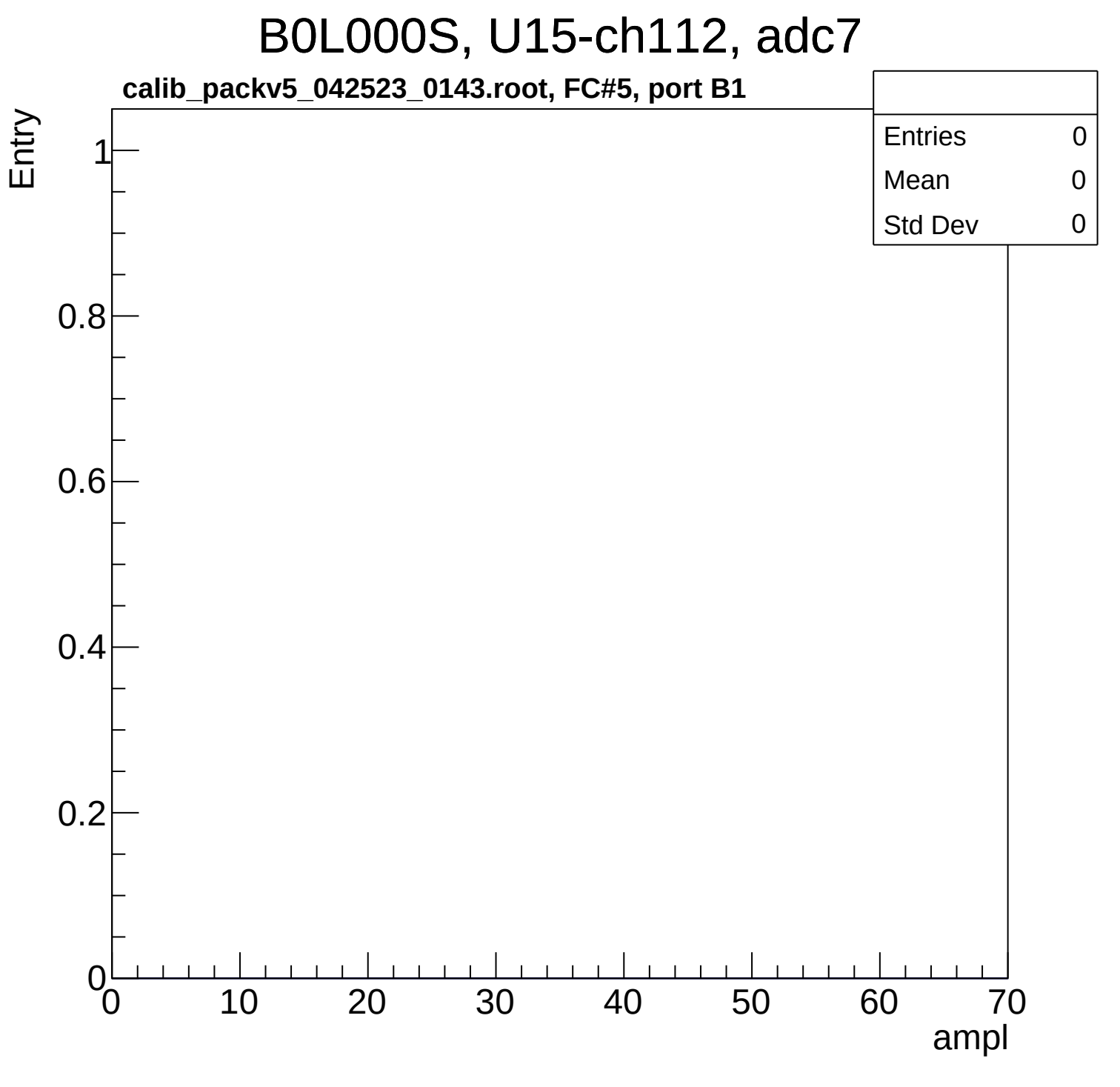
B0L000S, U15-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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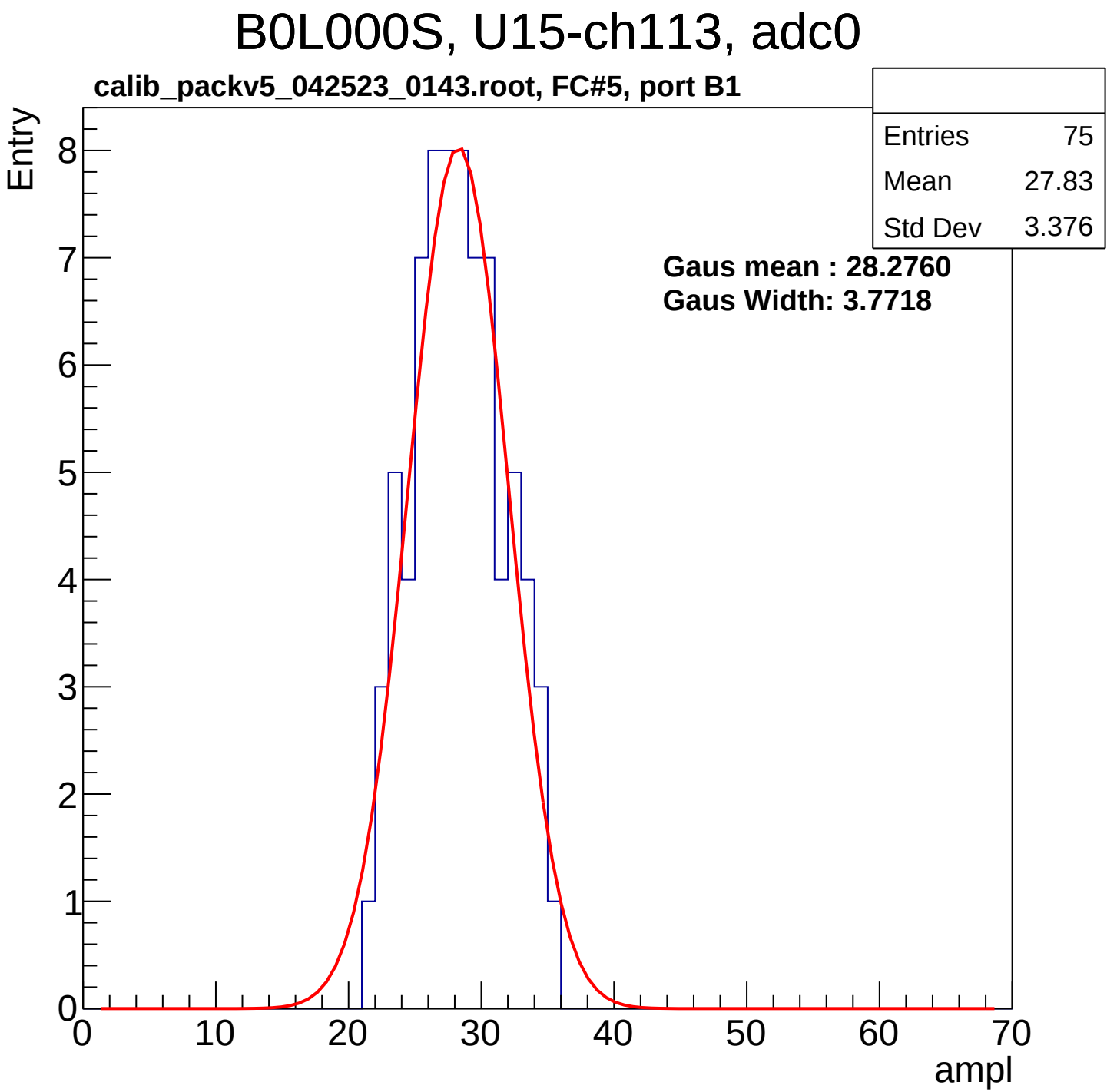
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.83
Std Dev 3.376
Gaus mean : 28.2760
Gaus Width: 3.7718
Entry
ampl
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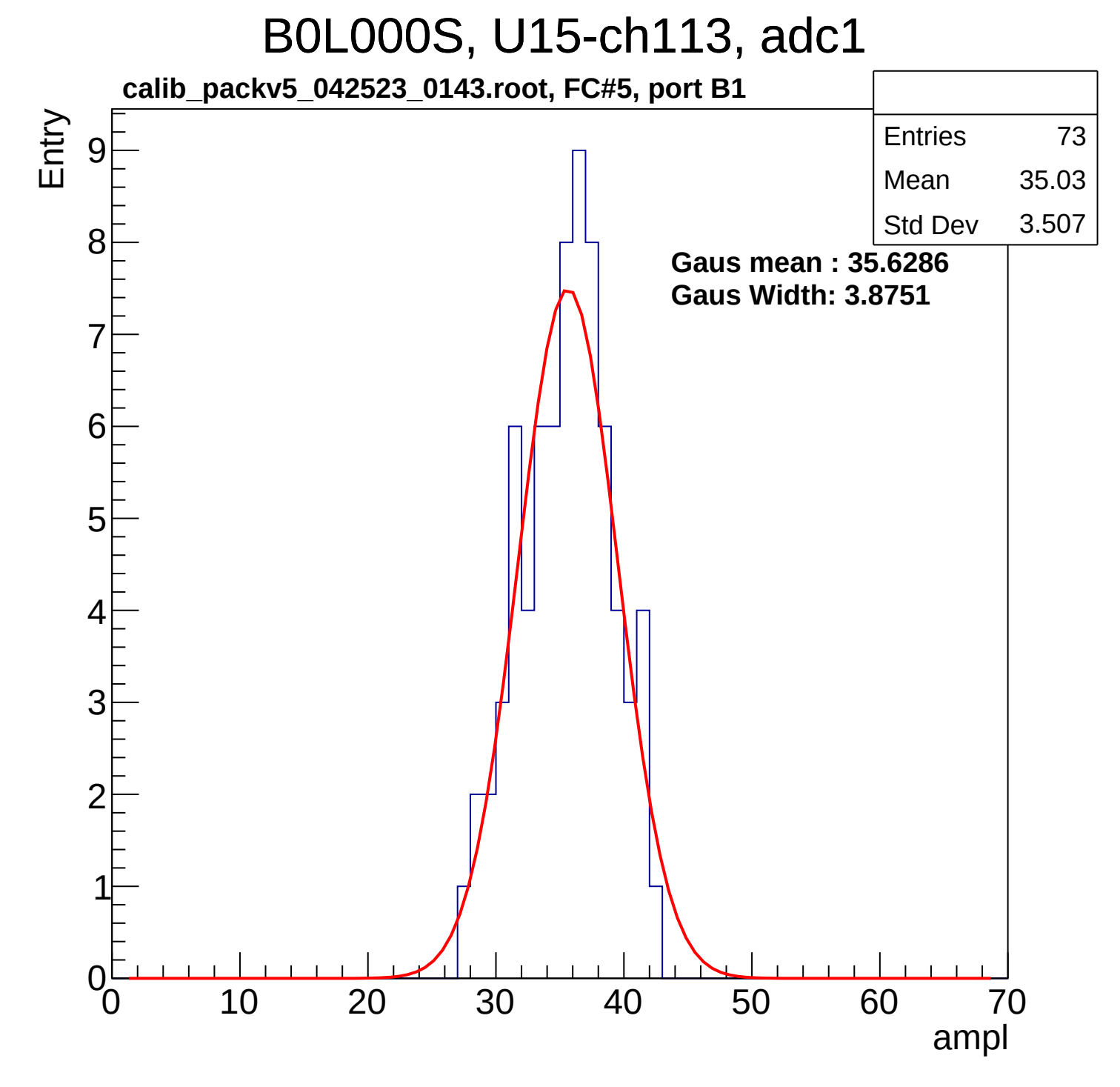
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.03
Std Dev 3.507
Gaus mean : 35.6286
Gaus Width: 3.8751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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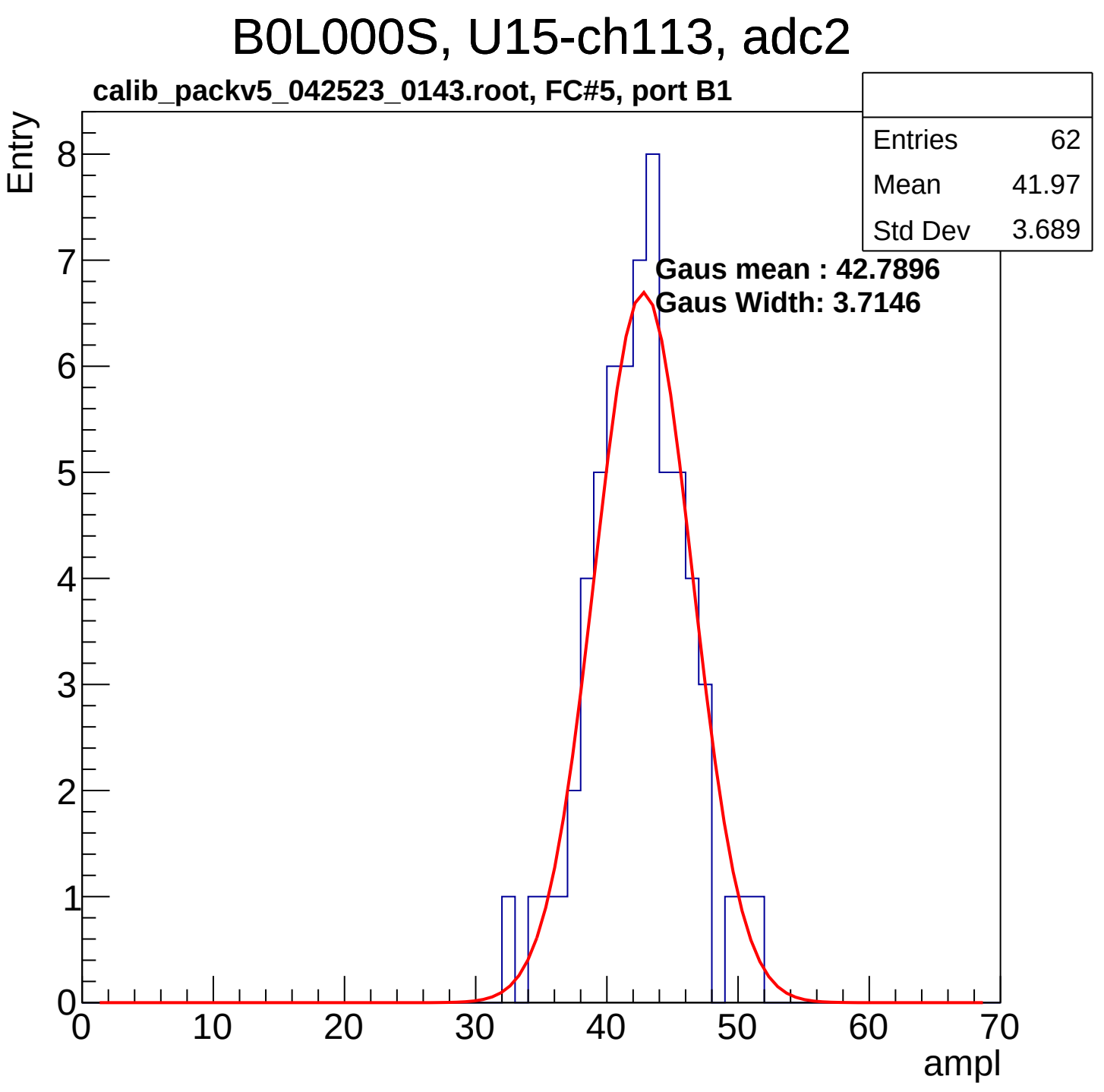
B0L000S, U15-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.97
Std Dev 3.689
Gaus mean : 42.7896
Gaus Width: 3.7146
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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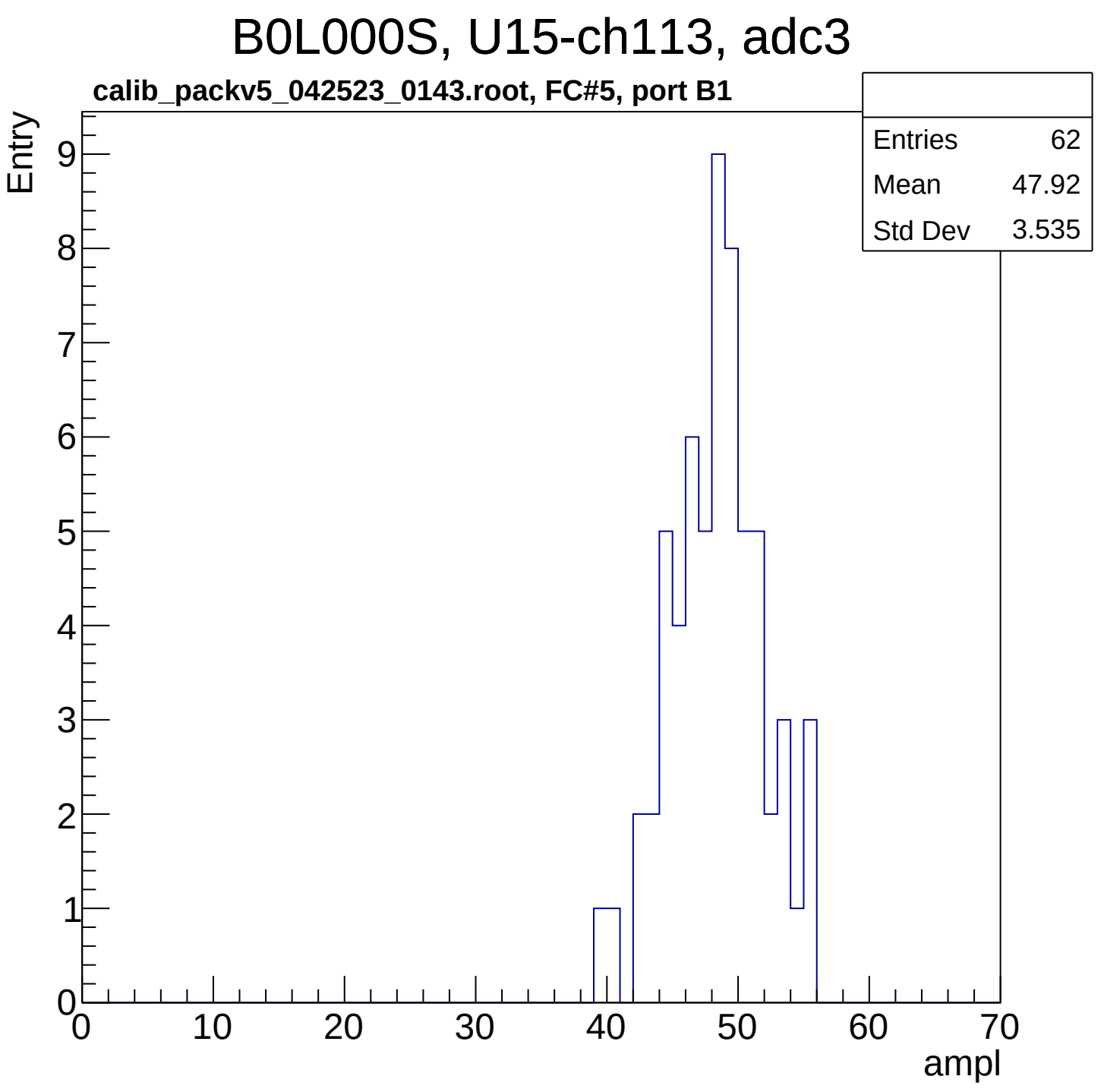
B0L000S, U15-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 47.92
Std Dev 3.535
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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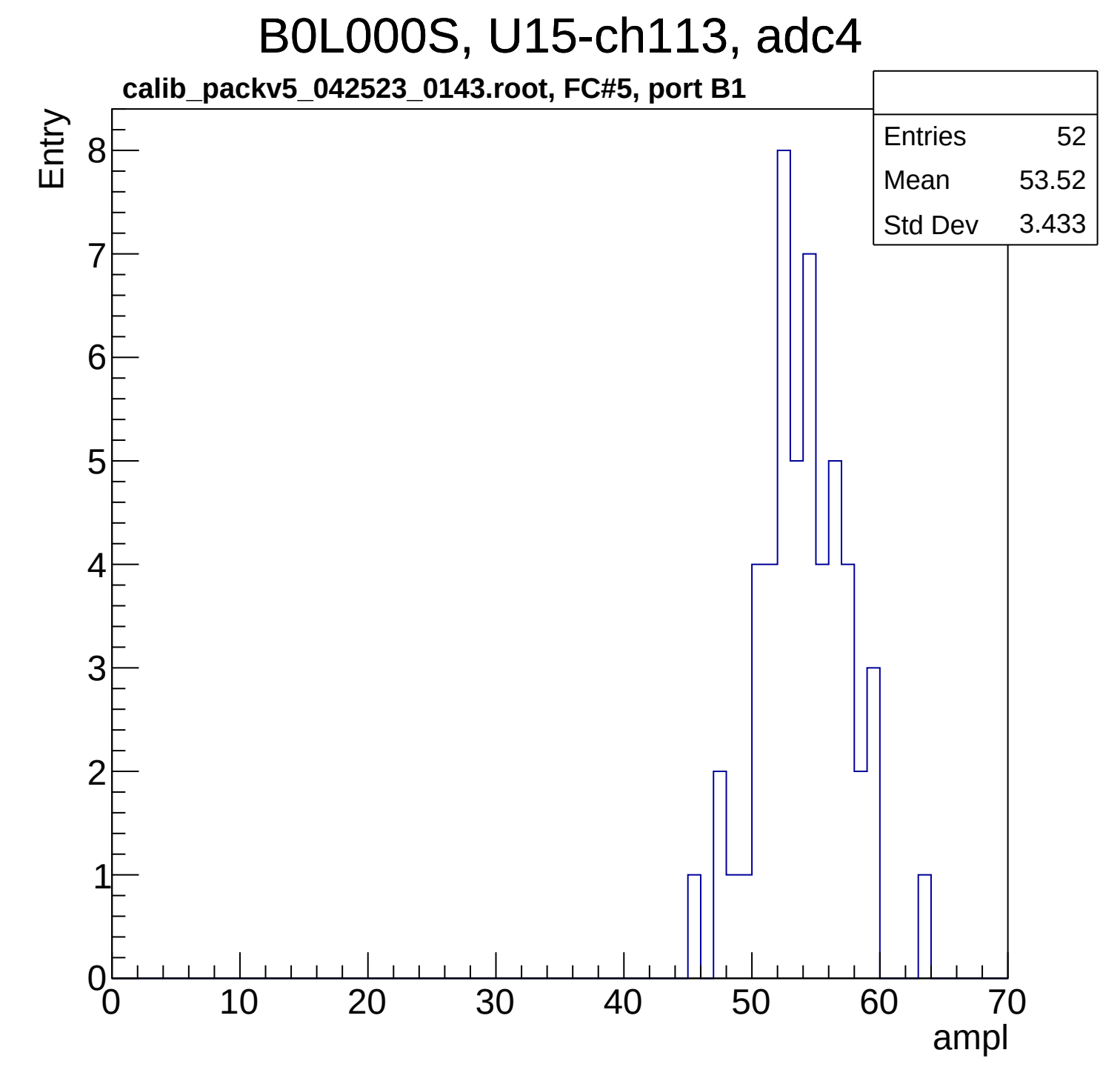
B0L000S, U15-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.52
Std Dev 3.433
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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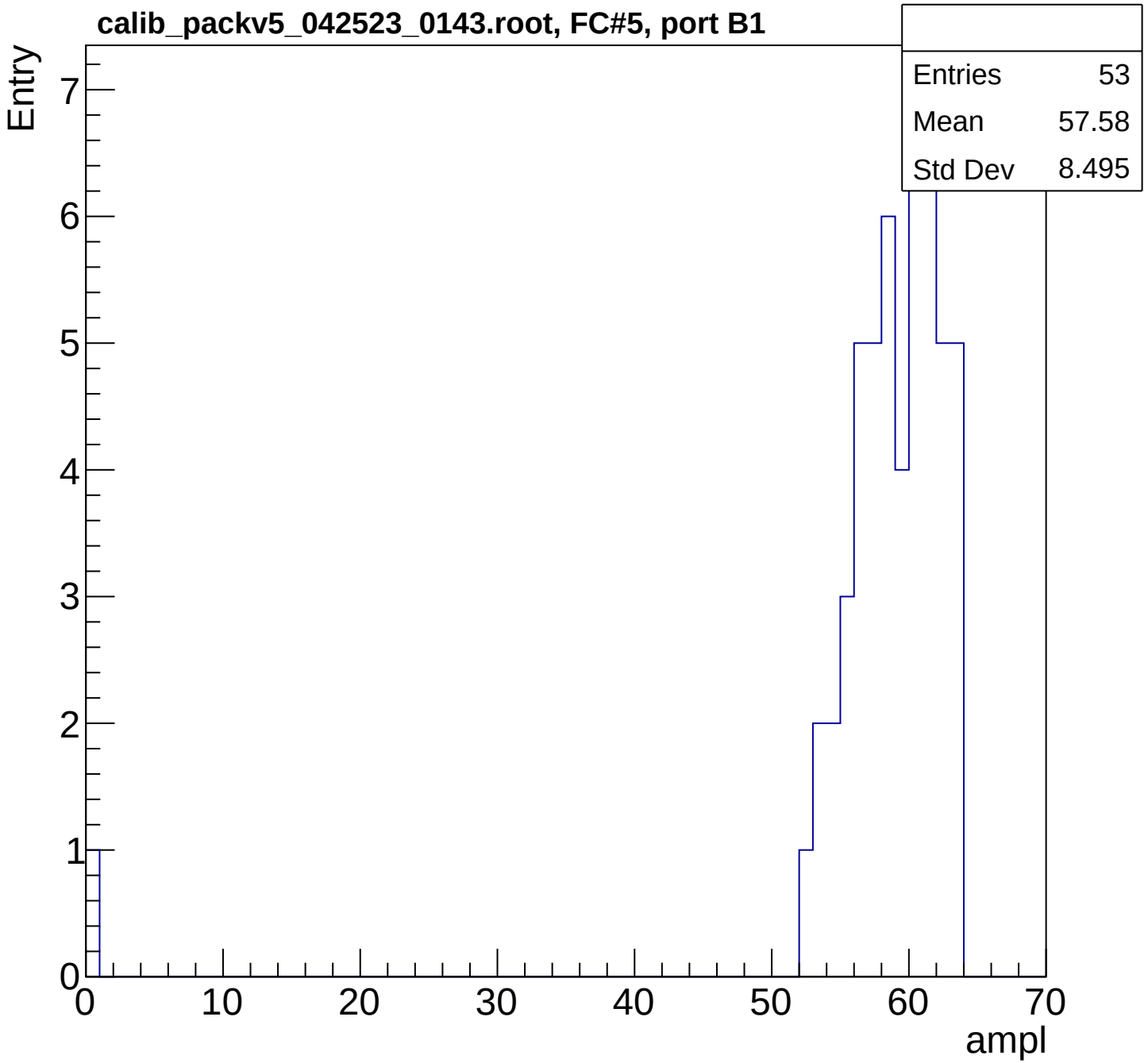
B0L000S, U15-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.58
Std Dev 8.495
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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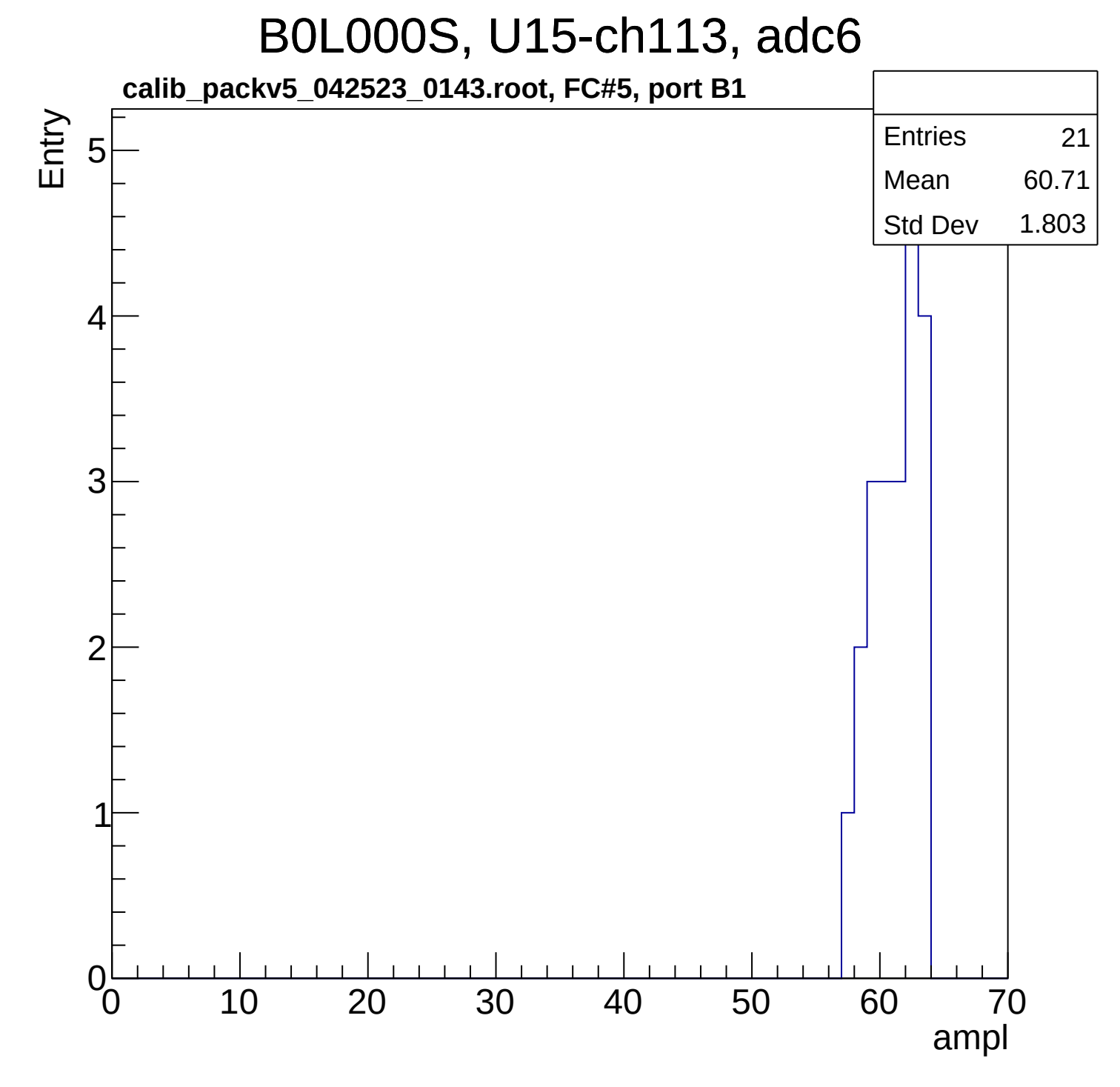
B0L000S, U15-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 60.71
Std Dev 1.803
Entry
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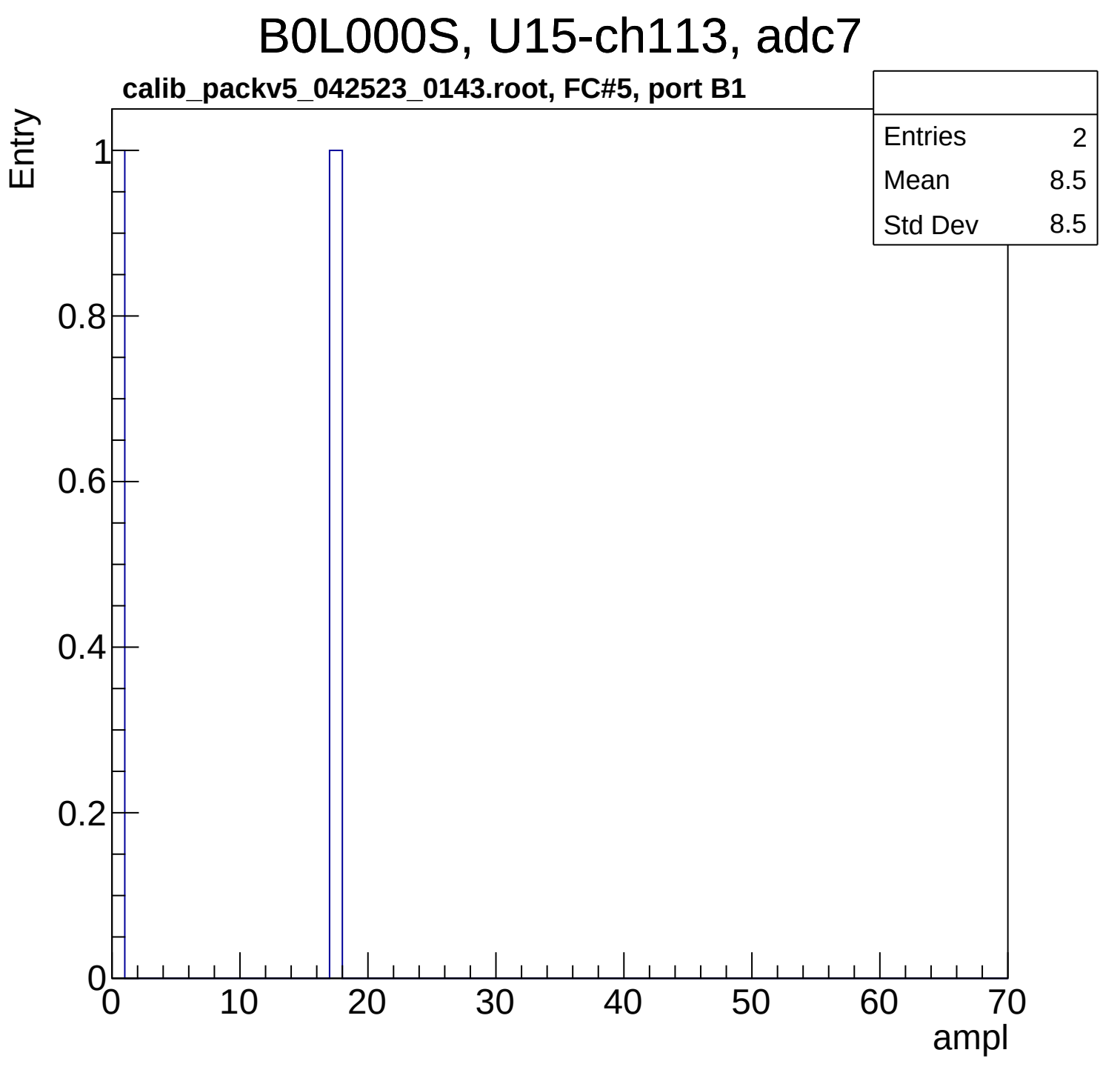
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 8.5
Std Dev 8.5
Entry
ampl
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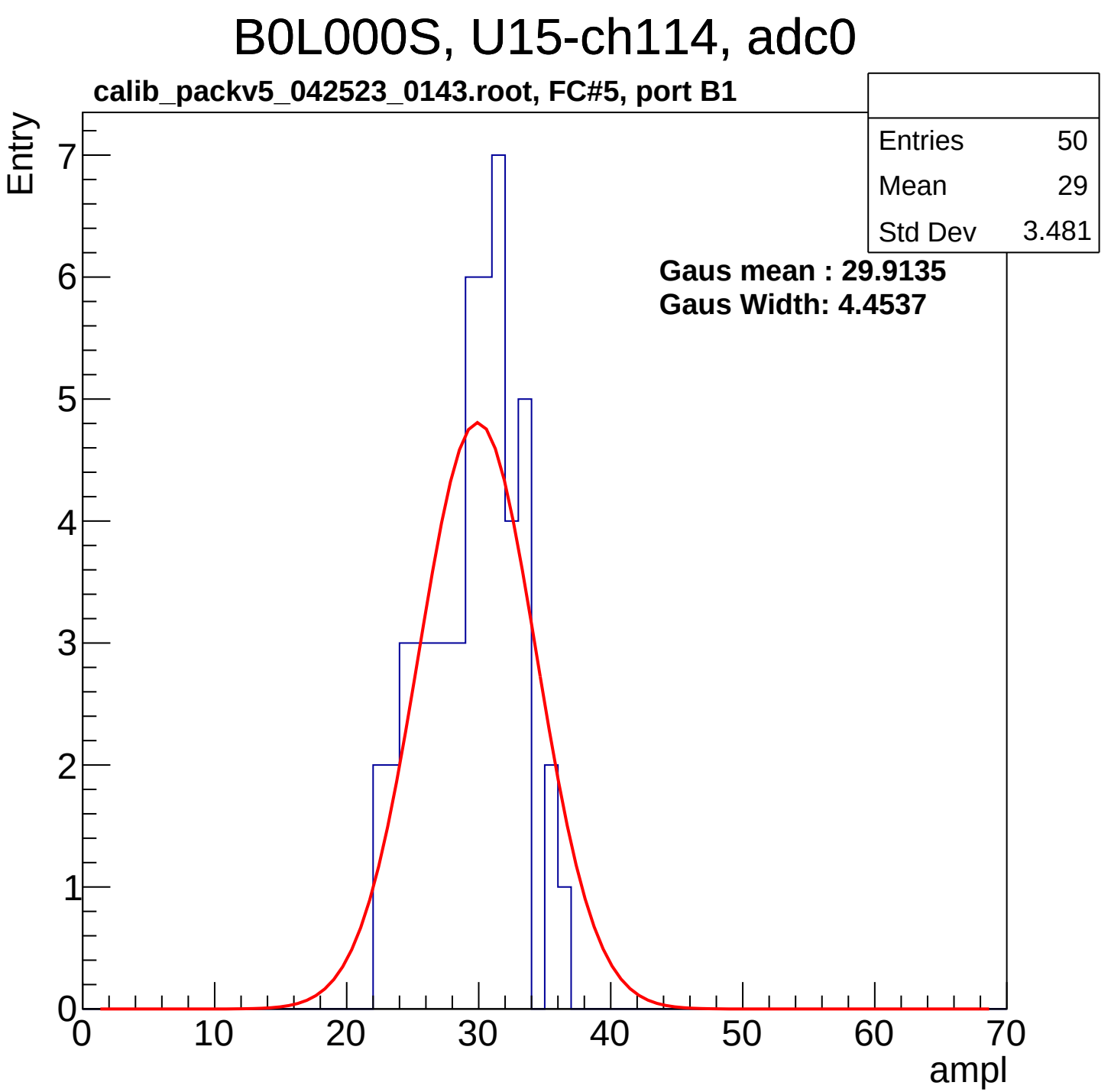
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U15-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 29
Std Dev 3.481
Gaus mean : 29.9135
Gaus Width: 4.4537
Entry
ampl
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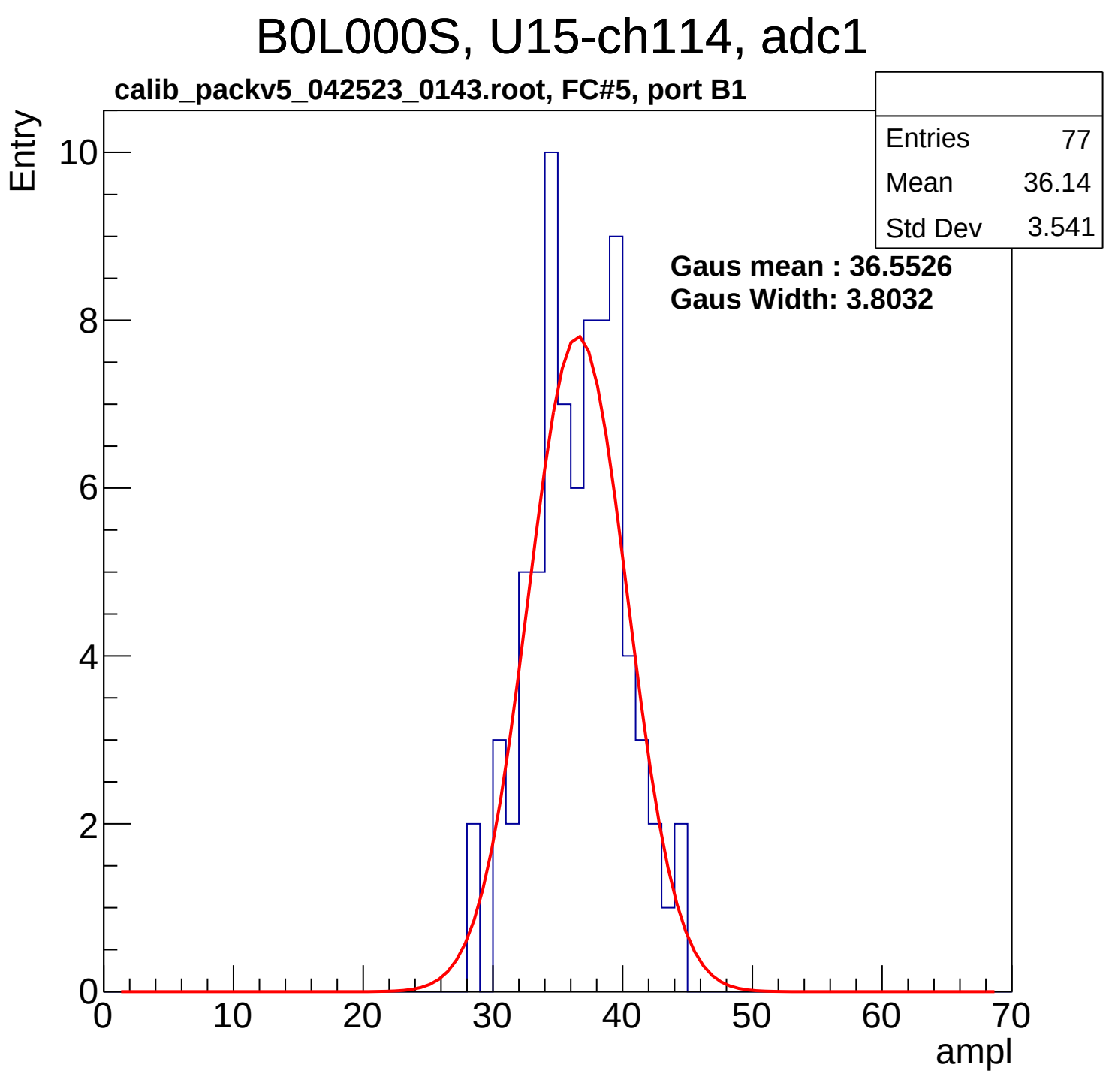
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.14
Std Dev 3.541
Gaus mean : 36.5526
Gaus Width: 3.8032
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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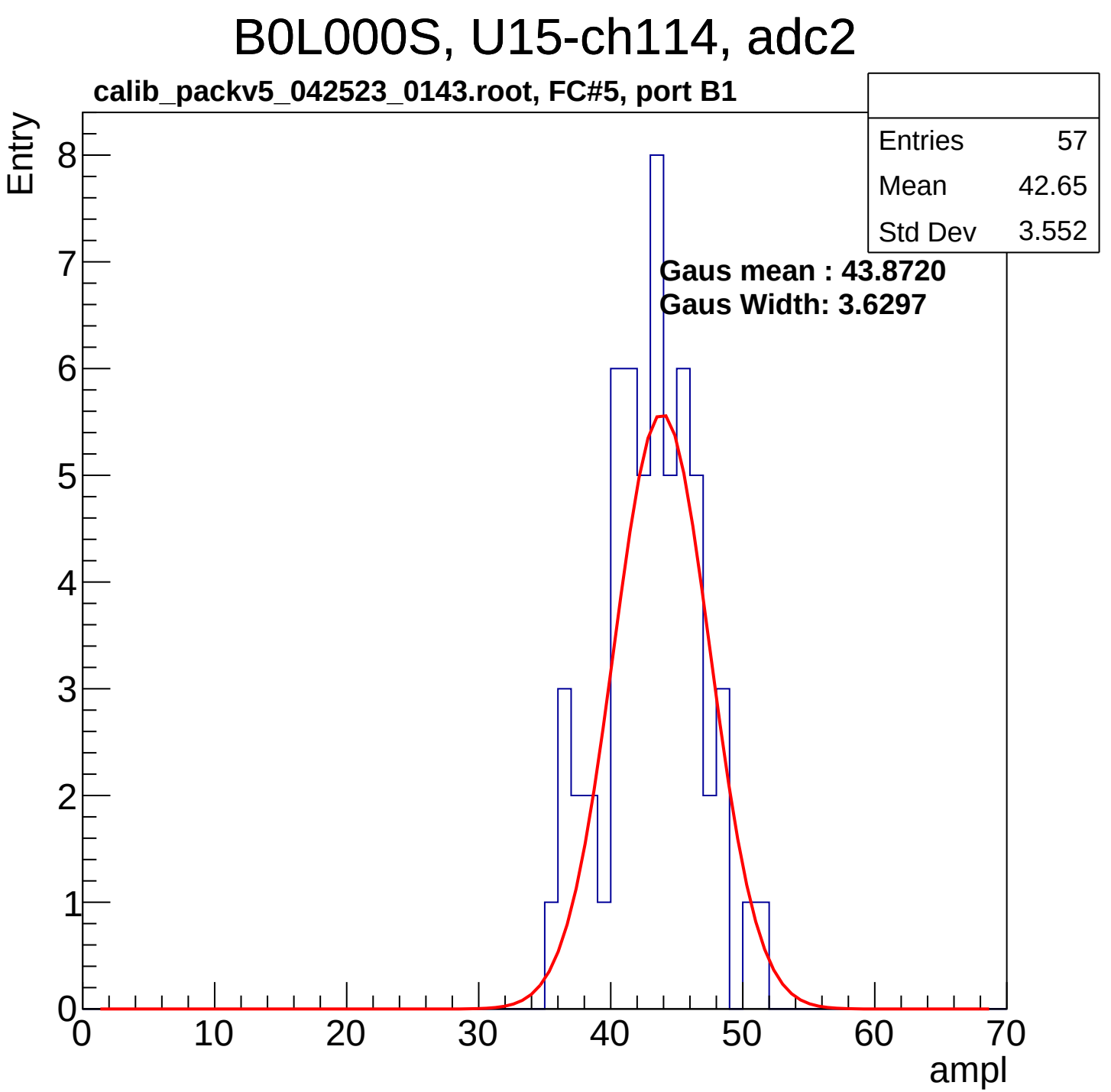
B0L000S, U15-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.65
Std Dev 3.552
Gaus mean : 43.8720
Gaus Width: 3.6297
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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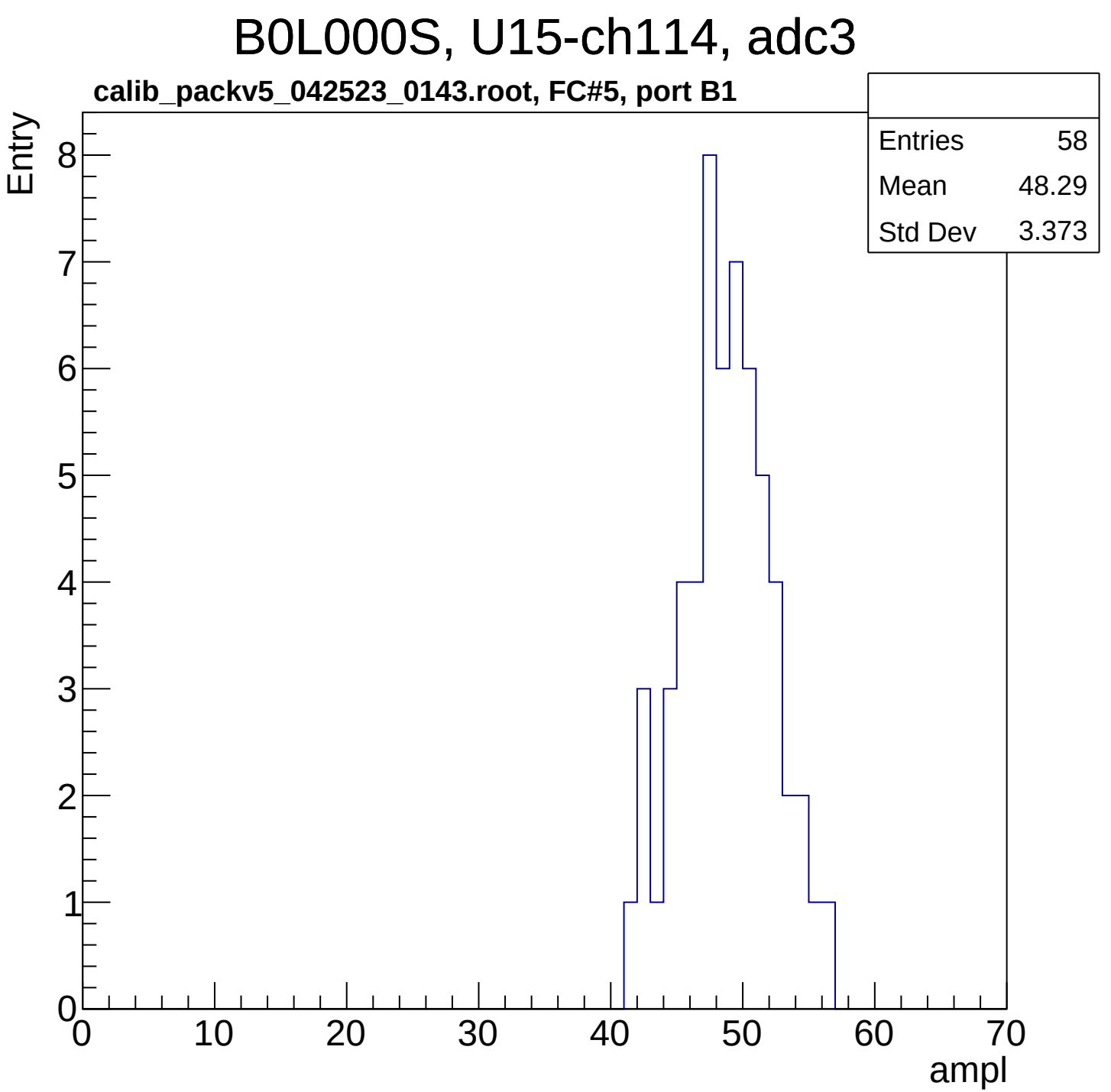
B0L000S, U15-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.29
Std Dev 3.373
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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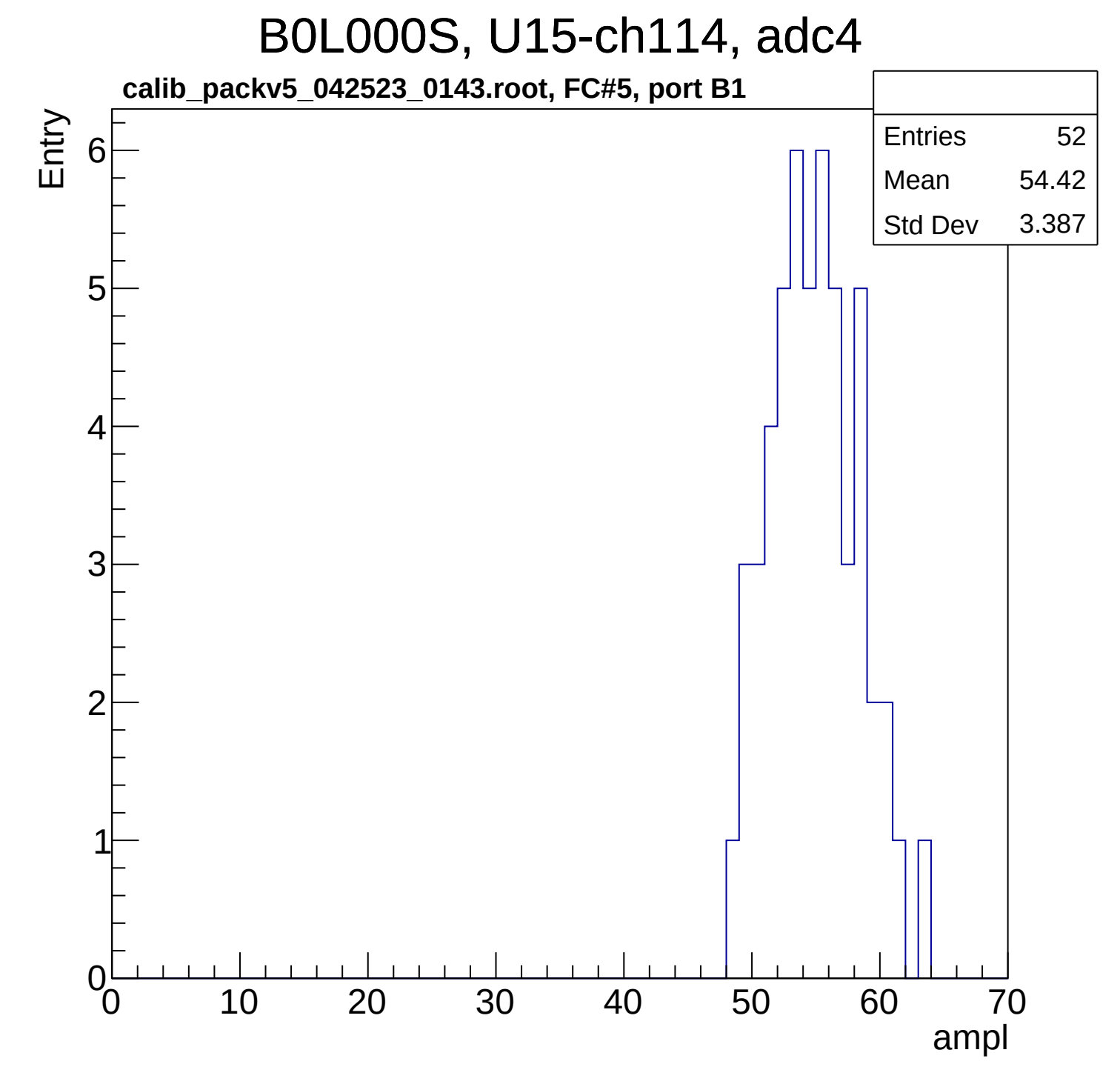
B0L000S, U15-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.42
Std Dev 3.387
Entry
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3
4
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10
20
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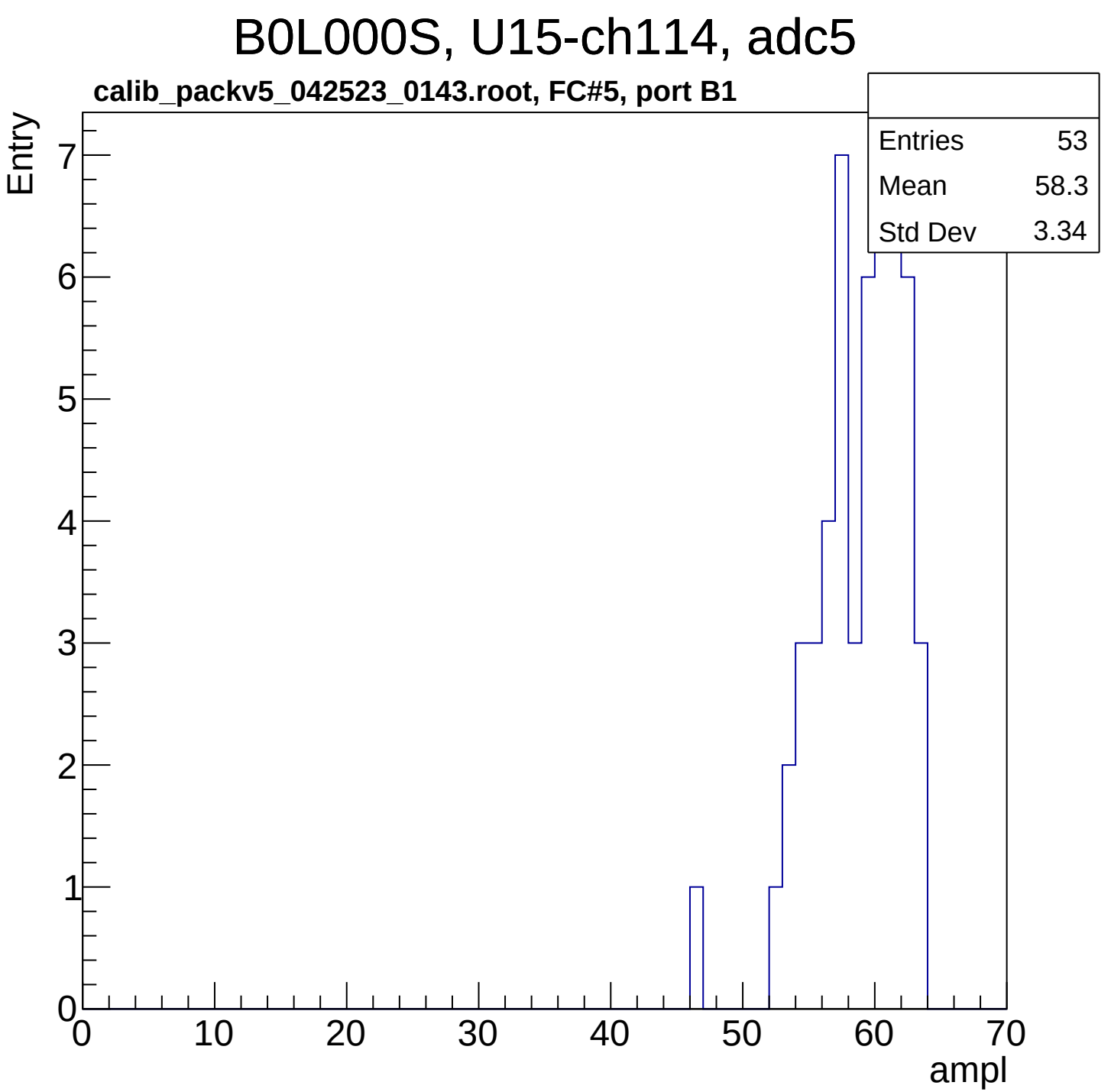
50
60
70
ampl


B0L000S, U15-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.3
Std Dev 3.34
Entry
ampl
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3
4
5
6
7
0
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20
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40
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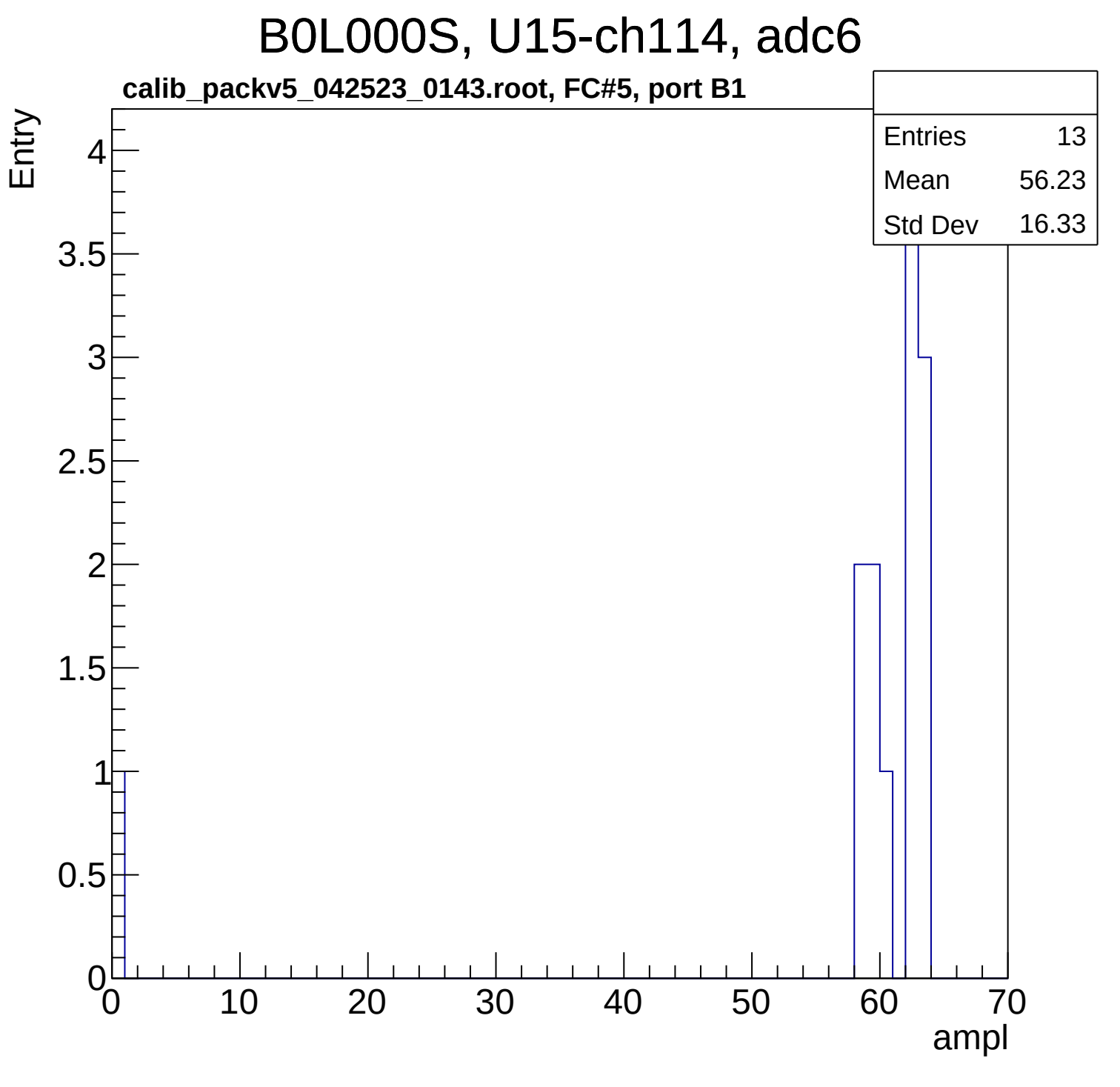
60
70


B0L000S, U15-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.23
Std Dev 16.33
Entry
ampl
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1.5
2
2.5
3
3.5
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0
10
20
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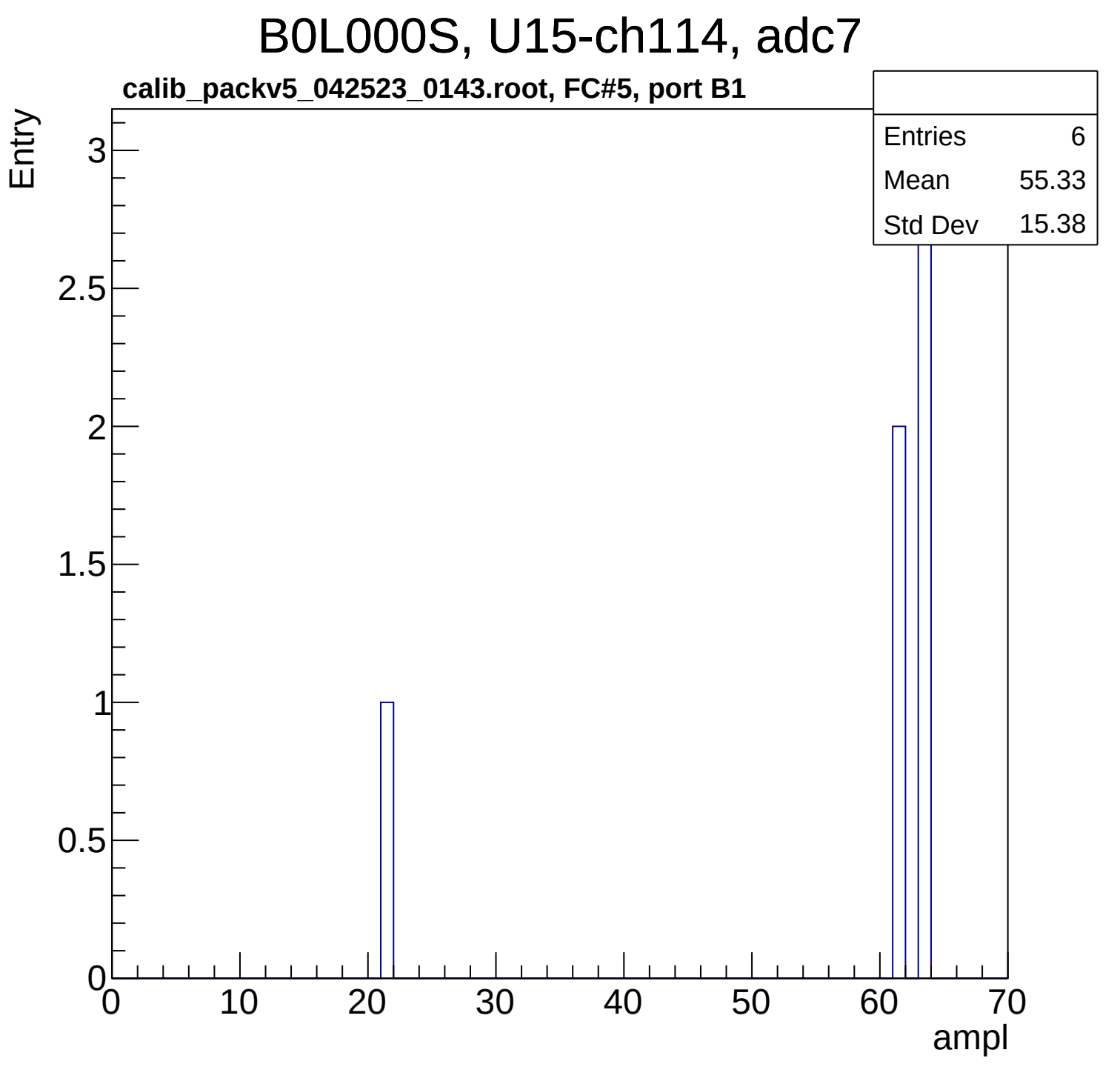
50
60
70


B0L000S, U15-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 55.33
Std Dev 15.38
Entry
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1.5
2
2.5
3
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10
20
30
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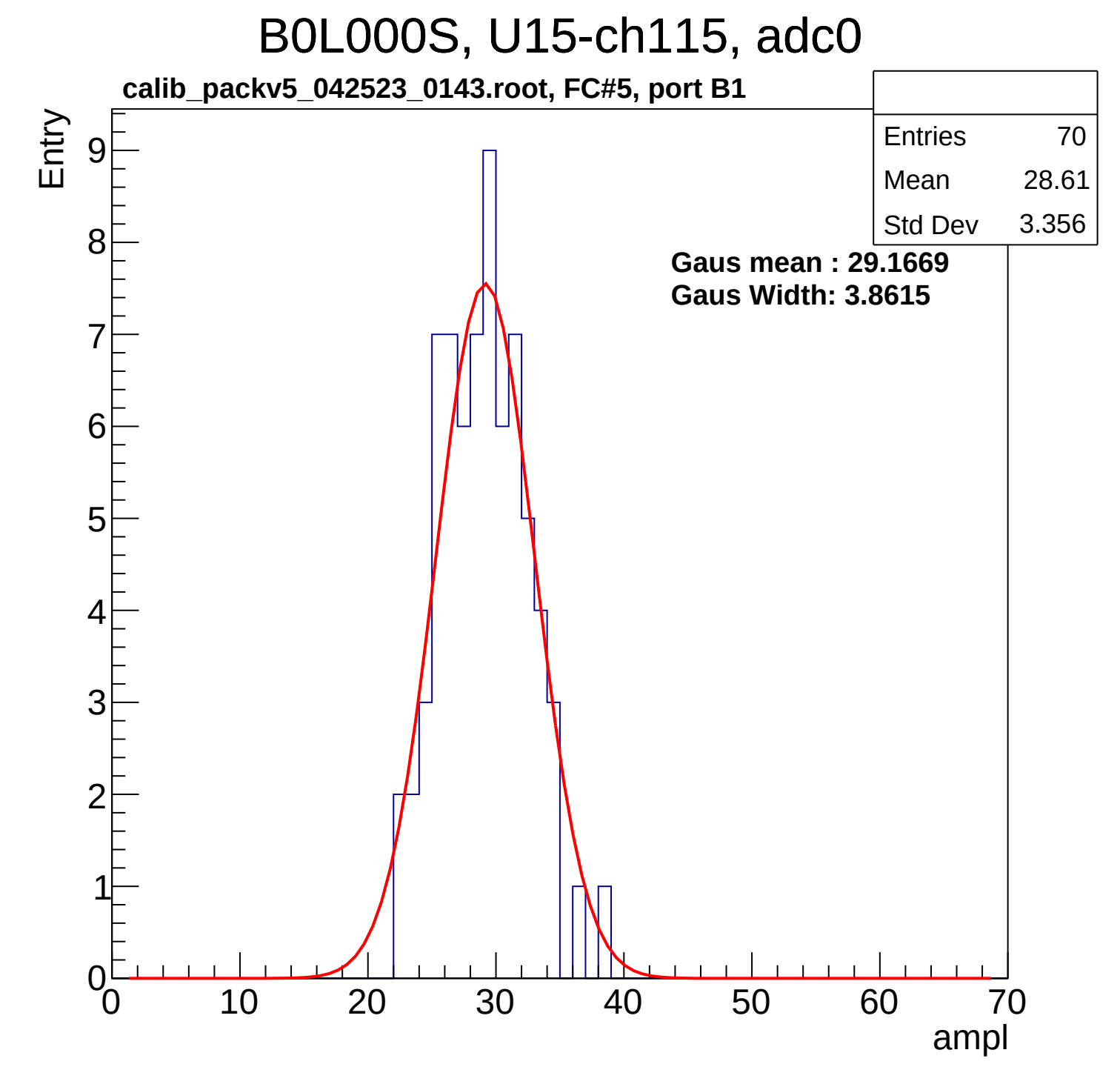
50
60
70
ampl


B0L000S, U15-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.61
Std Dev 3.356
Gaus mean : 29.1669
Gaus Width: 3.8615
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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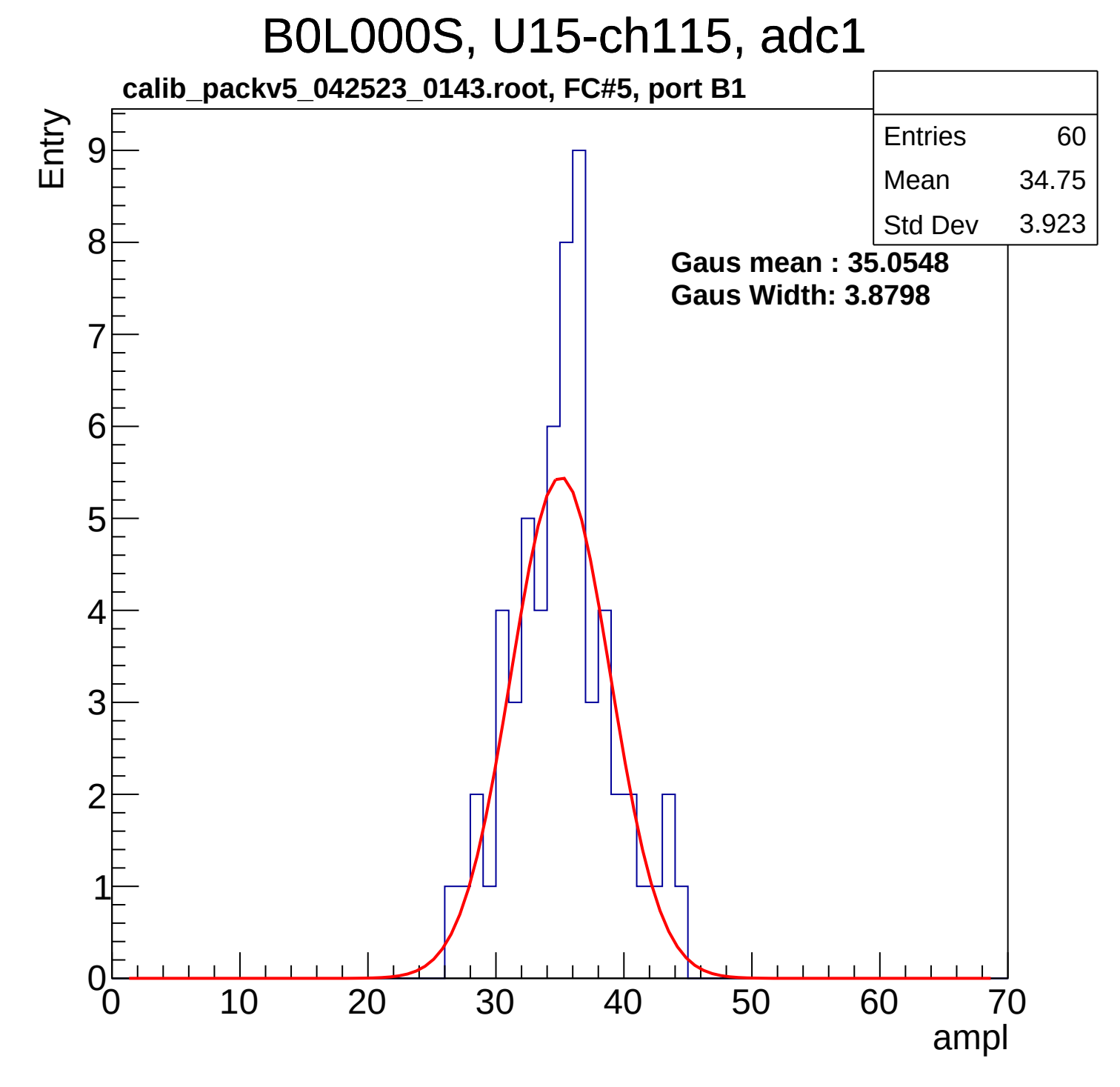
B0L000S, U15-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 34.75
Std Dev 3.923
Gaus mean : 35.0548
Gaus Width: 3.8798
Entry
ampl
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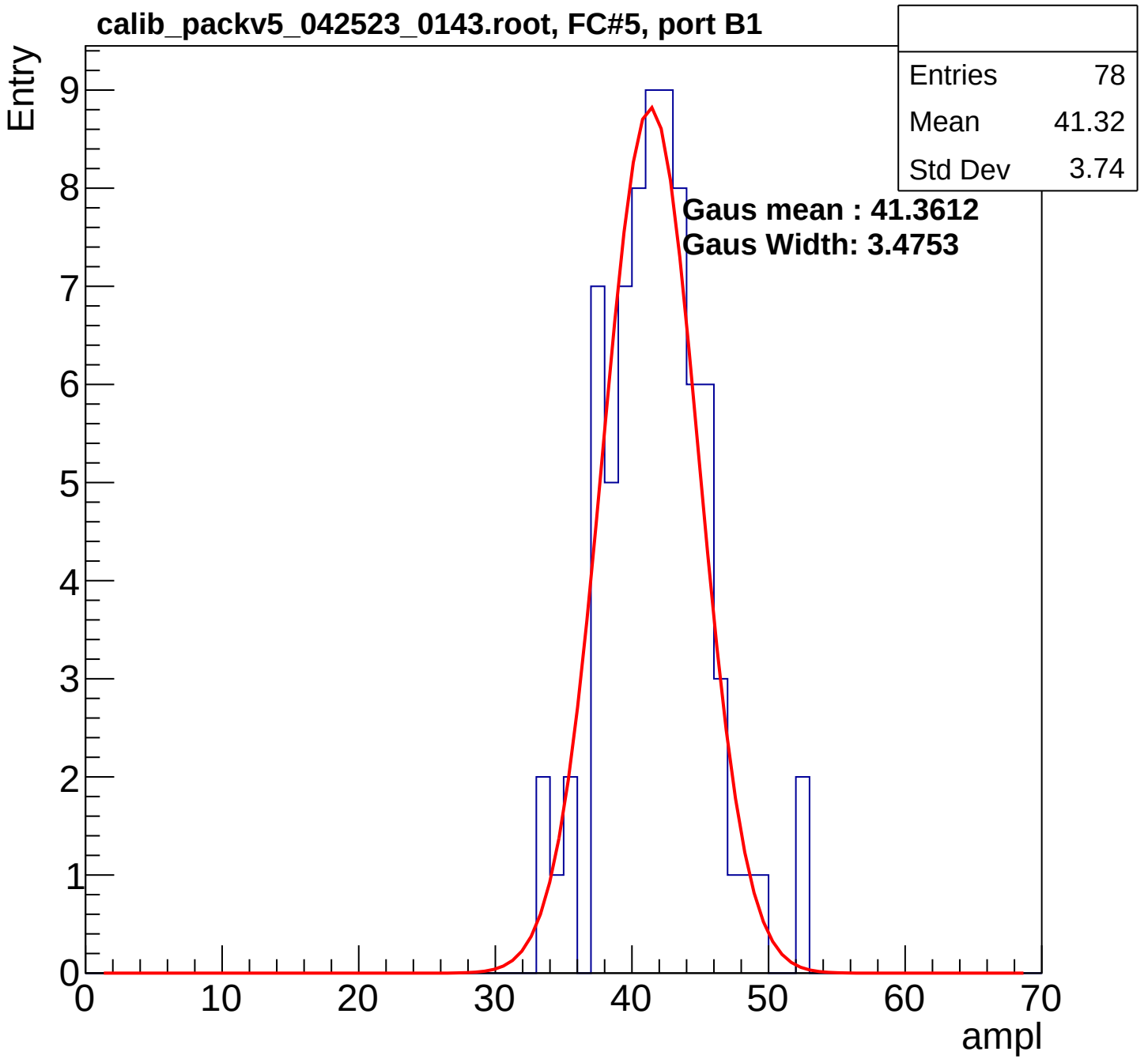
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 41.32
Std Dev 3.74
Gaus mean : 41.3612
Gaus Width: 3.4753
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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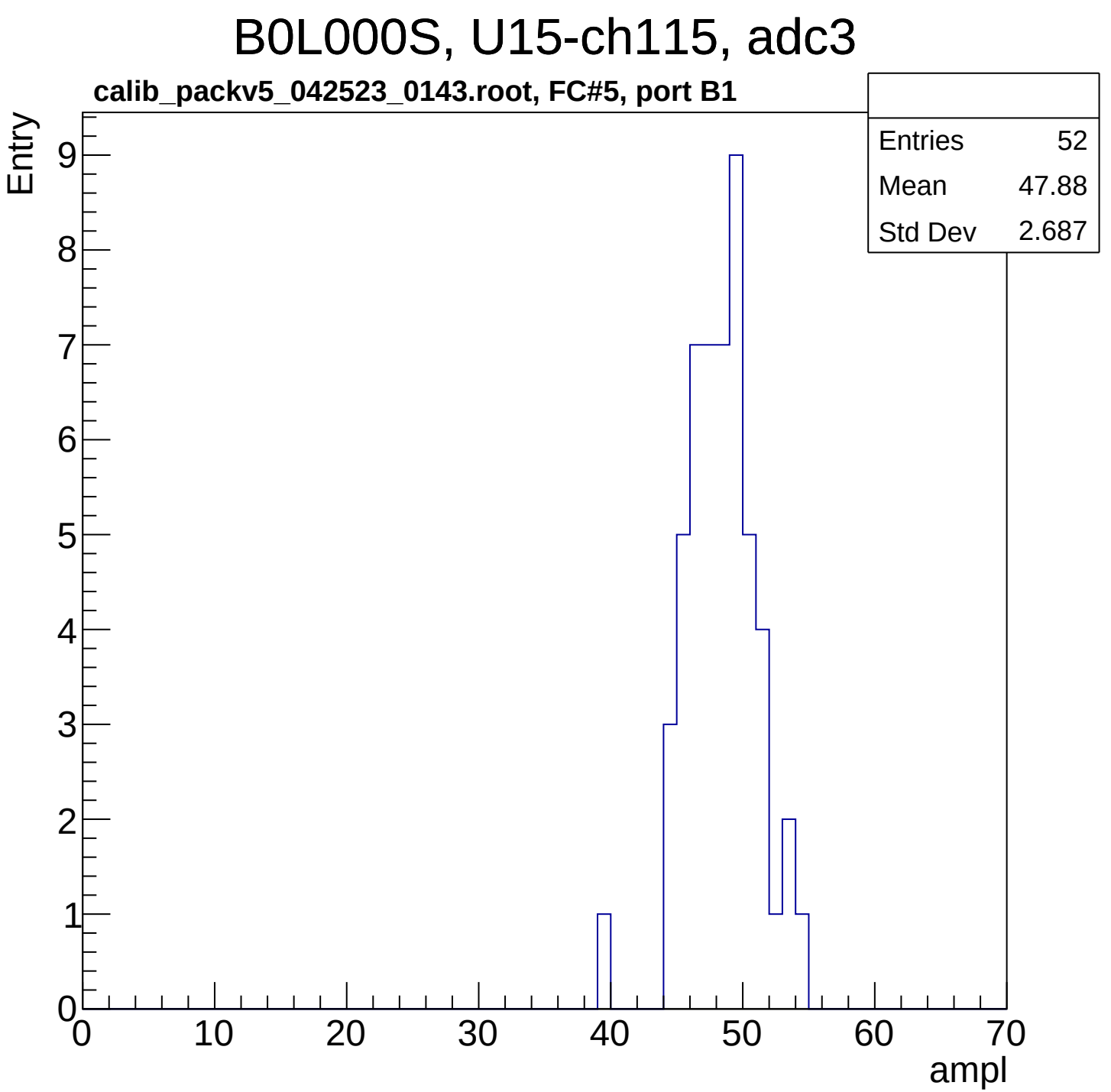
B0L000S, U15-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 47.88
Std Dev 2.687
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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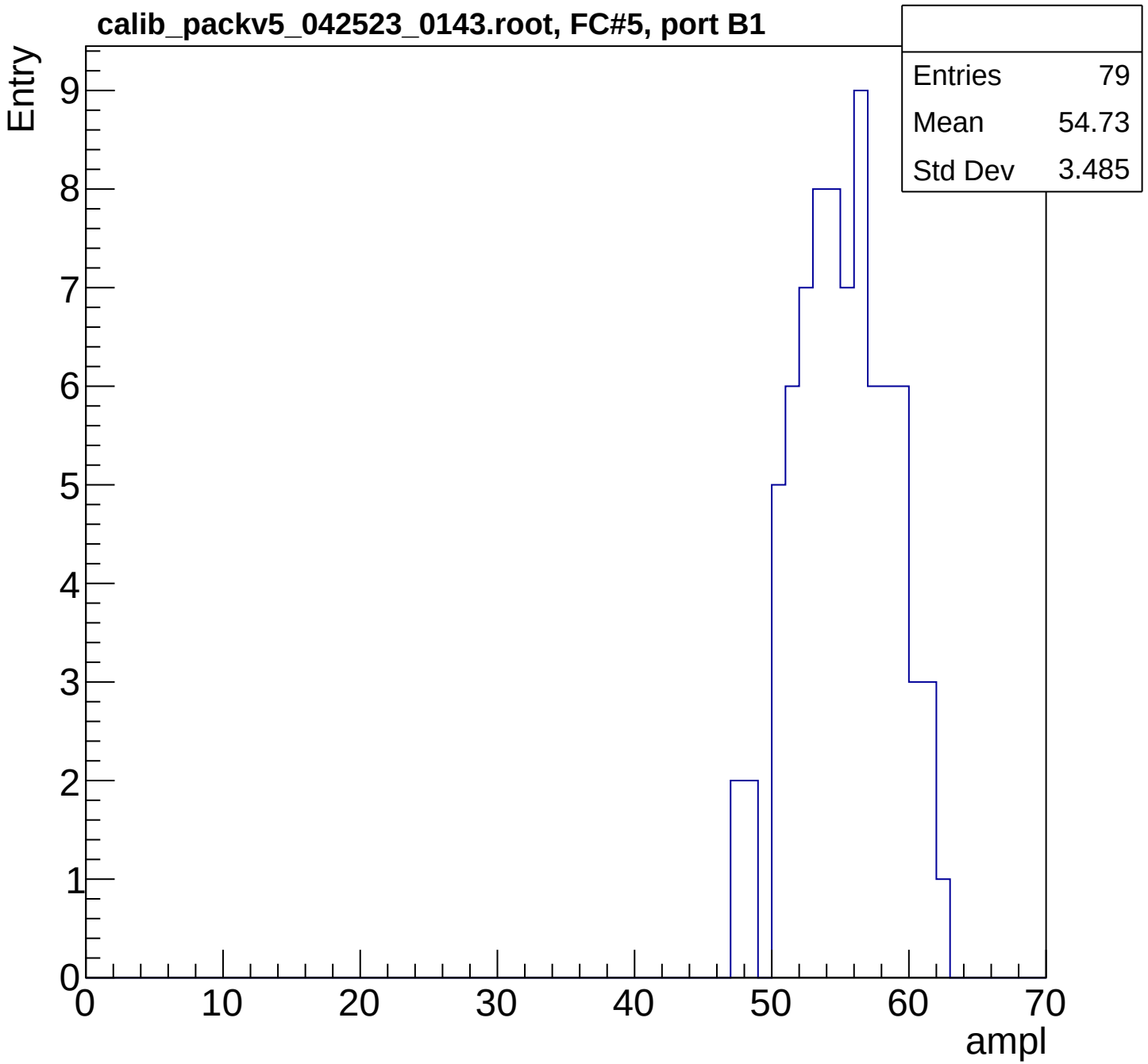
B0L000S, U15-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 54.73
Std Dev 3.485
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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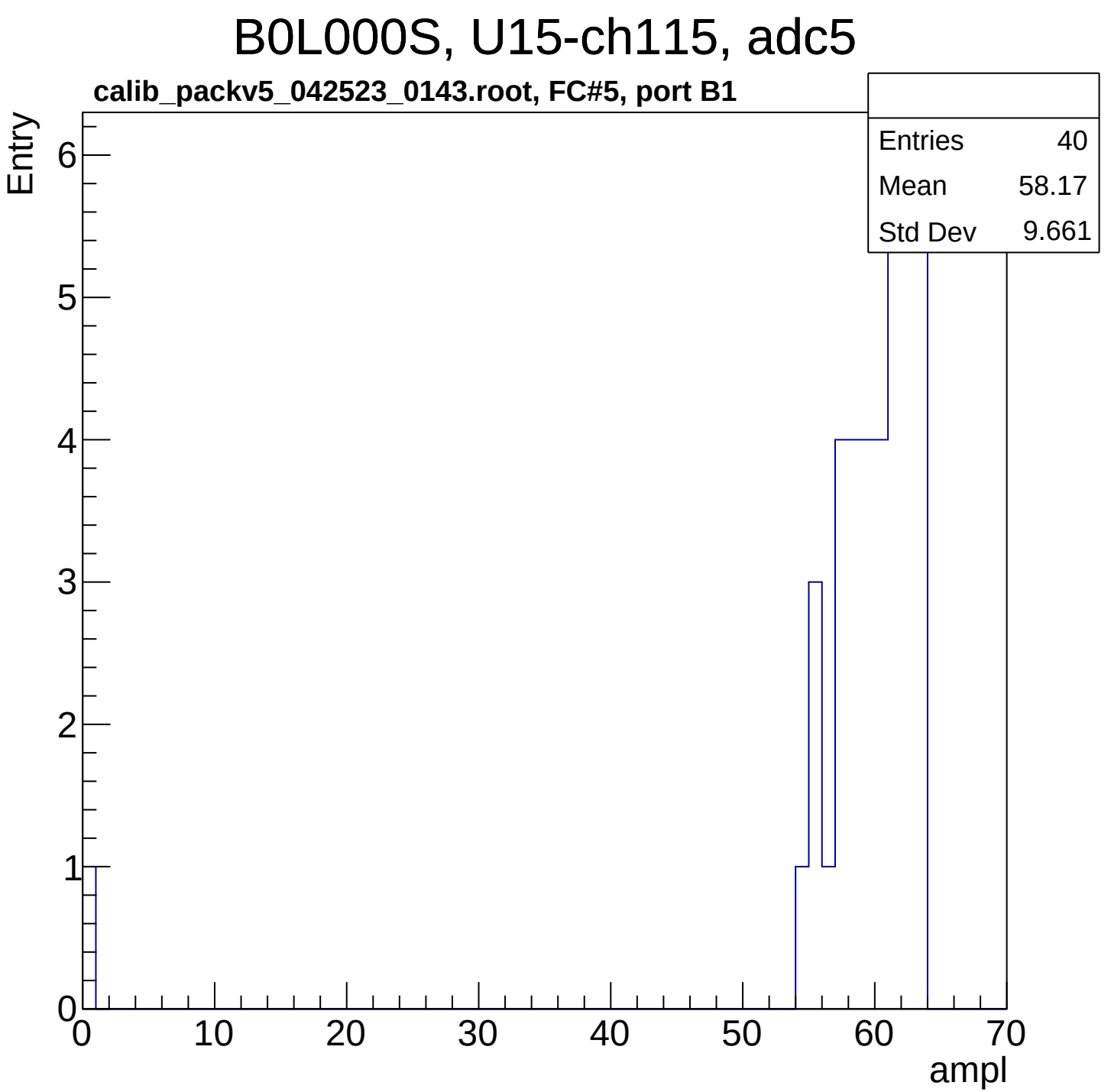
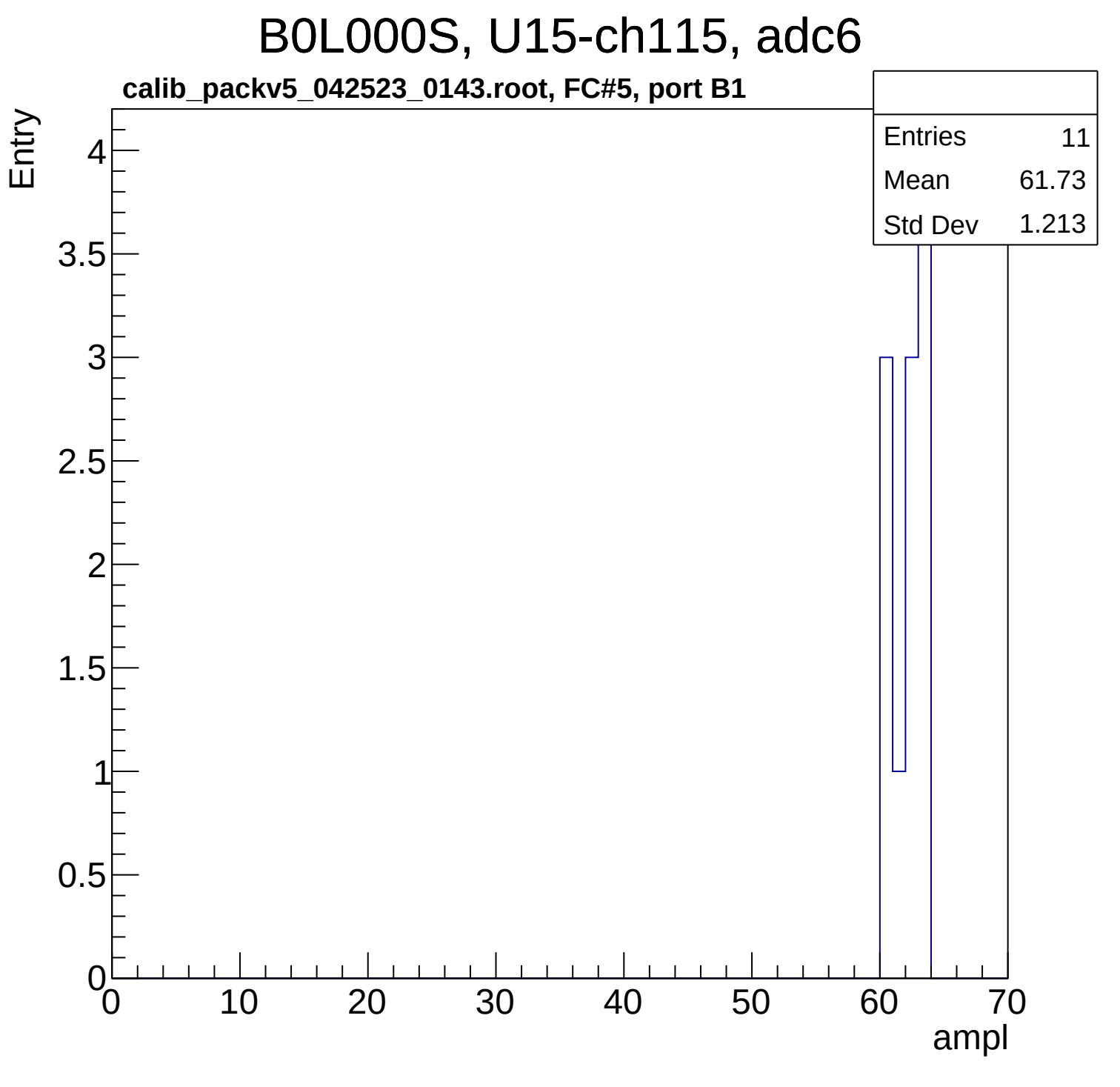

B0L000S, U15-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.17
Std Dev 9.661
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.73
Std Dev 1.213
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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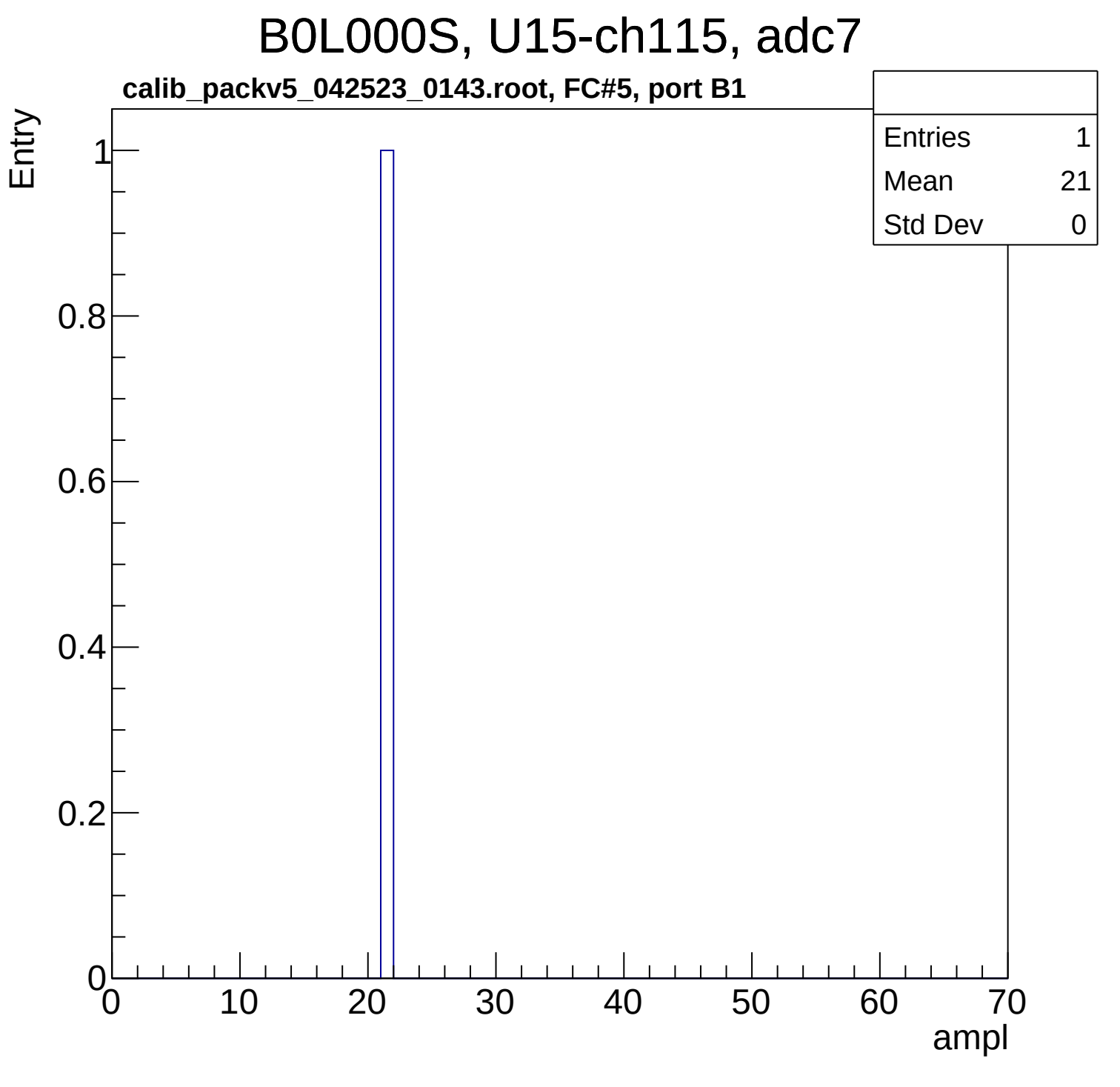
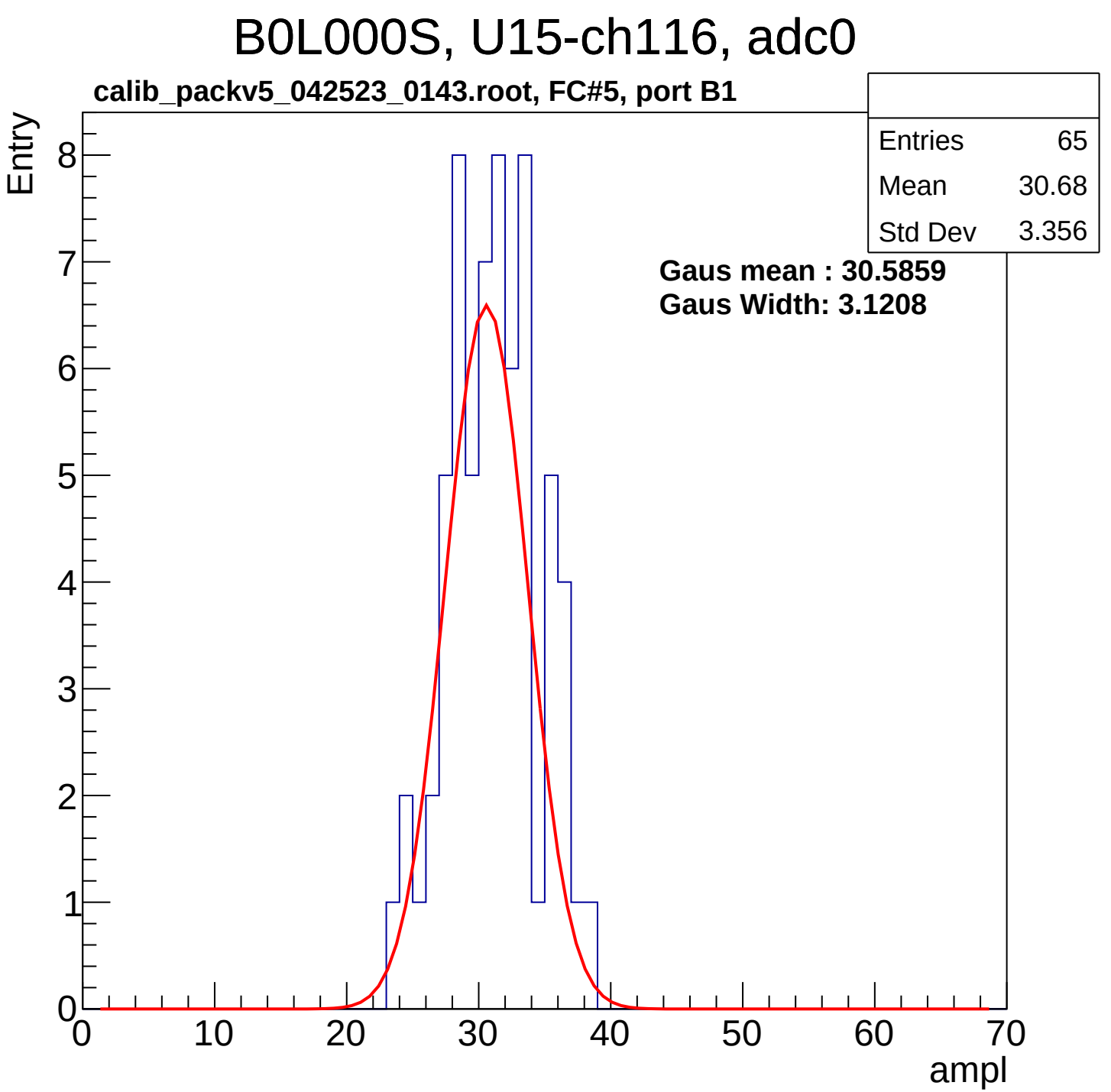

B0L000S, U15-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.68
Std Dev 3.356
Gaus mean : 30.5859
Gaus Width: 3.1208
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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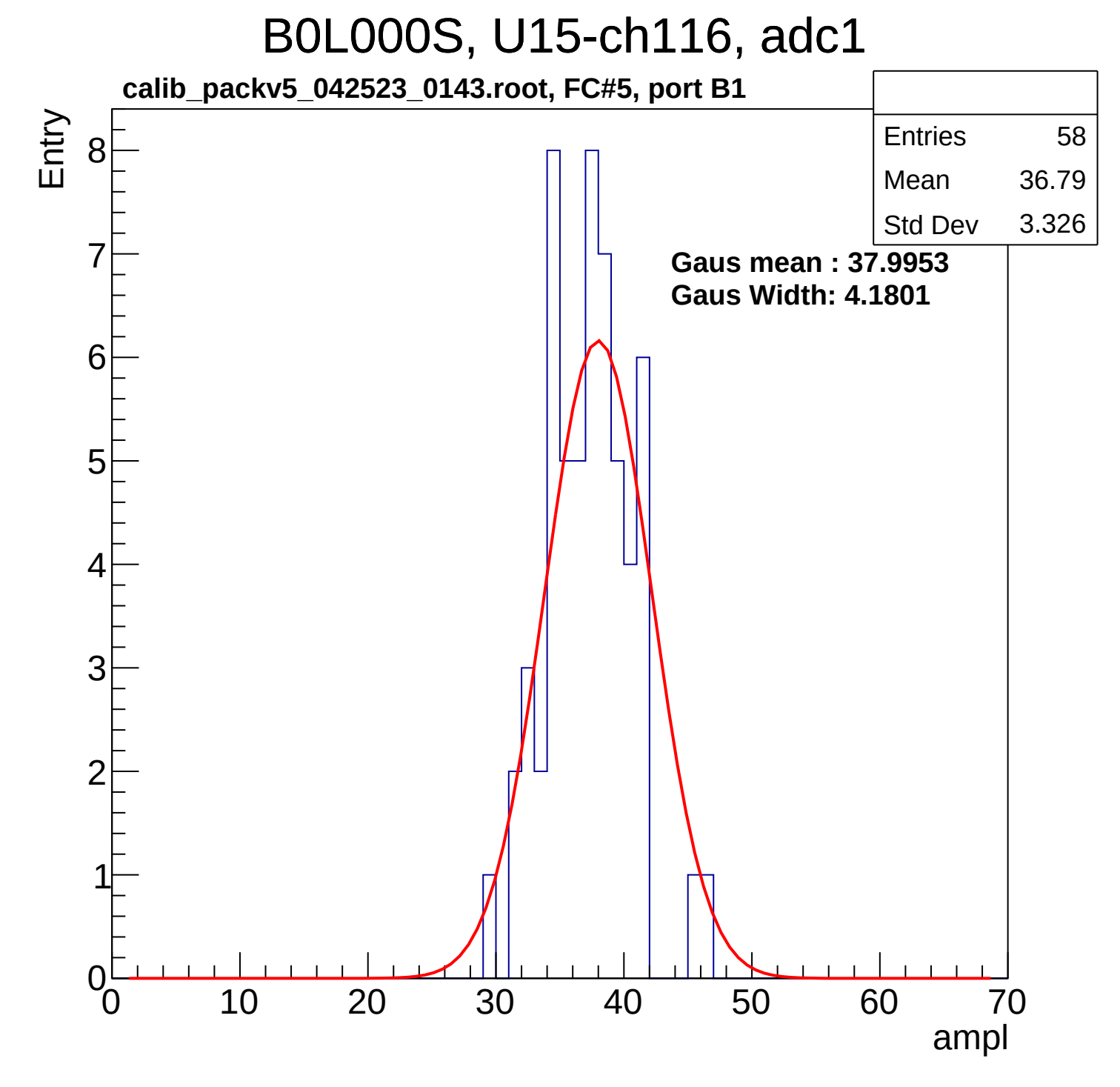
B0L000S, U15-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 36.79
Std Dev 3.326
Gaus mean : 37.9953
Gaus Width: 4.1801
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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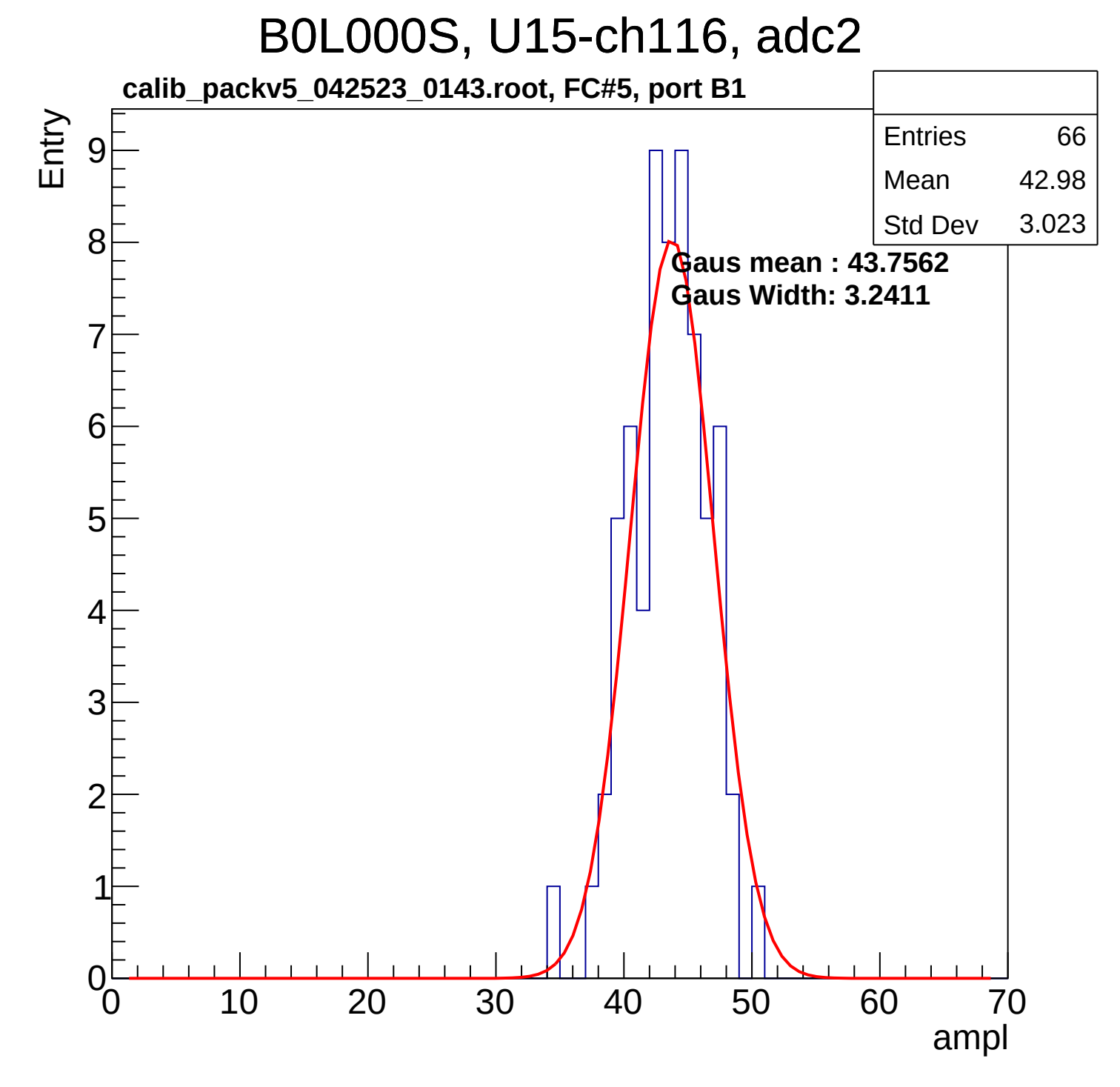
B0L000S, U15-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.98
Std Dev 3.023
Gaus mean : 43.7562
Gaus Width: 3.2411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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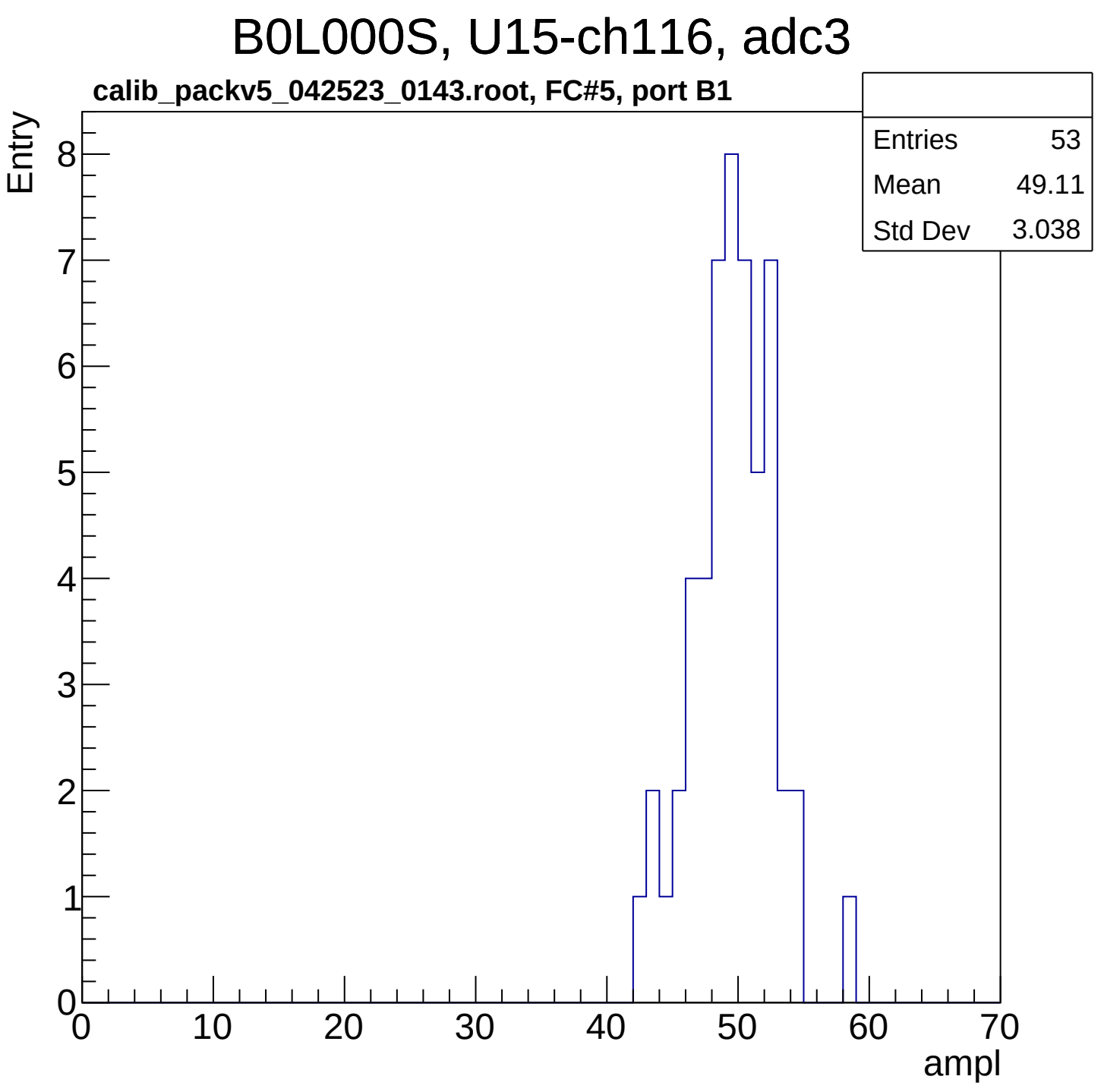
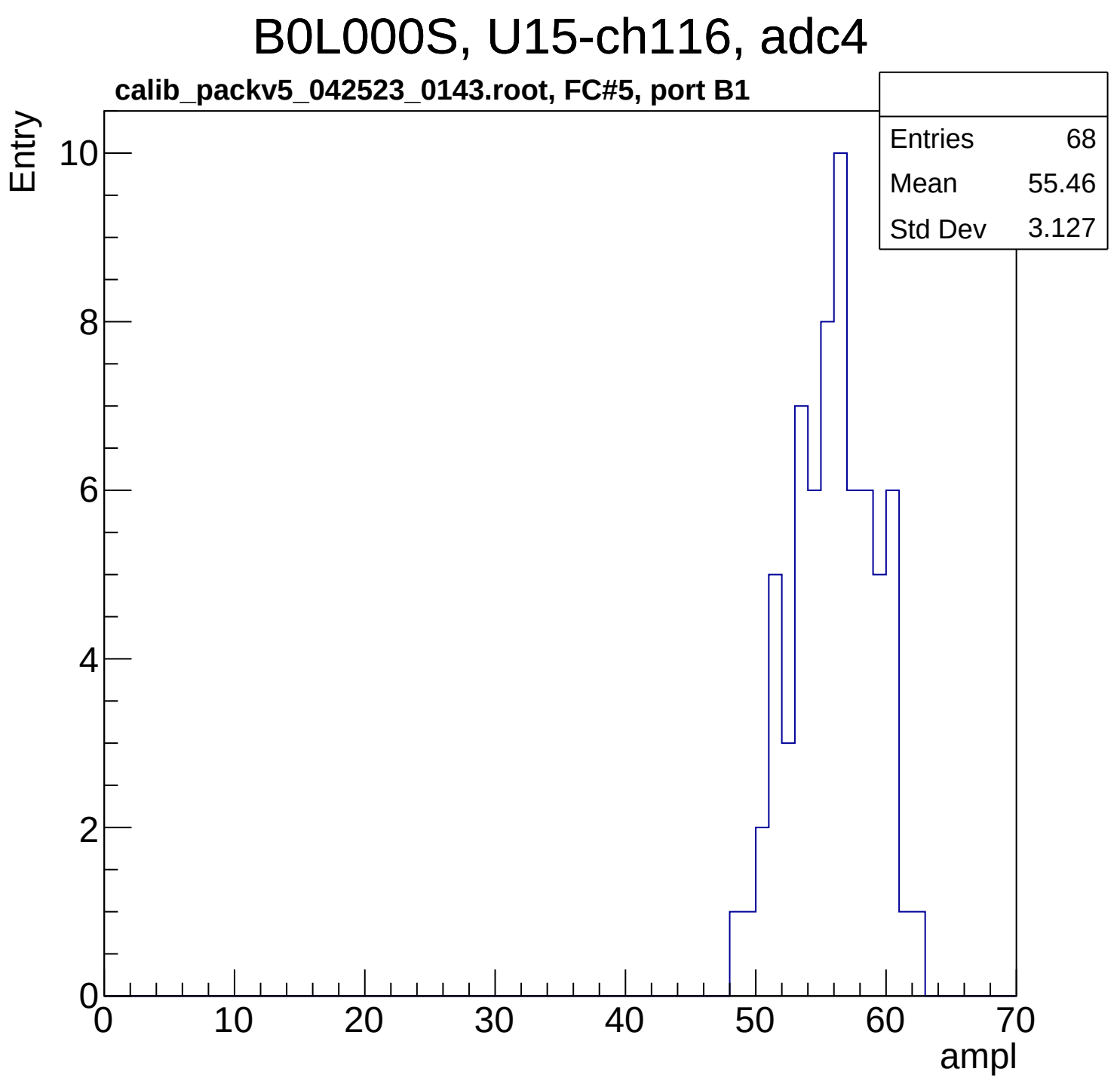
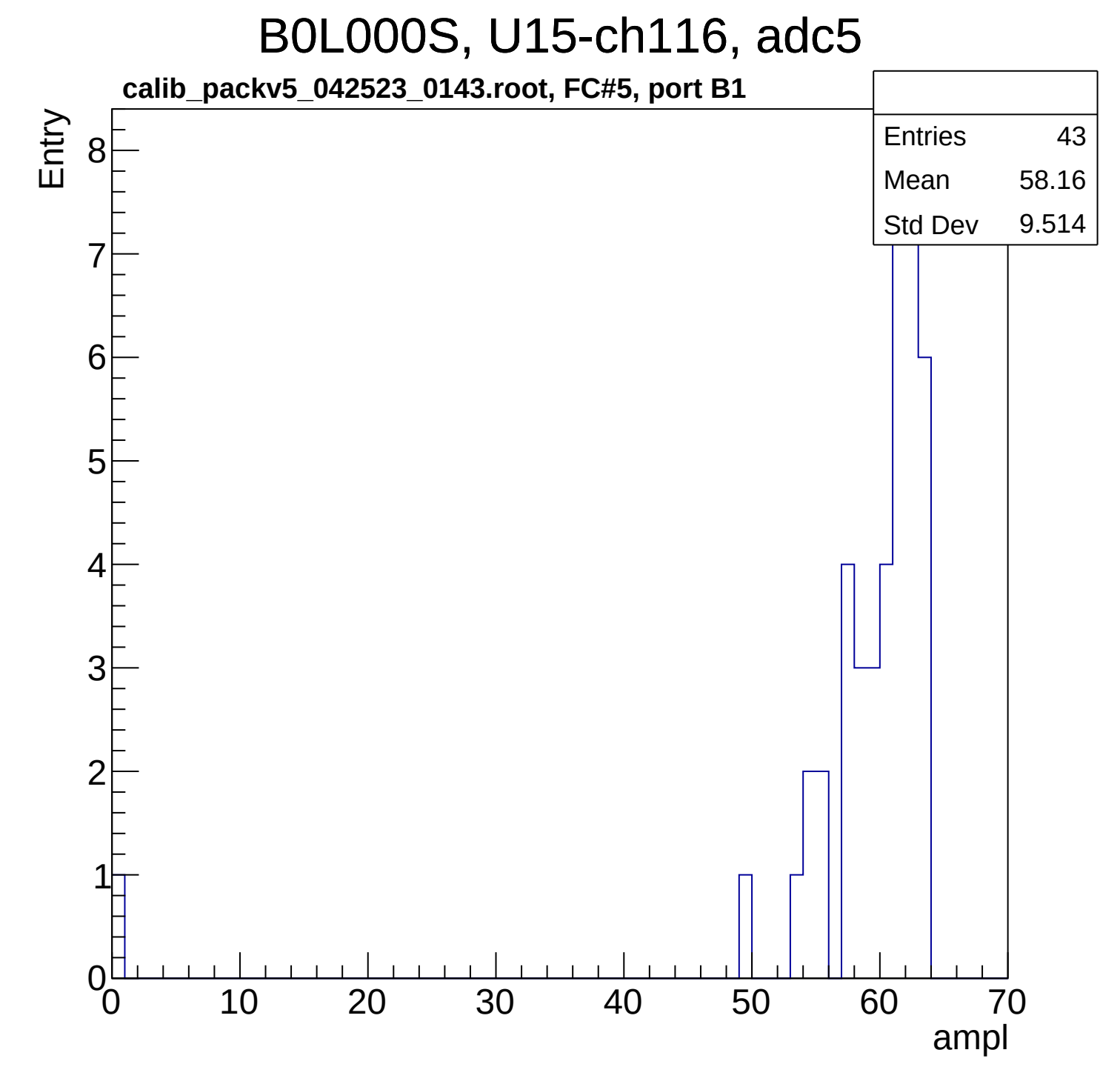

B0L000S, U15-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 49.11
Std Dev 3.038
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.46
Std Dev 3.127
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.16
Std Dev 9.514
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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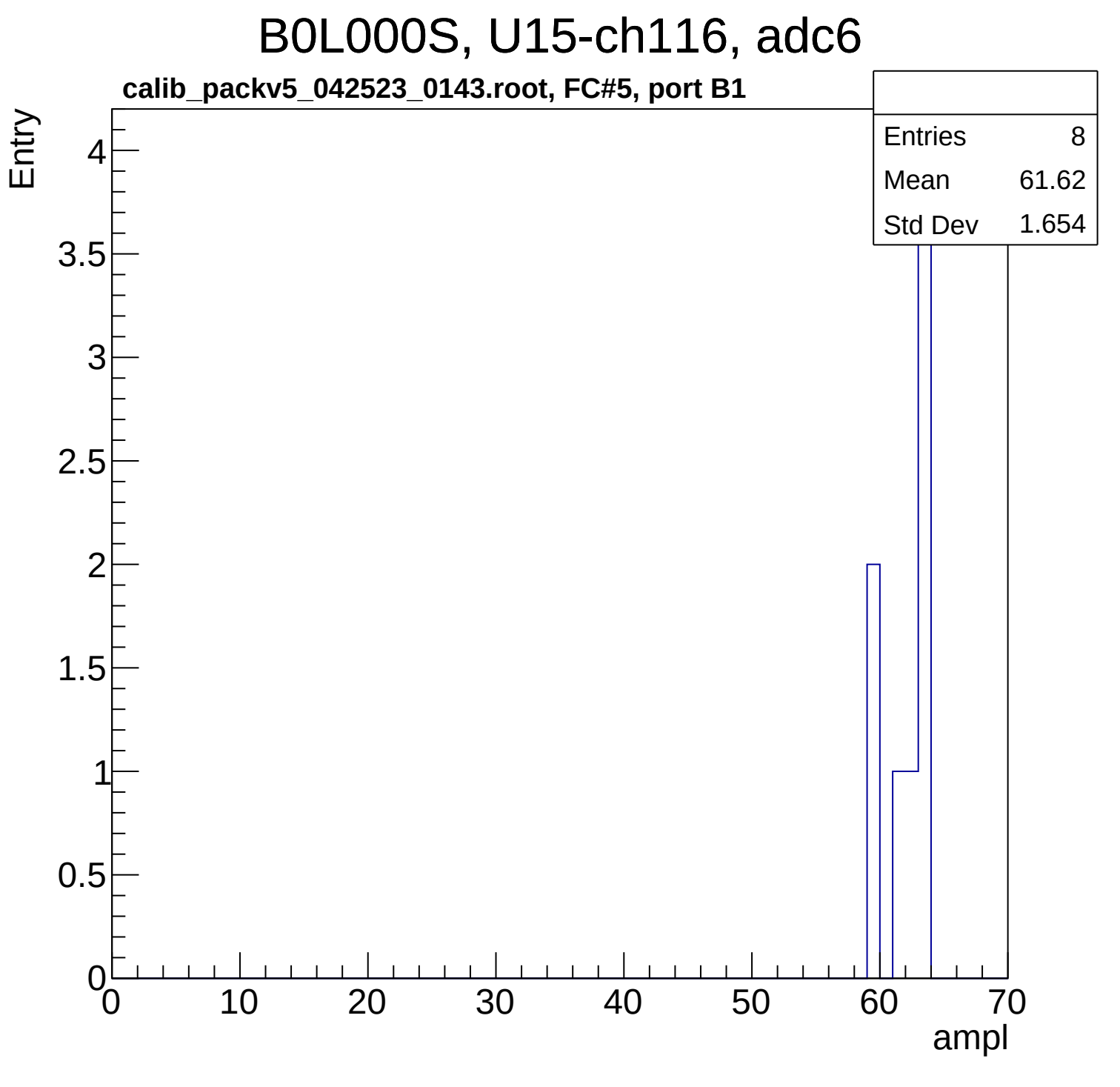
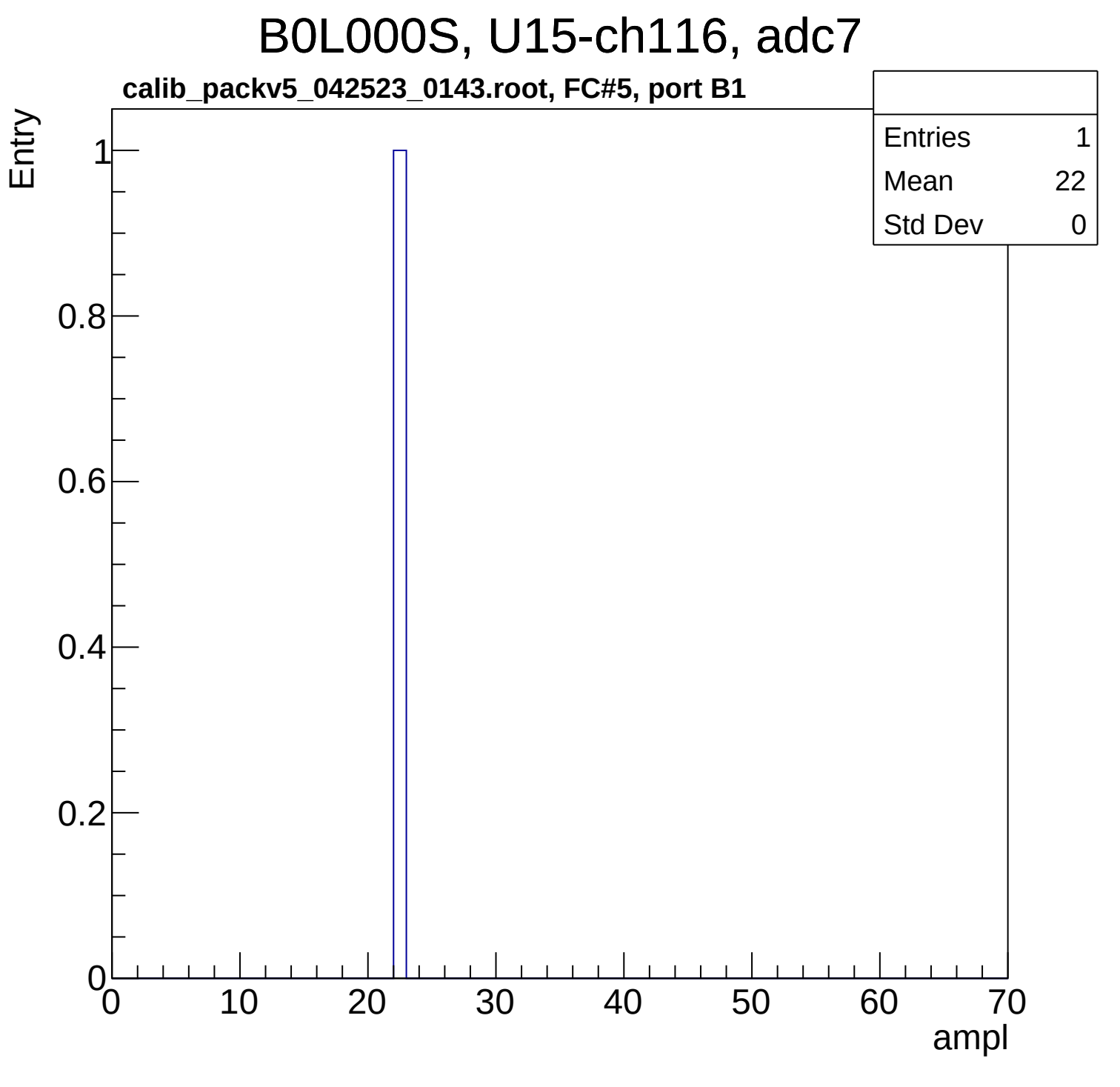
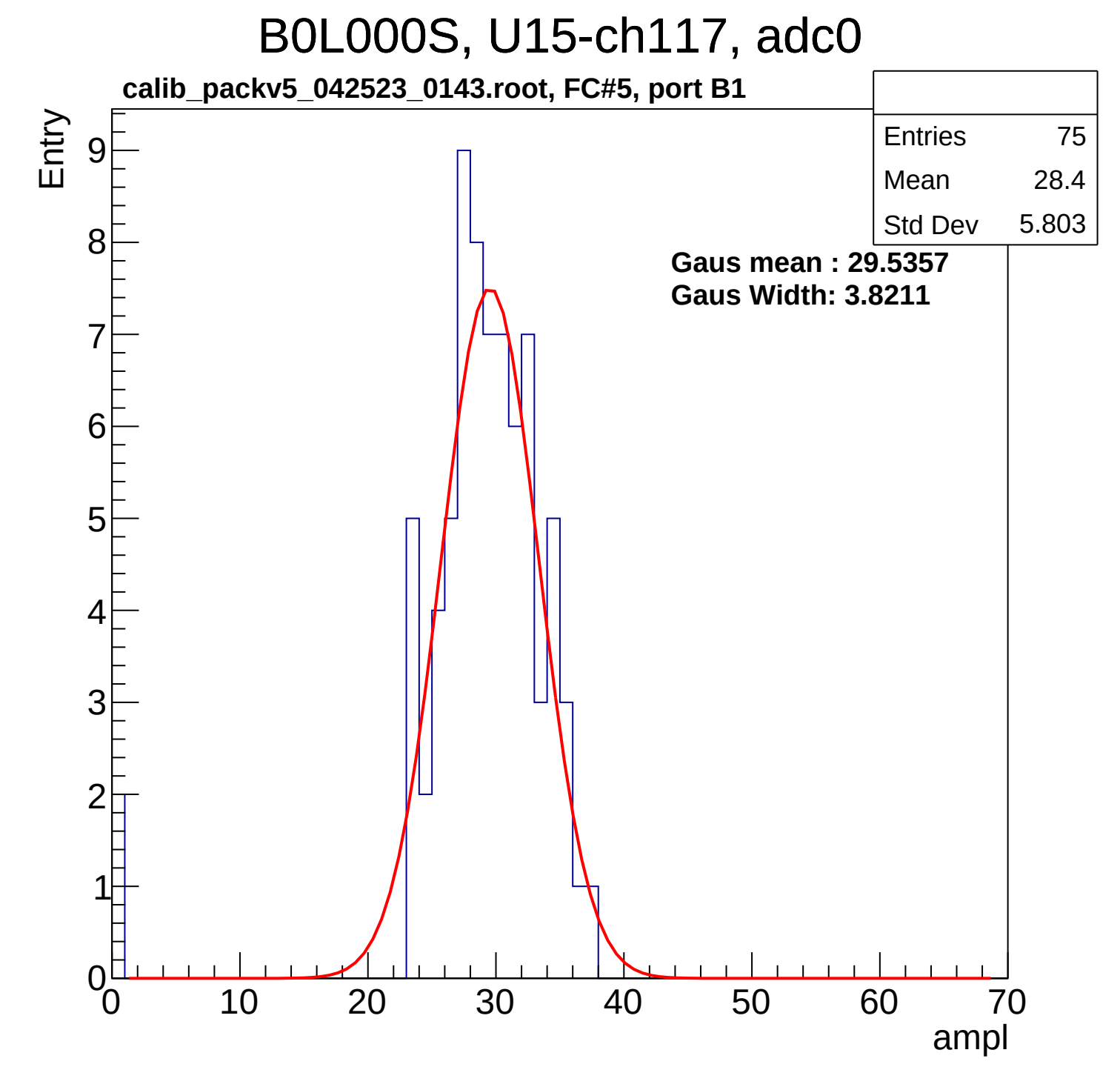
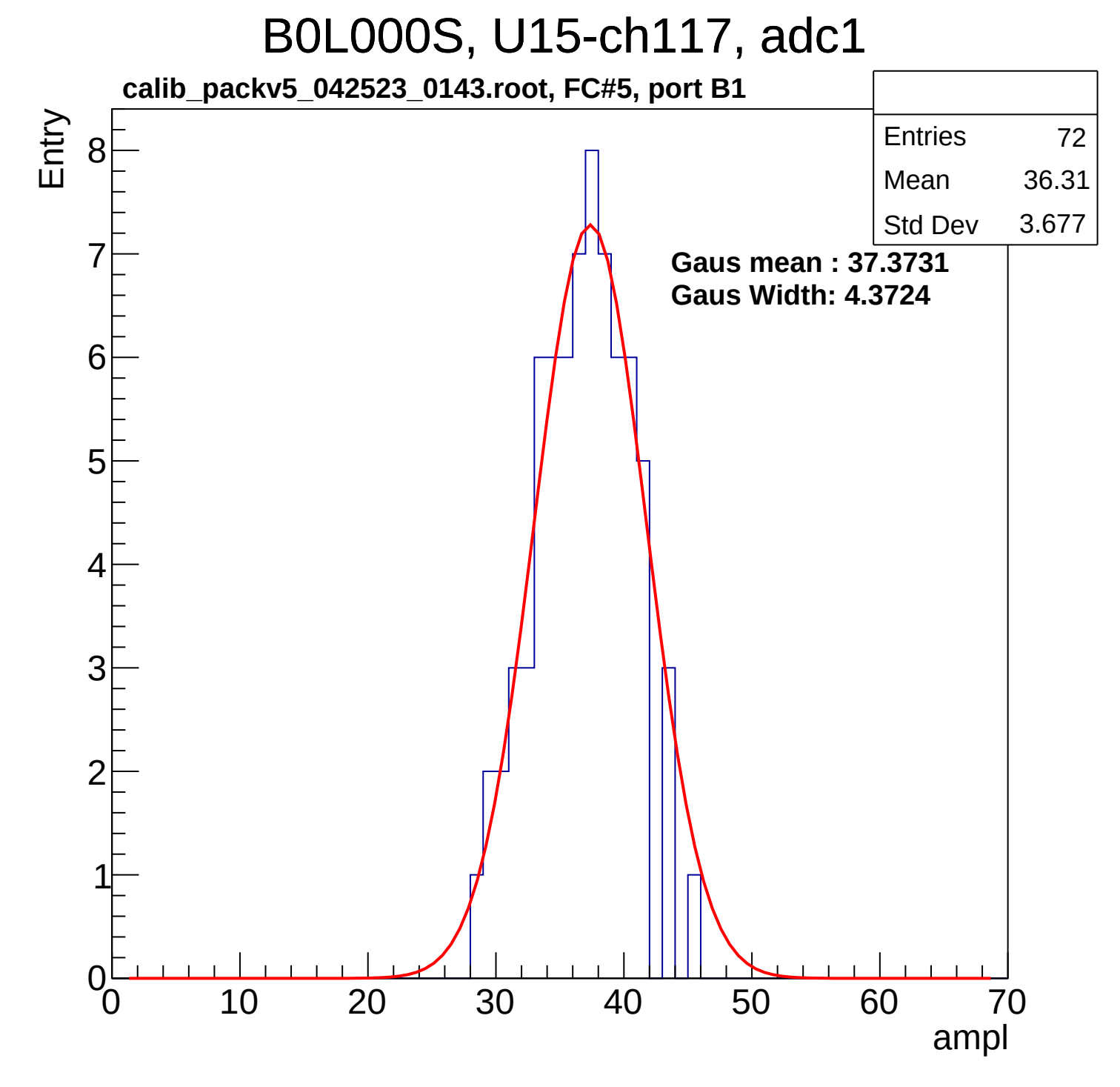
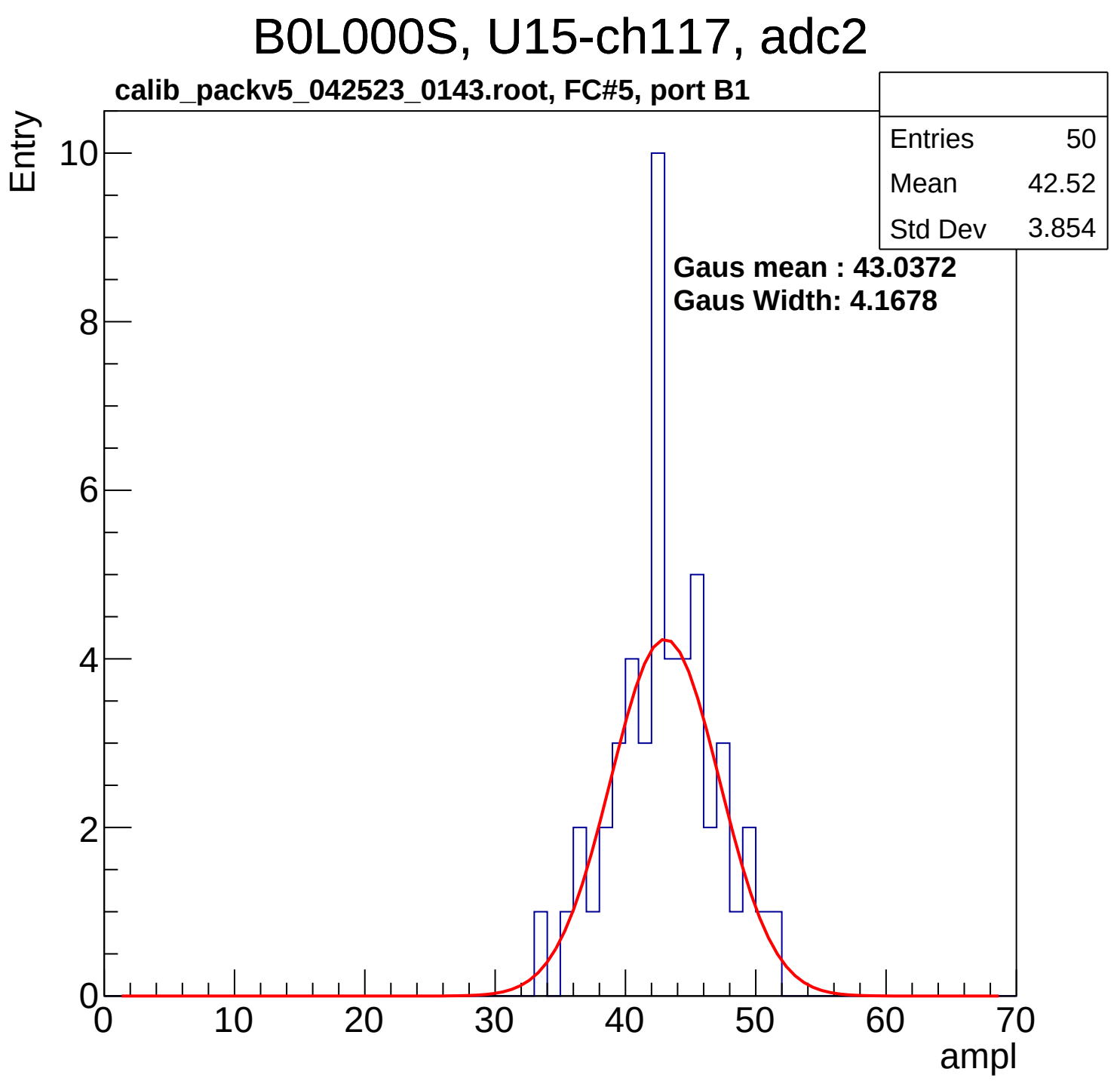
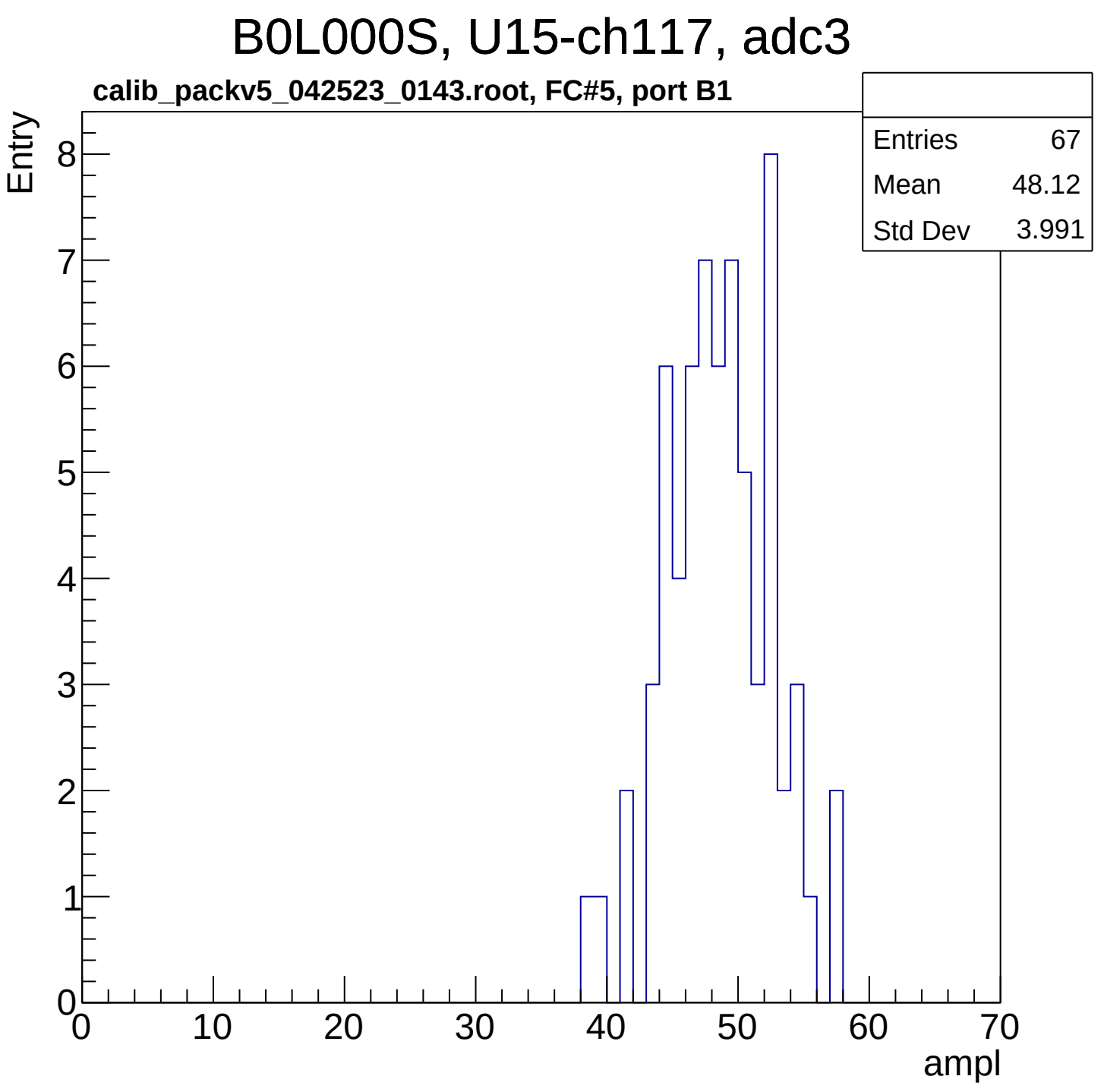

B0L000S, U15-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.62
Std Dev 1.654
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

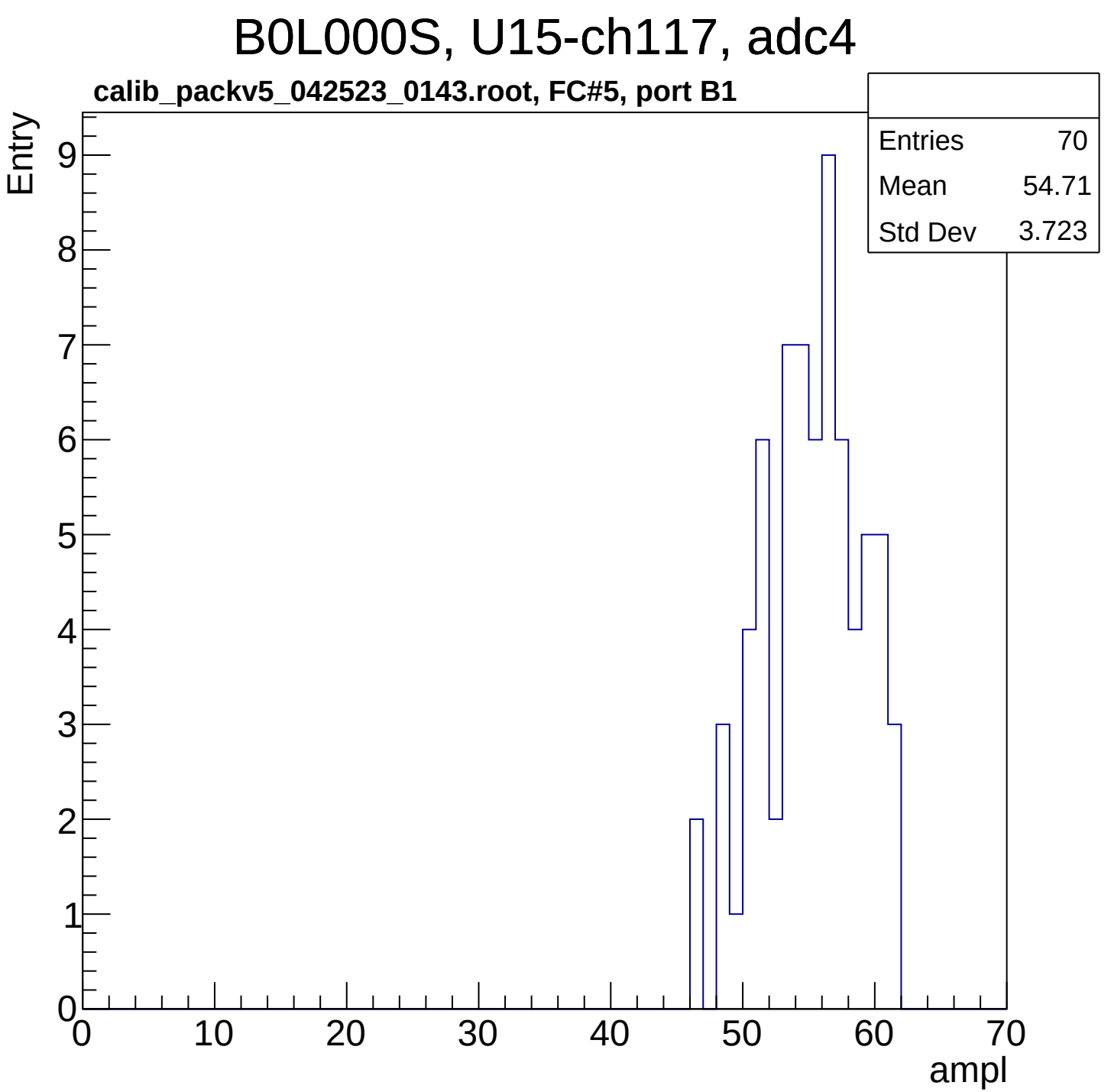

B0L000S, U15-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

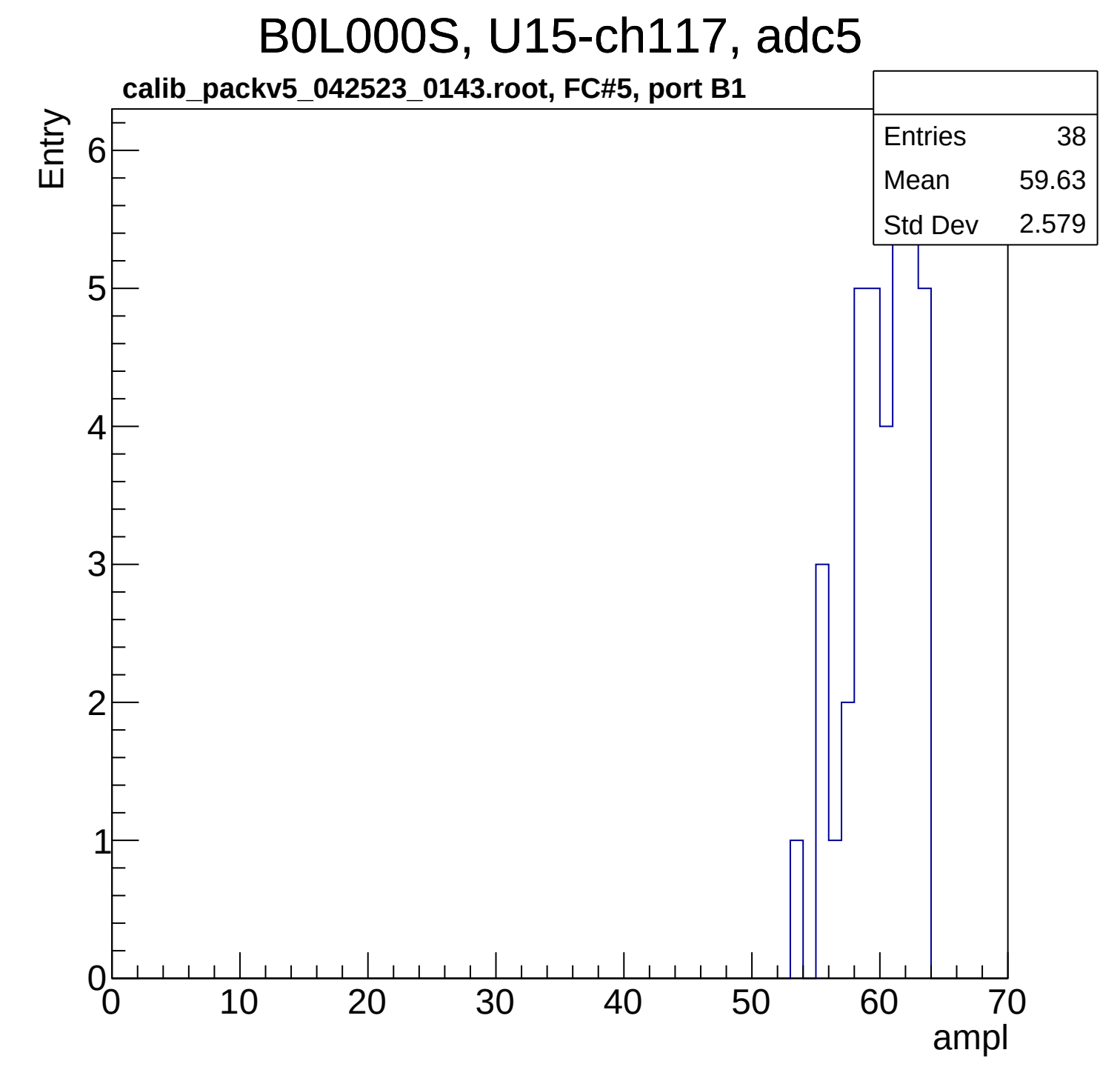

B0L000S, U15-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.4
Std Dev 5.803
Gaus mean : 29.5357
Gaus Width: 3.8211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

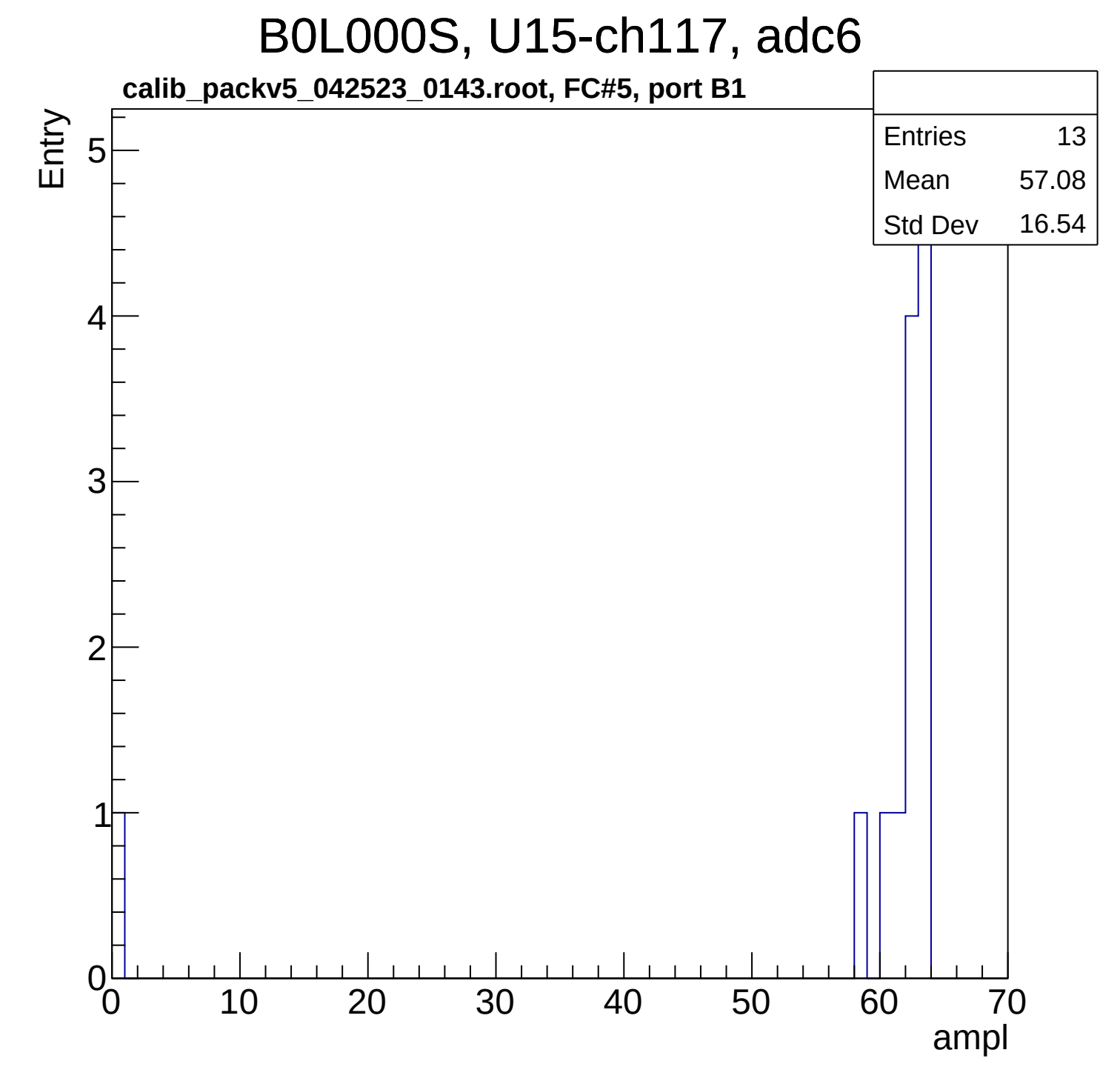

B0L000S, U15-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.31
Std Dev 3.677
Gaus mean : 37.3731
Gaus Width: 4.3724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

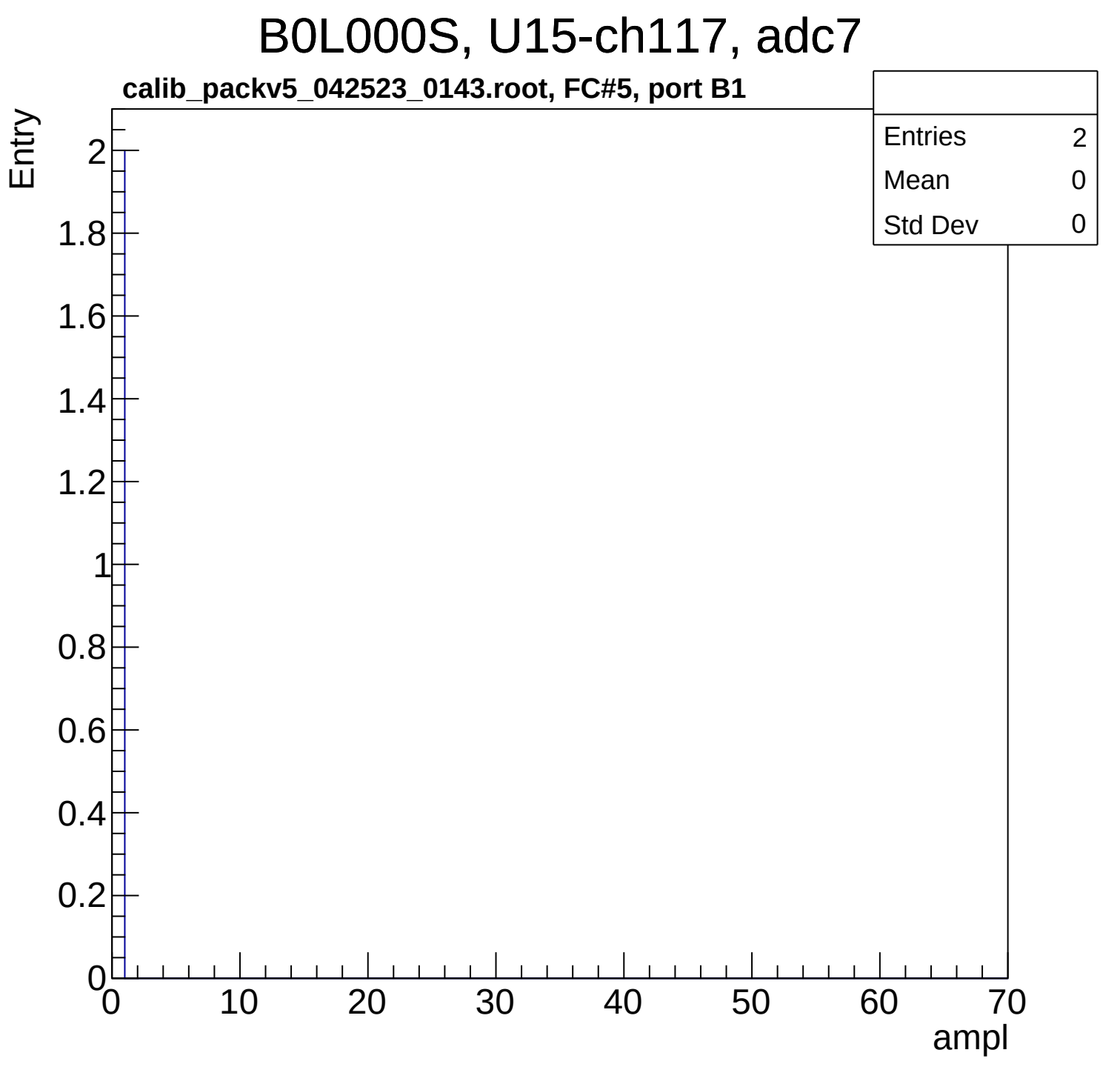

B0L000S, U15-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.52
Std Dev 3.854
Gaus mean : 43.0372
Gaus Width: 4.1678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

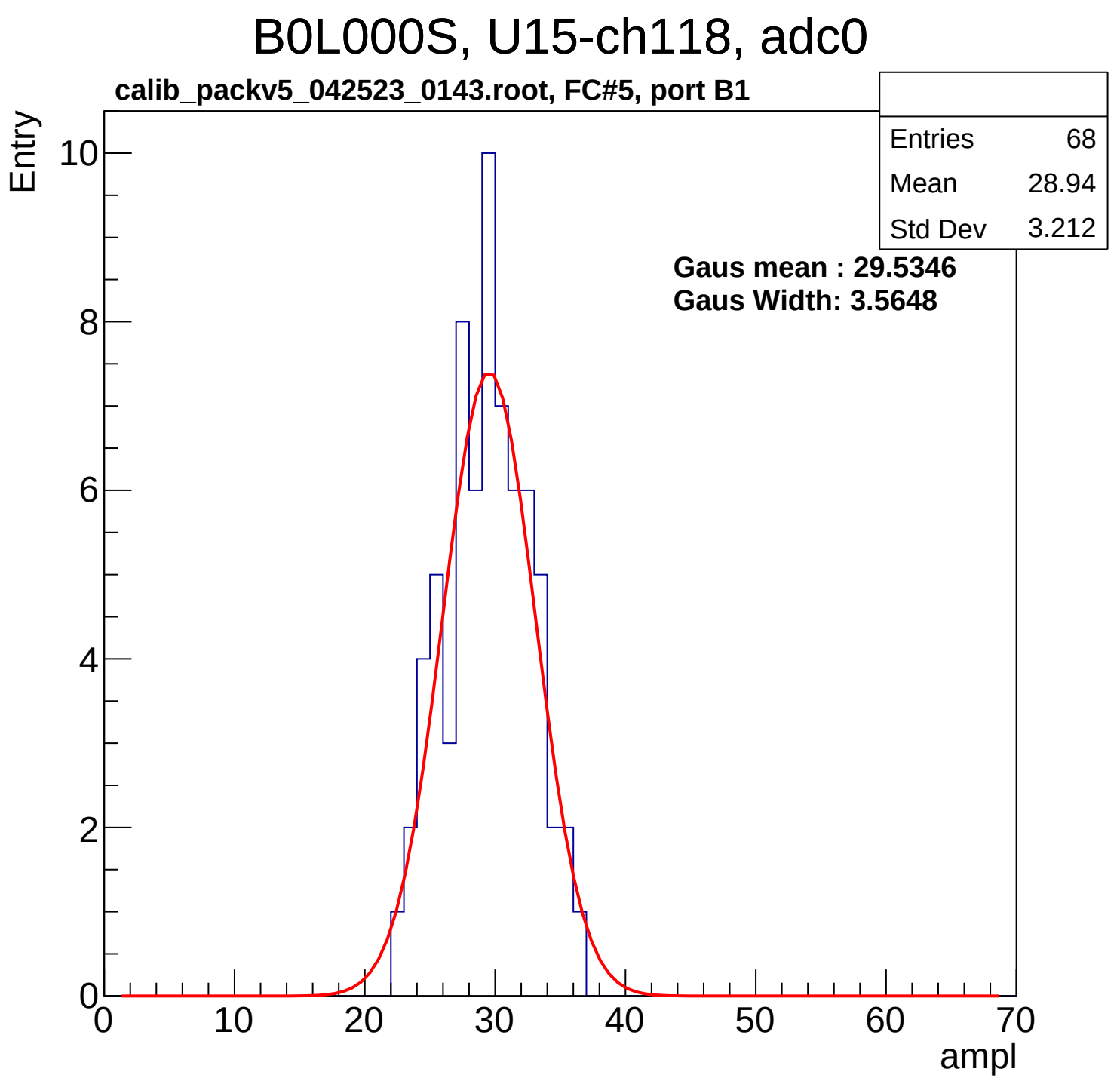

B0L000S, U15-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.12
Std Dev 3.991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 54.71
Std Dev 3.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.63
Std Dev 2.579
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.08
Std Dev 16.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.94
Std Dev 3.212
Gaus mean : 29.5346
Gaus Width: 3.5648
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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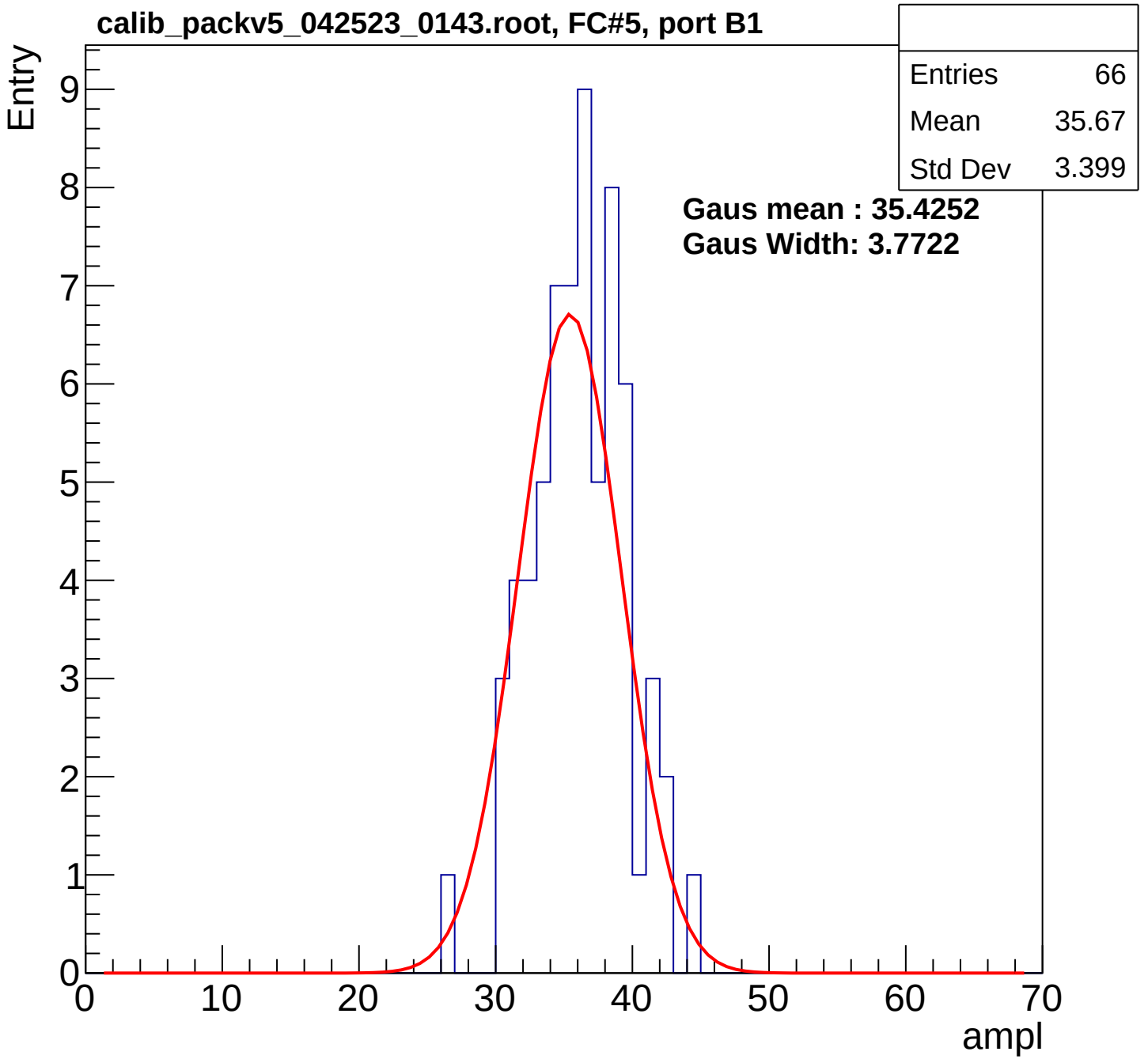
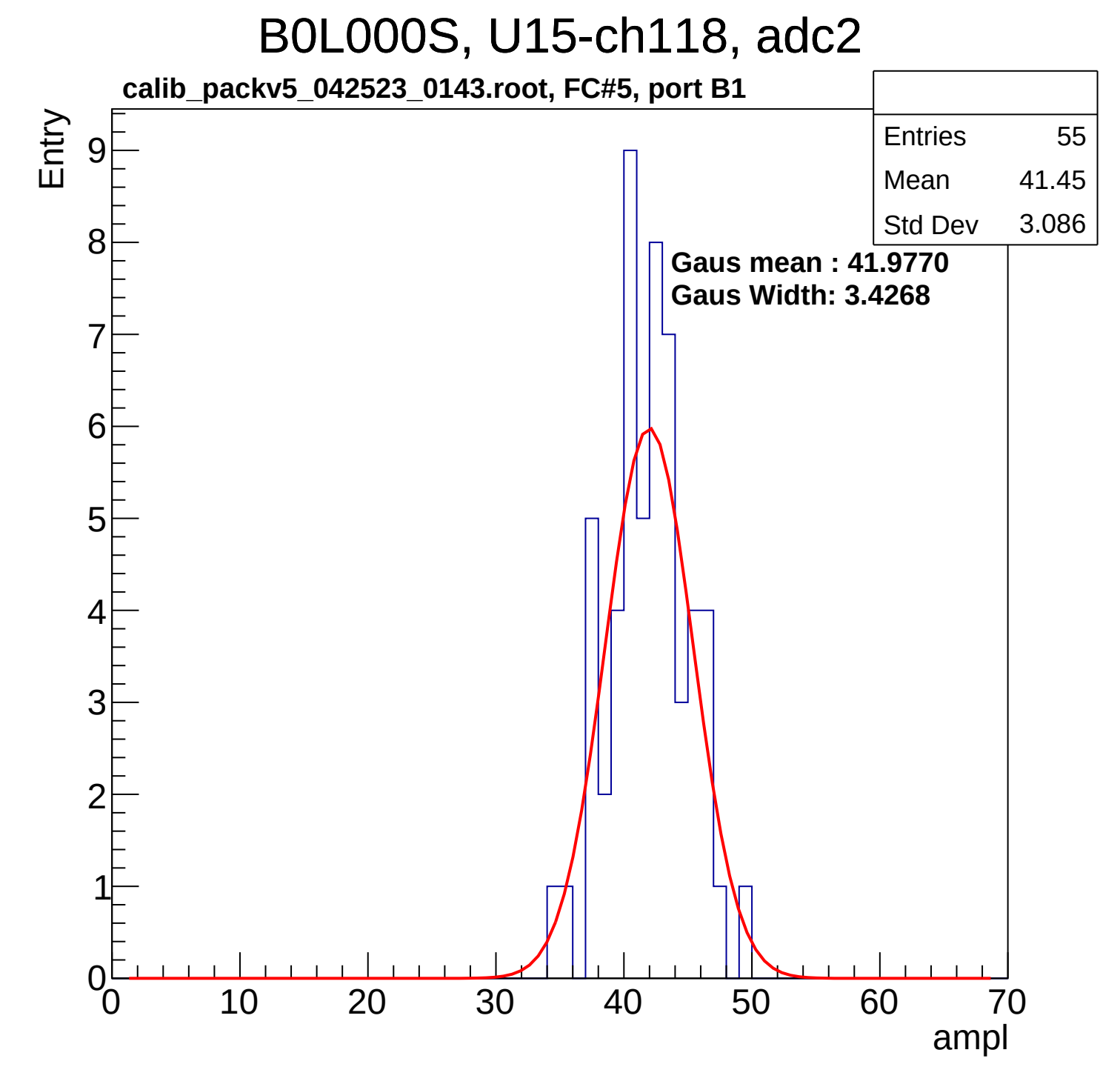

B0L000S, U15-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.67
Std Dev 3.399
Gaus mean : 35.4252
Gaus Width: 3.7722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.45
Std Dev 3.086
Gaus mean : 41.9770
Gaus Width: 3.4268
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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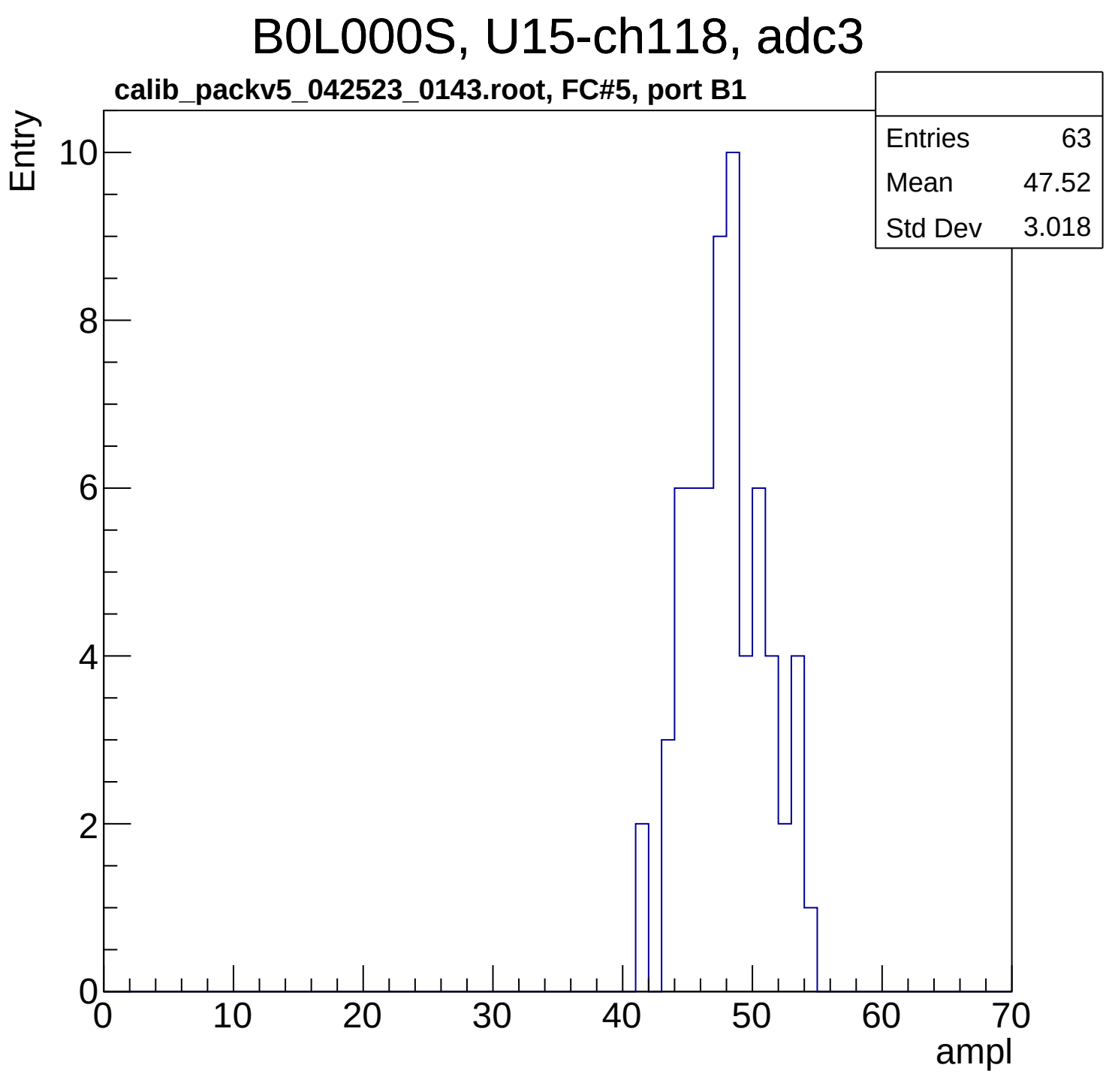
B0L000S, U15-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 47.52
Std Dev 3.018
Entry
ampl
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2
4
6
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10
0
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40
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60
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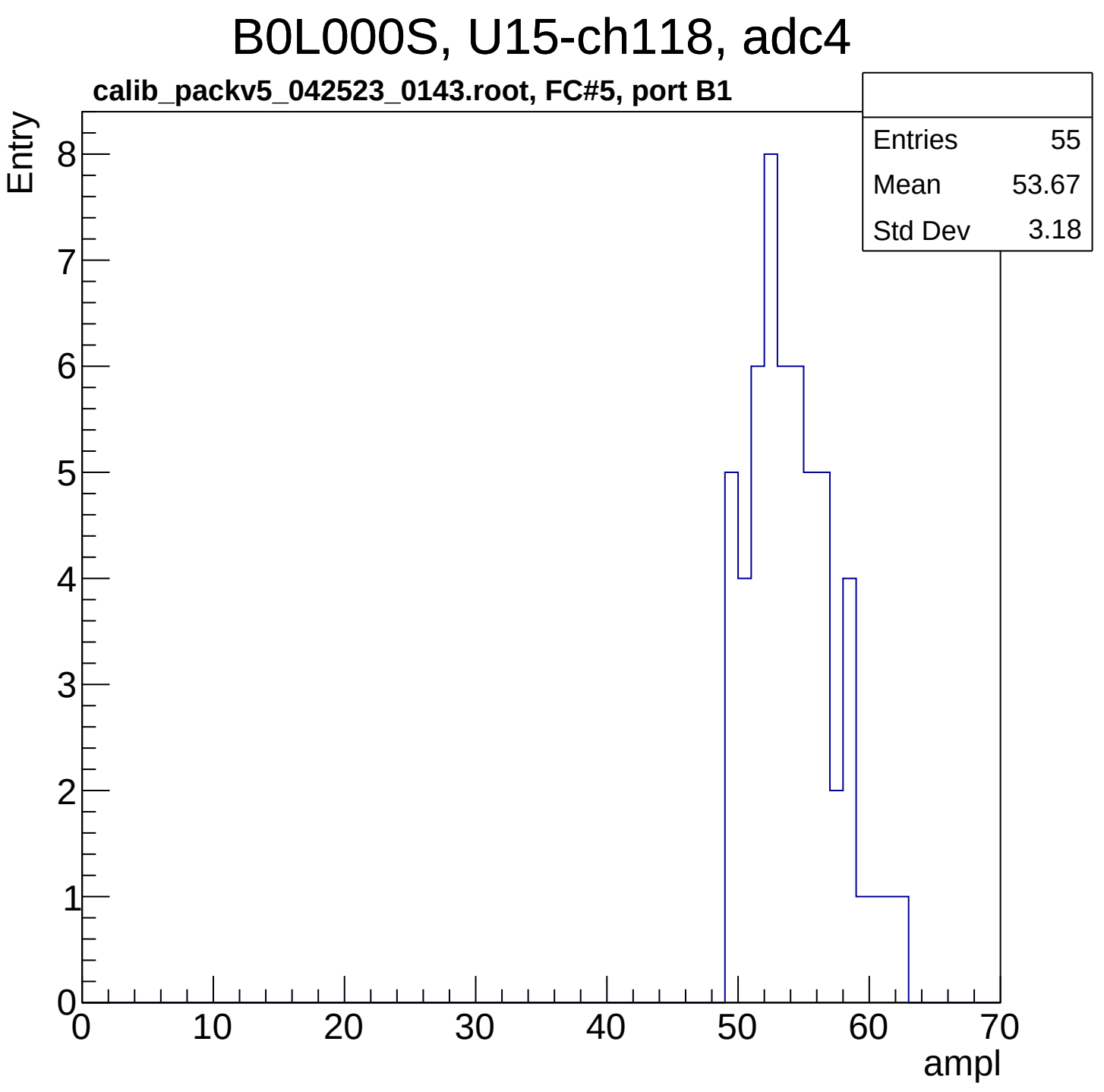
B0L000S, U15-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.67
Std Dev 3.18
Entry
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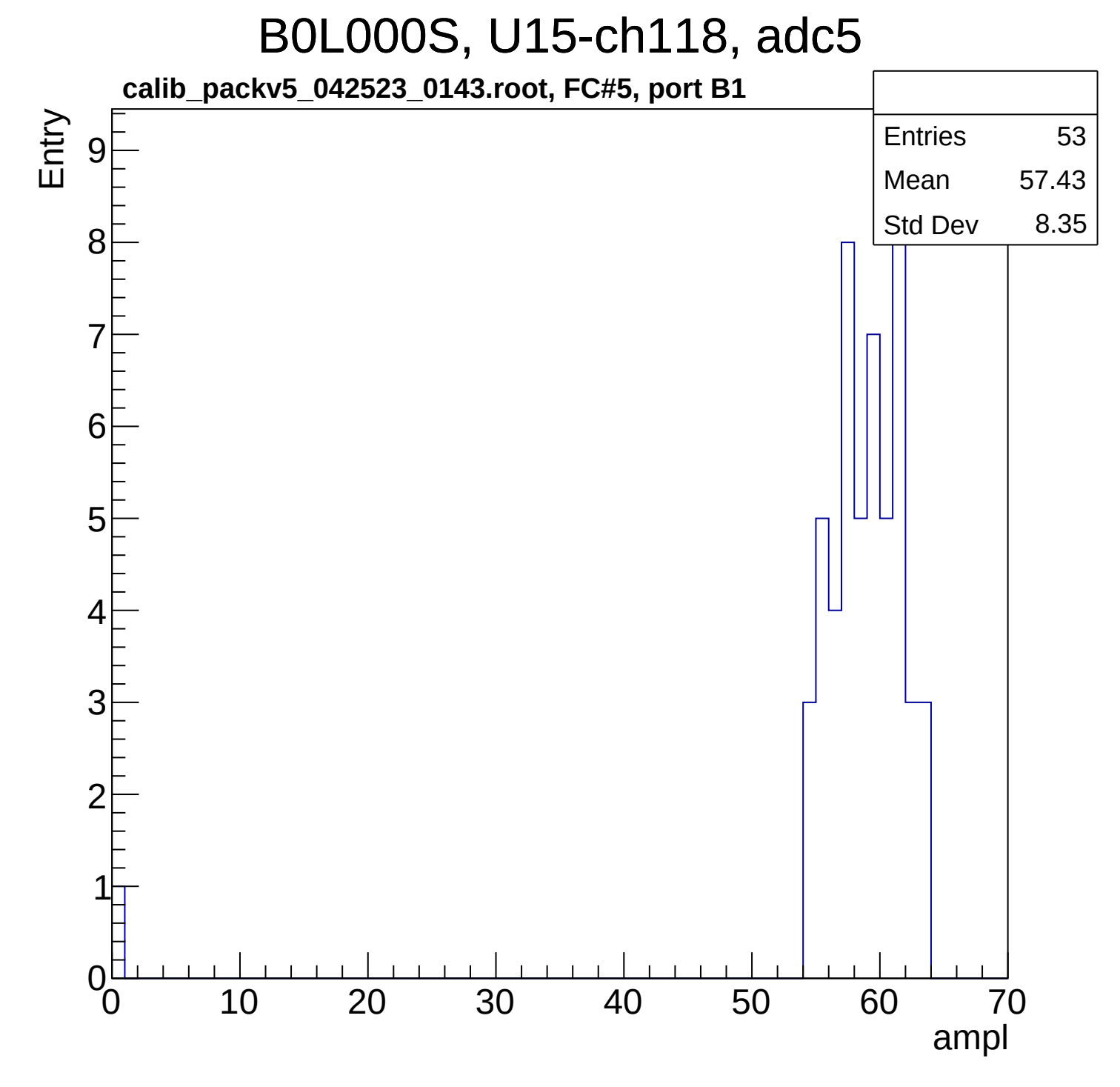
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.43
Std Dev 8.35
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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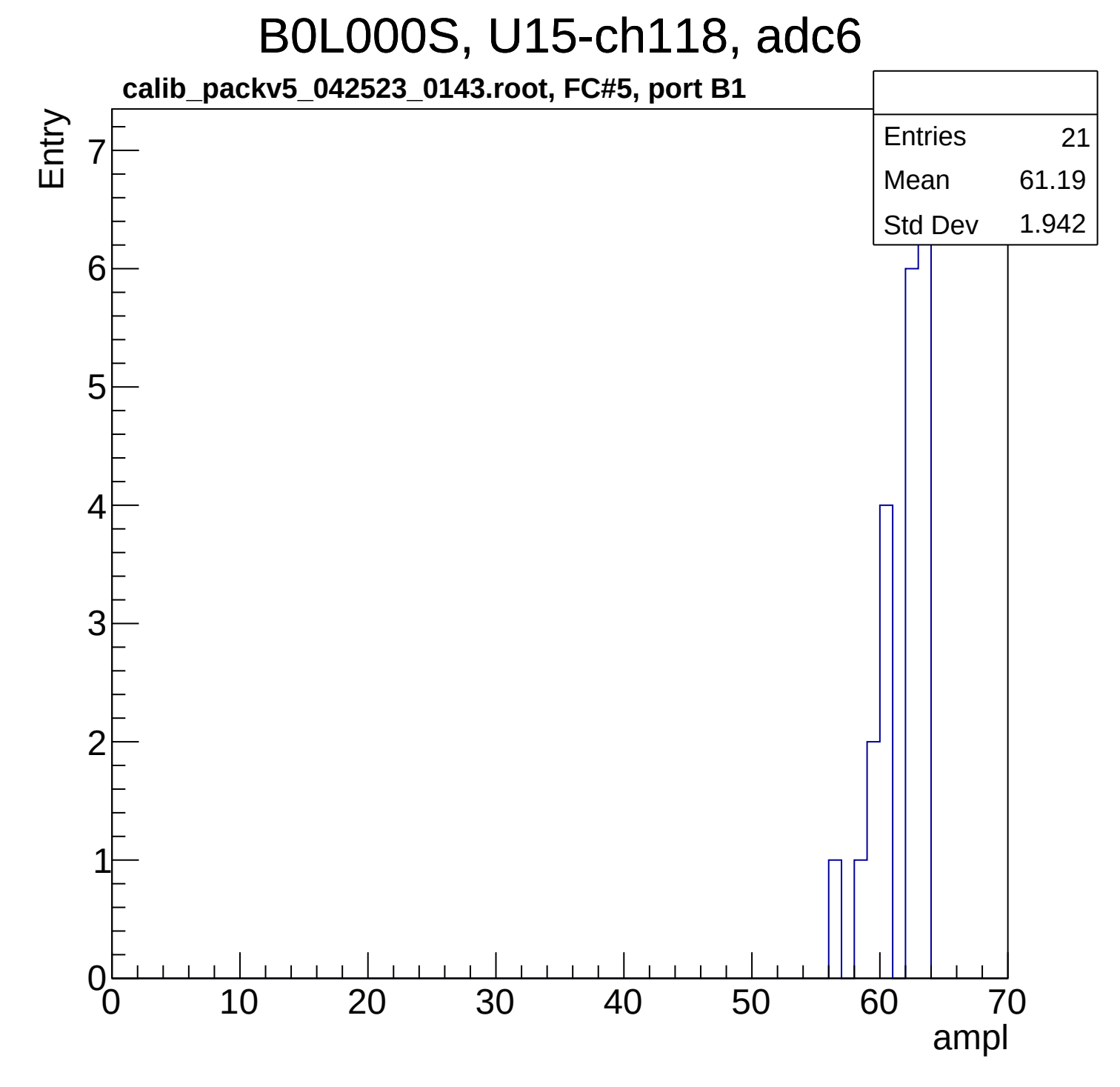
B0L000S, U15-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.19
Std Dev 1.942
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
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60
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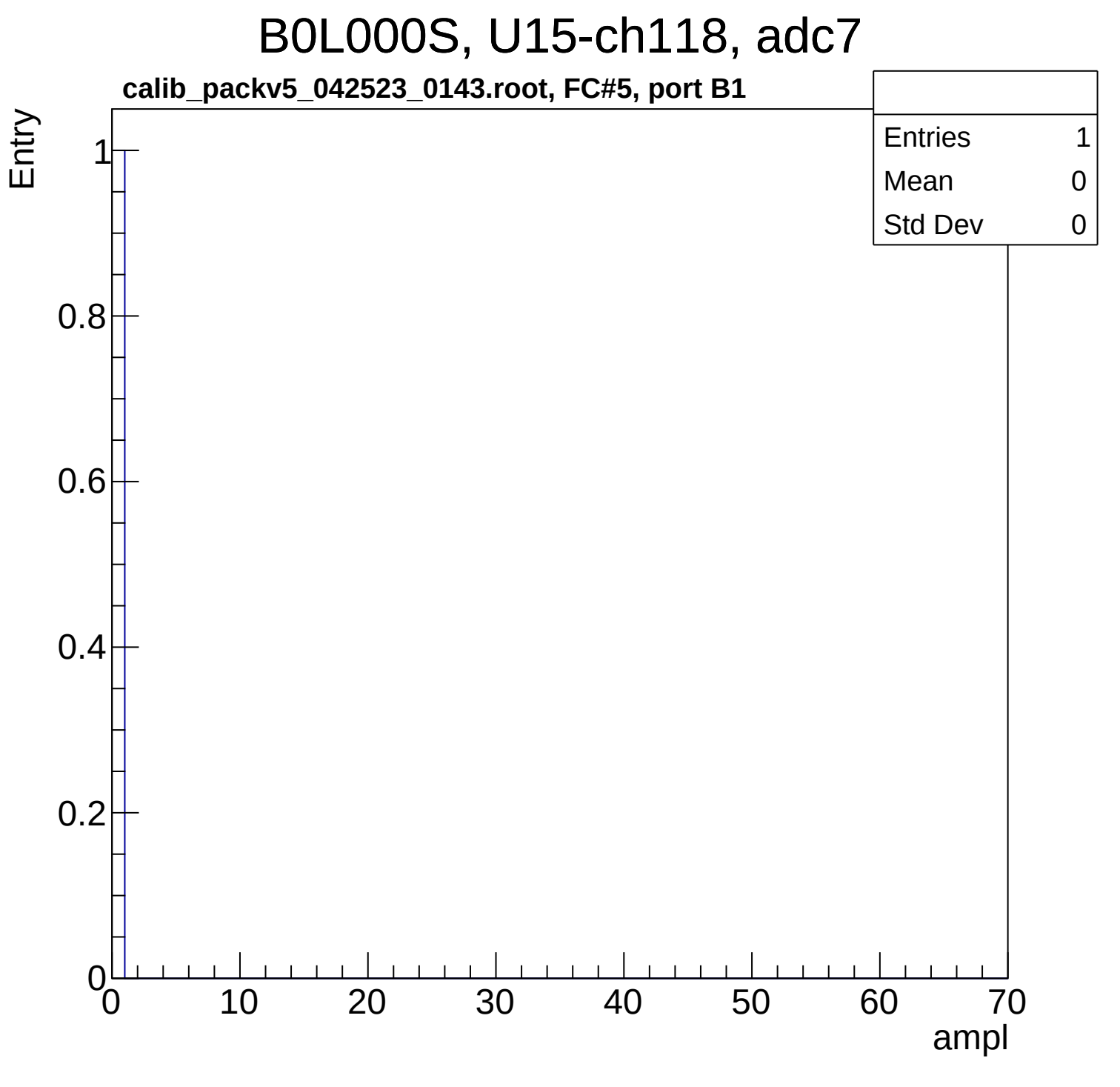
B0L000S, U15-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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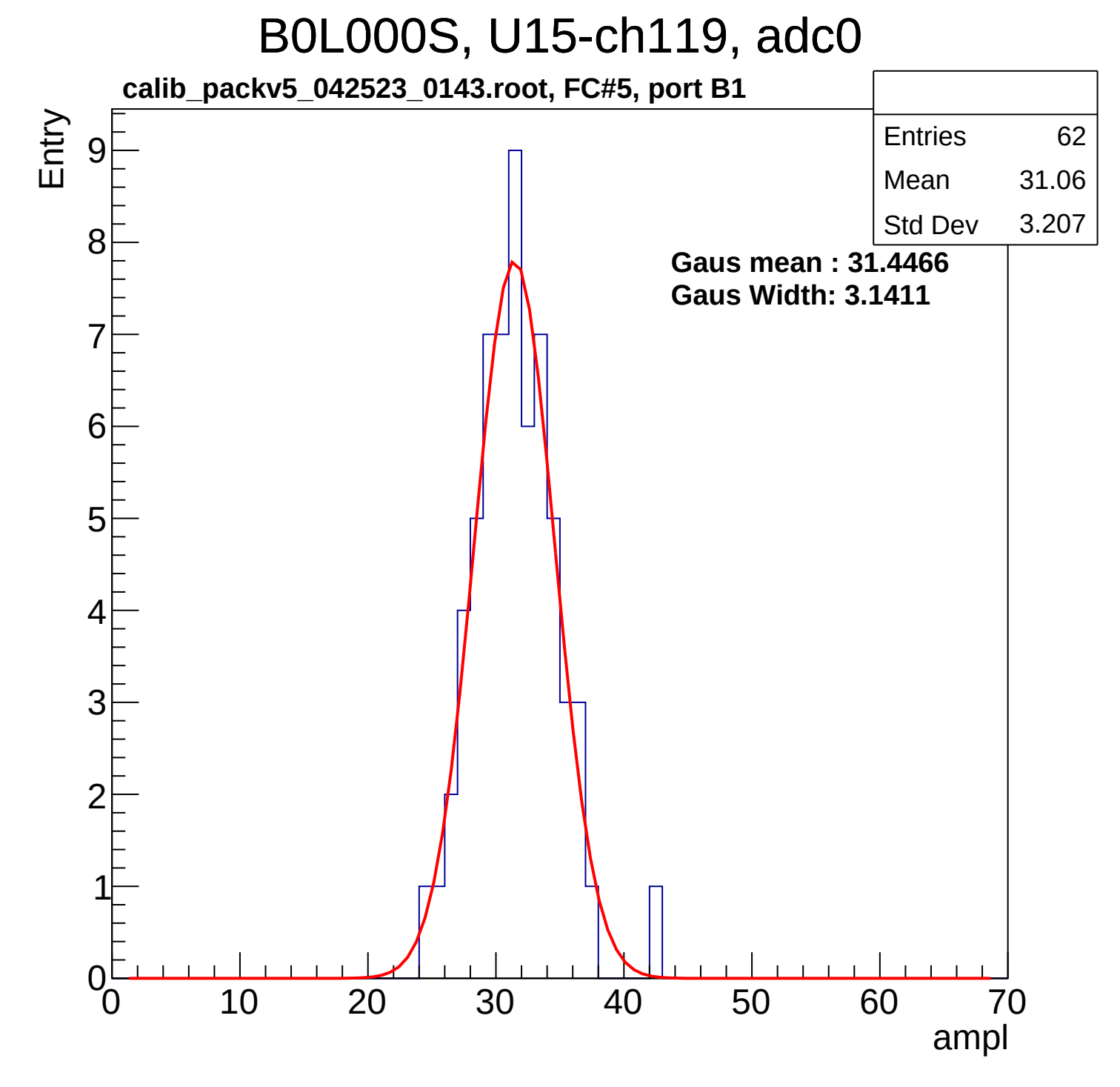
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 31.06
Std Dev 3.207
Gaus mean : 31.4466
Gaus Width: 3.1411
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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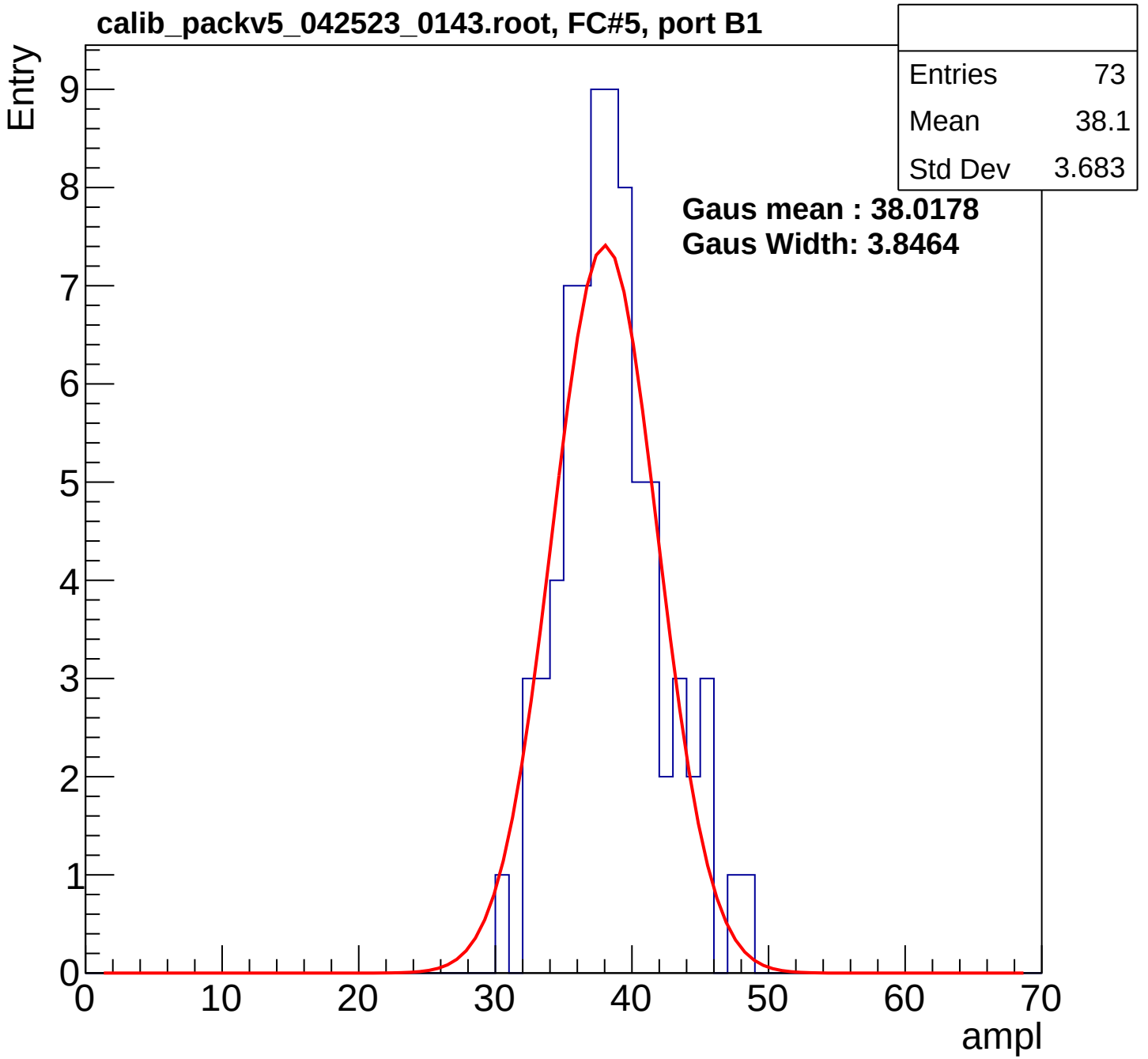
B0L000S, U15-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 38.1
Std Dev 3.683
Gaus mean : 38.0178
Gaus Width: 3.8464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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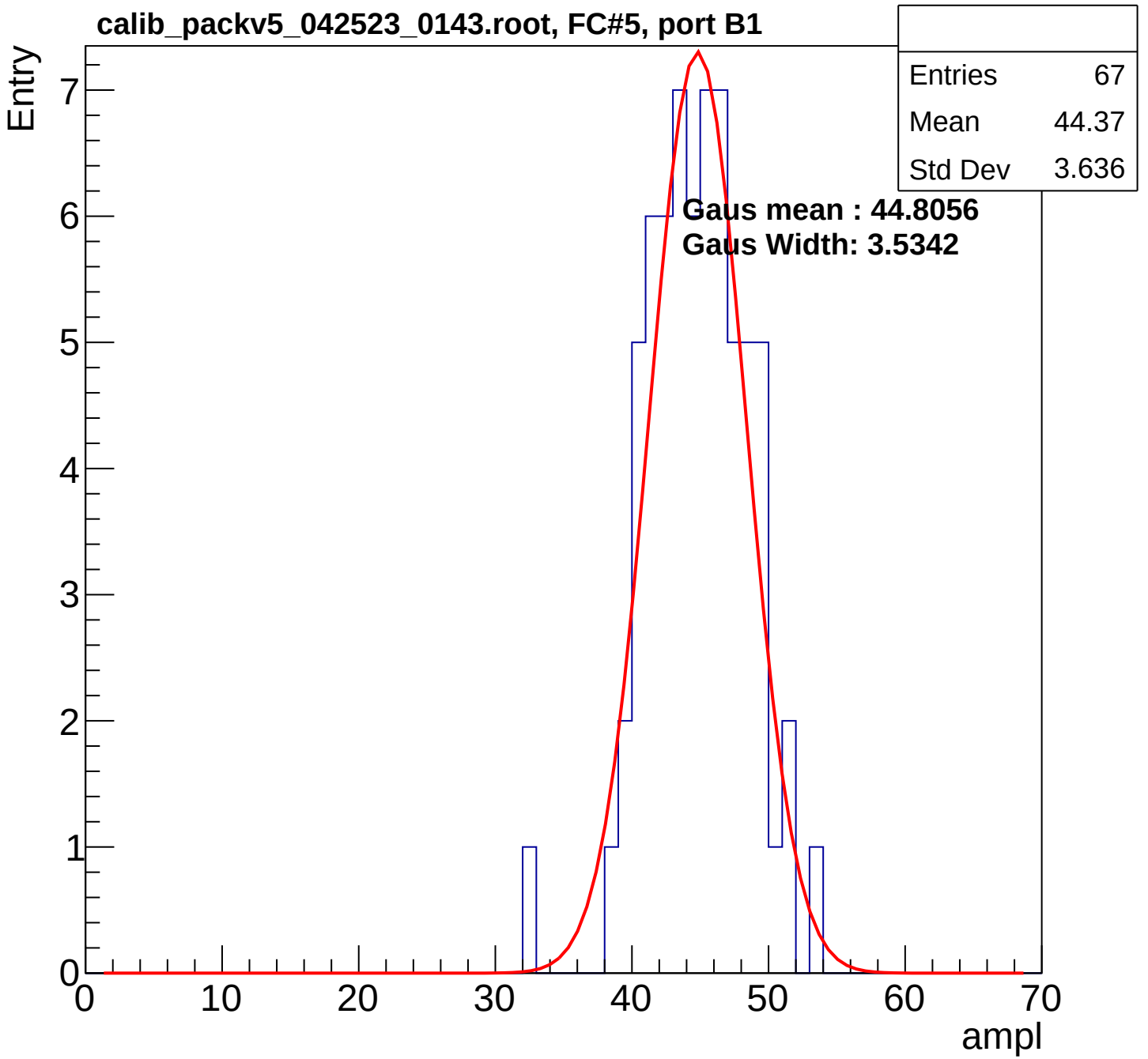
B0L000S, U15-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.37
Std Dev 3.636
Gaus mean : 44.8056
Gaus Width: 3.5342
Entry
ampl
0
1
2
3
4
5
6
7
0
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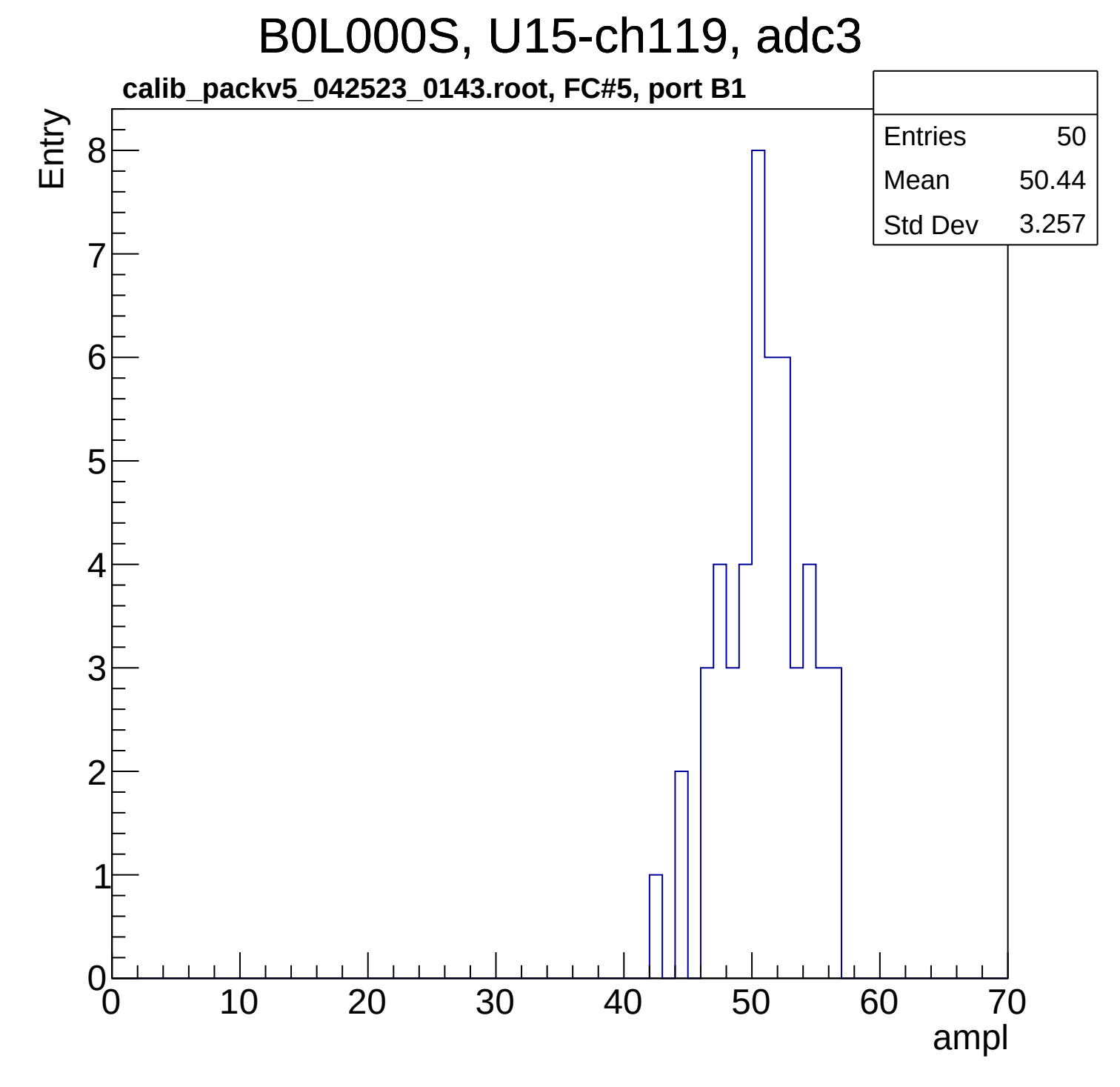
B0L000S, U15-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 50.44
Std Dev 3.257
Entry
ampl
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4
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7
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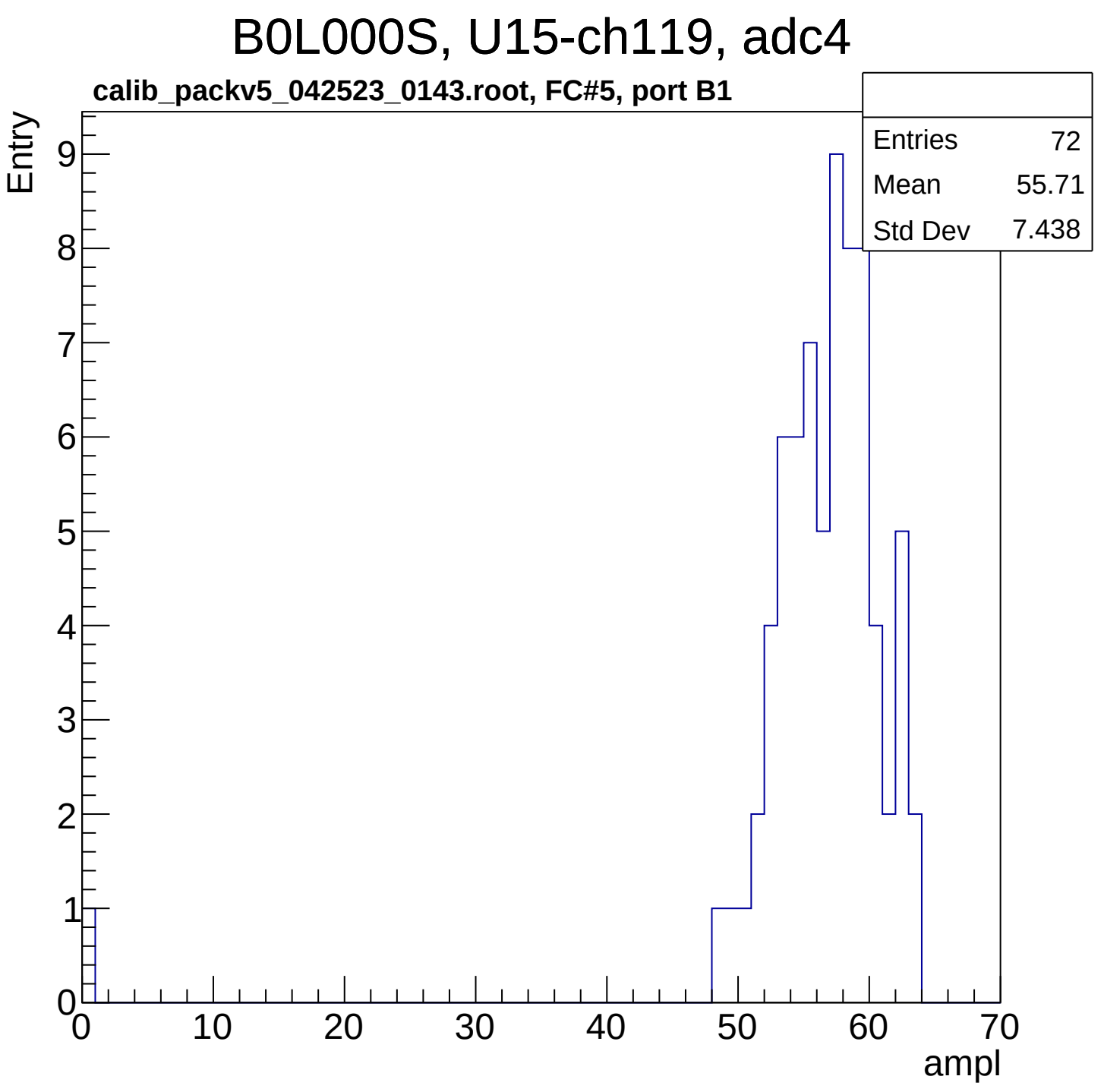
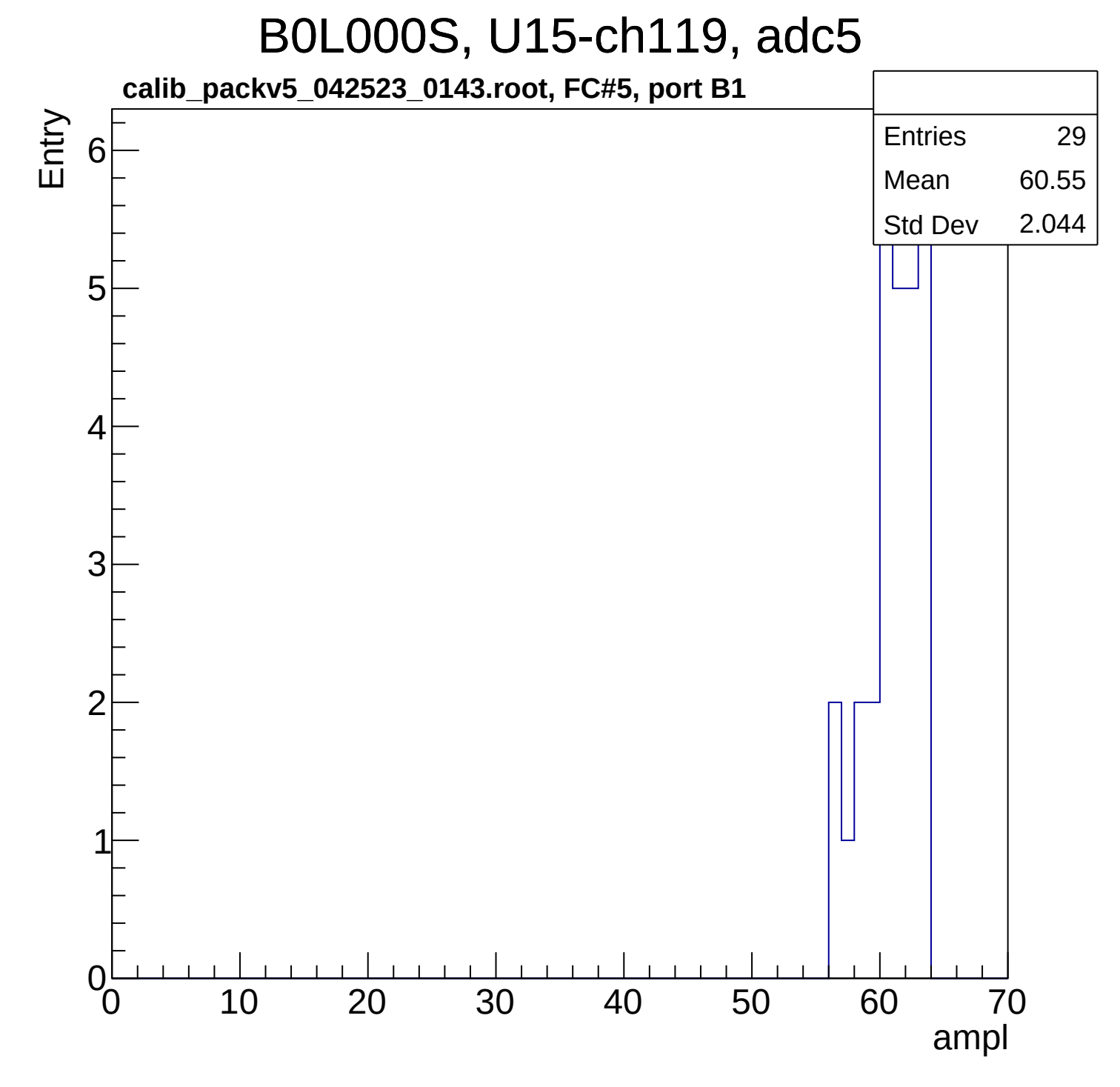

B0L000S, U15-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 55.71
Std Dev 7.438
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 60.55
Std Dev 2.044
Entry
0
1
2
3
4
5
6
0
10
20
30
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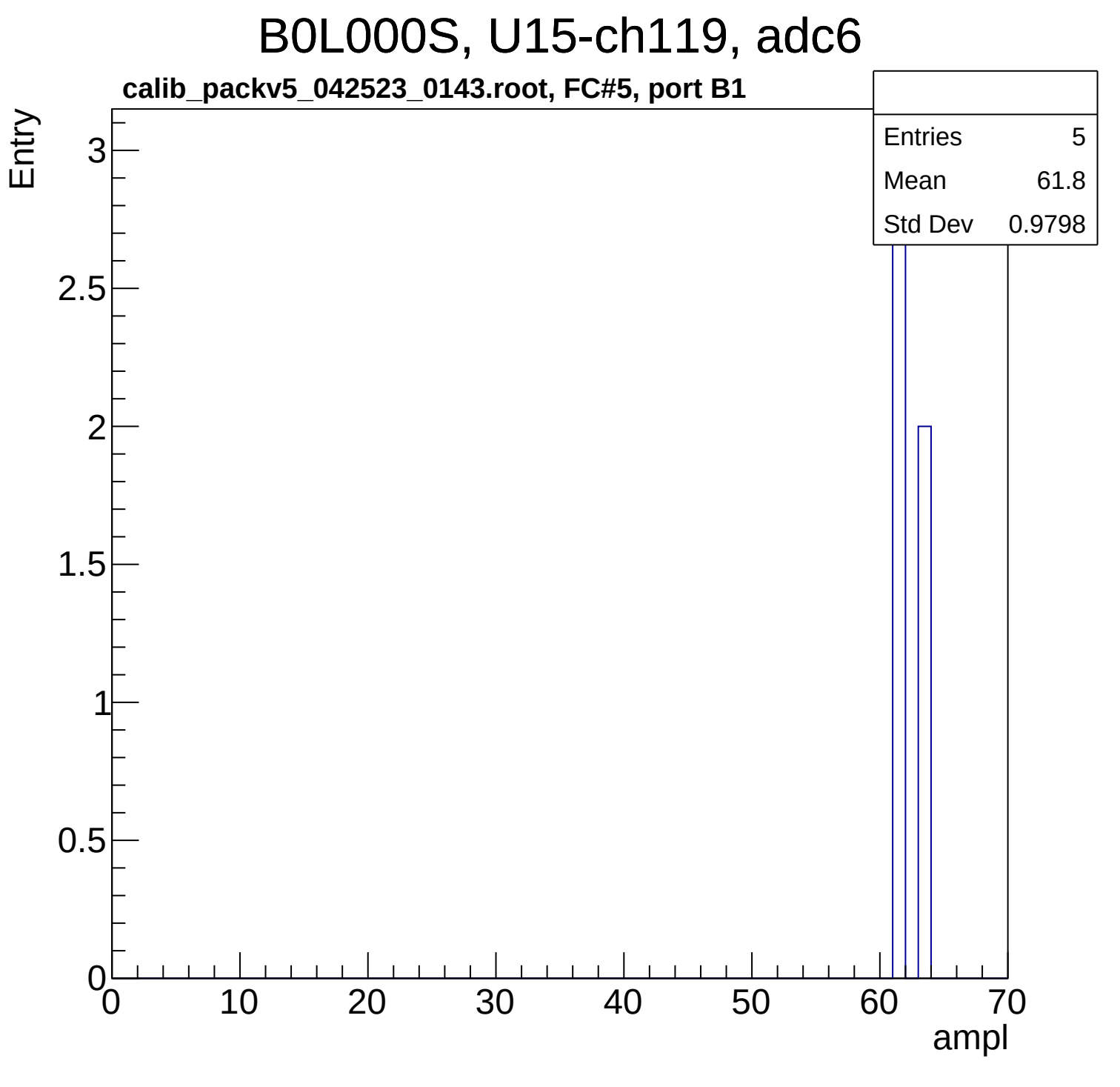
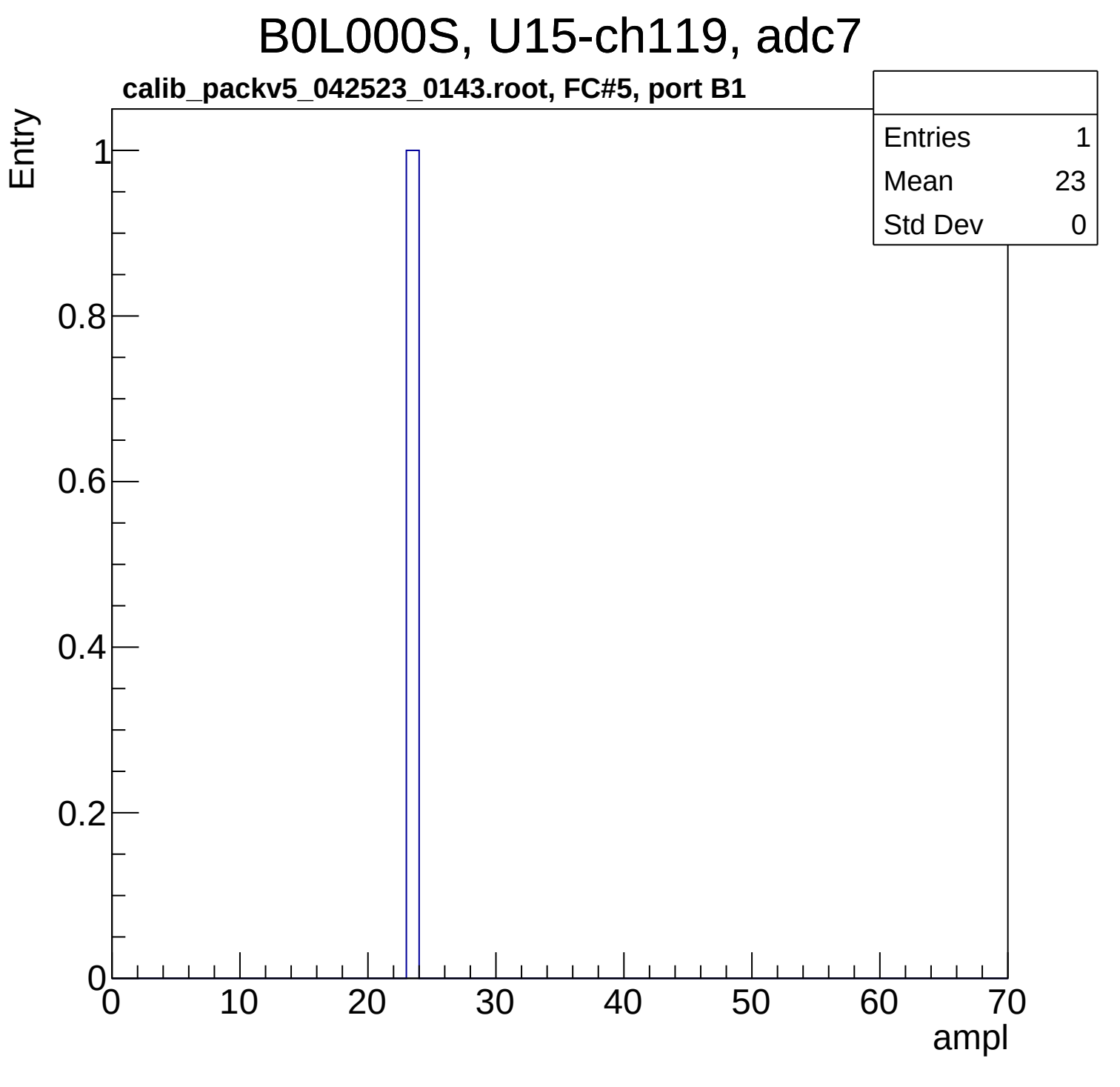
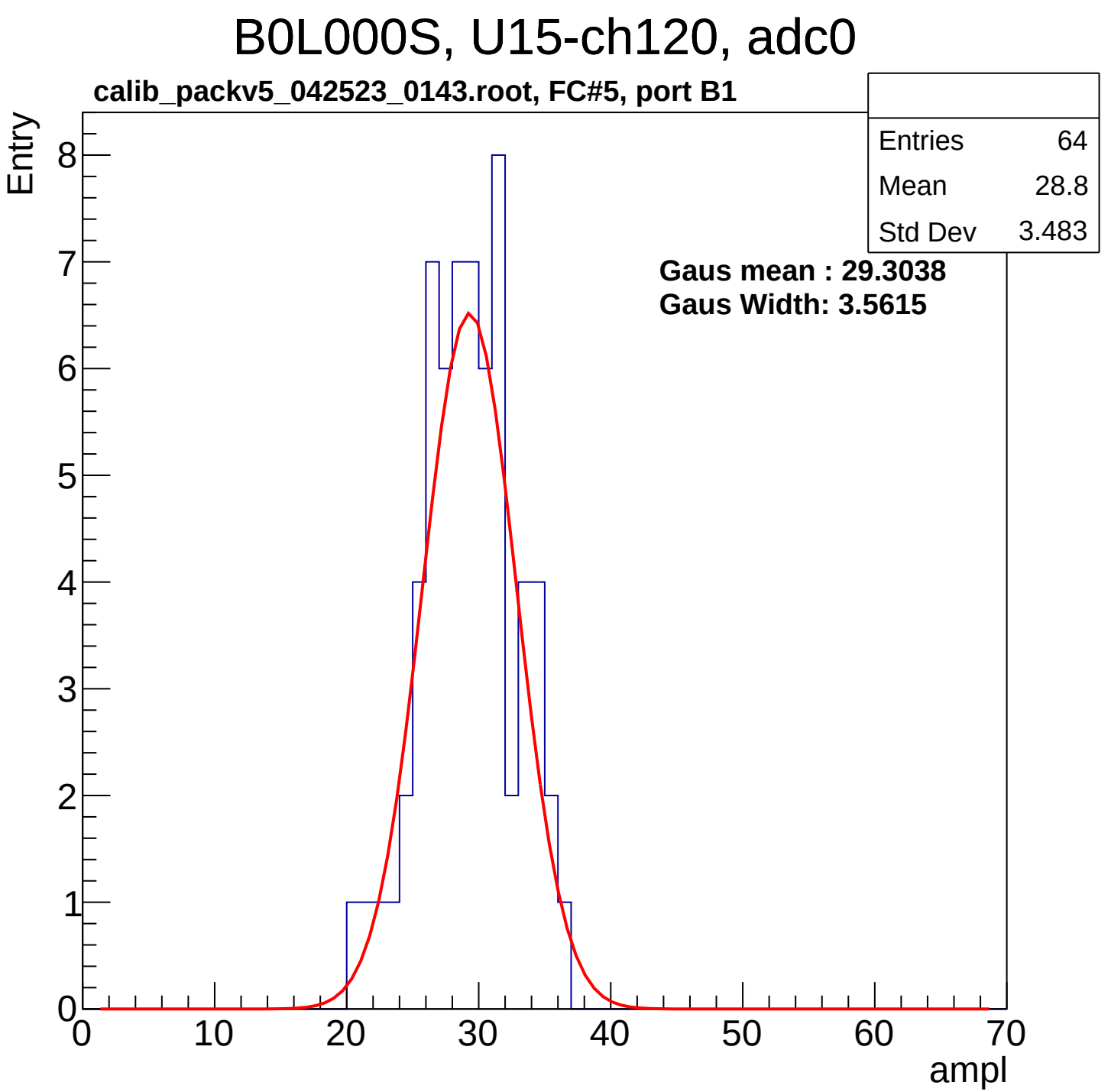
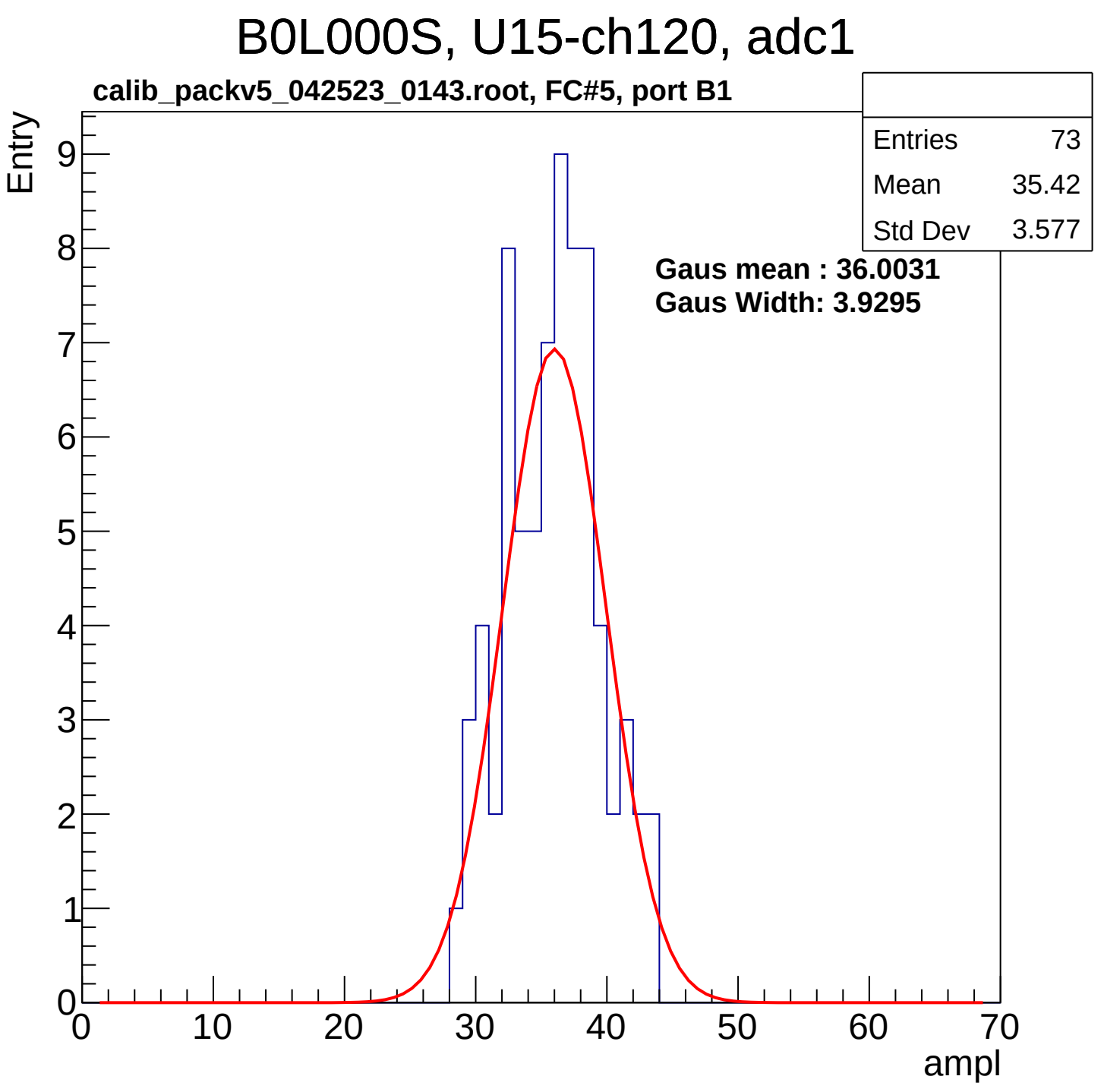
50
60
70
ampl


B0L000S, U15-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.8
Std Dev 3.483
Gaus mean : 29.3038
Gaus Width: 3.5615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.42
Std Dev 3.577
Gaus mean : 36.0031
Gaus Width: 3.9295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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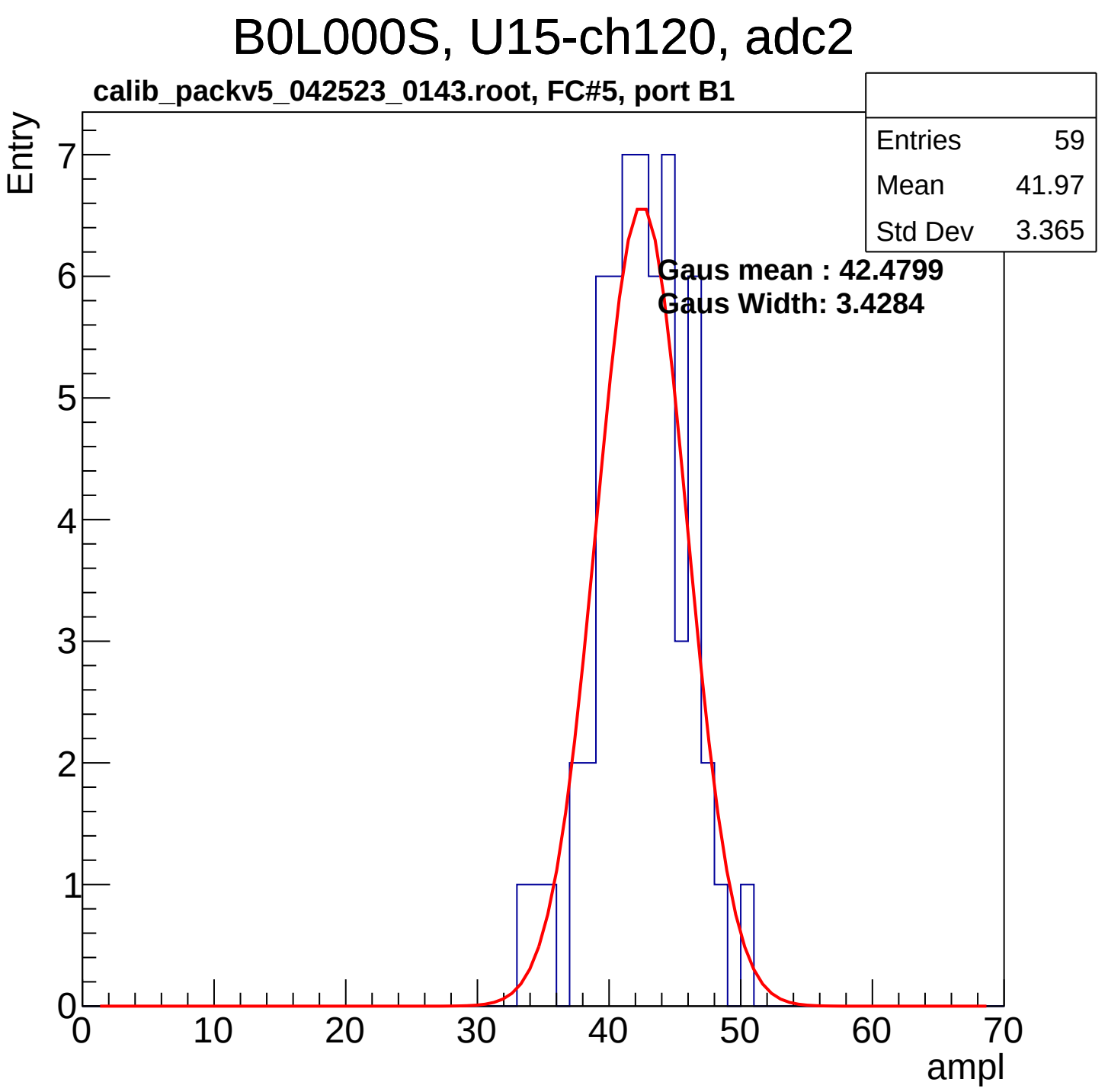
B0L000S, U15-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 41.97
Std Dev 3.365
Gaus mean : 42.4799
Gaus Width: 3.4284
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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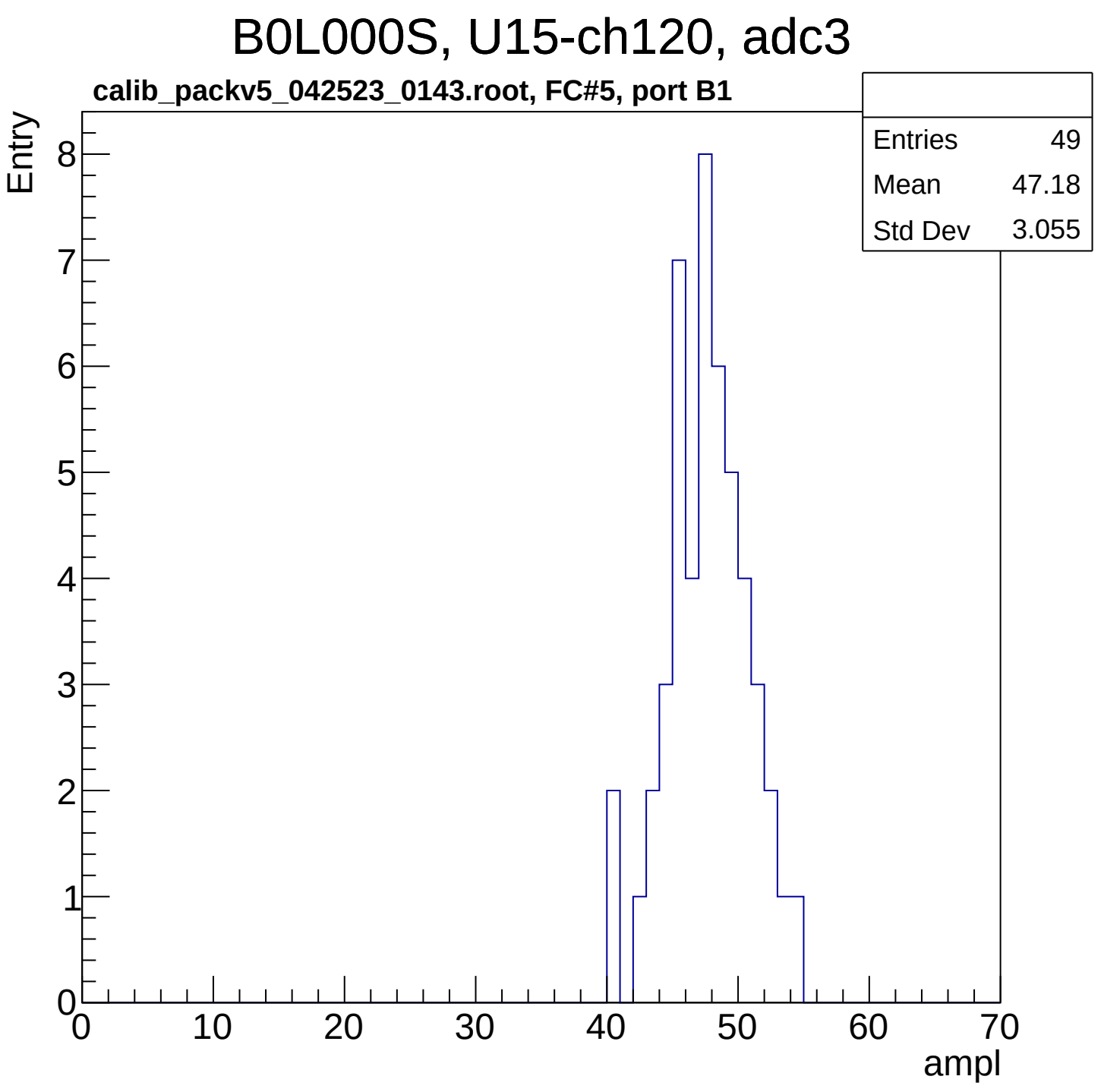
B0L000S, U15-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 47.18
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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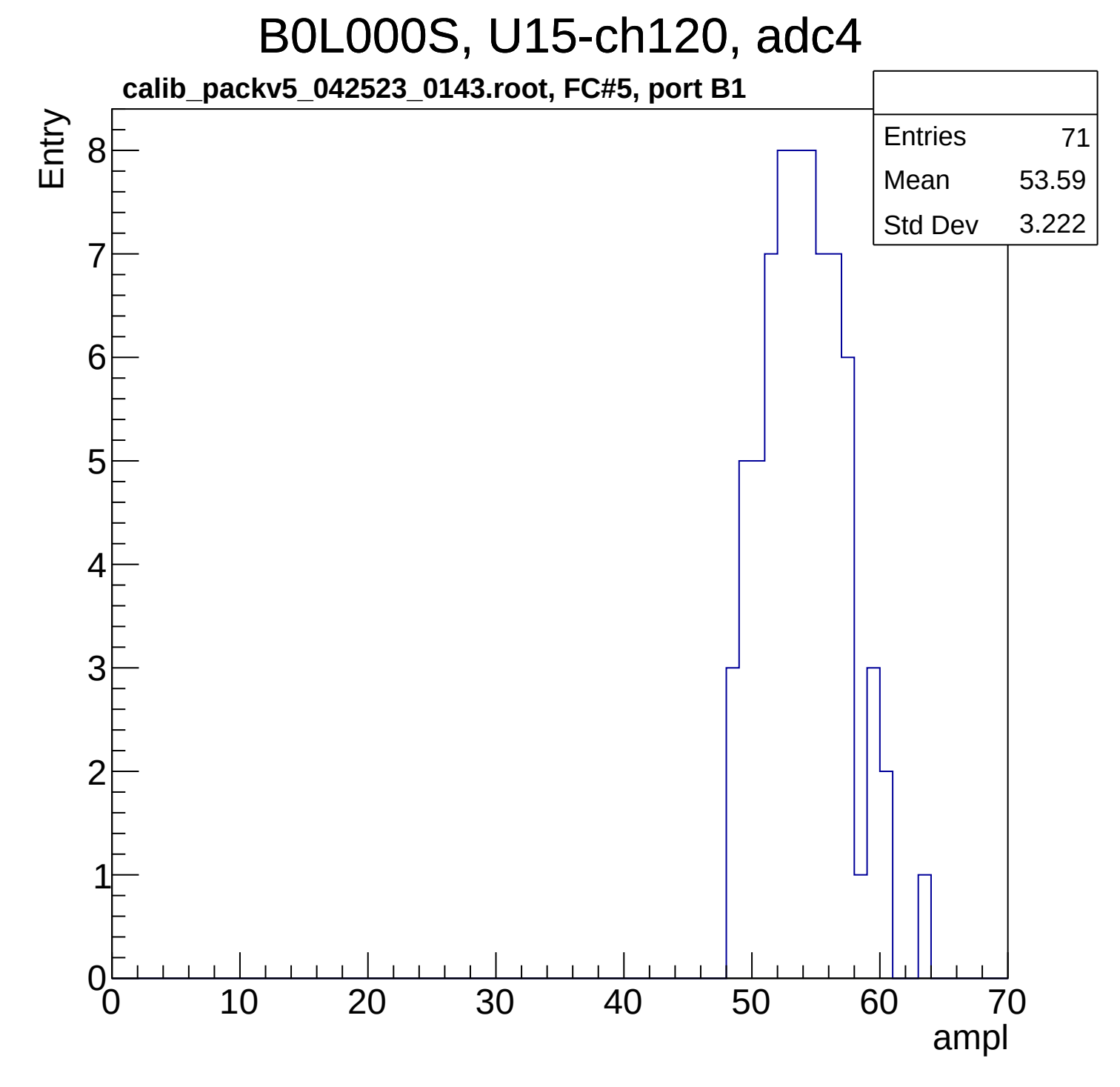
B0L000S, U15-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 53.59
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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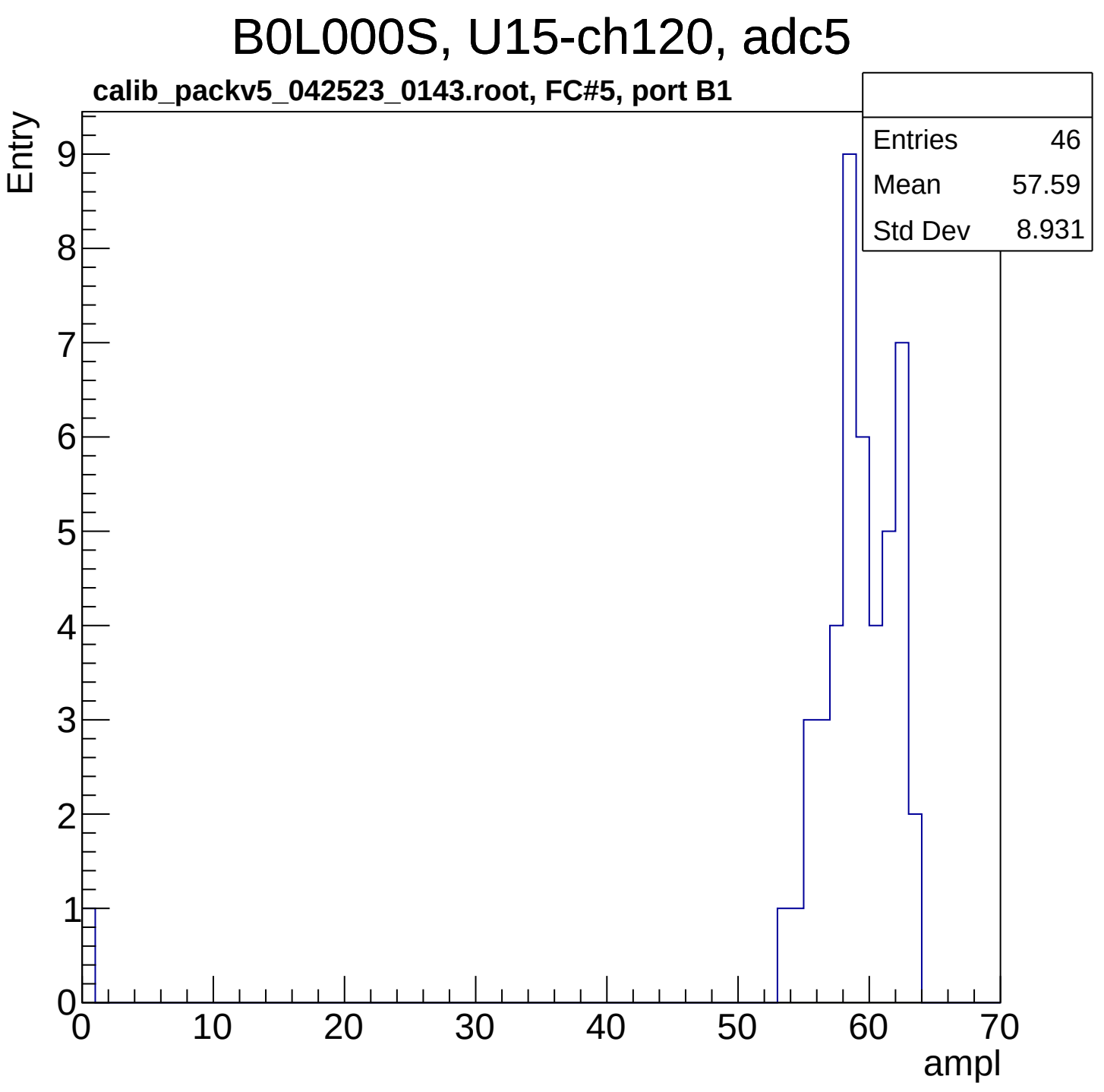
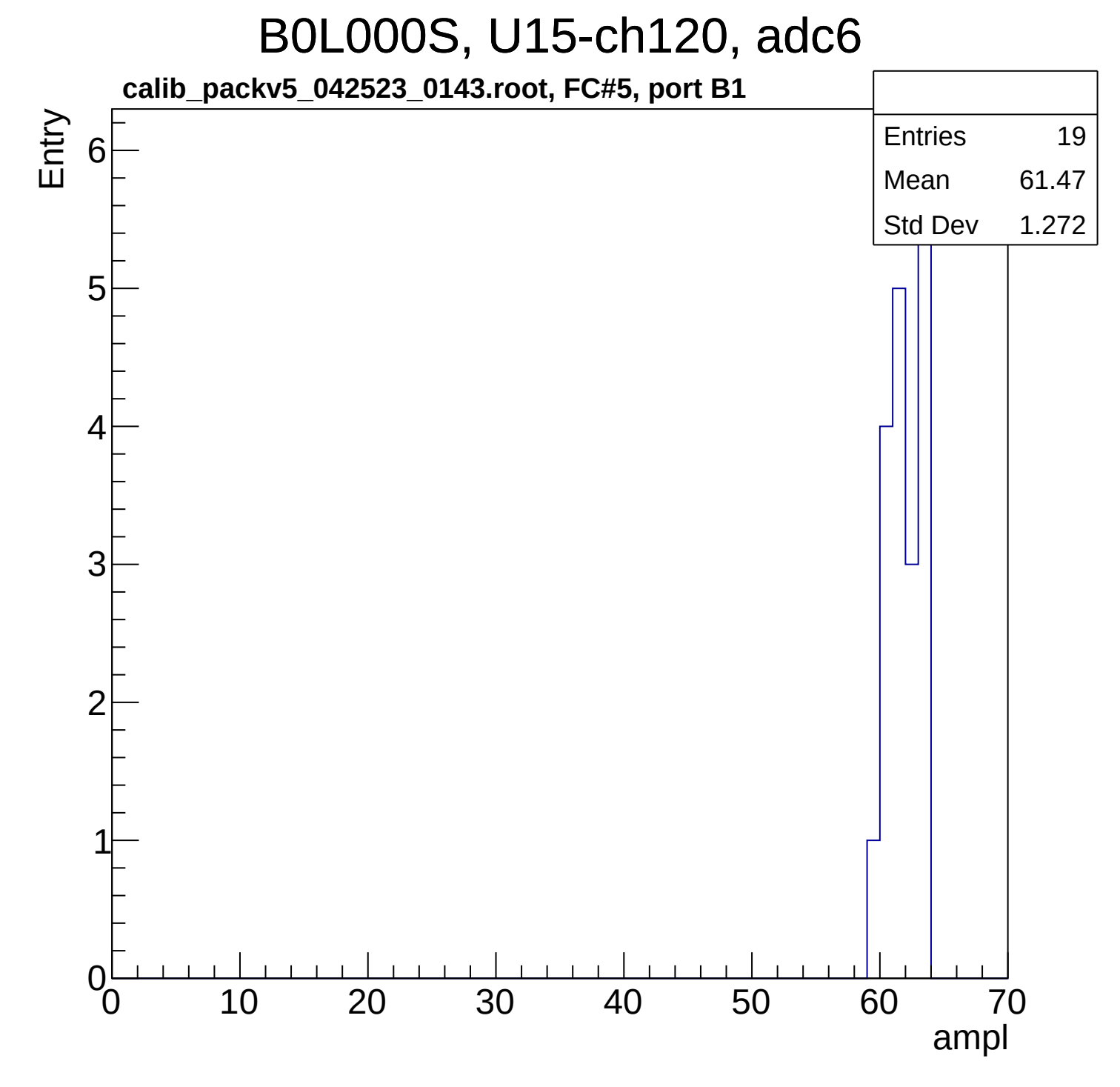
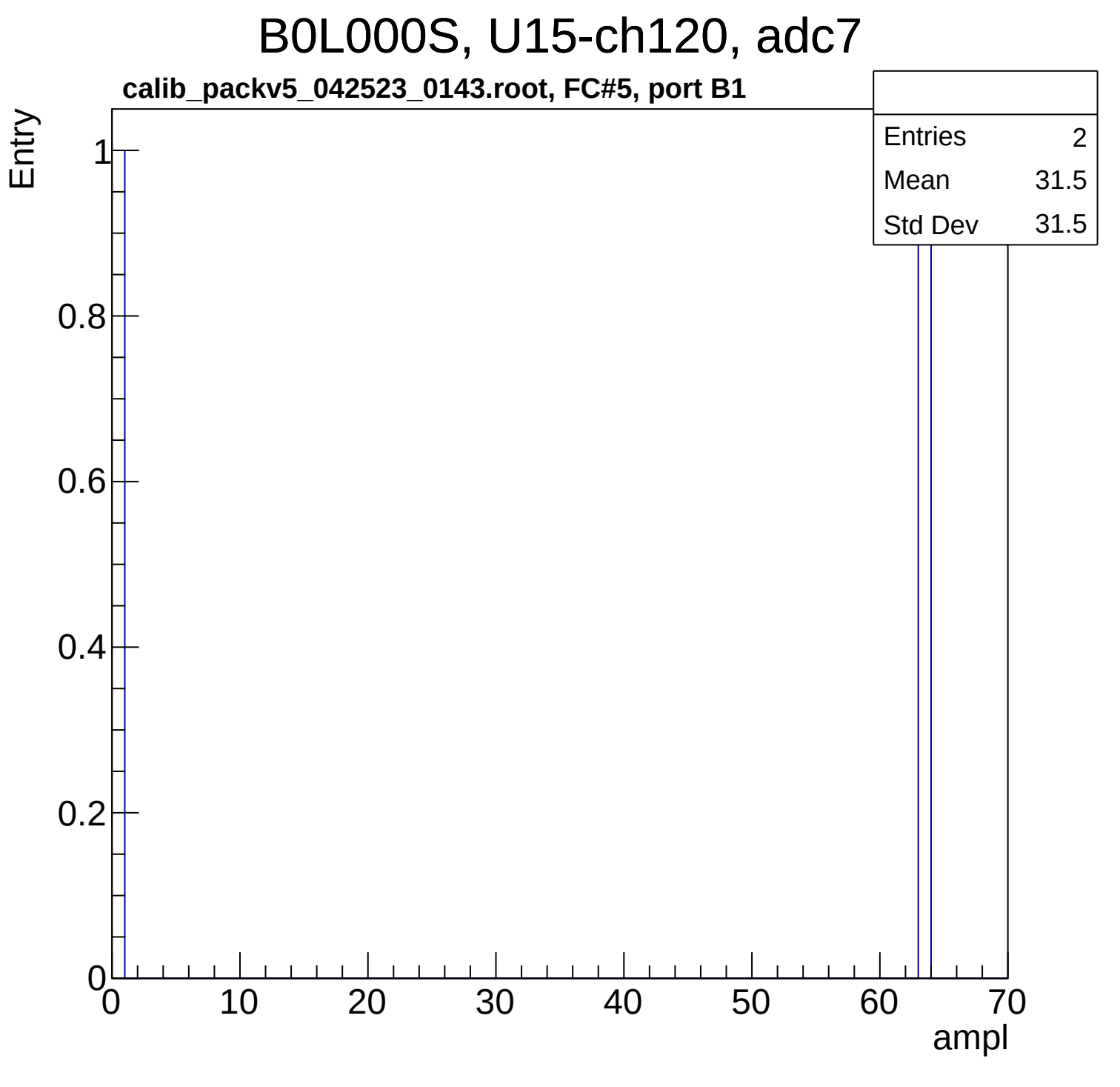
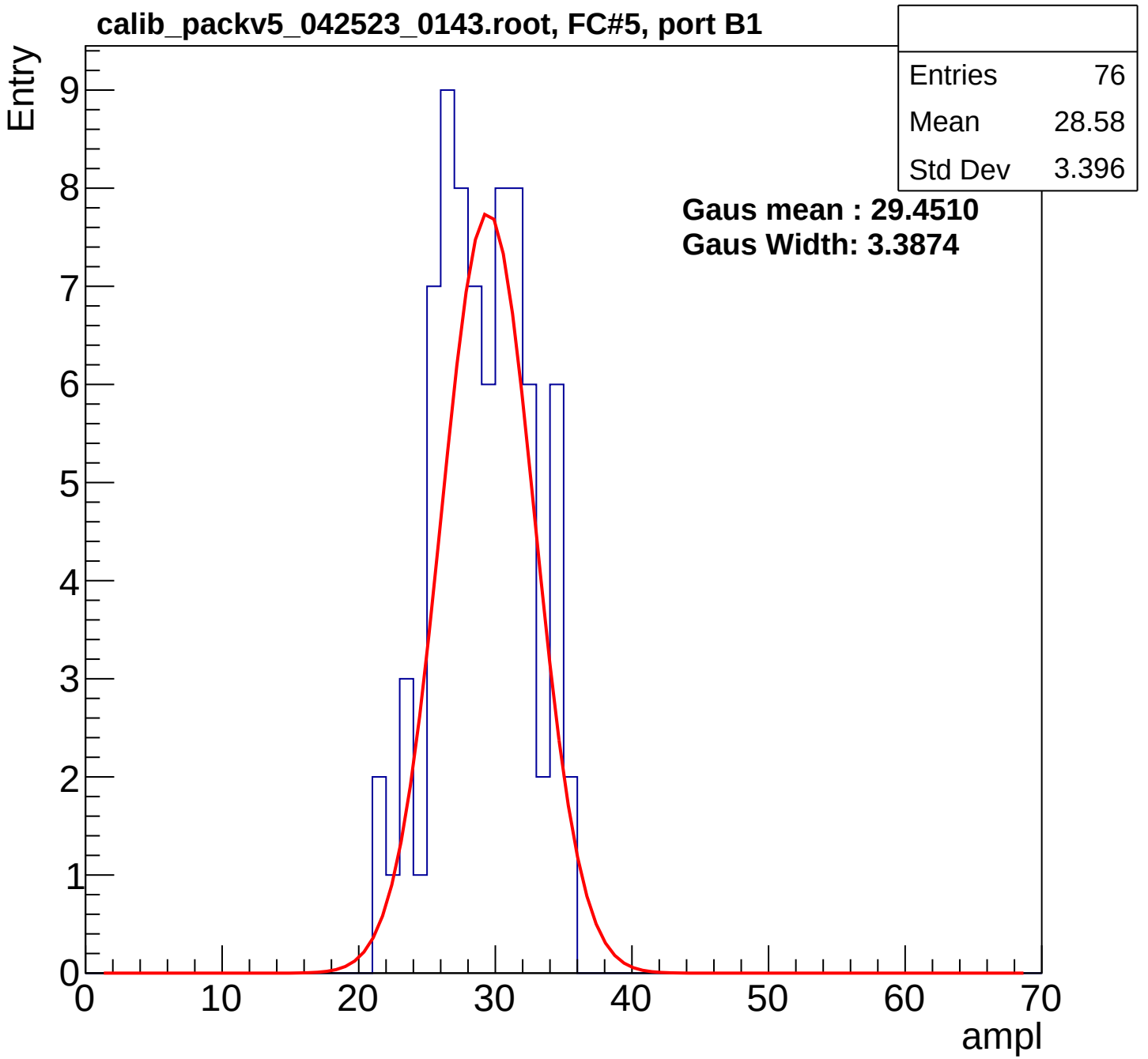
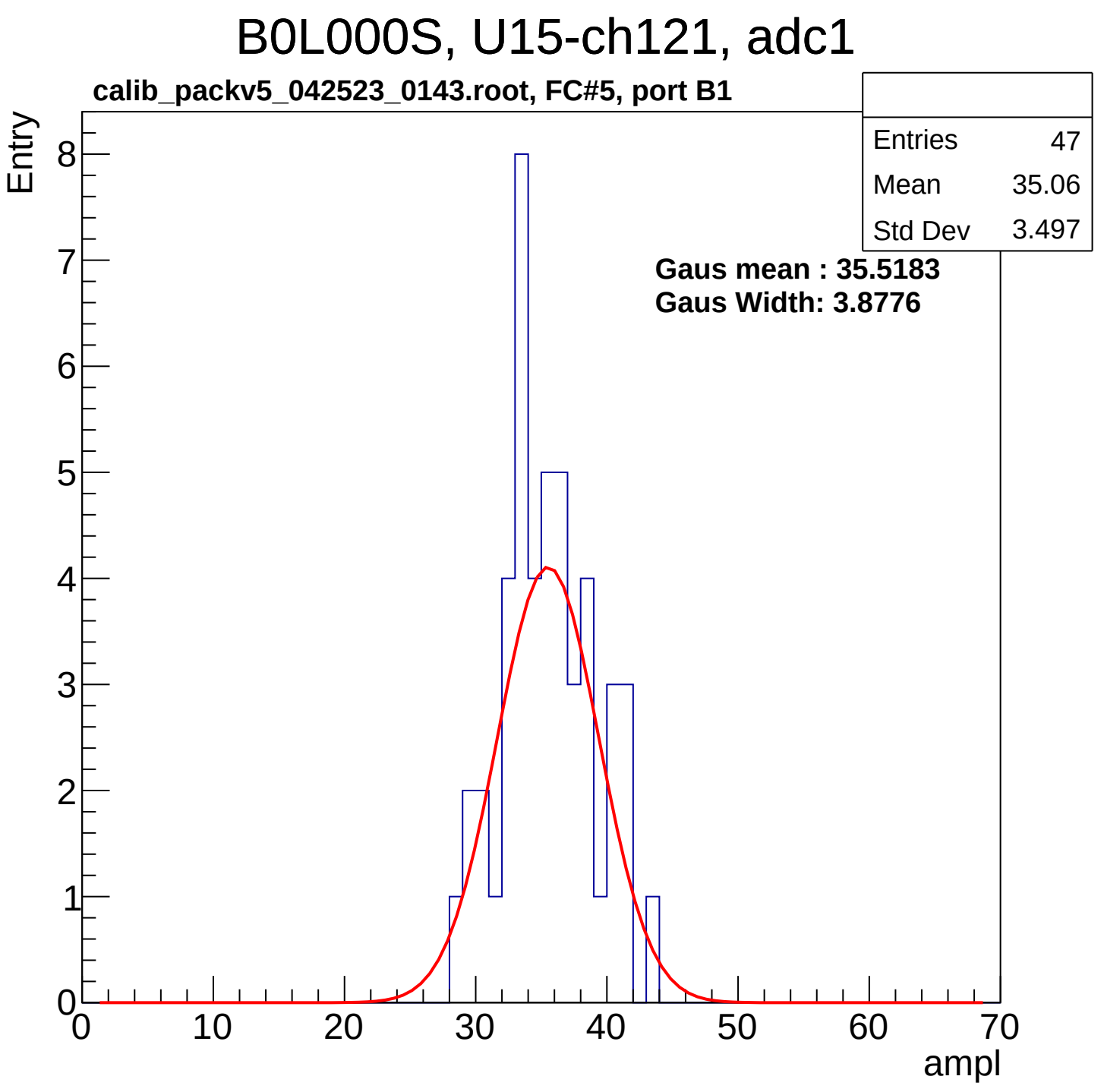
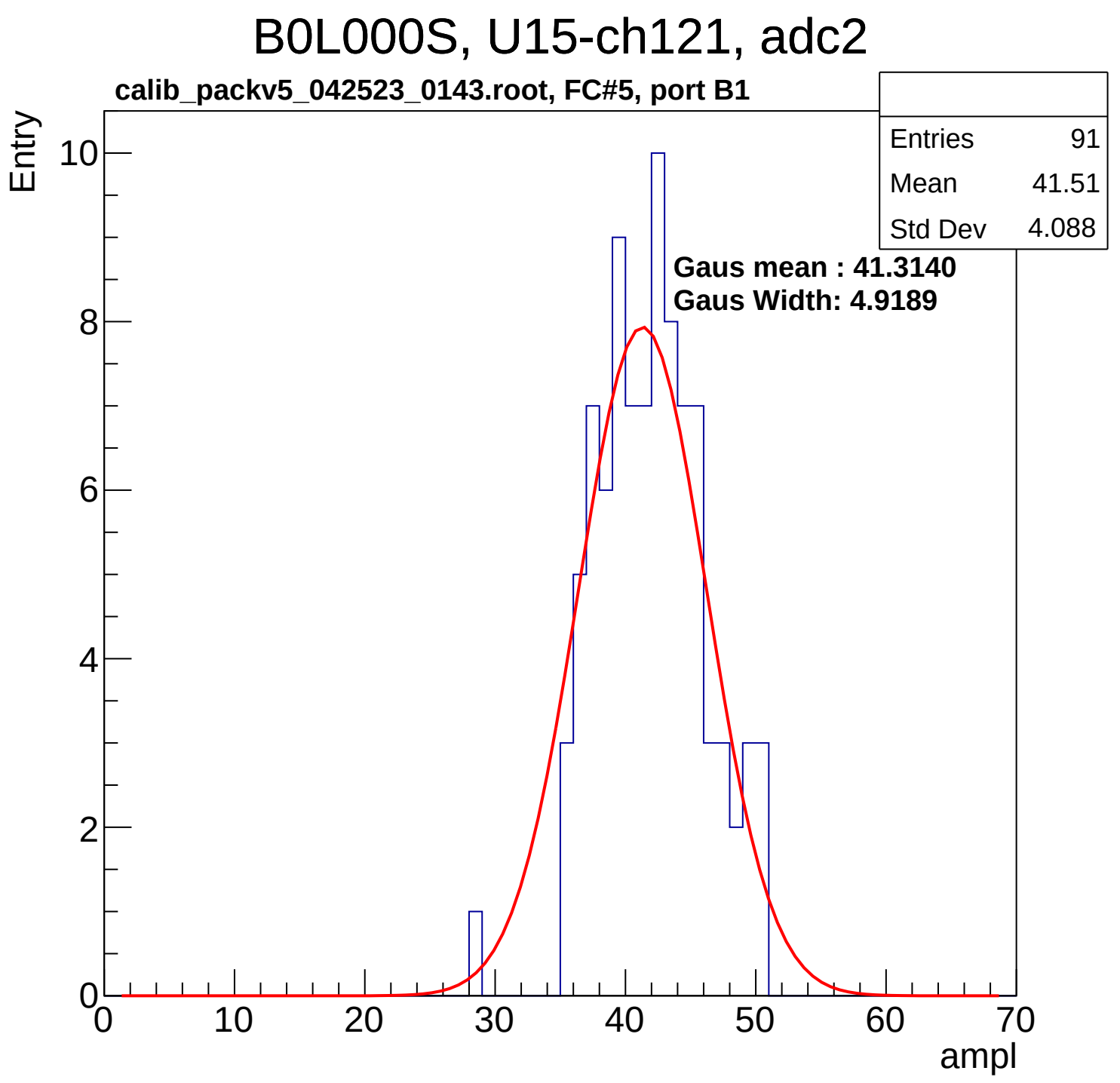

B0L000S, U15-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.59
Std Dev 8.931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

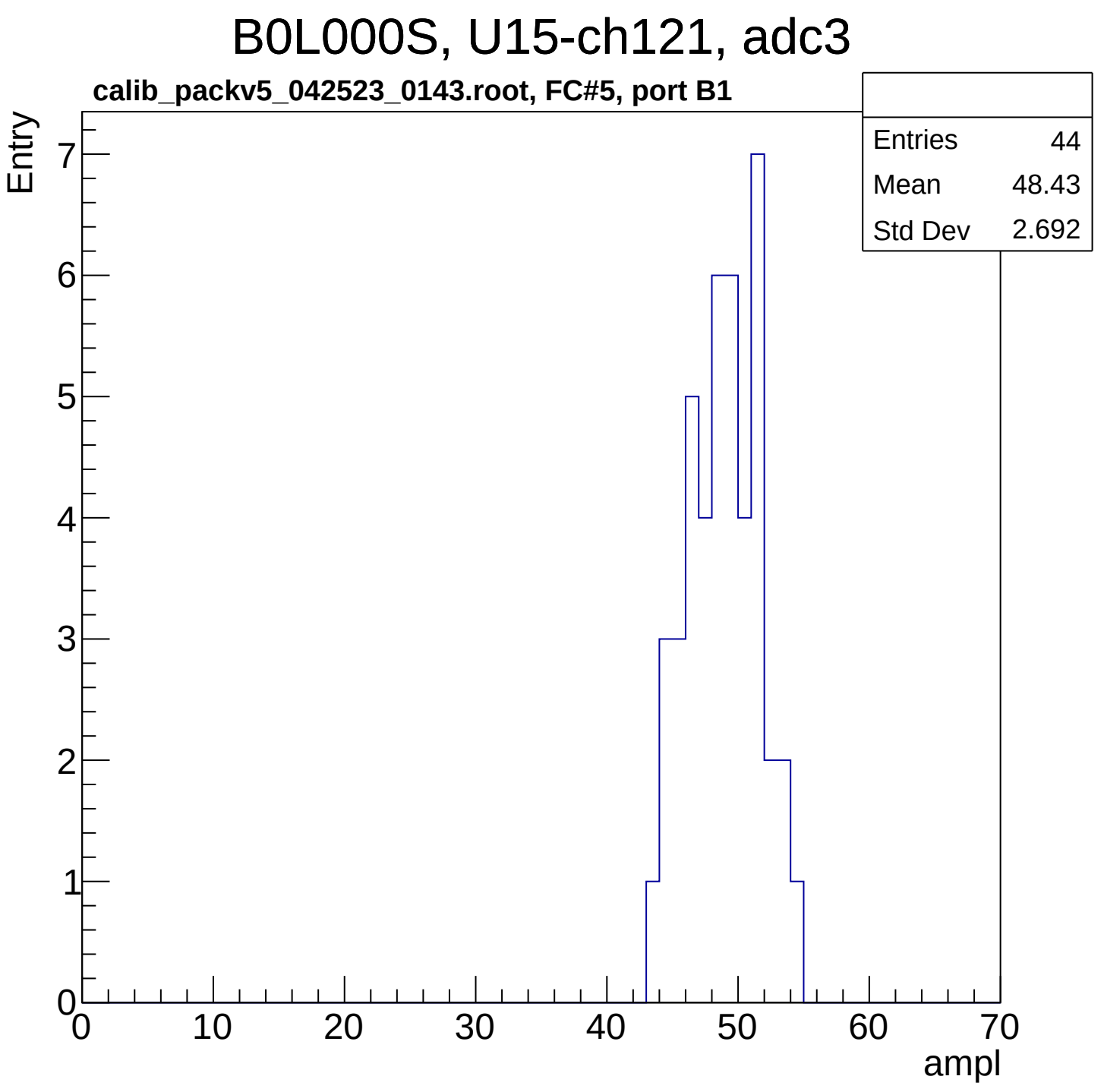

B0L000S, U15-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61.47
Std Dev 1.272
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

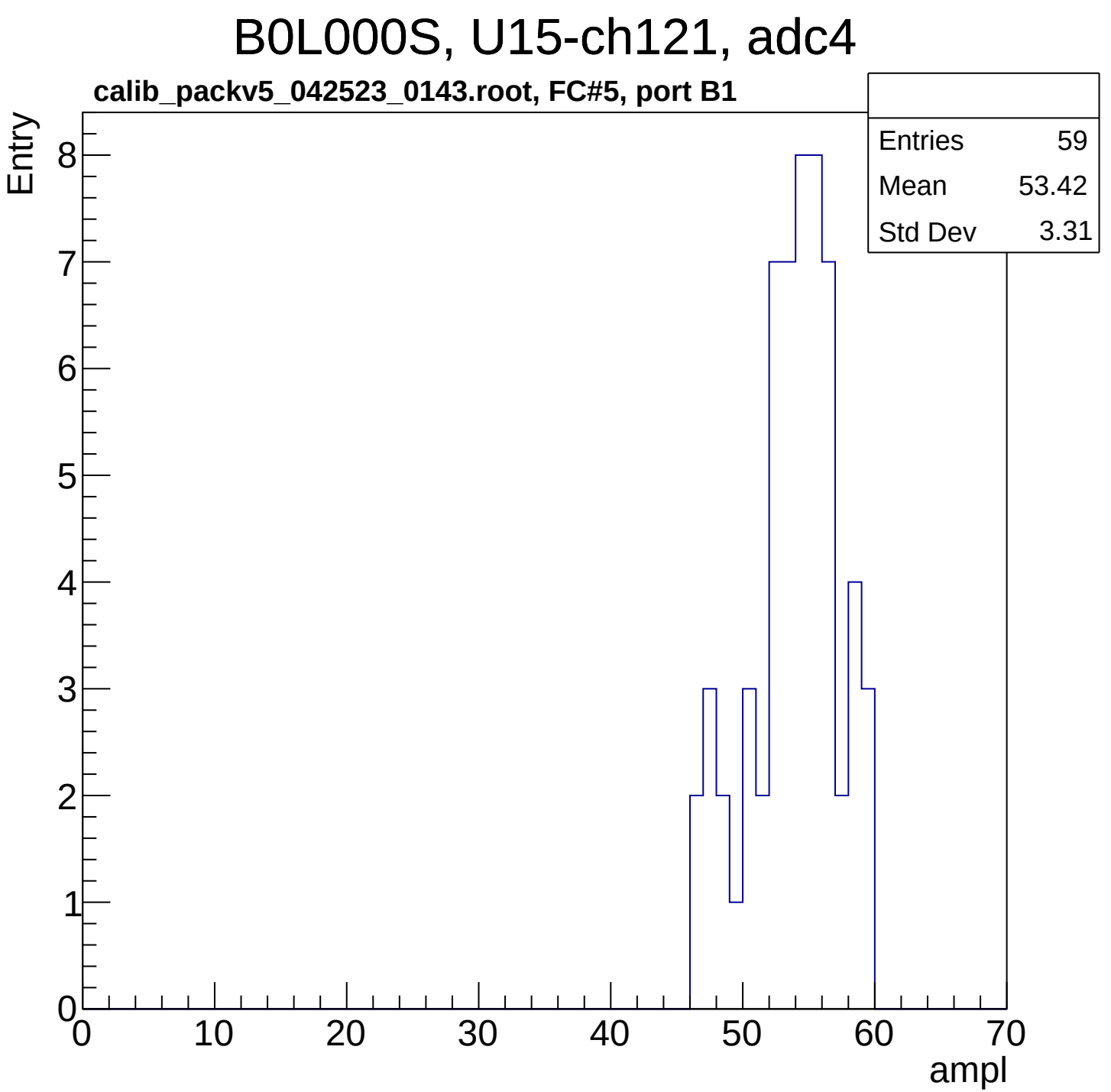

B0L000S, U15-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

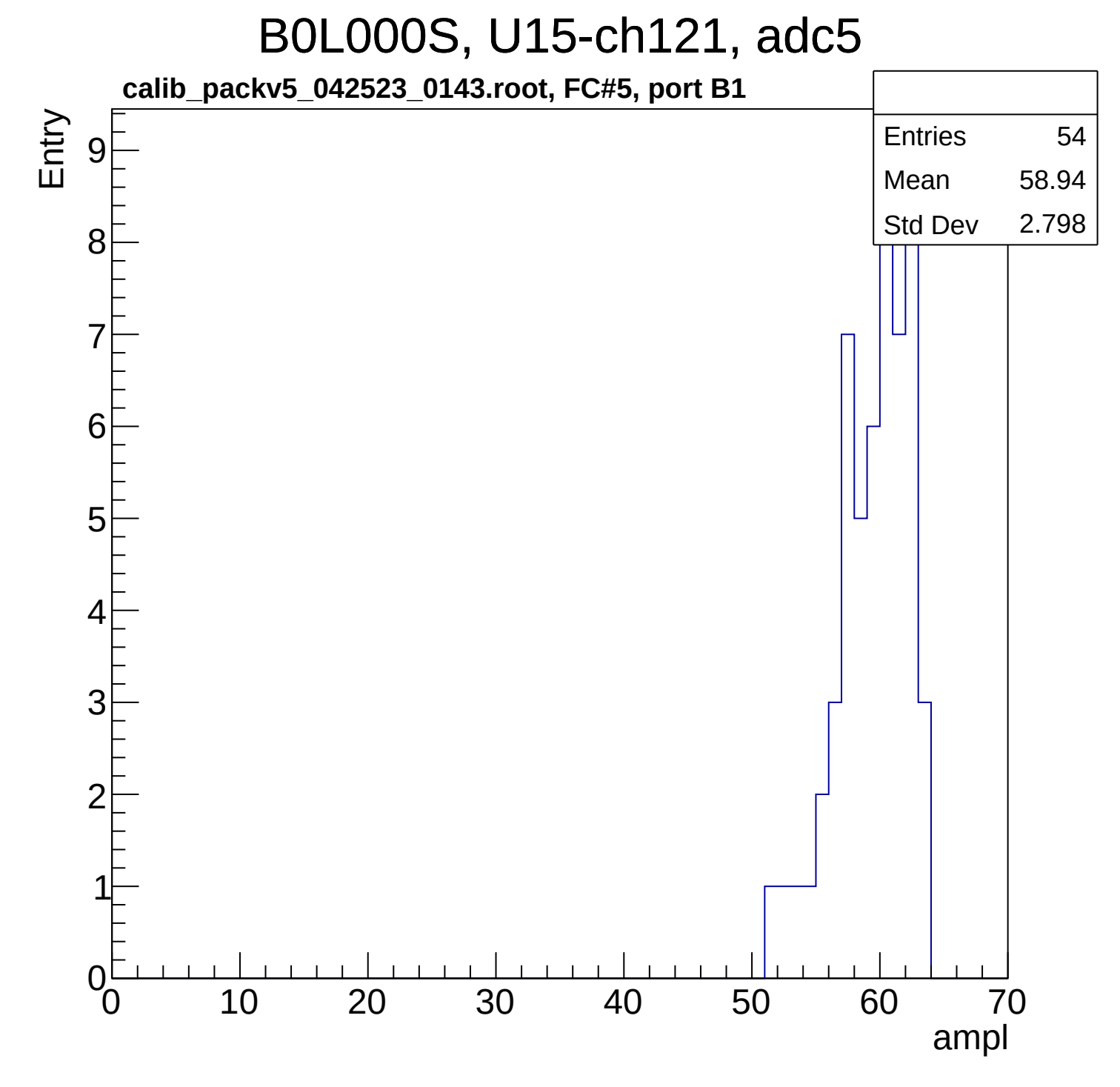

B0L000S, U15-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.58
Std Dev 3.396
Gaus mean : 29.4510
Gaus Width: 3.3874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 35.06
Std Dev 3.497
Gaus mean : 35.5183
Gaus Width: 3.8776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 41.51
Std Dev 4.088
Gaus mean : 41.3140
Gaus Width: 4.9189
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 48.43
Std Dev 2.692
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.42
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.94
Std Dev 2.798
Entry
ampl
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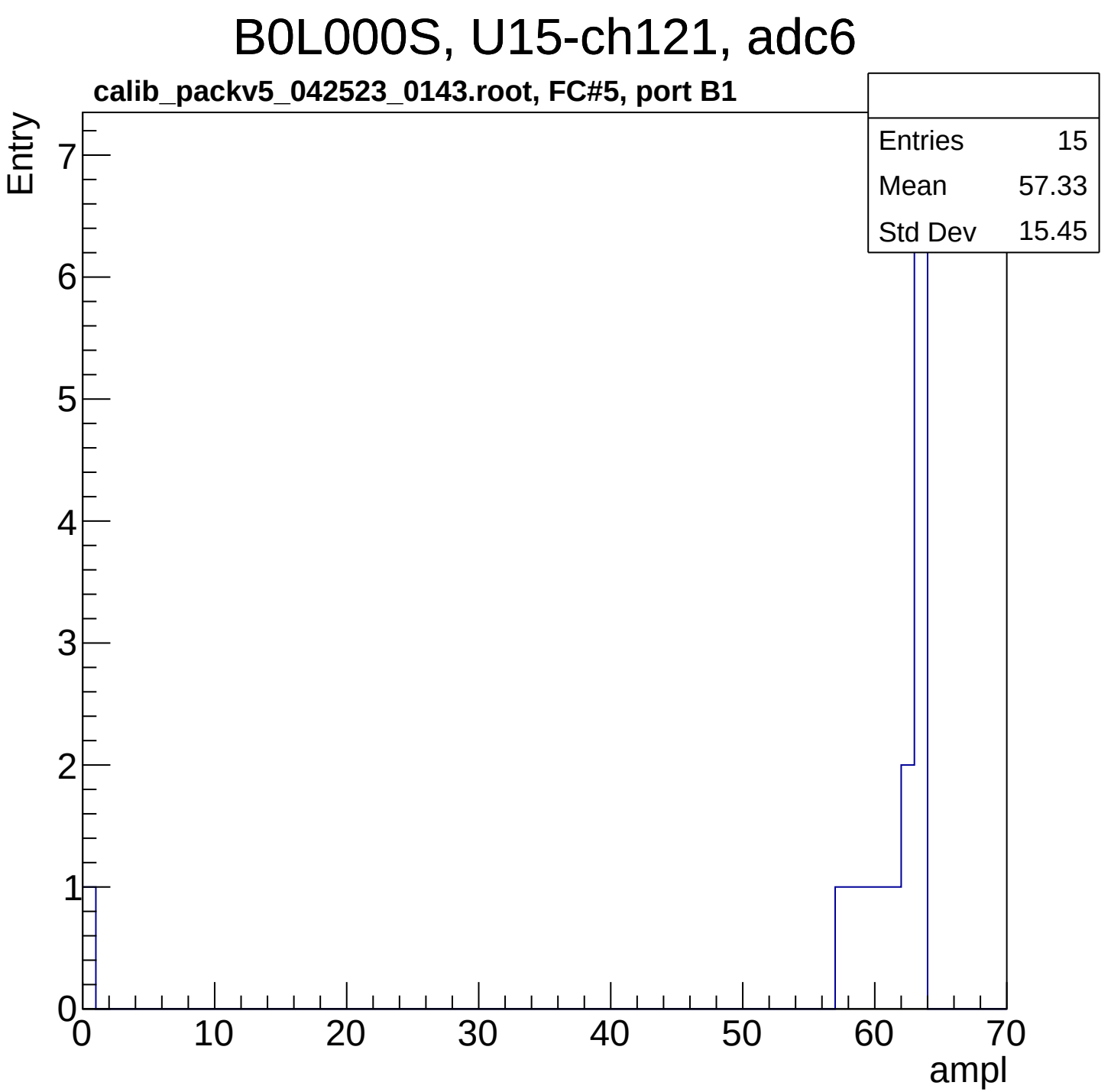
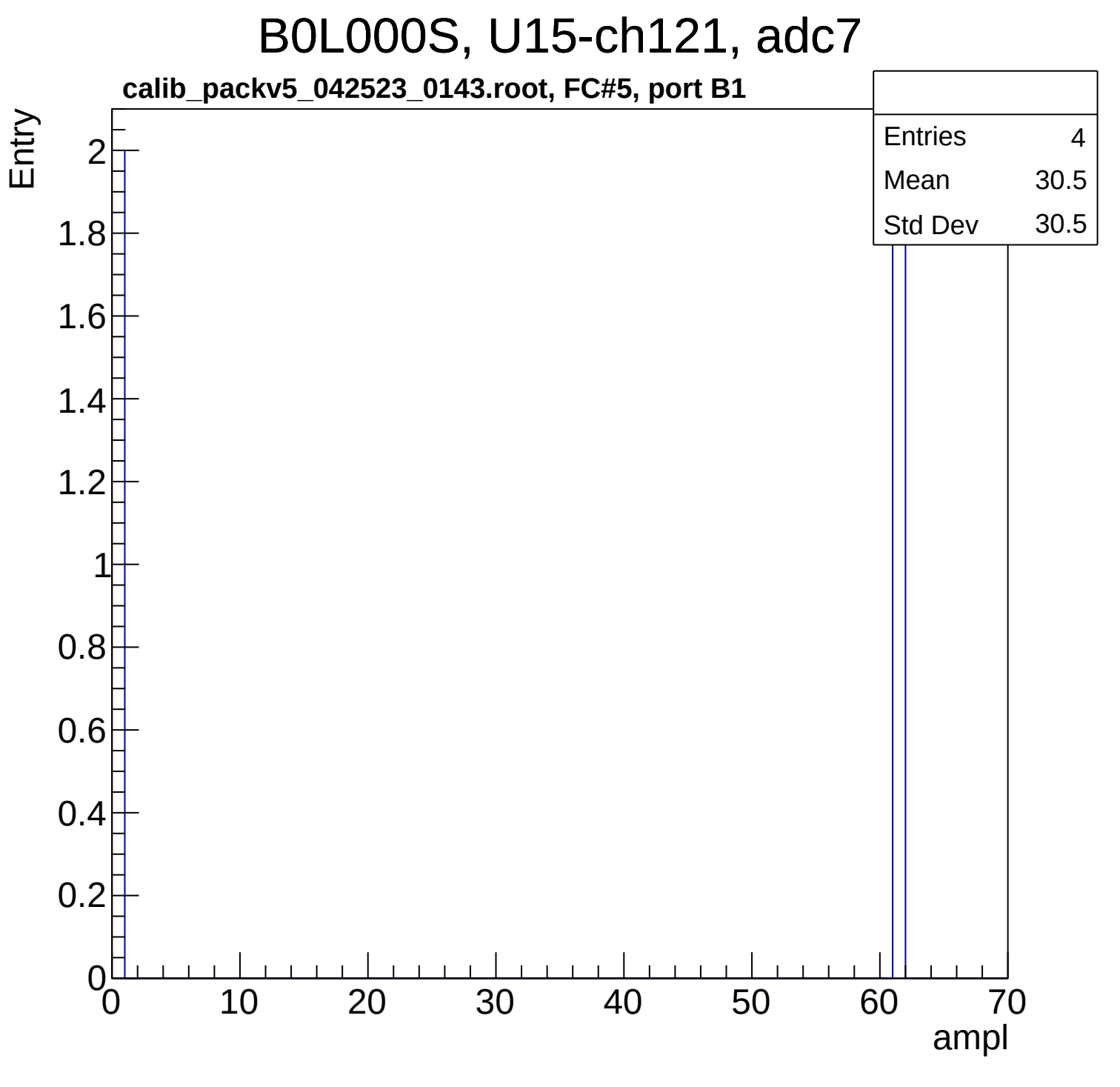
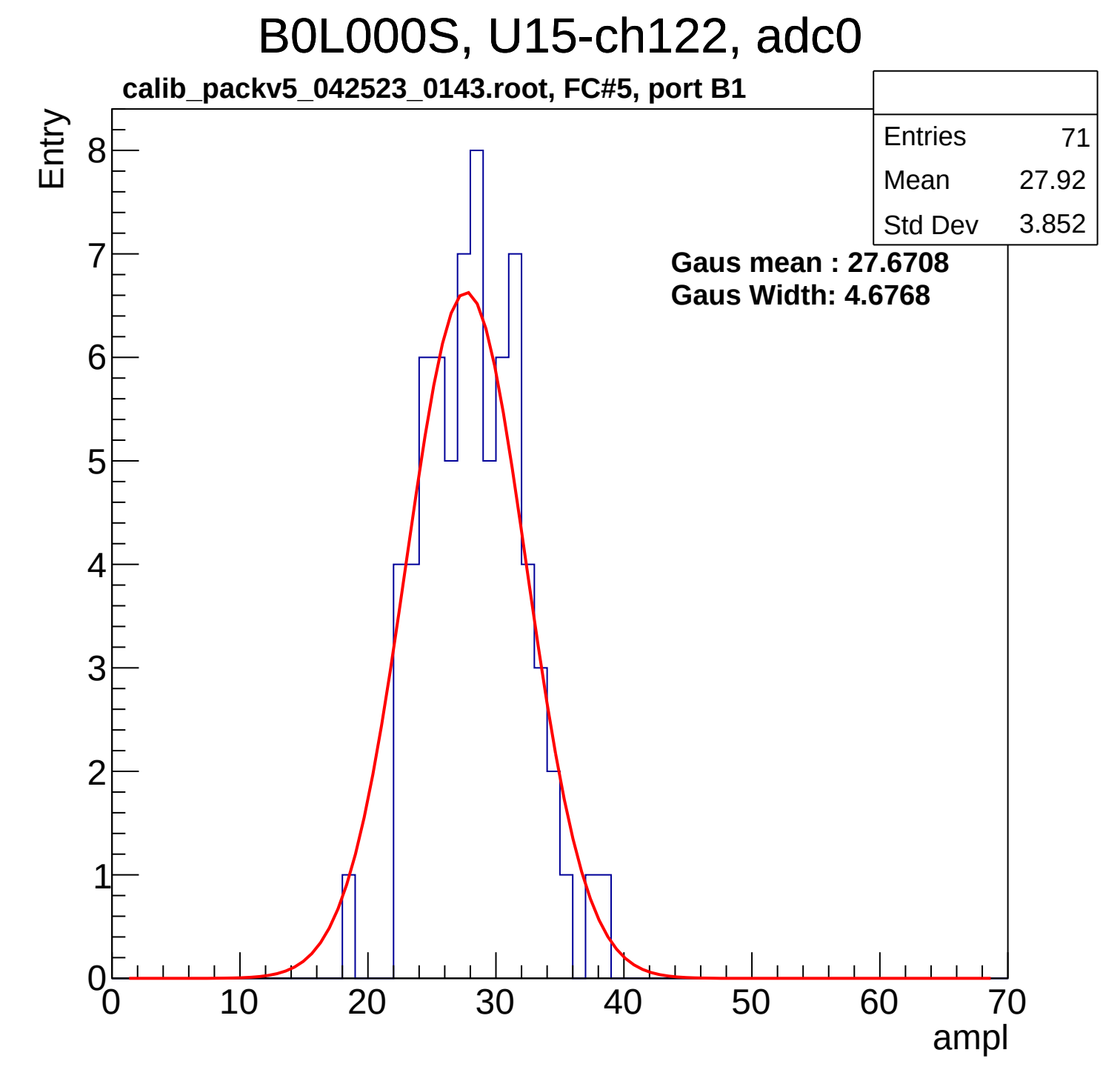
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.33
Std Dev 15.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 30.5
Std Dev 30.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.92
Std Dev 3.852
Gaus mean : 27.6708
Gaus Width: 4.6768
Entry
ampl
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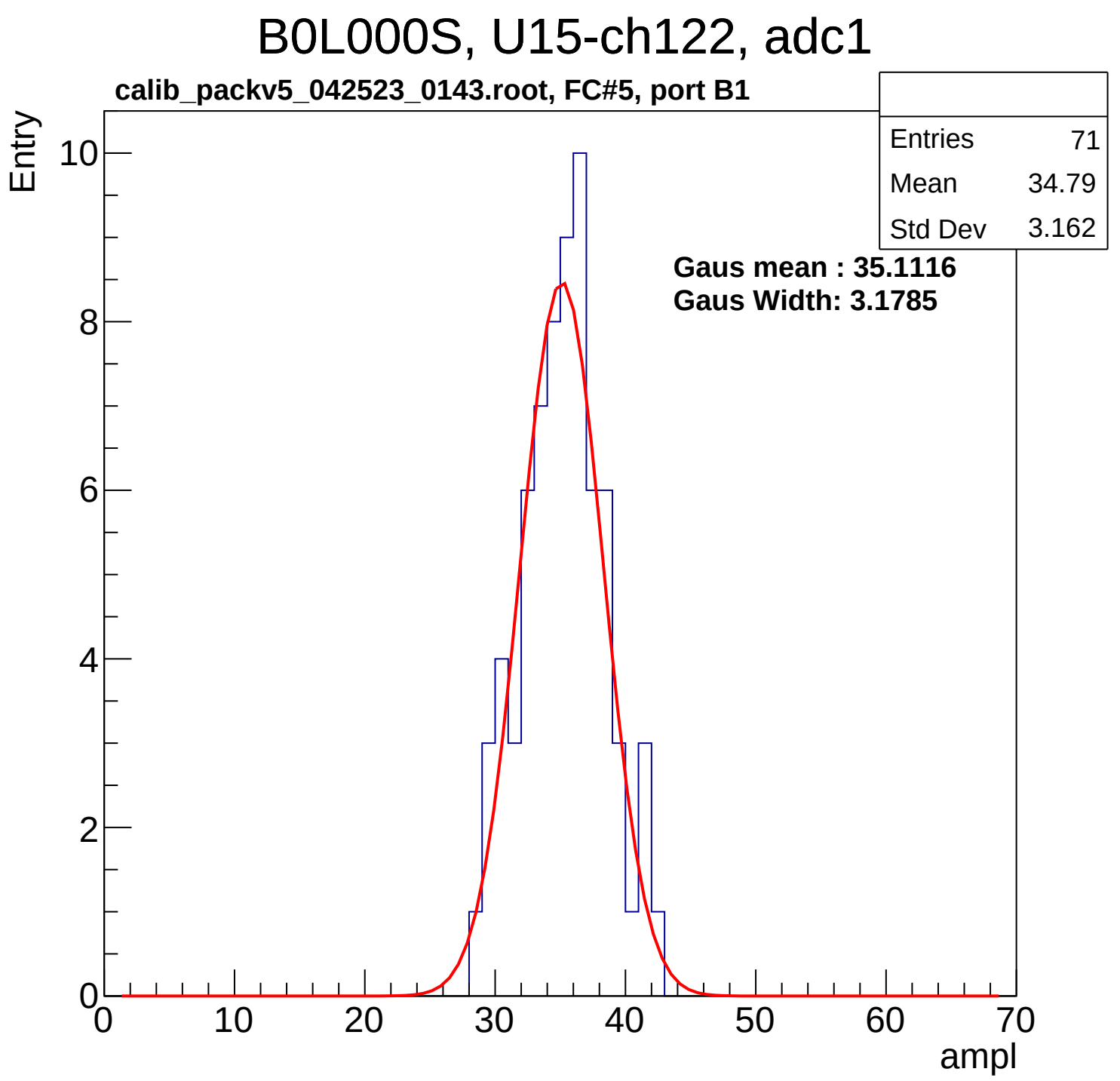
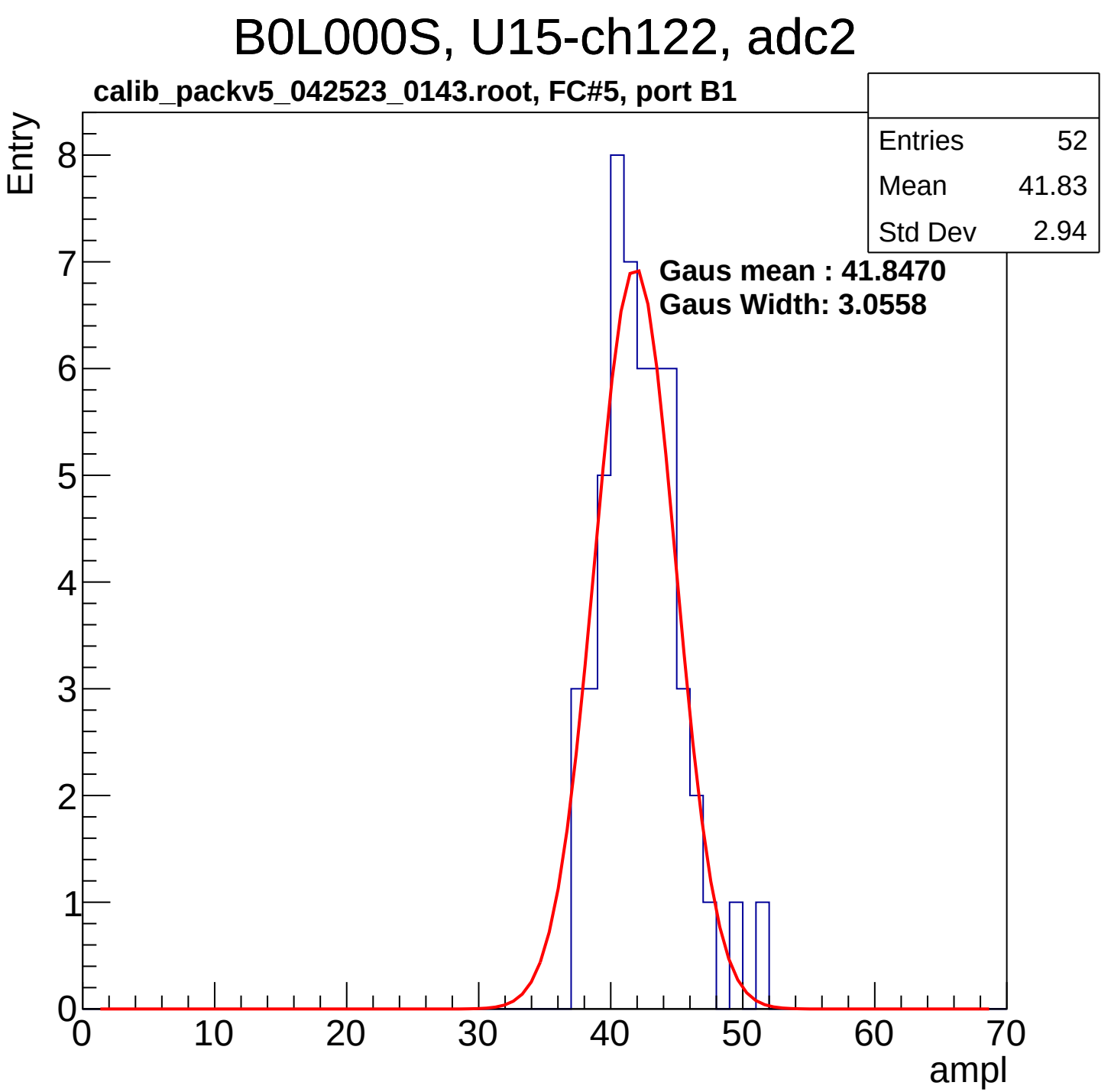
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 34.79
Std Dev 3.162
Gaus mean : 35.1116
Gaus Width: 3.1785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 41.83
Std Dev 2.94
Gaus mean : 41.8470
Gaus Width: 3.0558
Entry
ampl
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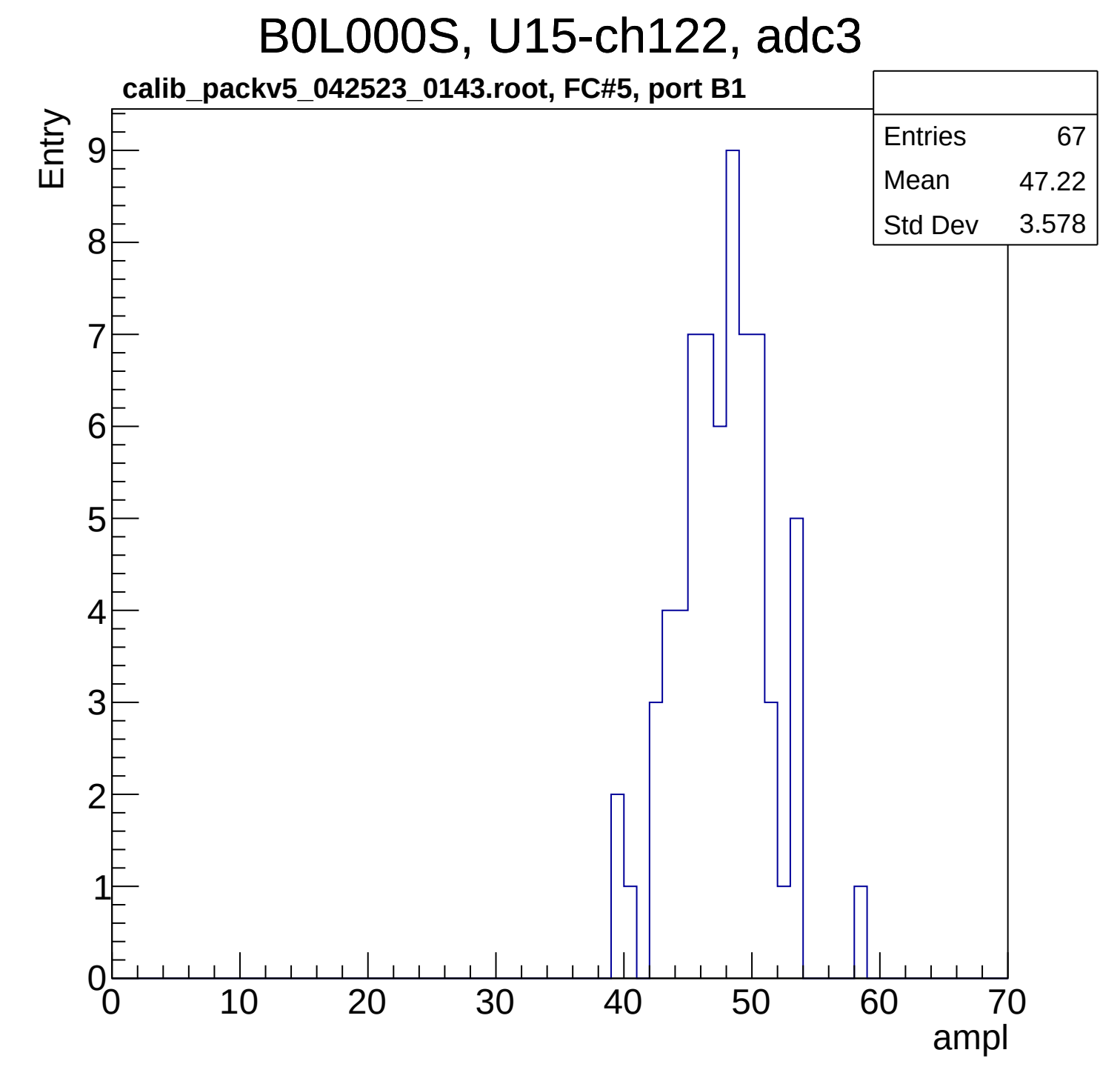
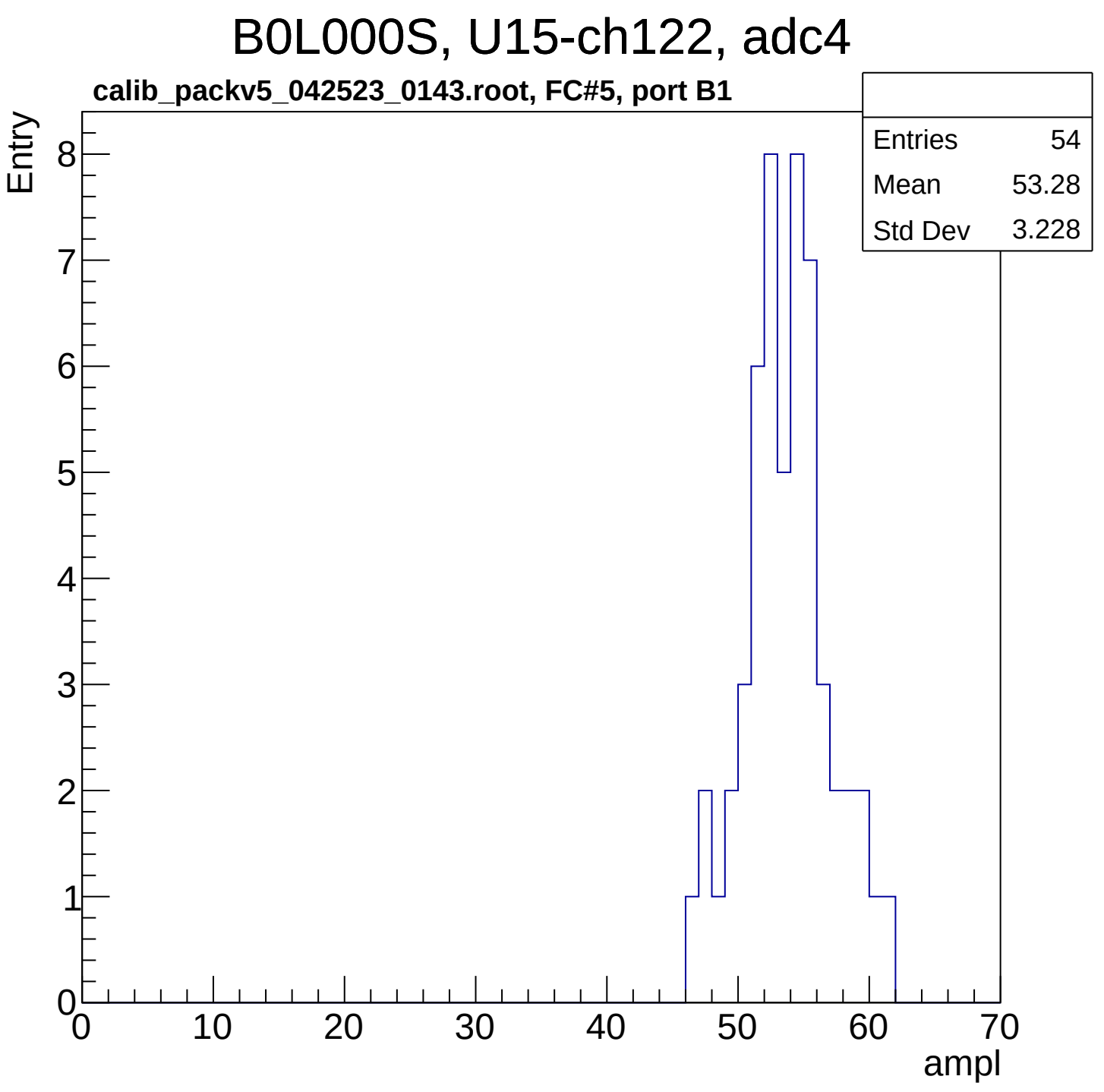
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.22
Std Dev 3.578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.28
Std Dev 3.228
Entry
ampl
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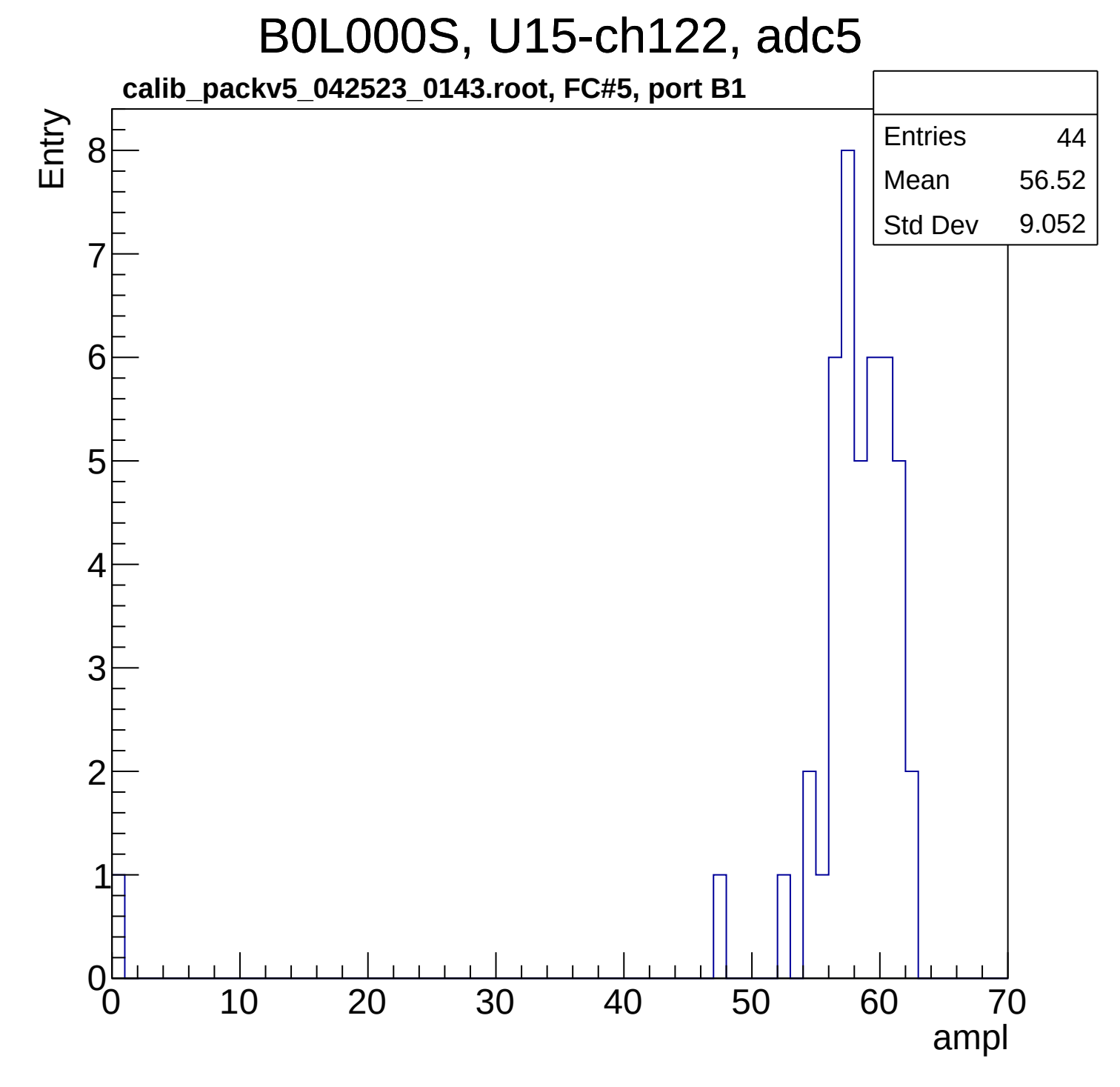
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 56.52
Std Dev 9.052
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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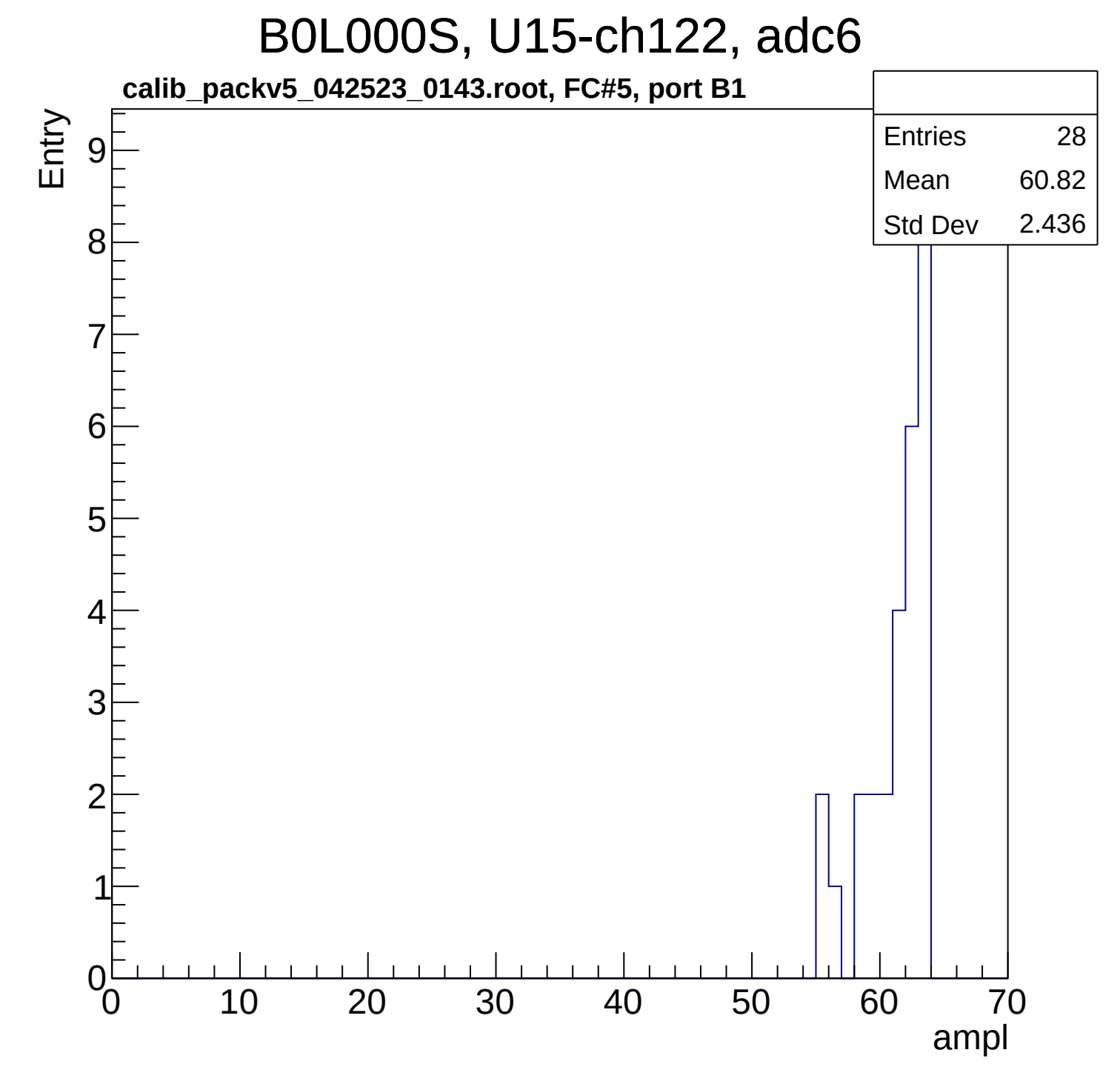
B0L000S, U15-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 60.82
Std Dev 2.436
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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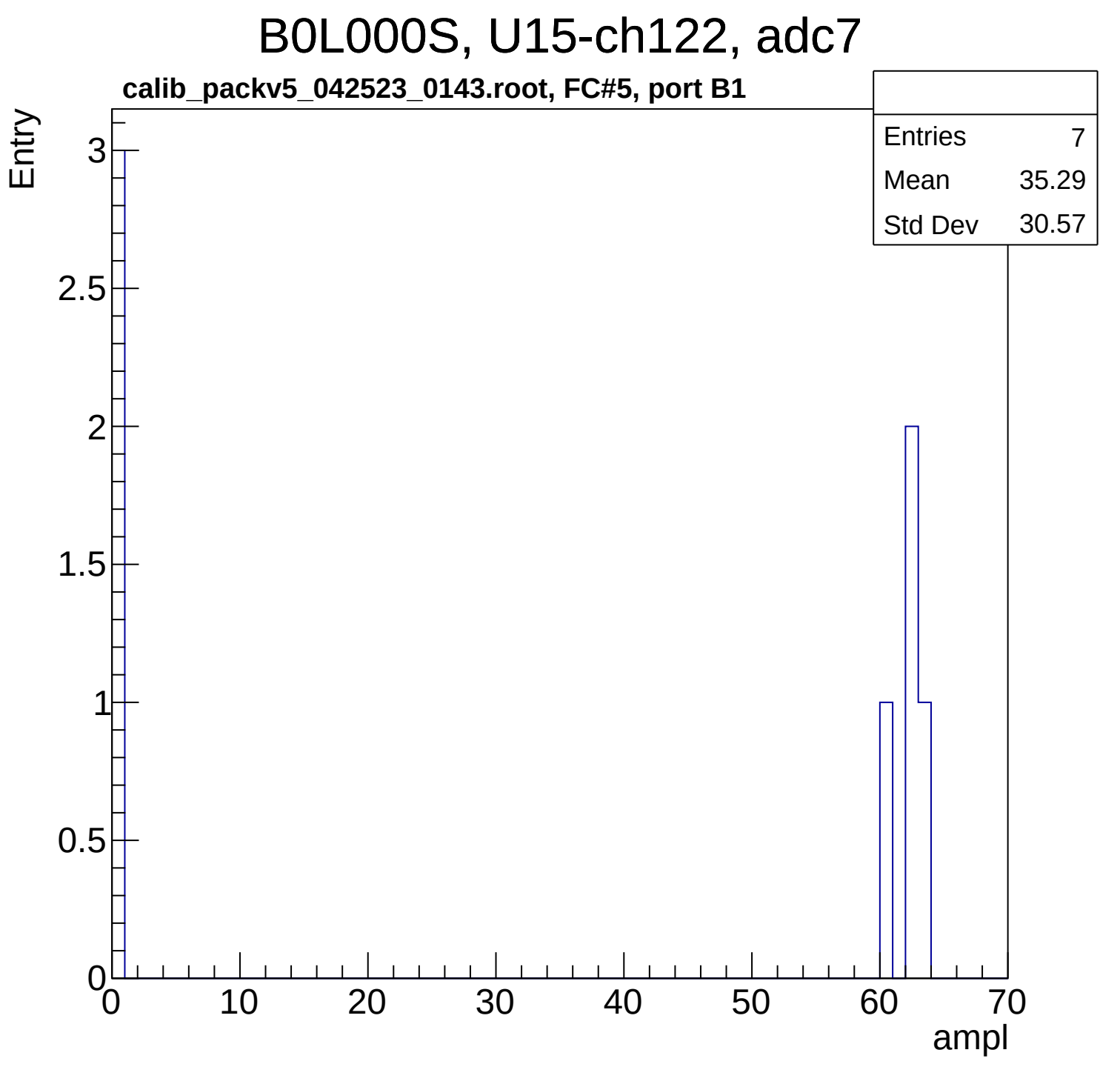
B0L000S, U15-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 35.29
Std Dev 30.57
Entry
ampl
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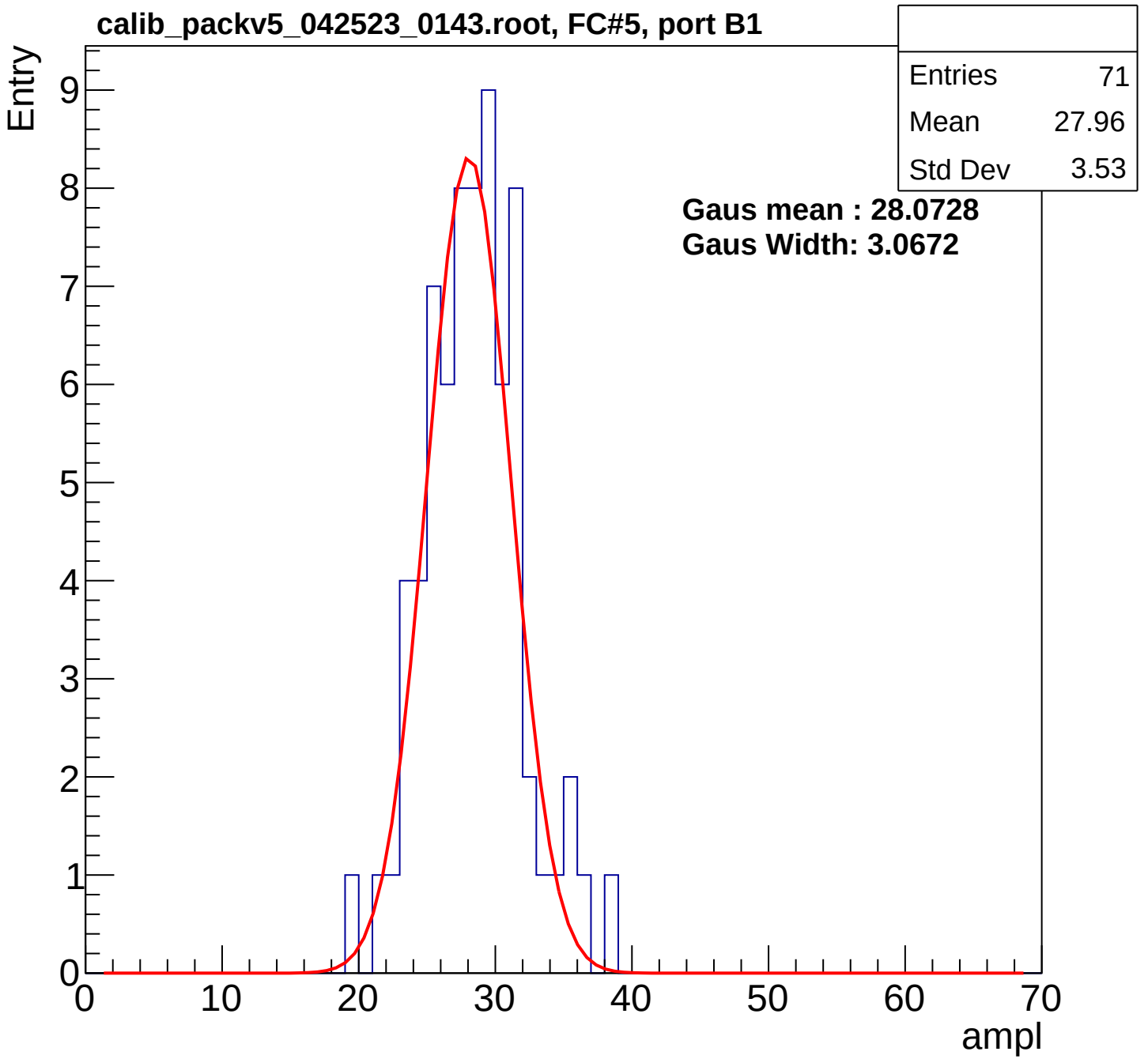
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.96
Std Dev 3.53
Gaus mean : 28.0728
Gaus Width: 3.0672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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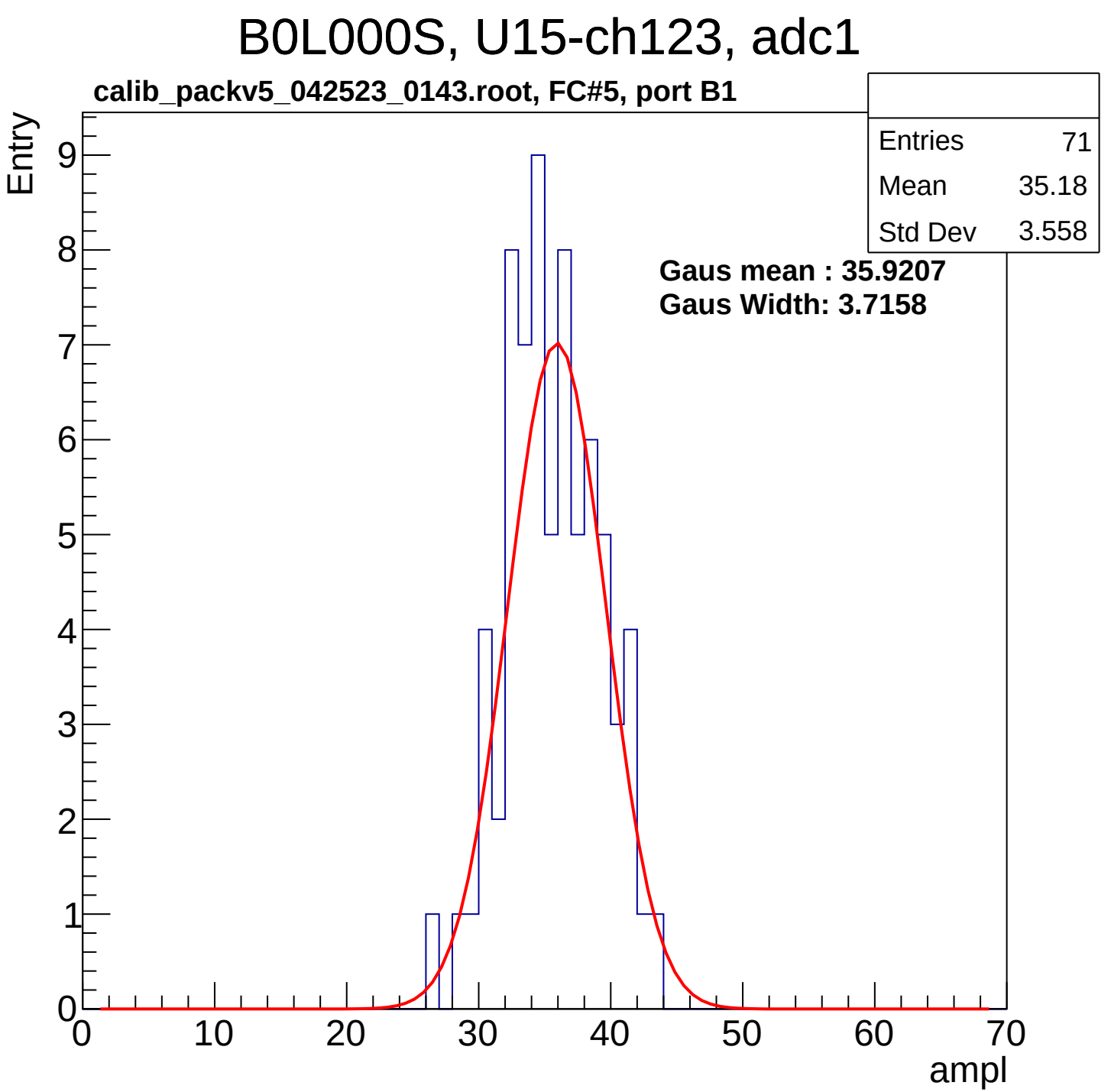
B0L000S, U15-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 35.18
Std Dev 3.558
Gaus mean : 35.9207
Gaus Width: 3.7158
Entry
ampl
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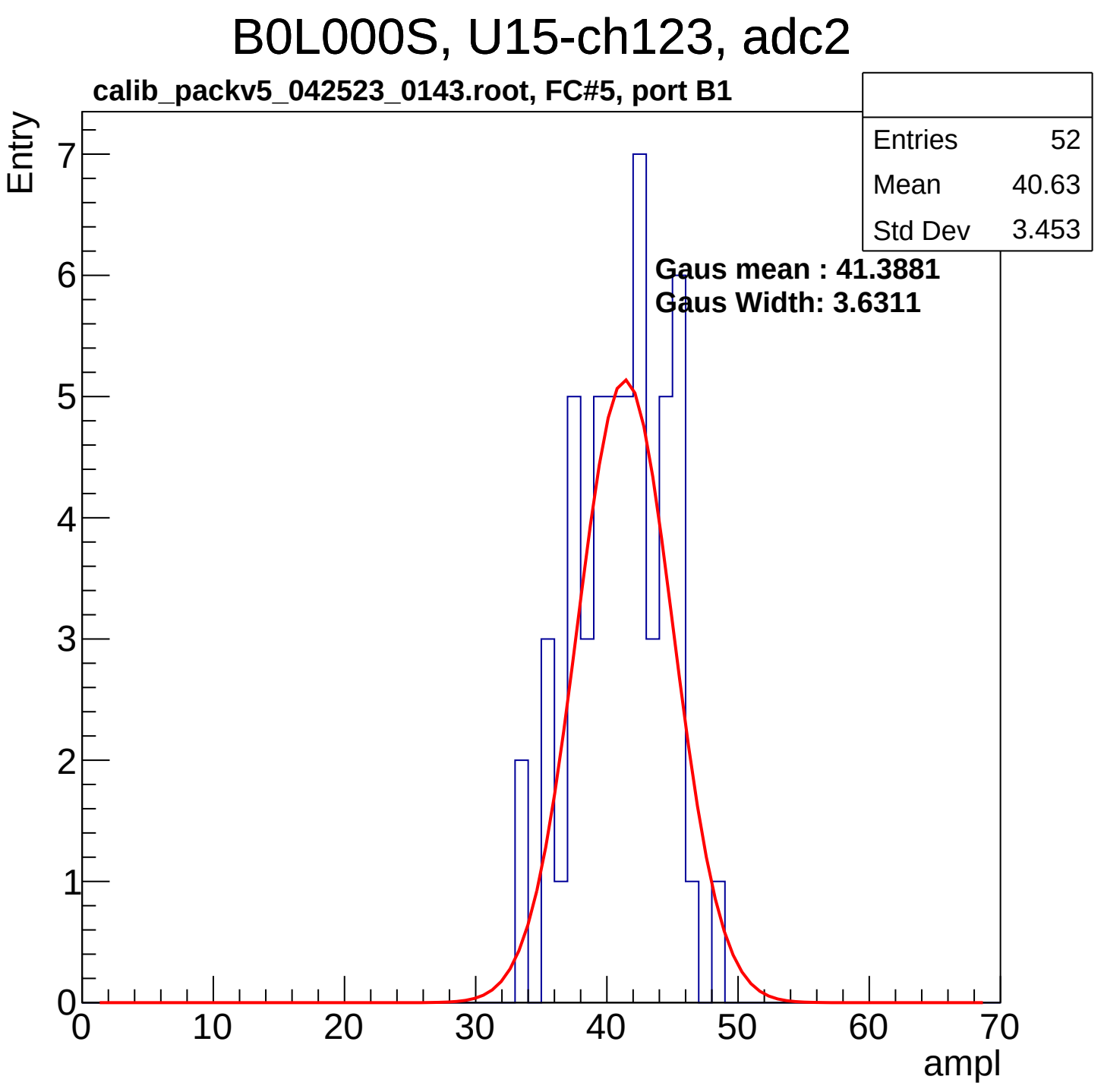
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U15-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 40.63
Std Dev 3.453
Gaus mean : 41.3881
Gaus Width: 3.6311
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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40
50
60
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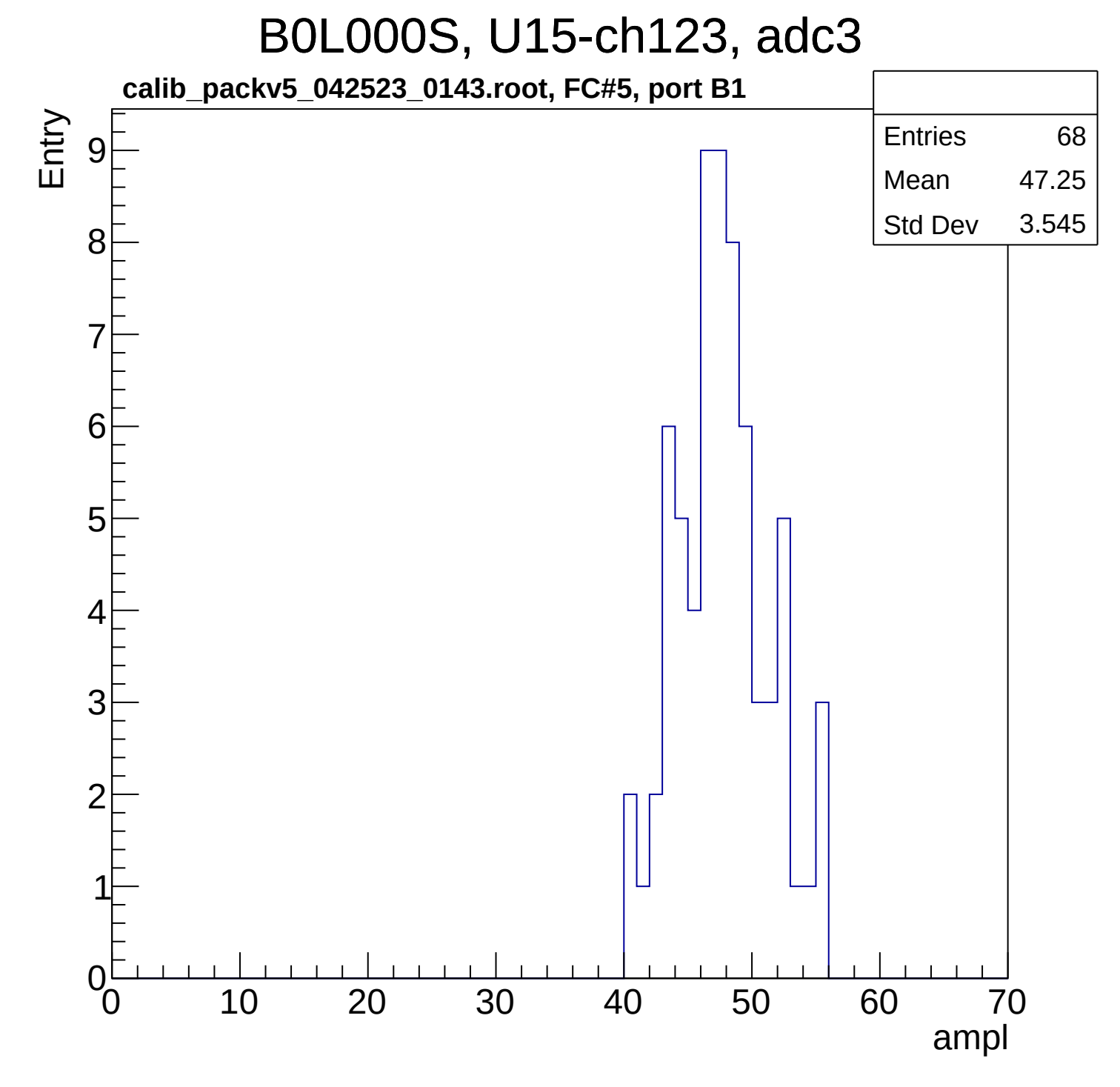
B0L000S, U15-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 47.25
Std Dev 3.545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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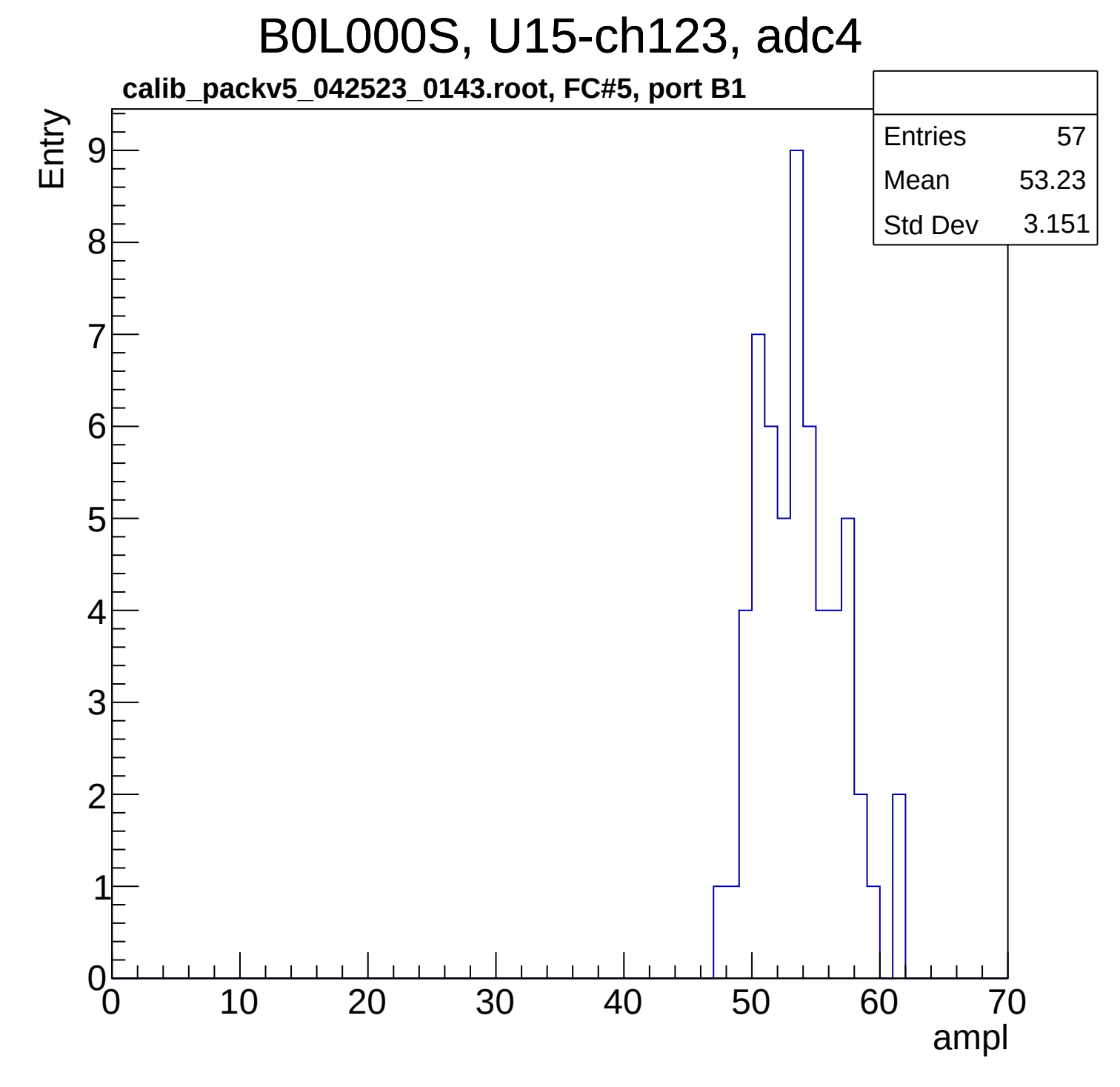
B0L000S, U15-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.23
Std Dev 3.151
Entry
0
1
2
3
4
5
6
7
8
9
0
10
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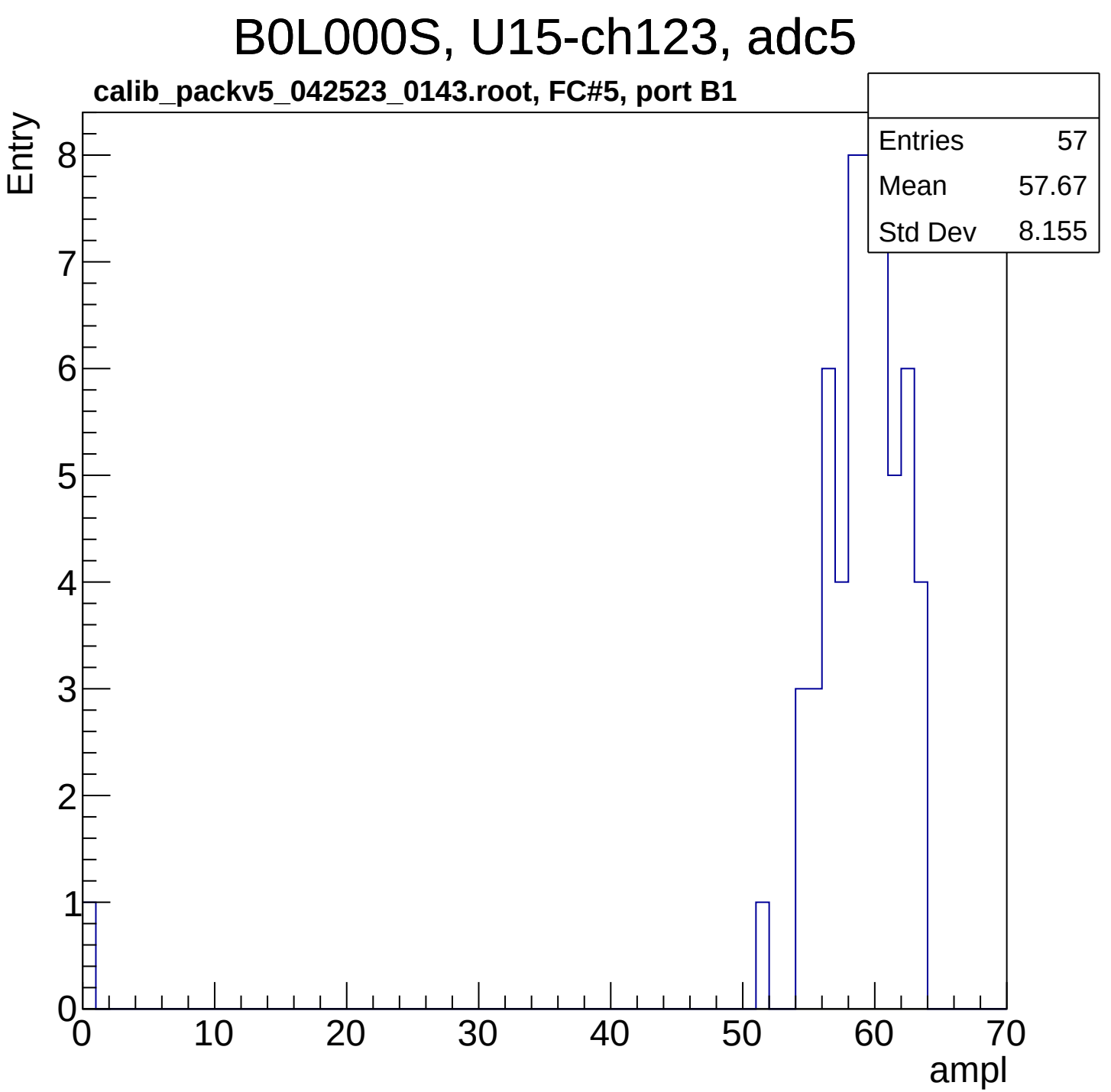
60
70
ampl


B0L000S, U15-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.67
Std Dev 8.155
Entry
ampl
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2
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0
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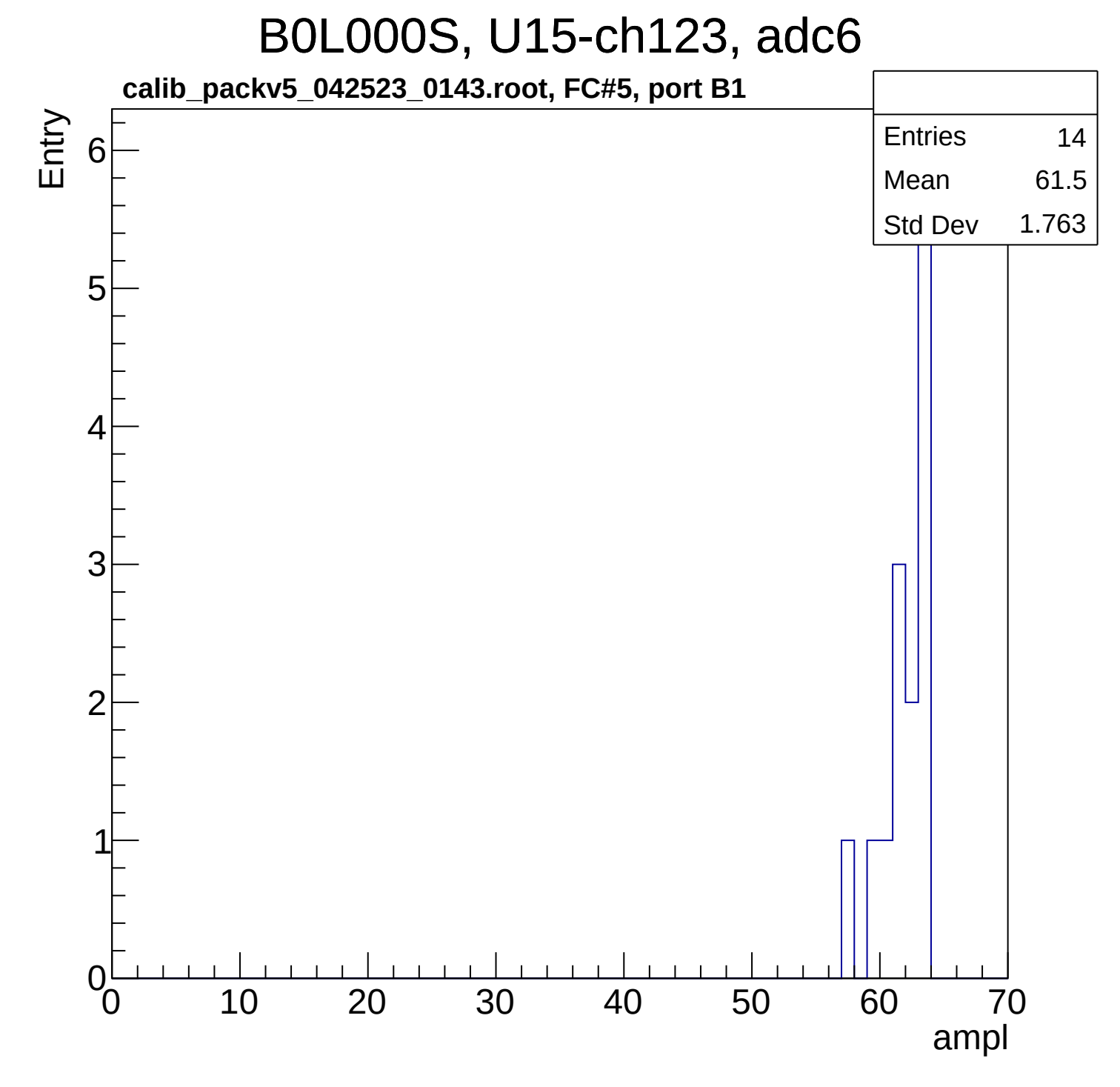
B0L000S, U15-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.5
Std Dev 1.763
Entry
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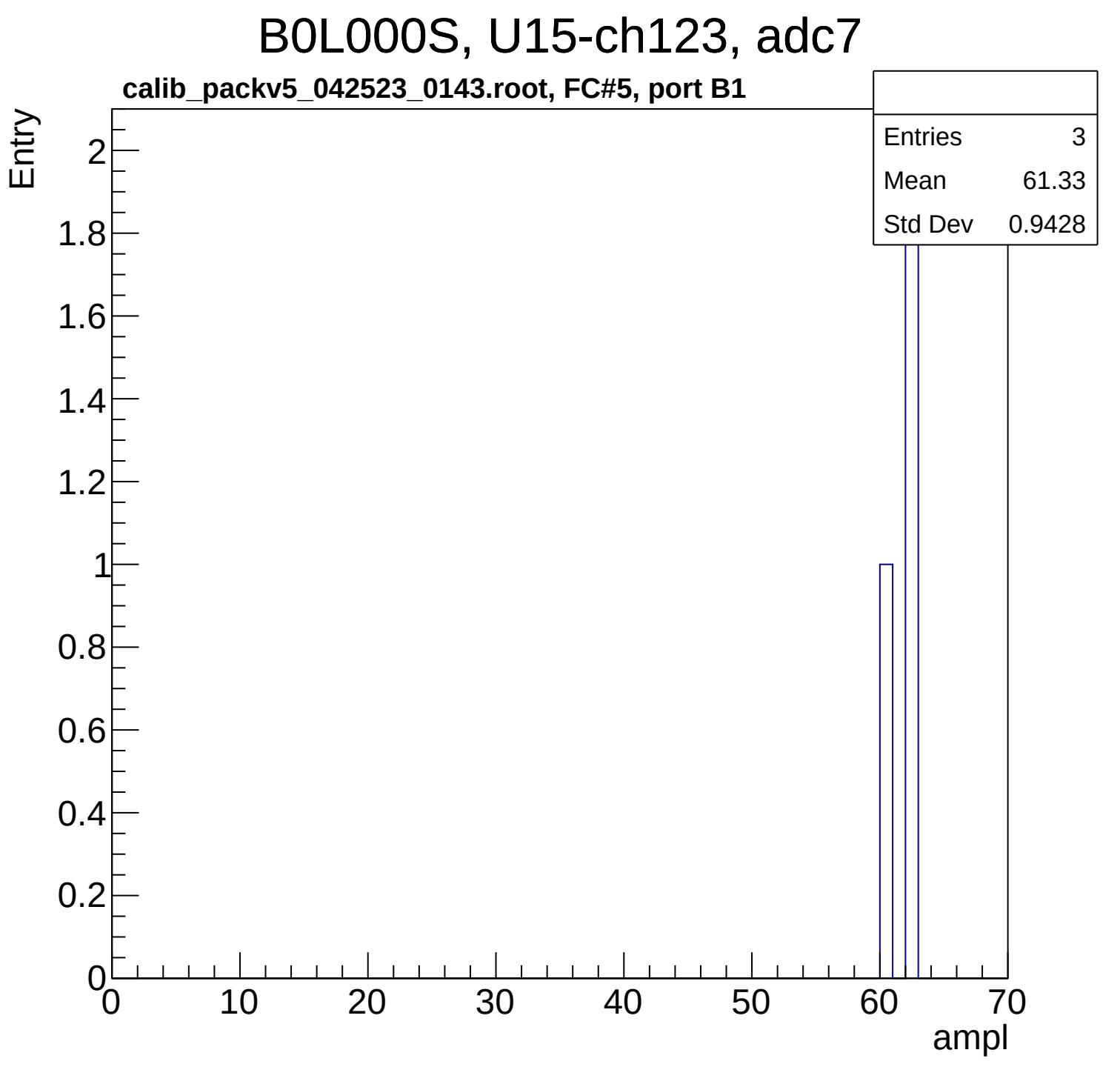
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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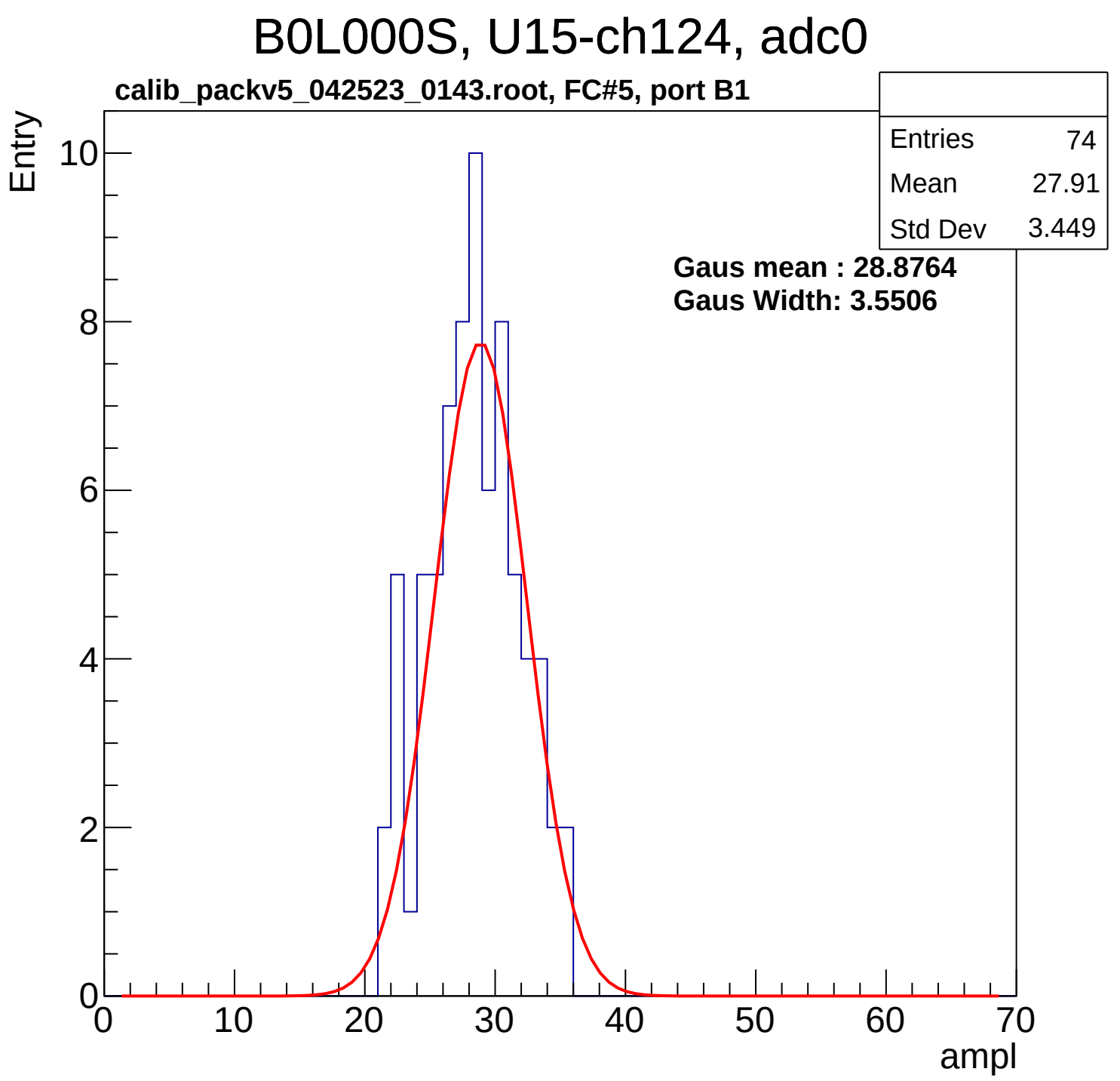
B0L000S, U15-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 27.91
Std Dev 3.449
Gaus mean : 28.8764
Gaus Width: 3.5506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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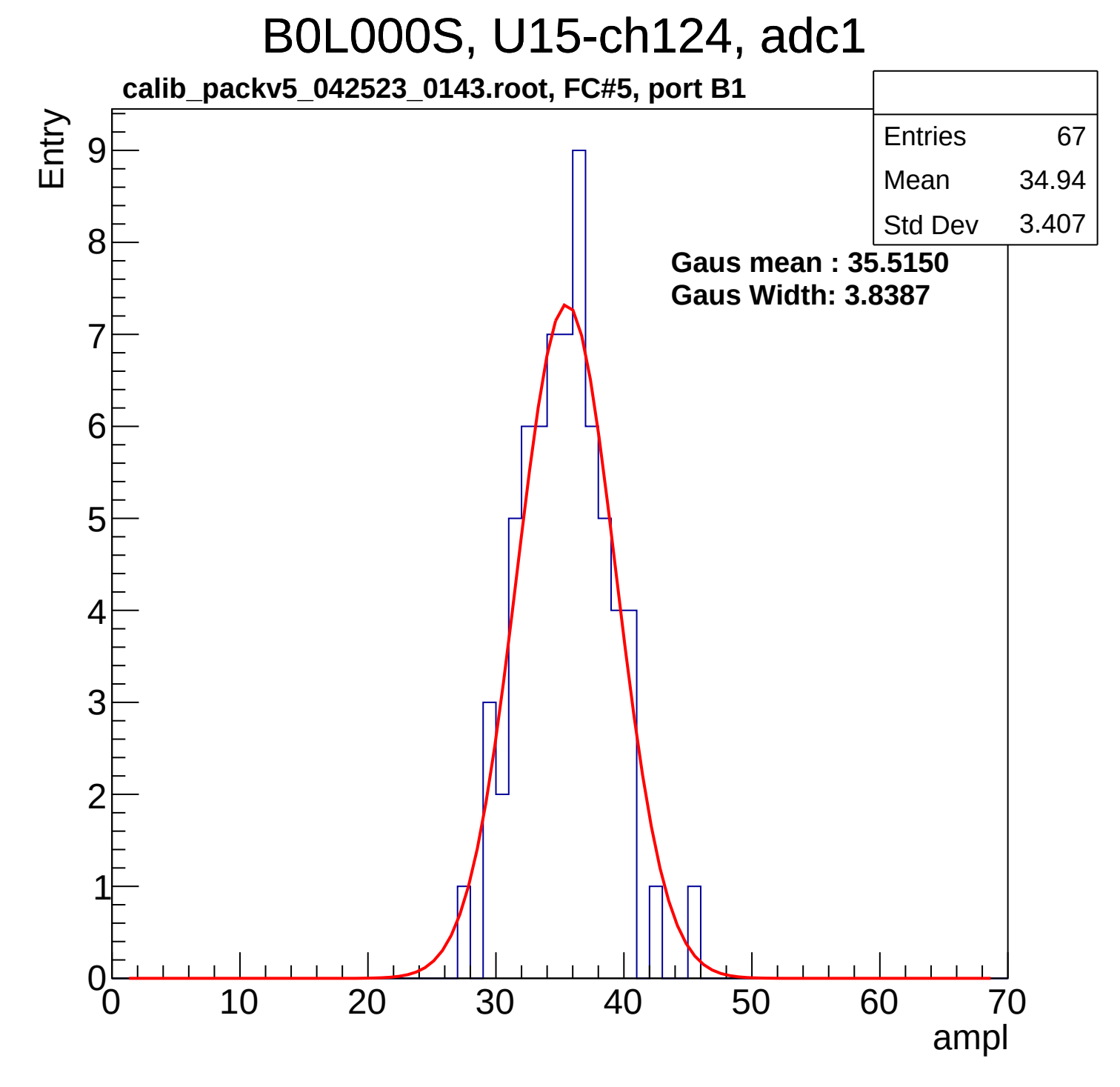
B0L000S, U15-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 34.94
Std Dev 3.407
Gaus mean : 35.5150
Gaus Width: 3.8387
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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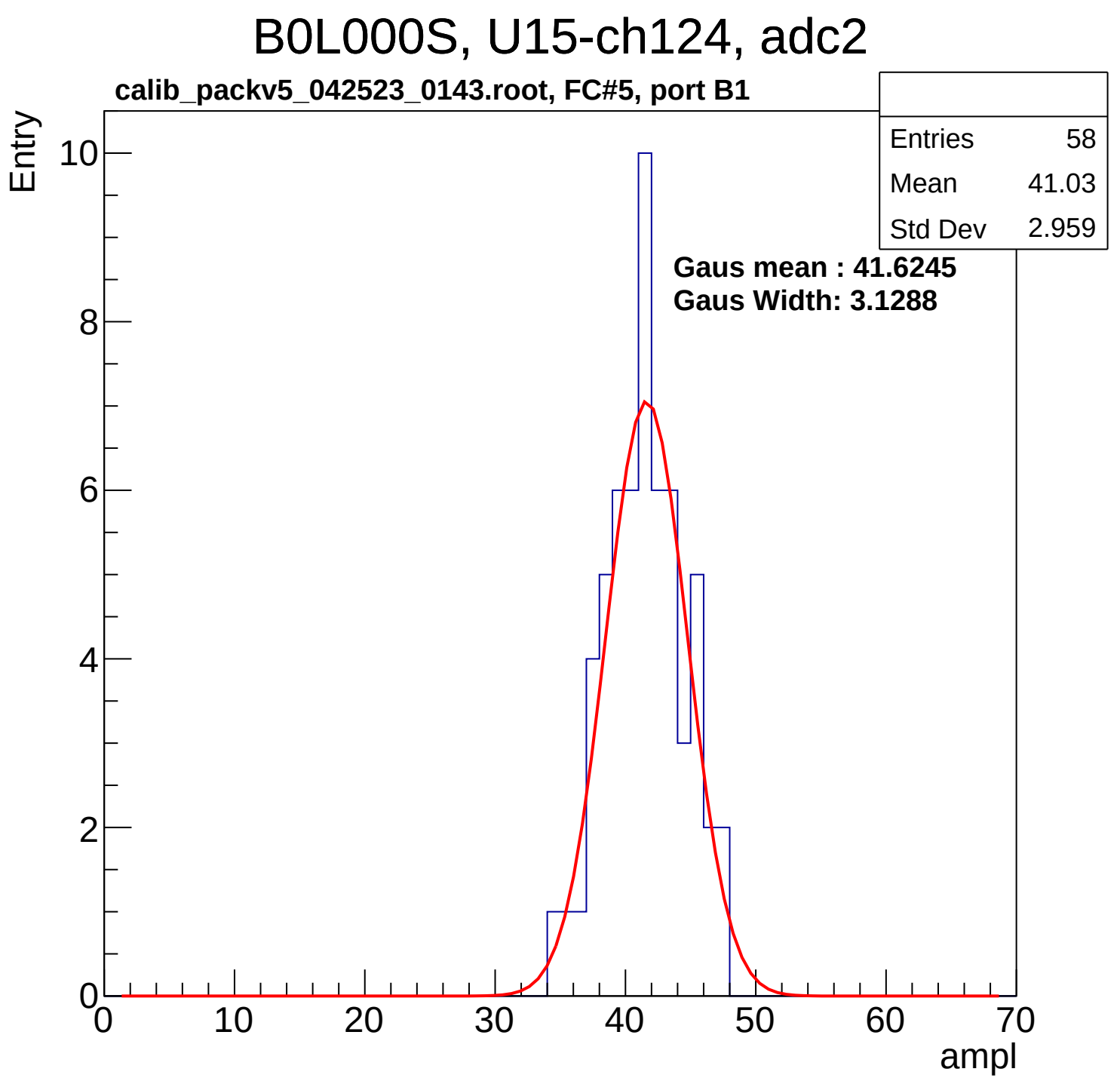
B0L000S, U15-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.03
Std Dev 2.959
Gaus mean : 41.6245
Gaus Width: 3.1288
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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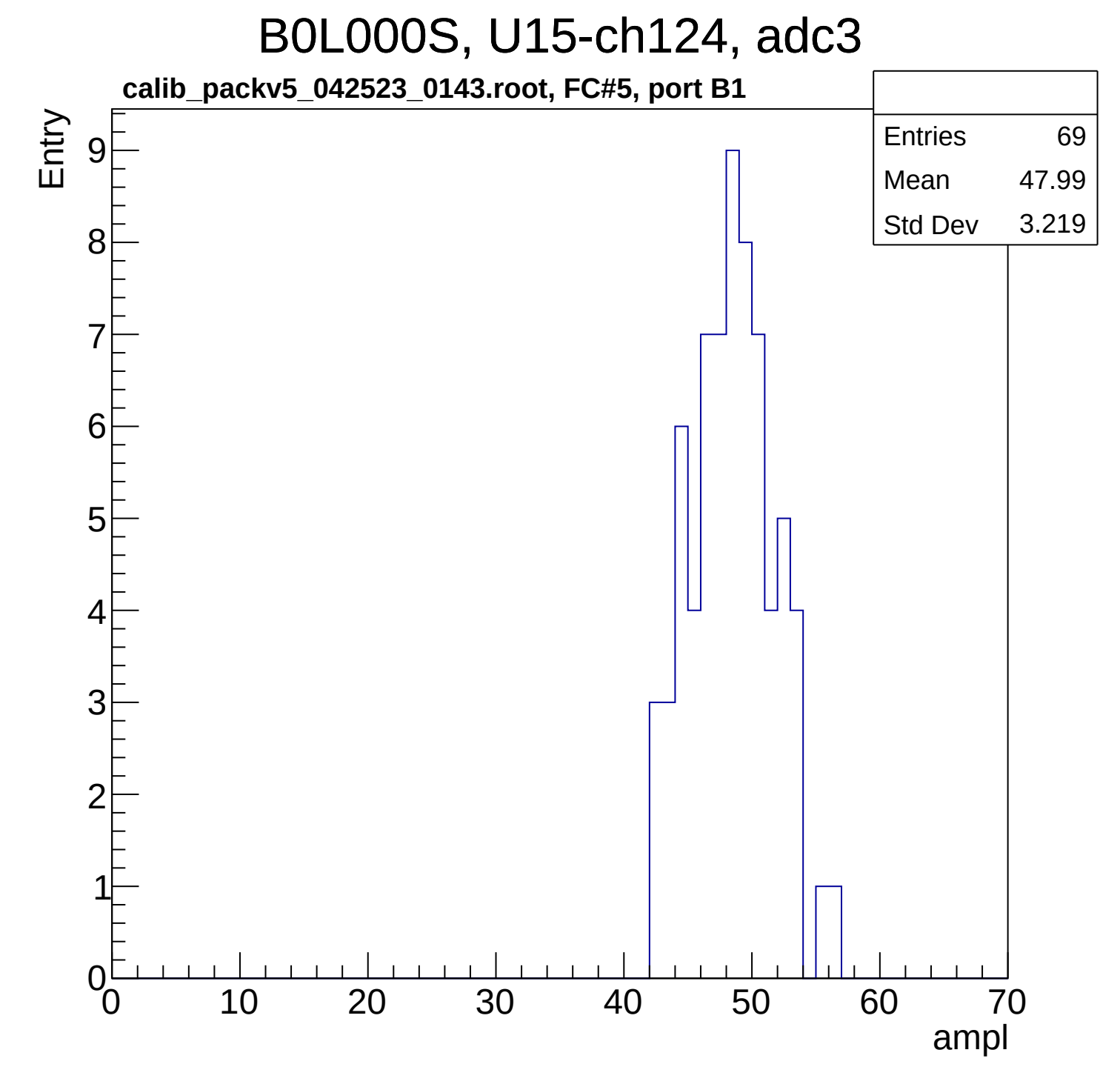
B0L000S, U15-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.99
Std Dev 3.219
Entry
ampl
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2
3
4
5
6
7
8
9
0
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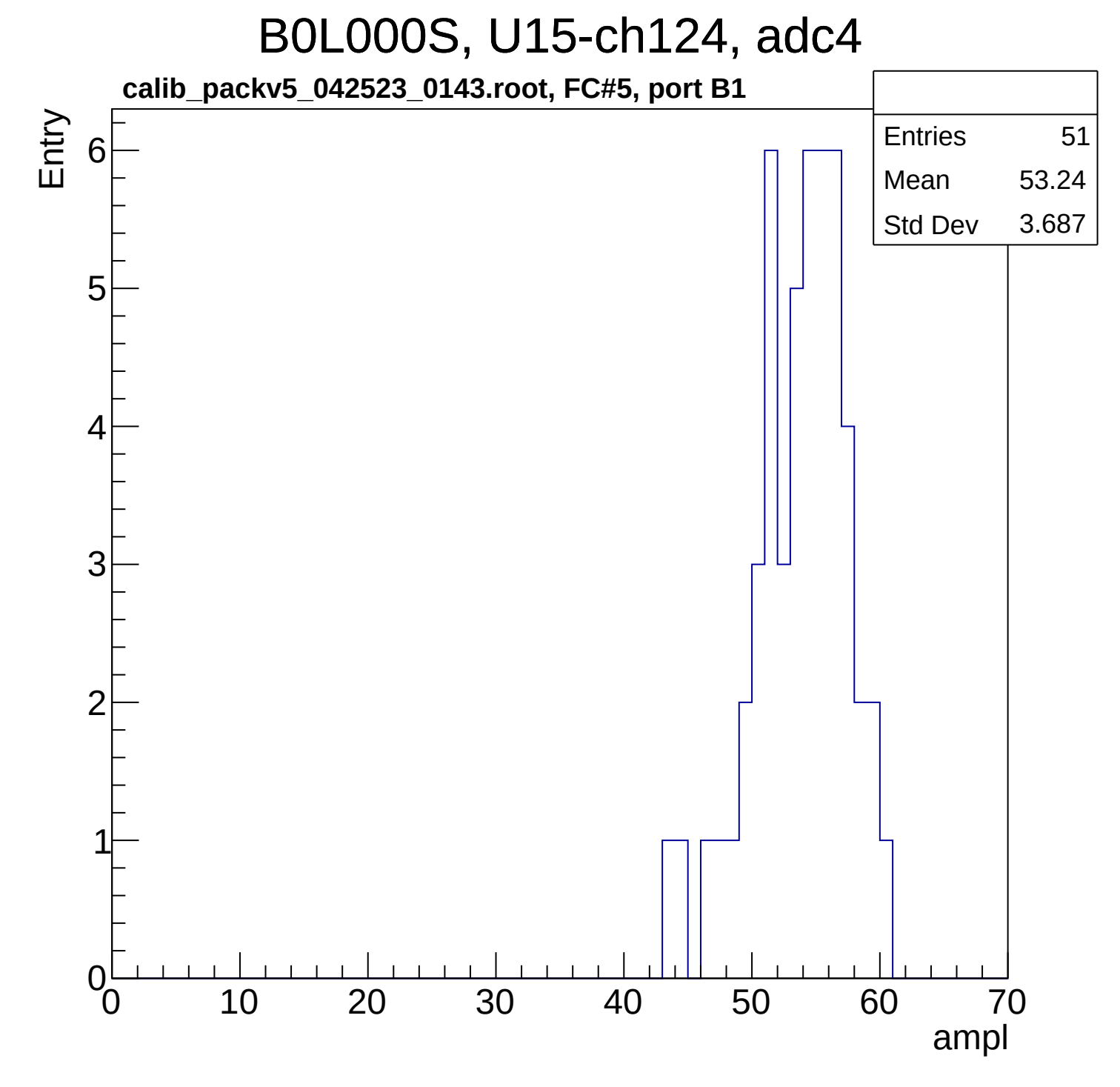
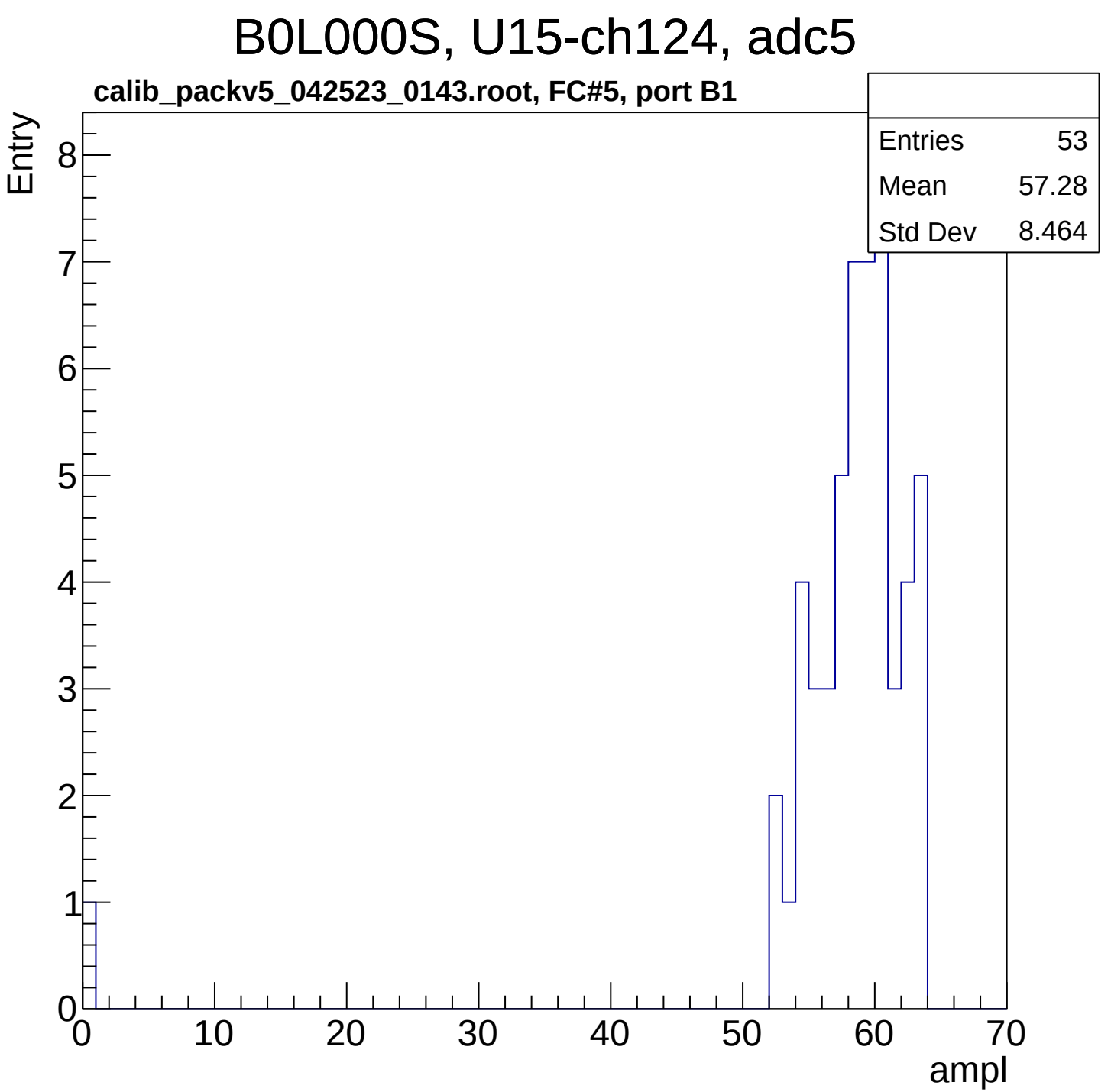

B0L000S, U15-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.24
Std Dev 3.687
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U15-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.28
Std Dev 8.464
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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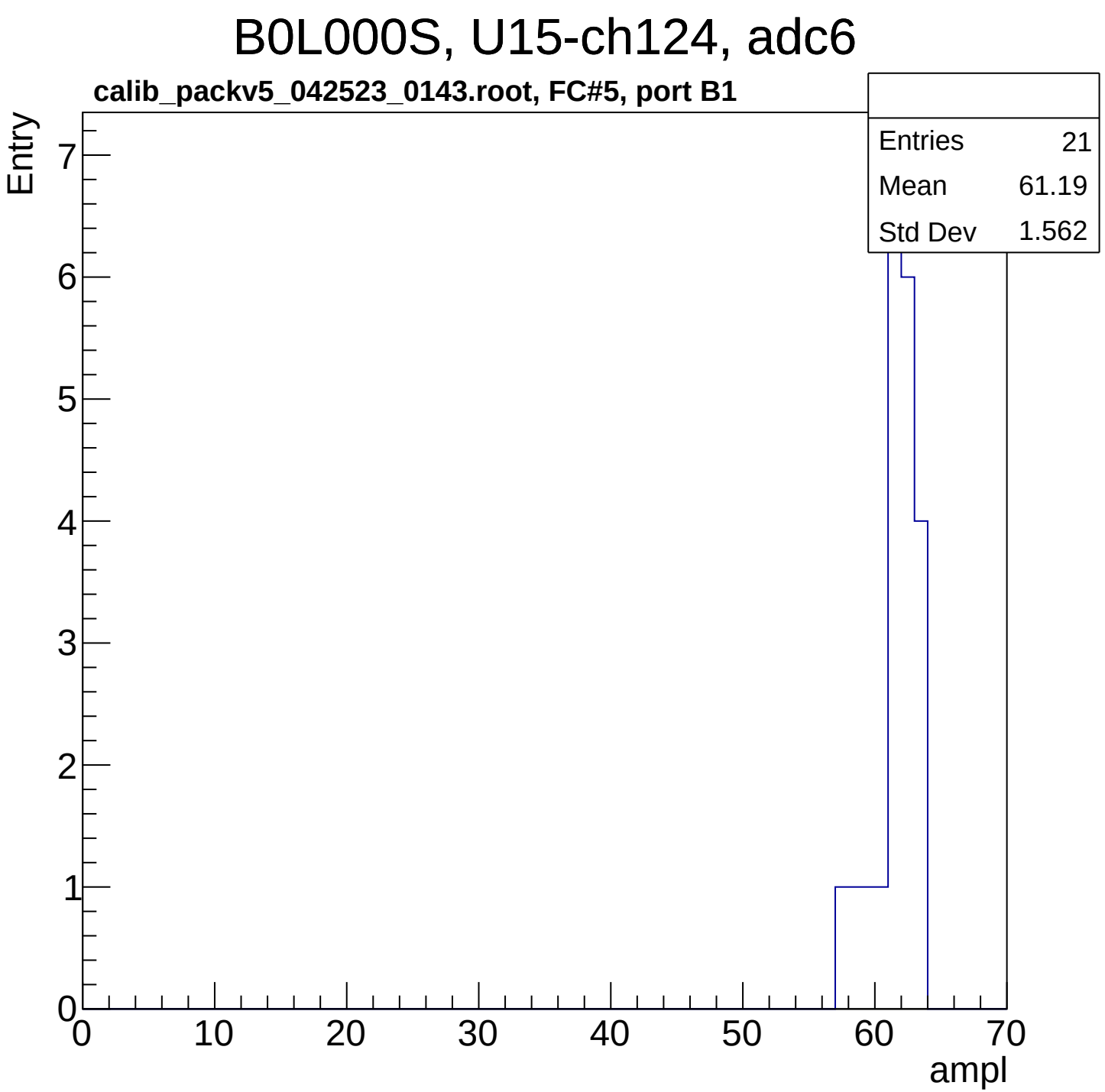
B0L000S, U15-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.19
Std Dev 1.562
Entry
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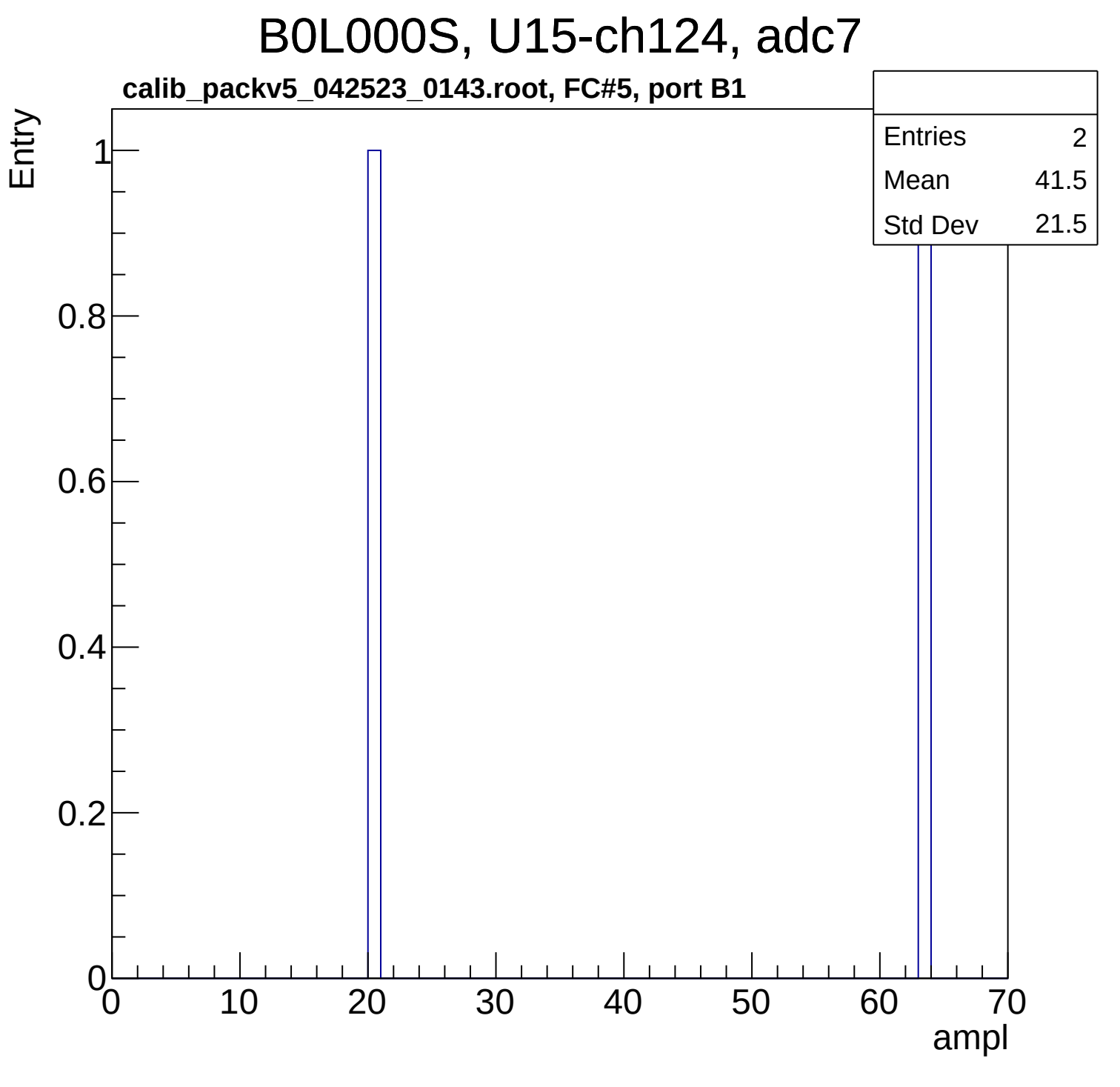
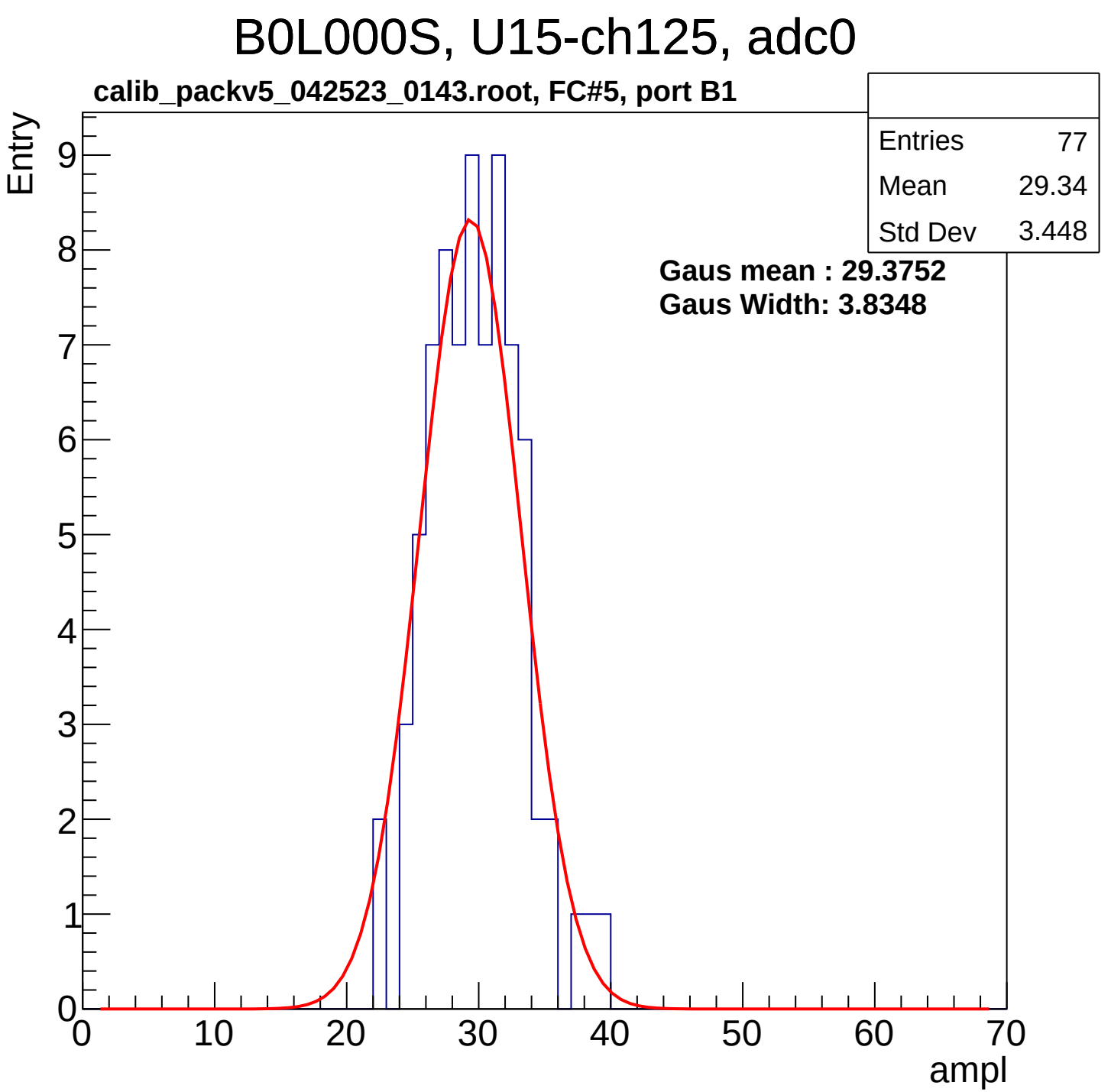
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U15-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.34
Std Dev 3.448
Gaus mean : 29.3752
Gaus Width: 3.8348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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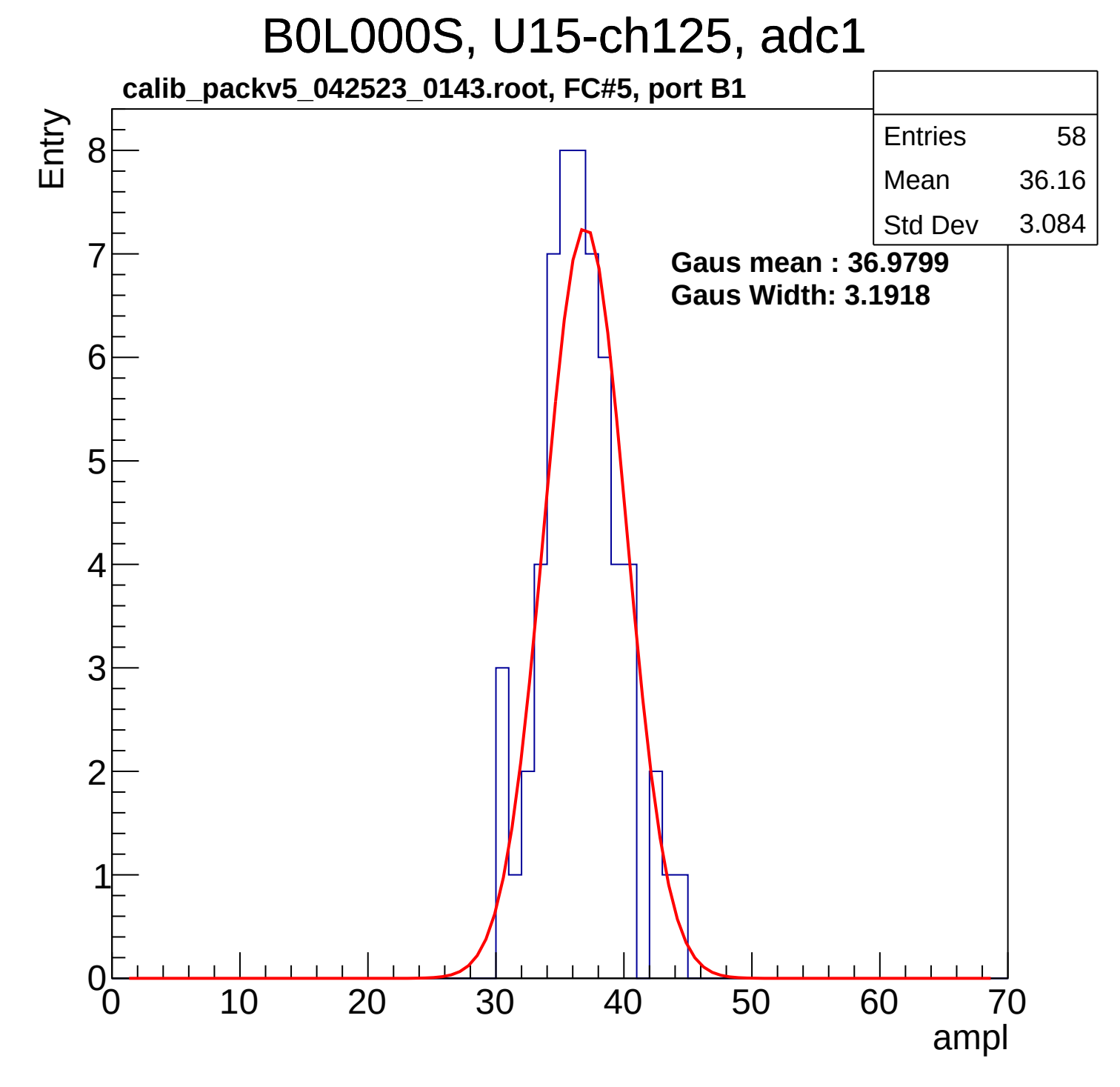
60
70


B0L000S, U15-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 36.16
Std Dev 3.084
Gaus mean : 36.9799
Gaus Width: 3.1918
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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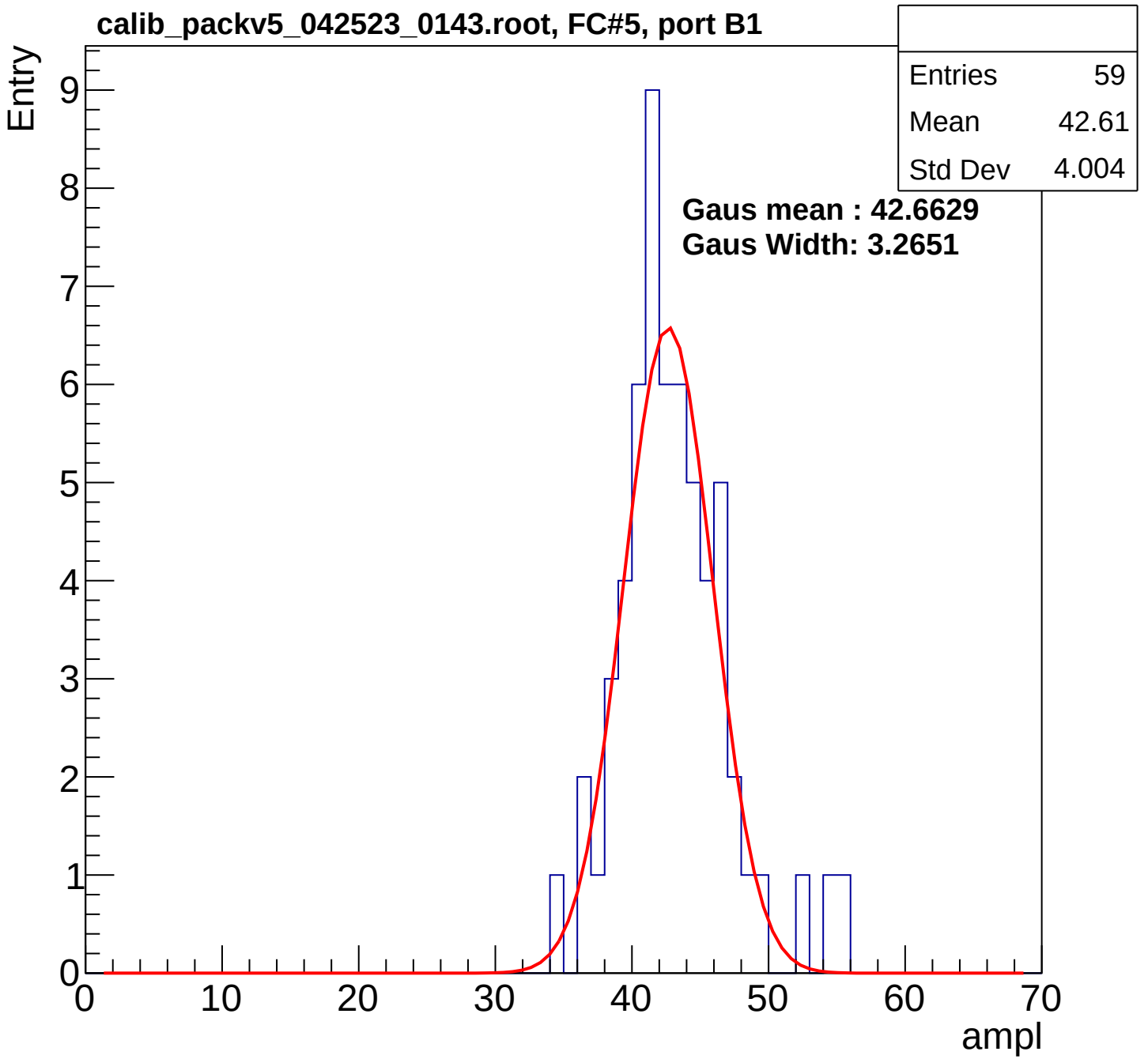
B0L000S, U15-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.61
Std Dev 4.004
Gaus mean : 42.6629
Gaus Width: 3.2651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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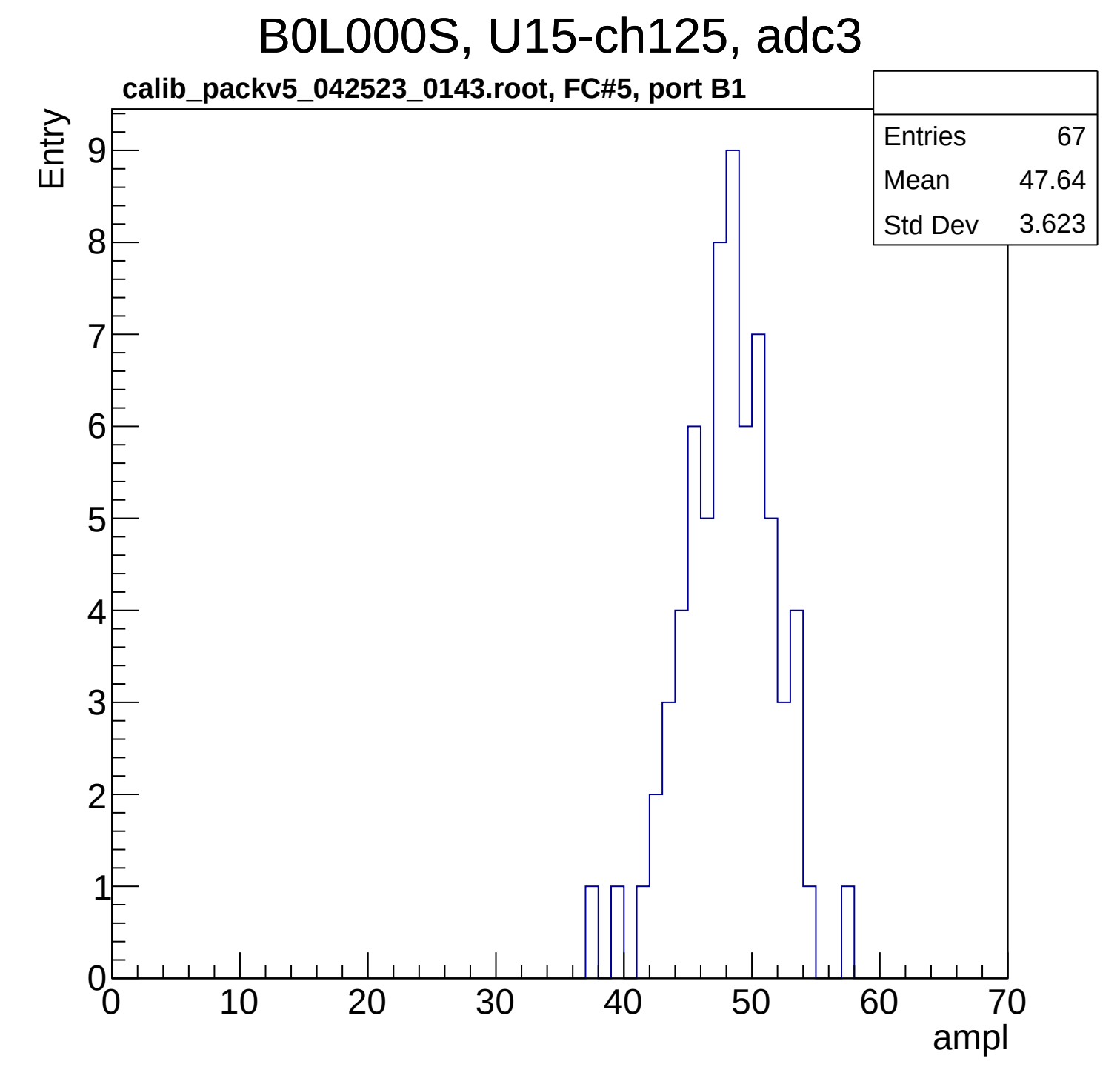
B0L000S, U15-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.64
Std Dev 3.623
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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60
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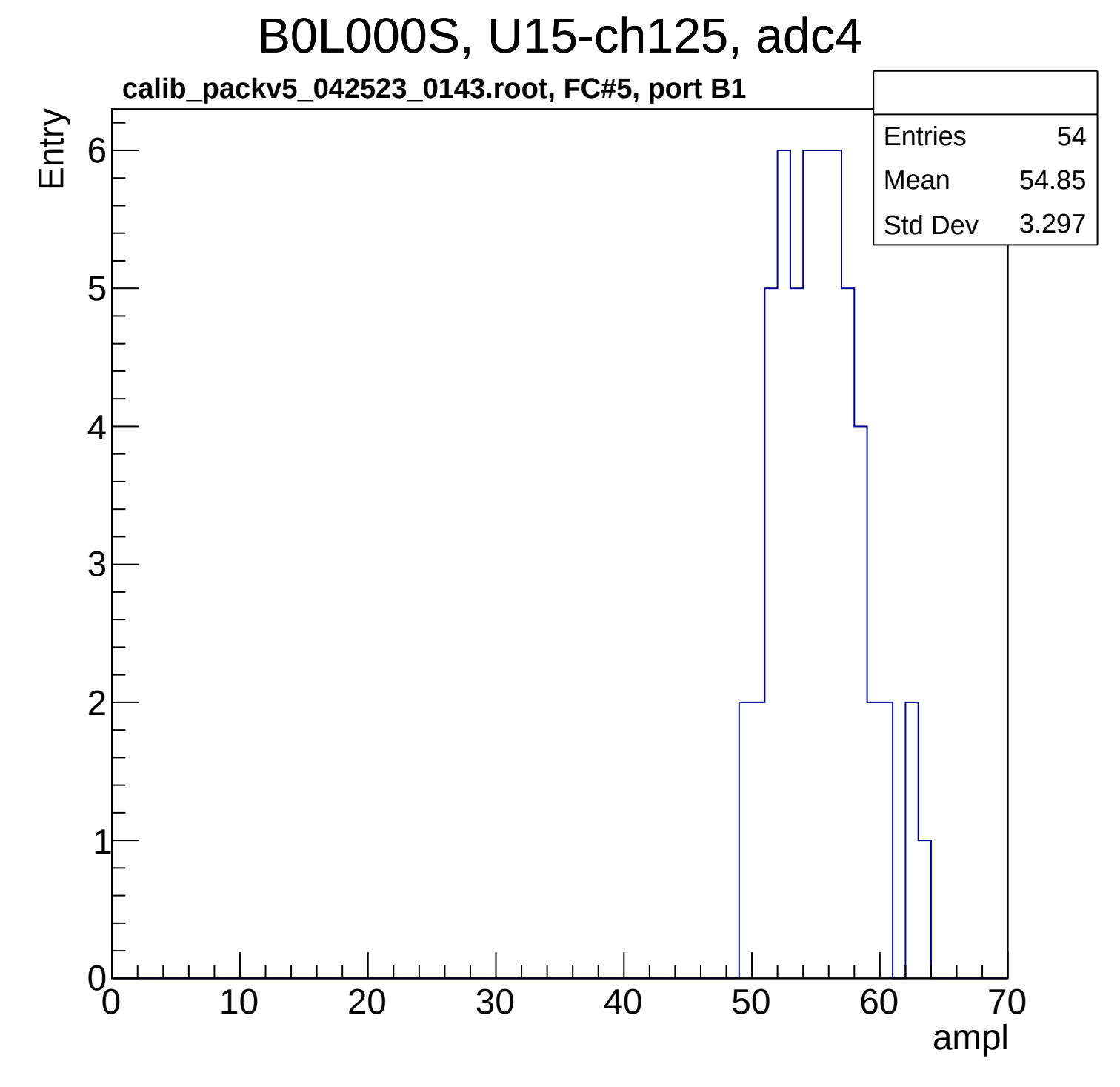
B0L000S, U15-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.85
Std Dev 3.297
Entry
ampl
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1
2
3
4
5
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0
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20
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40
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60
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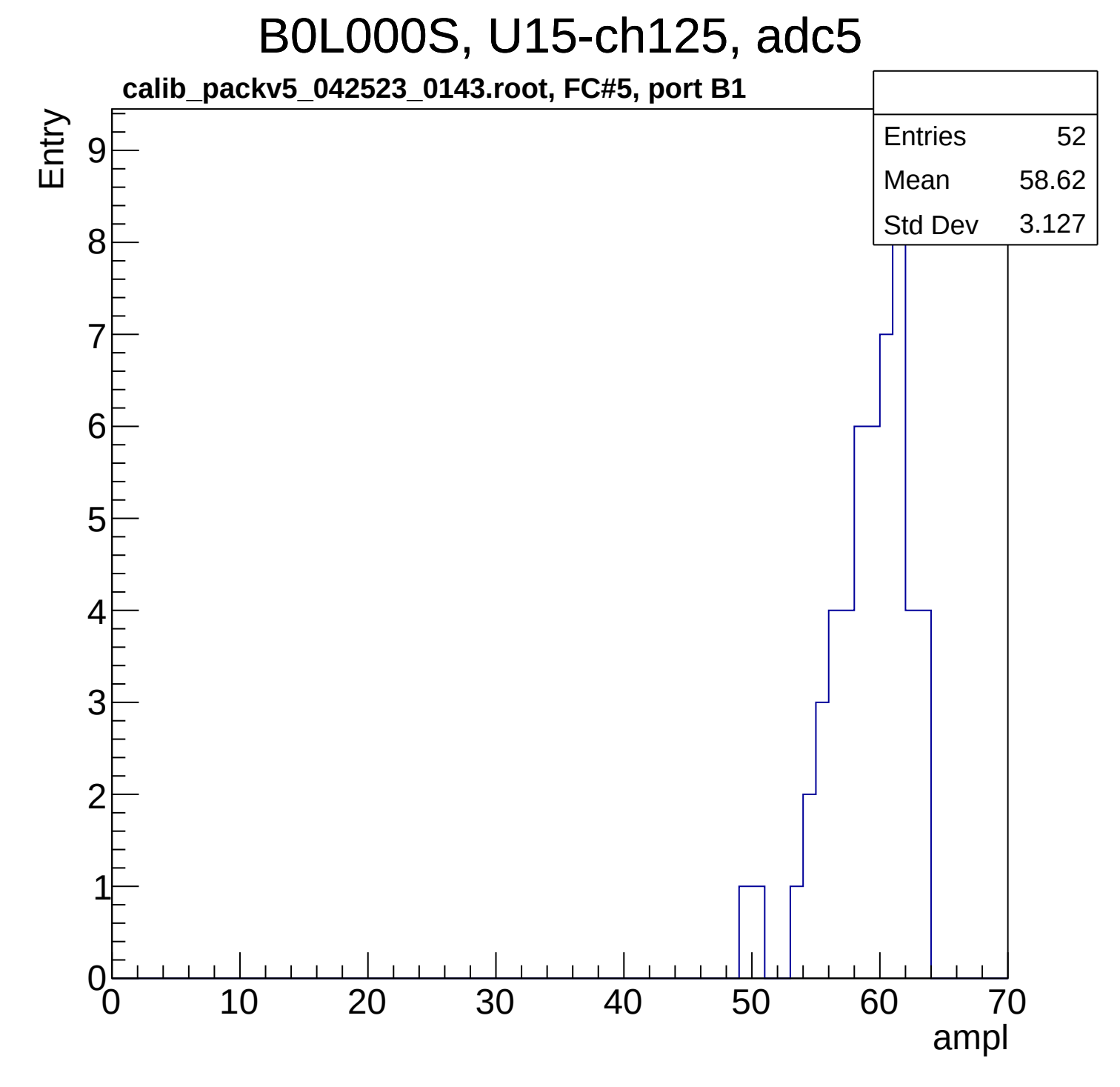
B0L000S, U15-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.62
Std Dev 3.127
Entry
ampl
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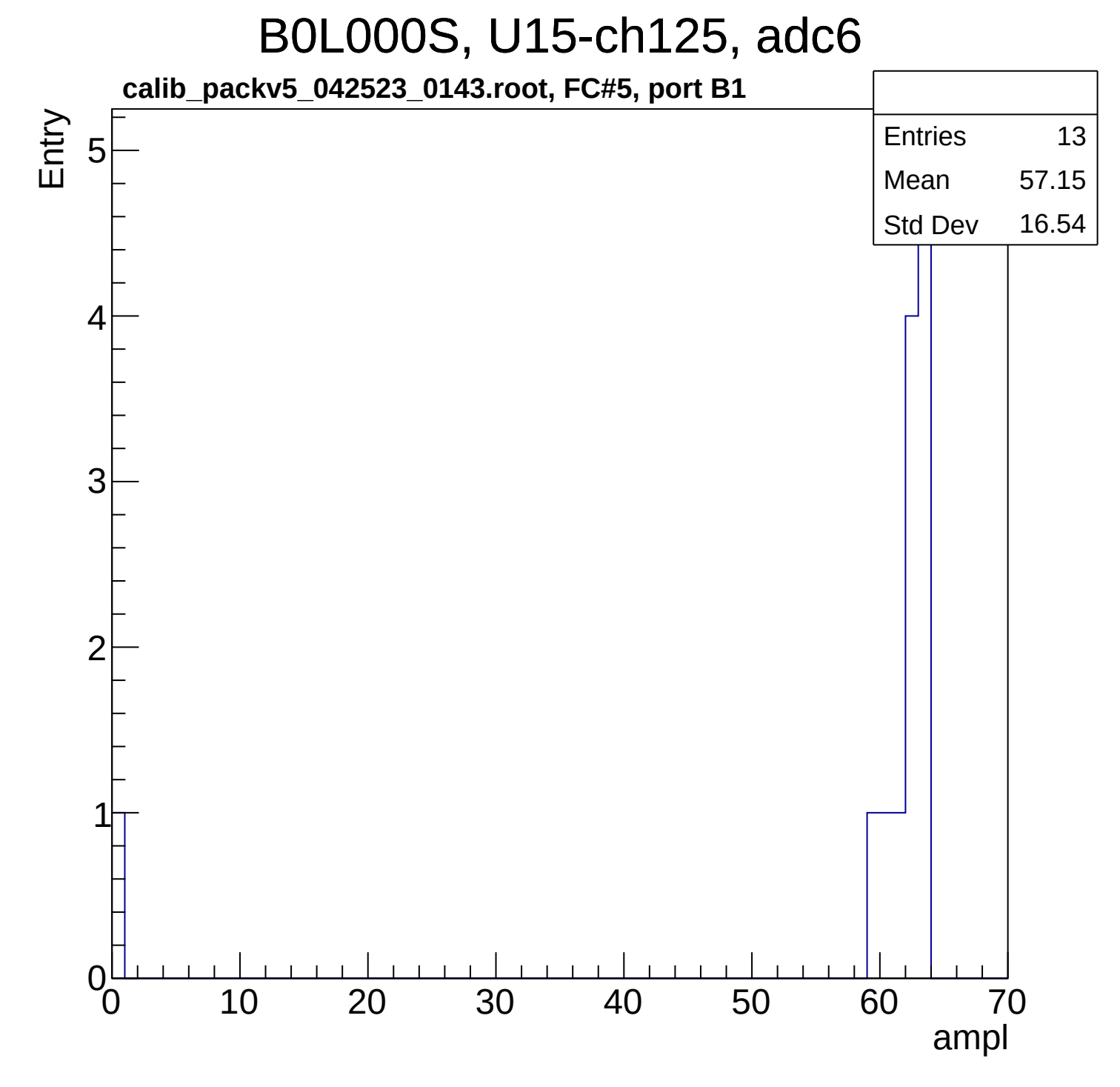
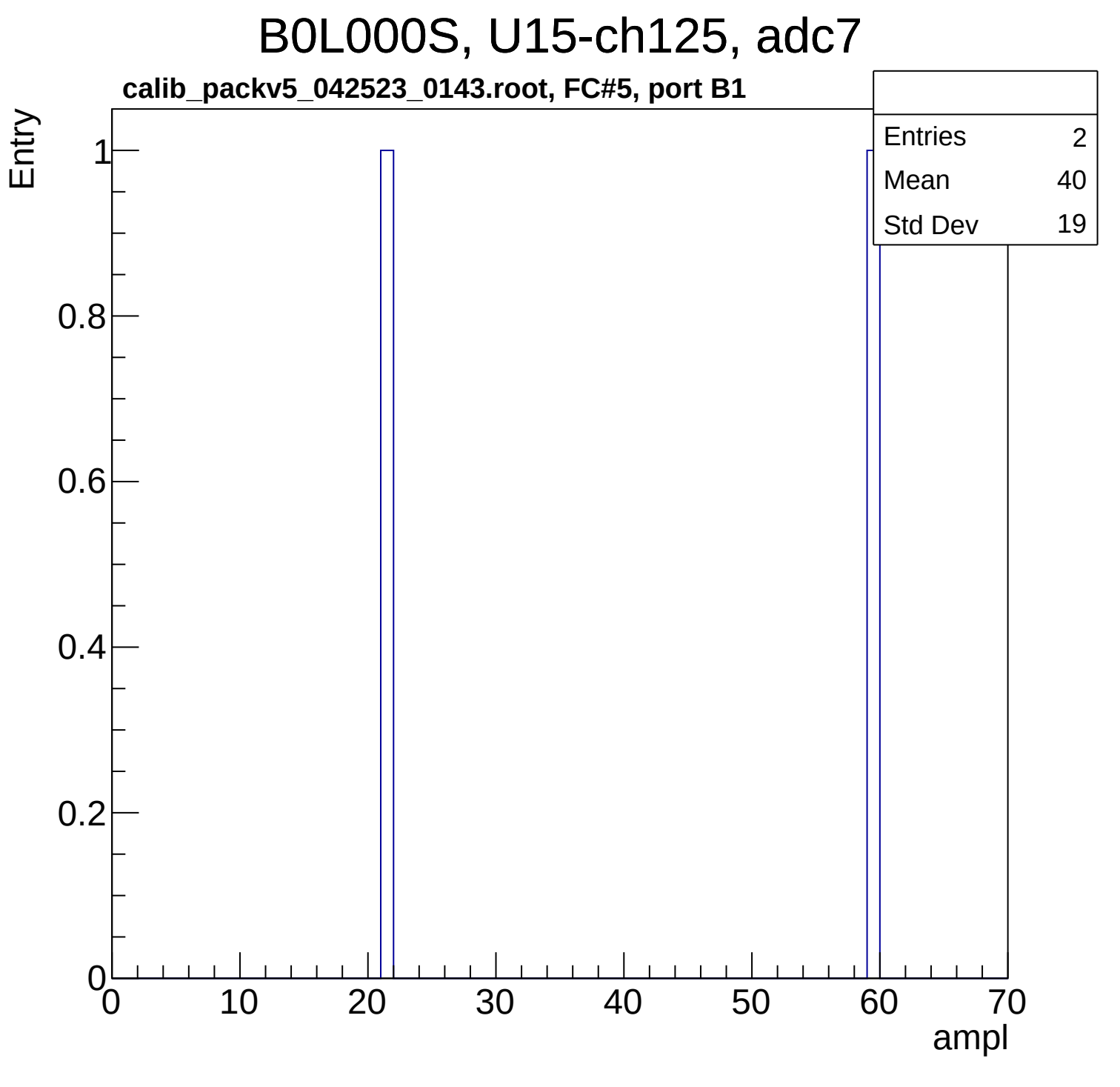
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U15-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.15
Std Dev 16.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 40
Std Dev 19
Entry
ampl
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0.2
0.4
0.6
0.8
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0
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40
50
60
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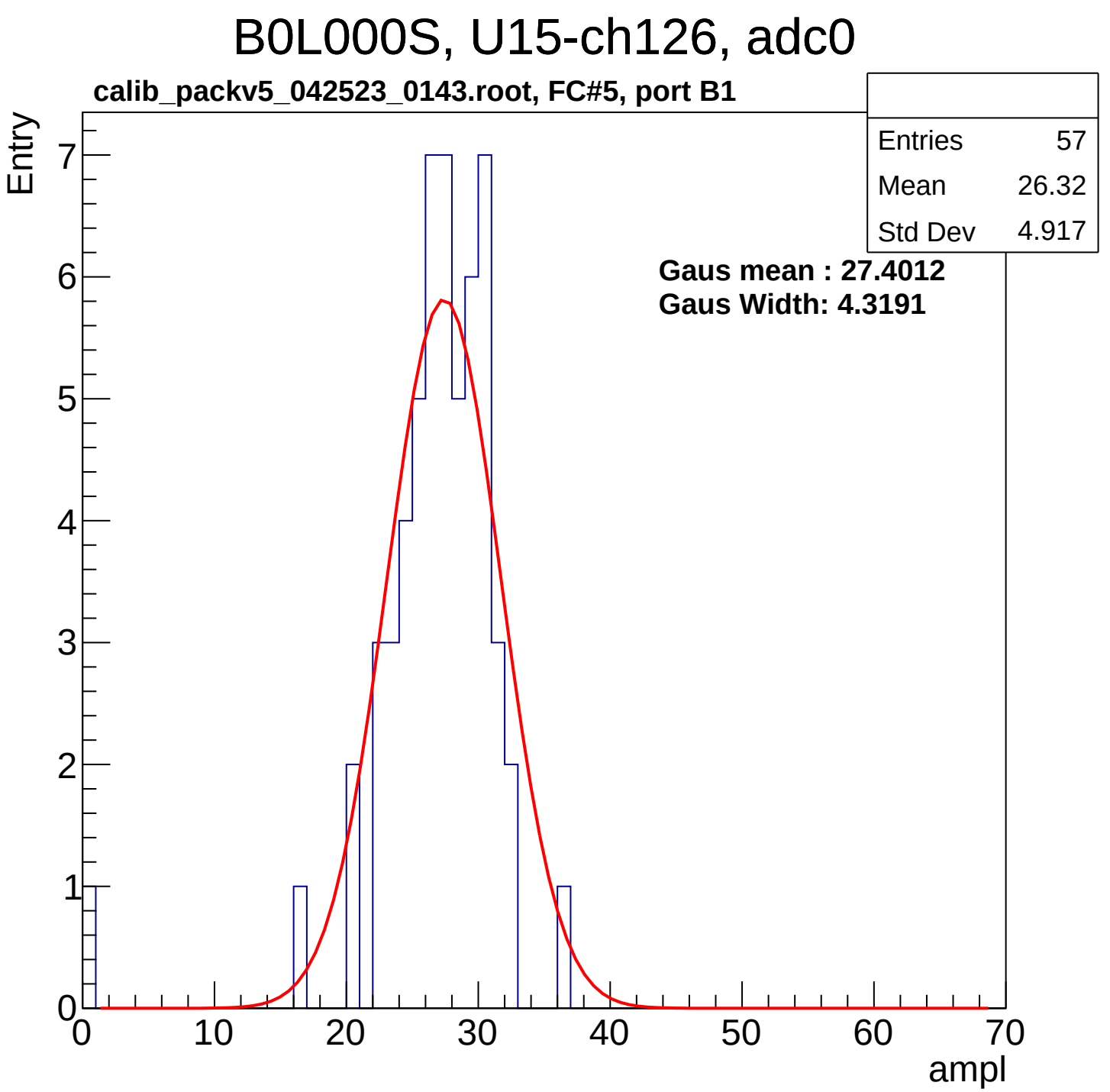
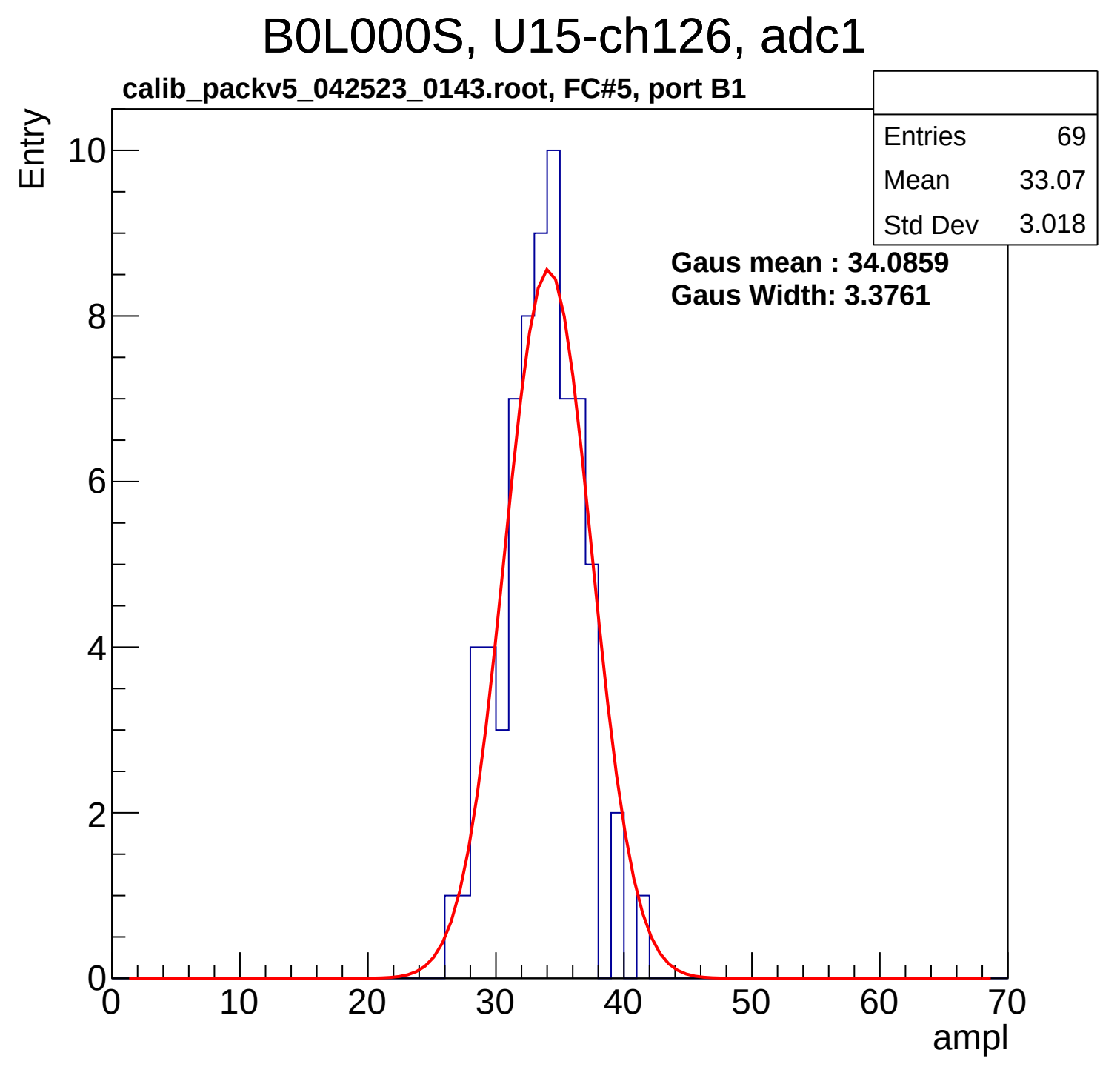

B0L000S, U15-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 26.32
Std Dev 4.917
Gaus mean : 27.4012
Gaus Width: 4.3191
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 33.07
Std Dev 3.018
Gaus mean : 34.0859
Gaus Width: 3.3761
Entry
ampl
0
2
4
6
8
10
0
10
20
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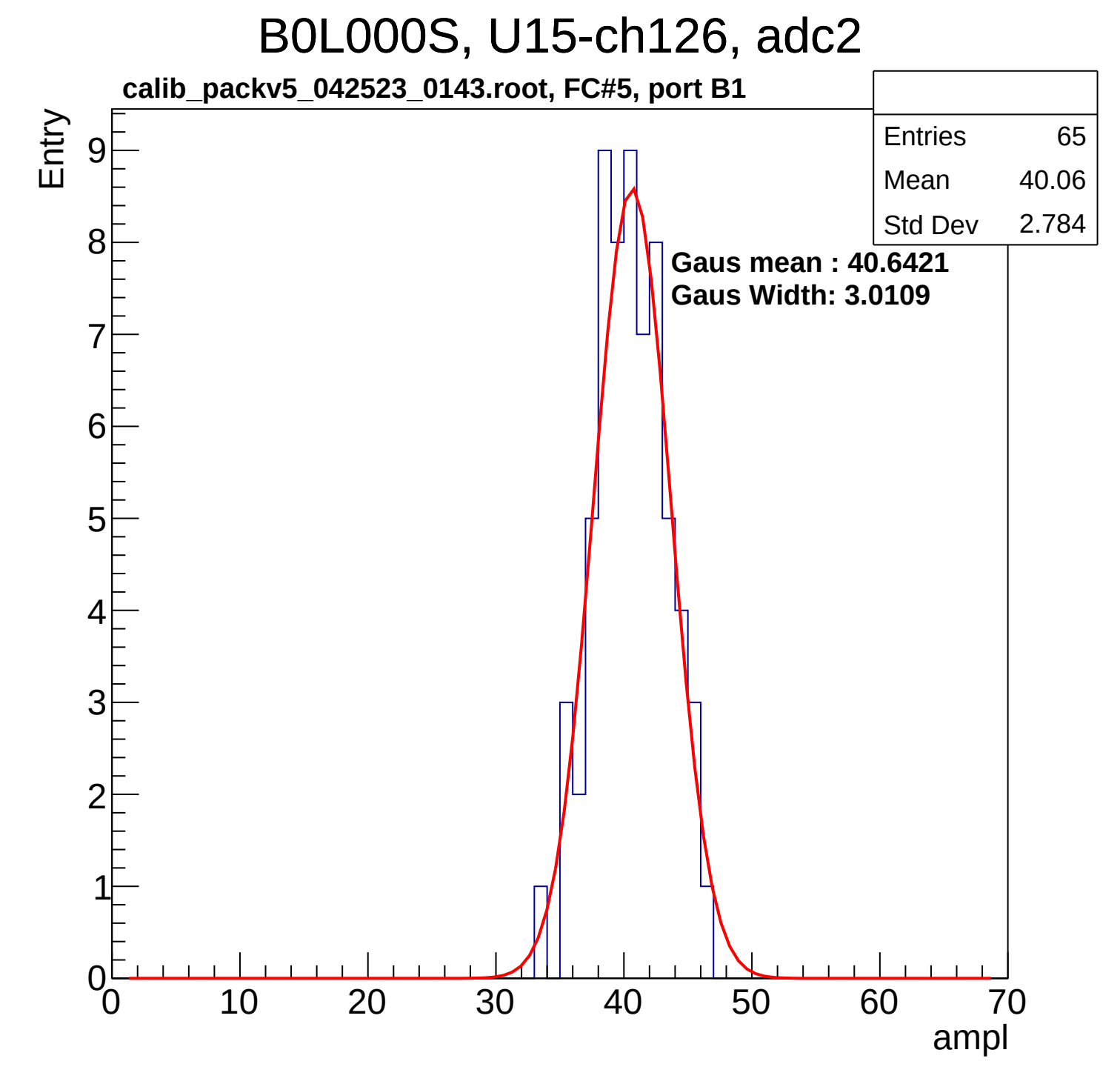
40
50
60
70


B0L000S, U15-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 40.06
Std Dev 2.784
Gaus mean : 40.6421
Gaus Width: 3.0109
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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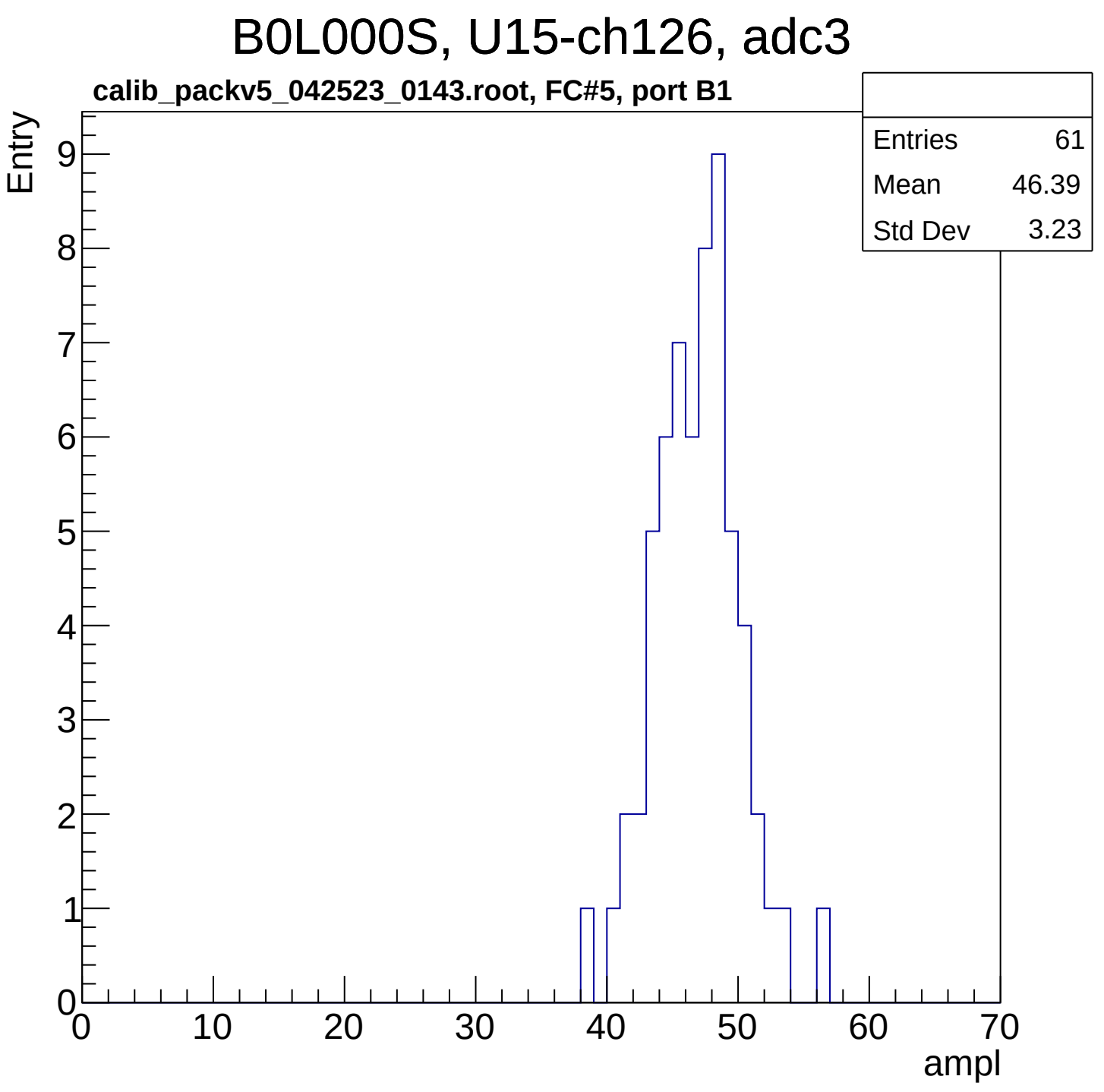
B0L000S, U15-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 46.39
Std Dev 3.23
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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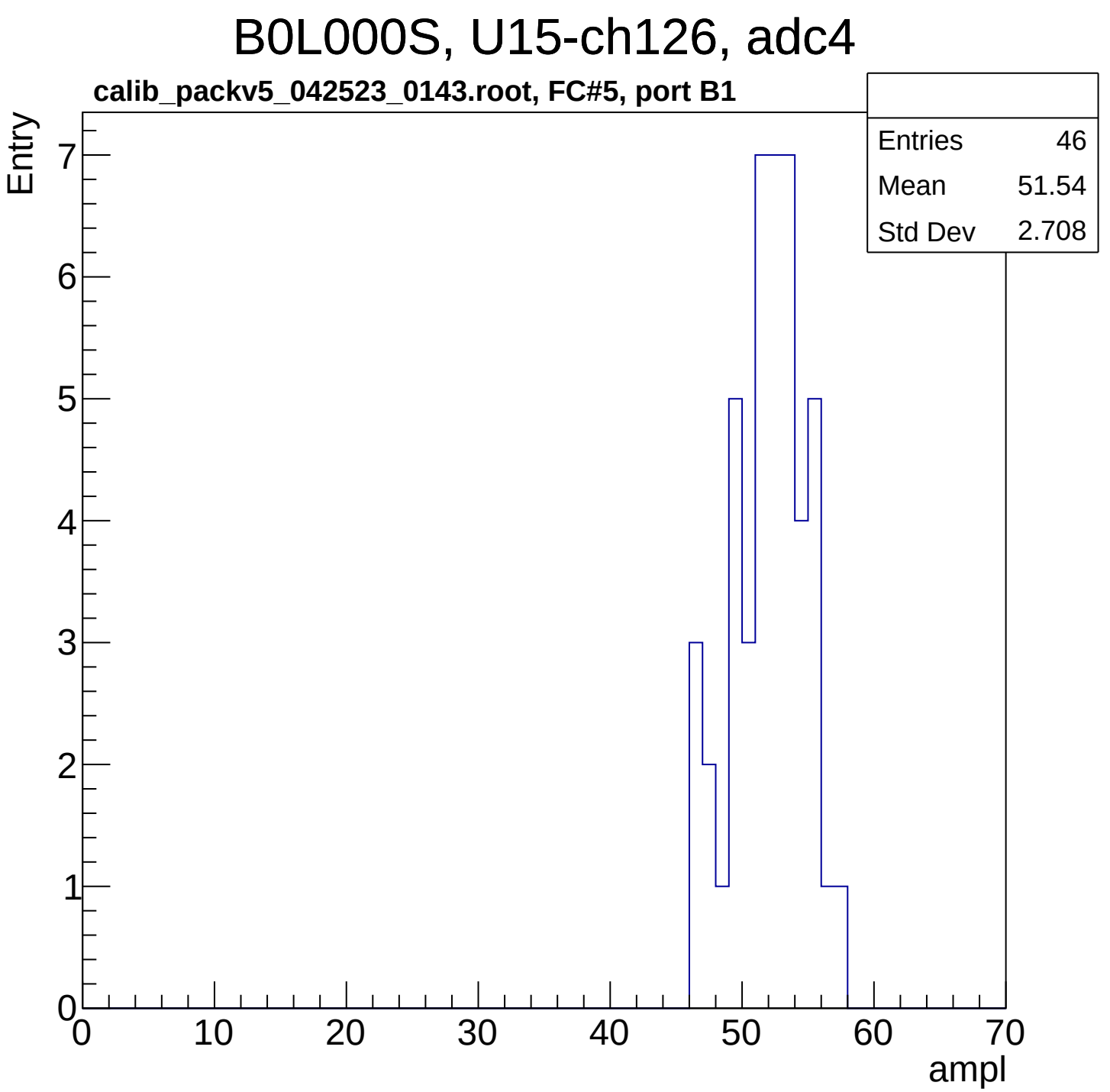
B0L000S, U15-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 51.54
Std Dev 2.708
Entry
ampl
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1
2
3
4
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0
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60
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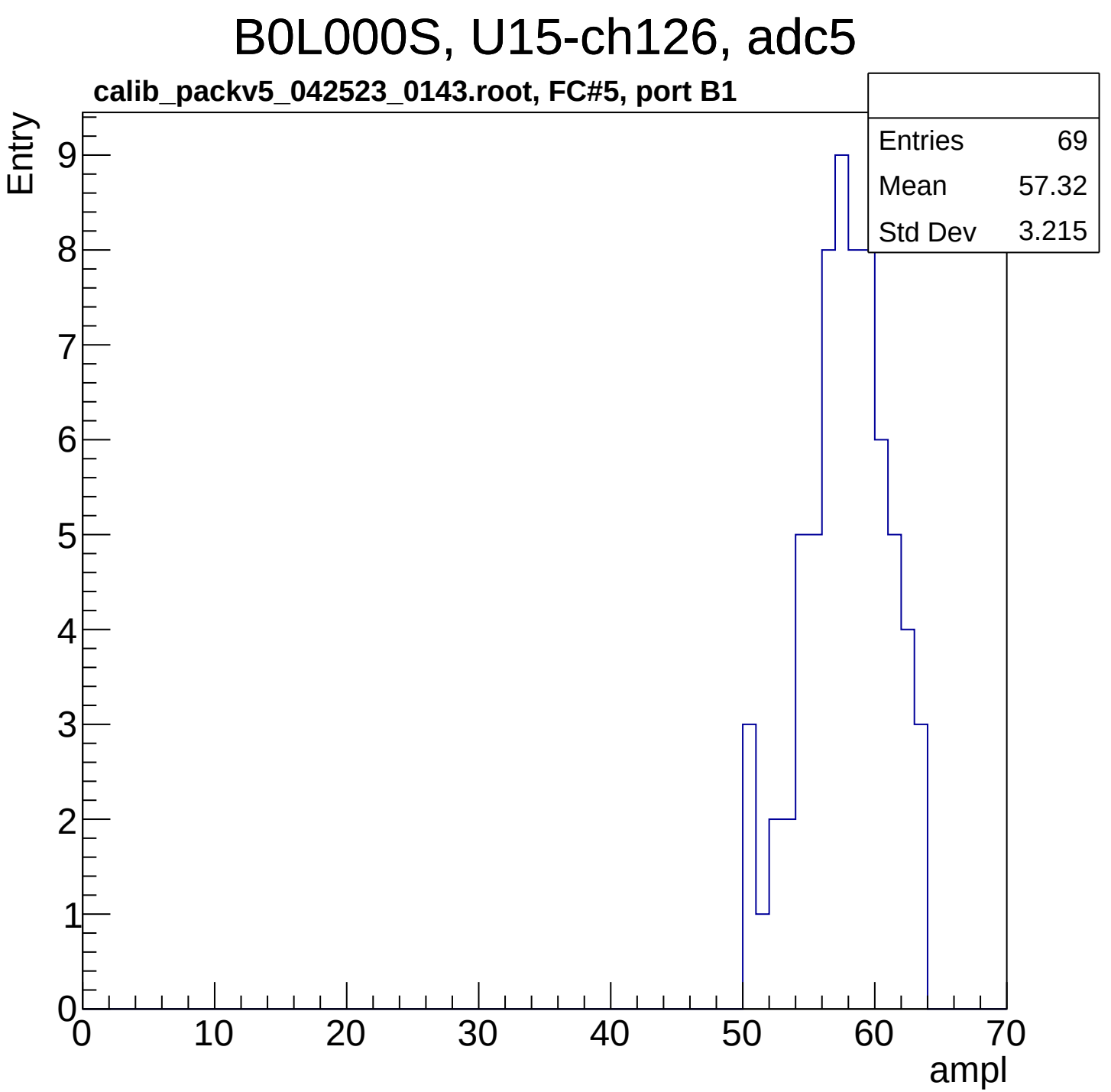
B0L000S, U15-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 57.32
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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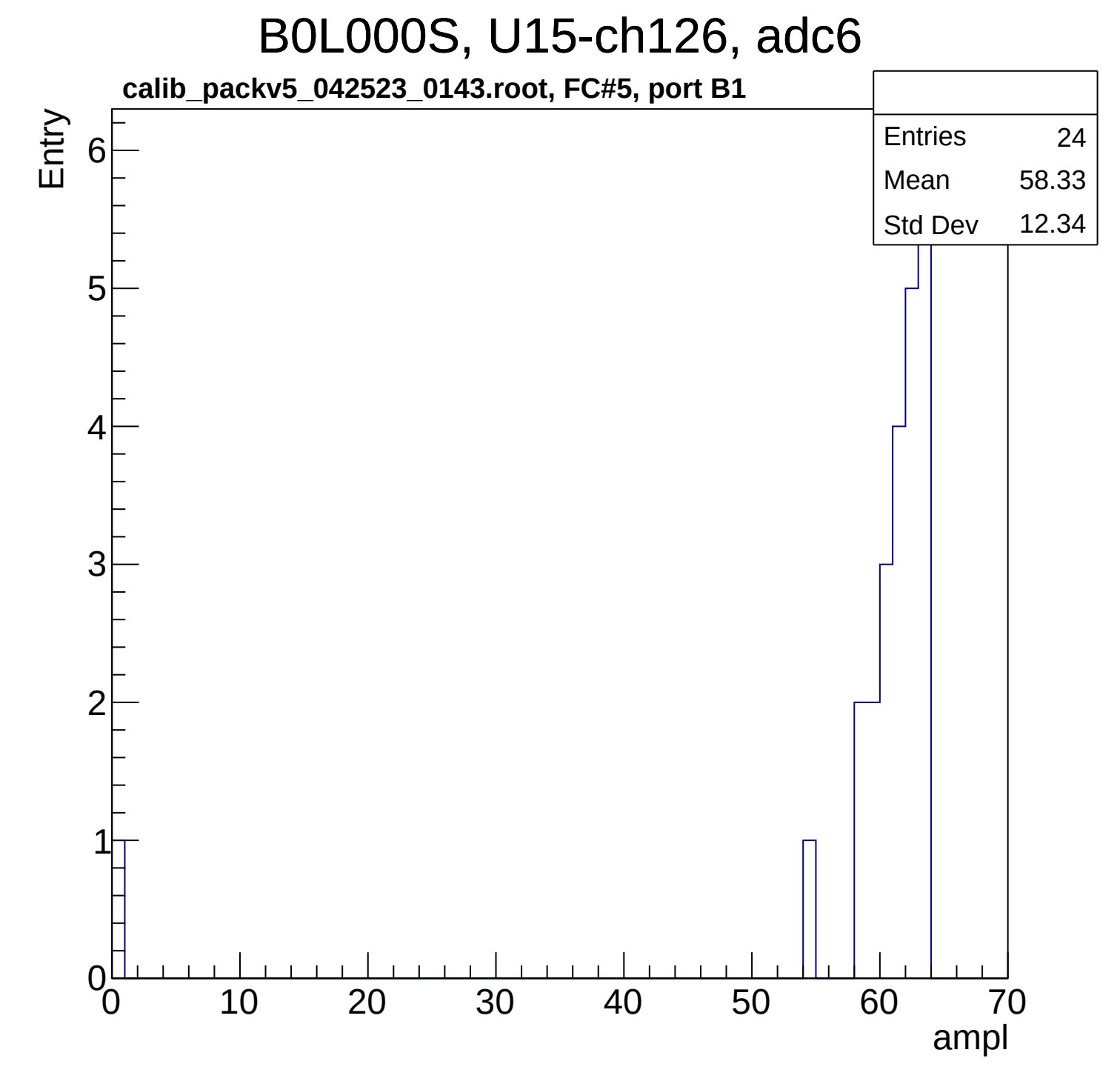
B0L000S, U15-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.33
Std Dev 12.34
Entry
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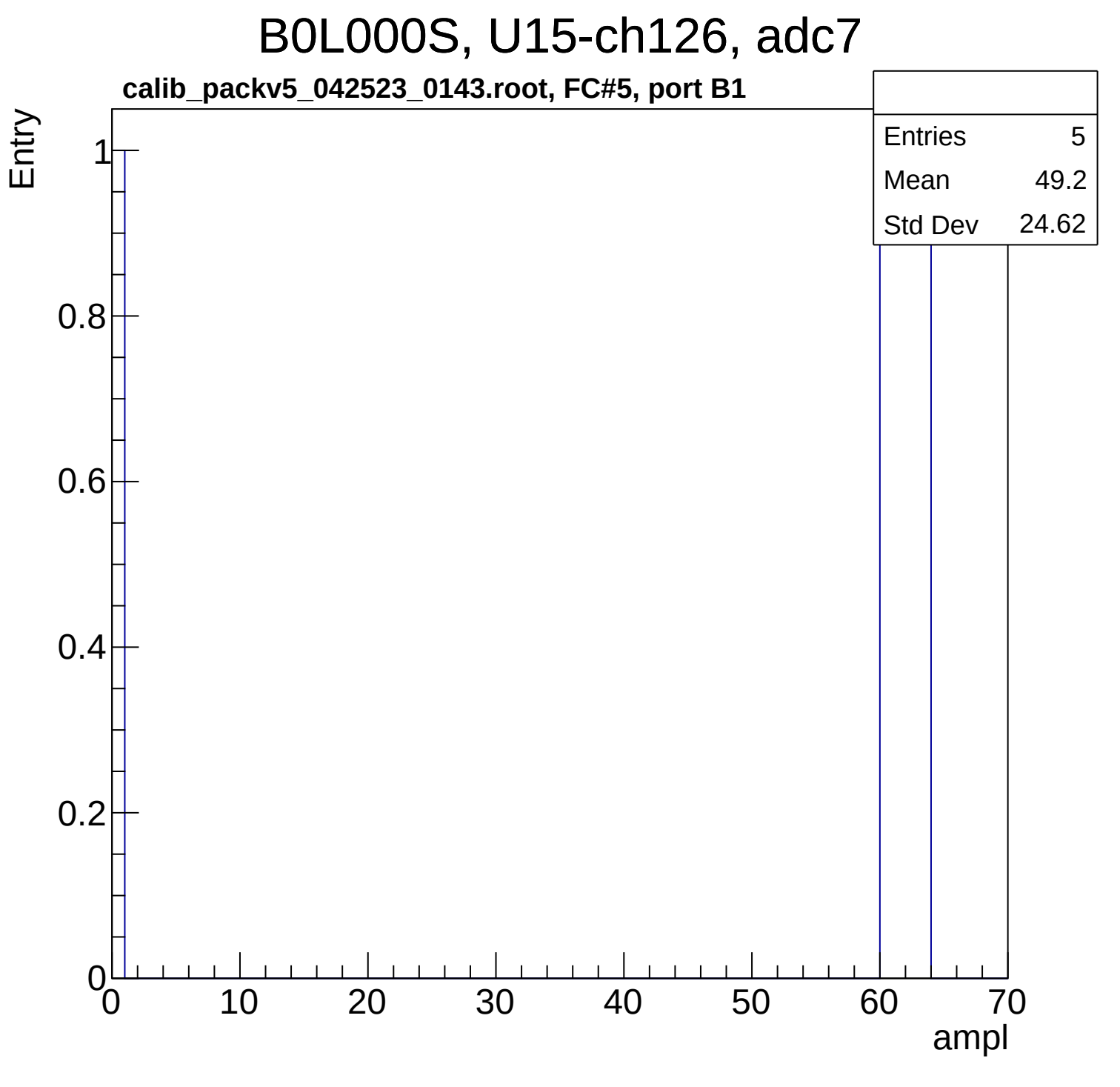
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.2
Std Dev 24.62
Entry
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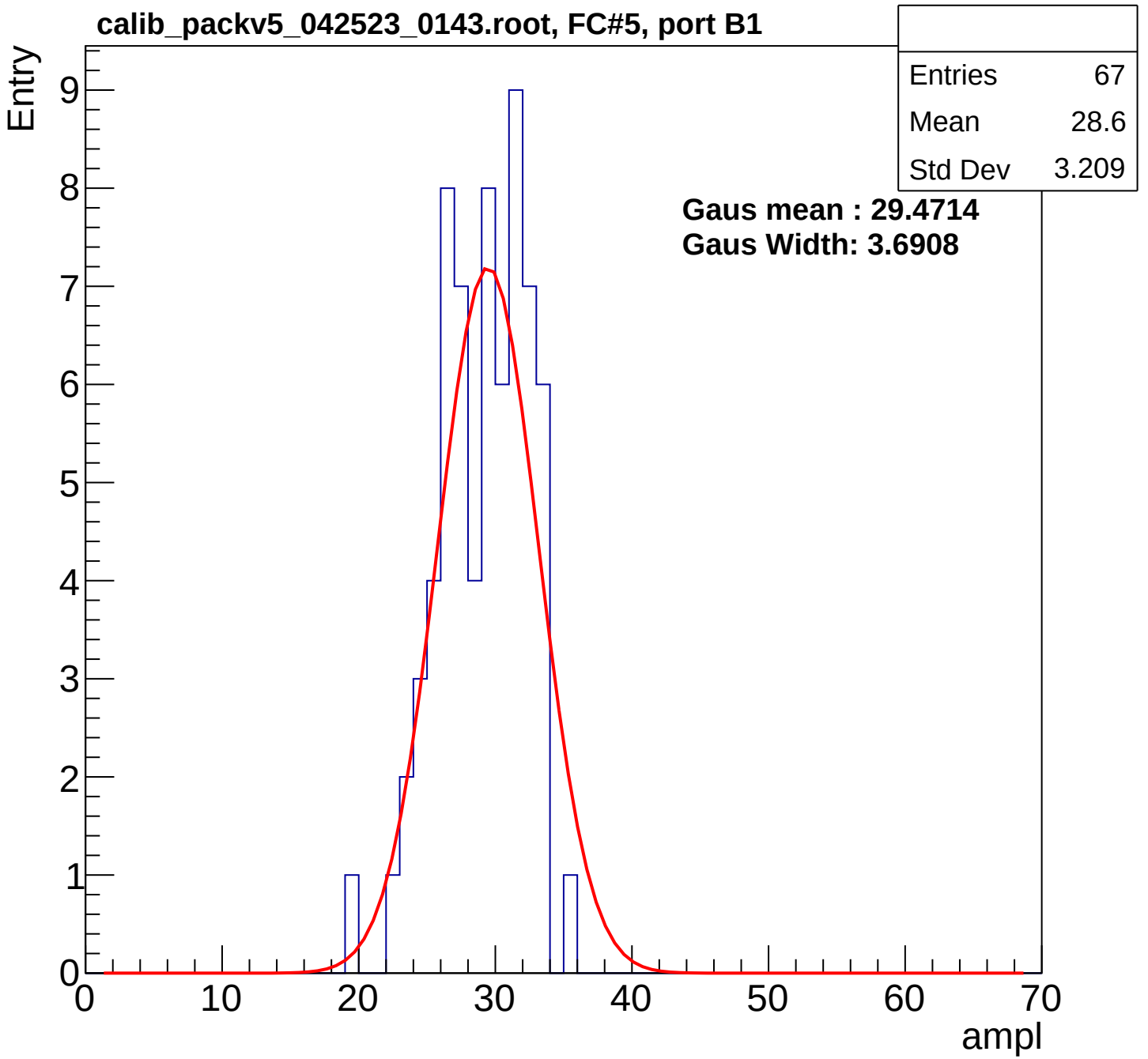
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.6
Std Dev 3.209
Gaus mean : 29.4714
Gaus Width: 3.6908
Entry
ampl
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3
4
5
6
7
8
9
0
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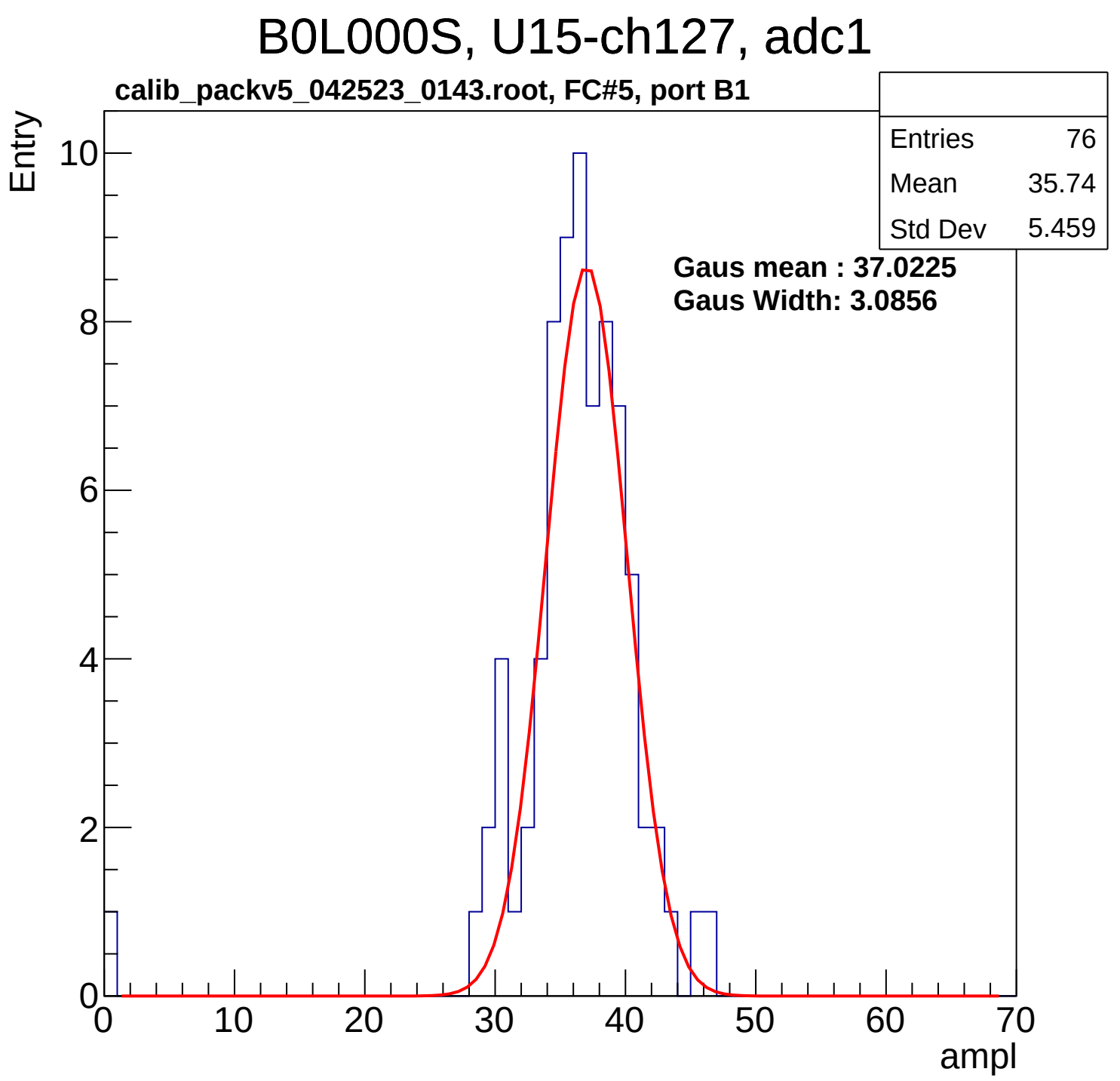
B0L000S, U15-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.74
Std Dev 5.459
Gaus mean : 37.0225
Gaus Width: 3.0856
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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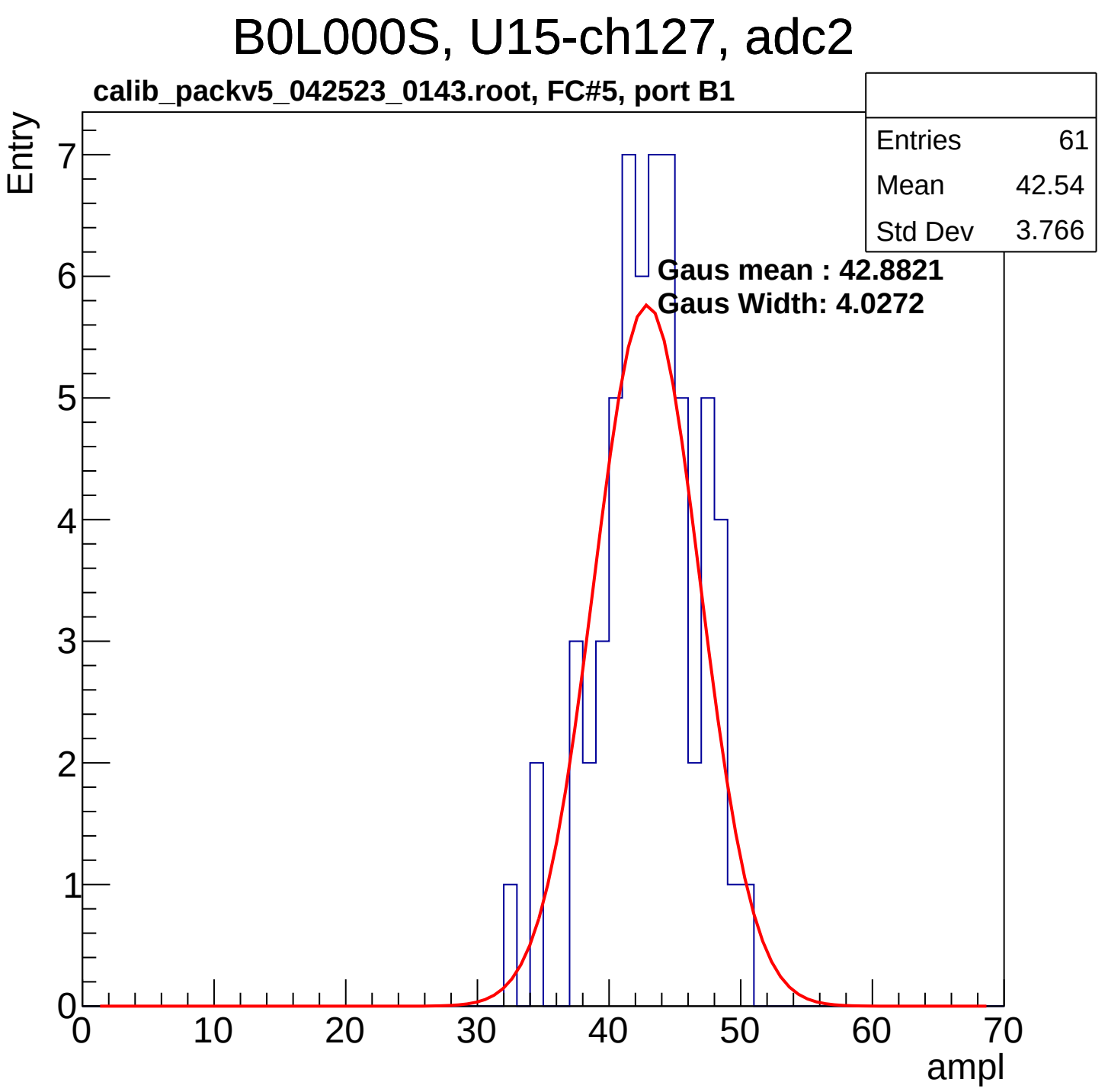
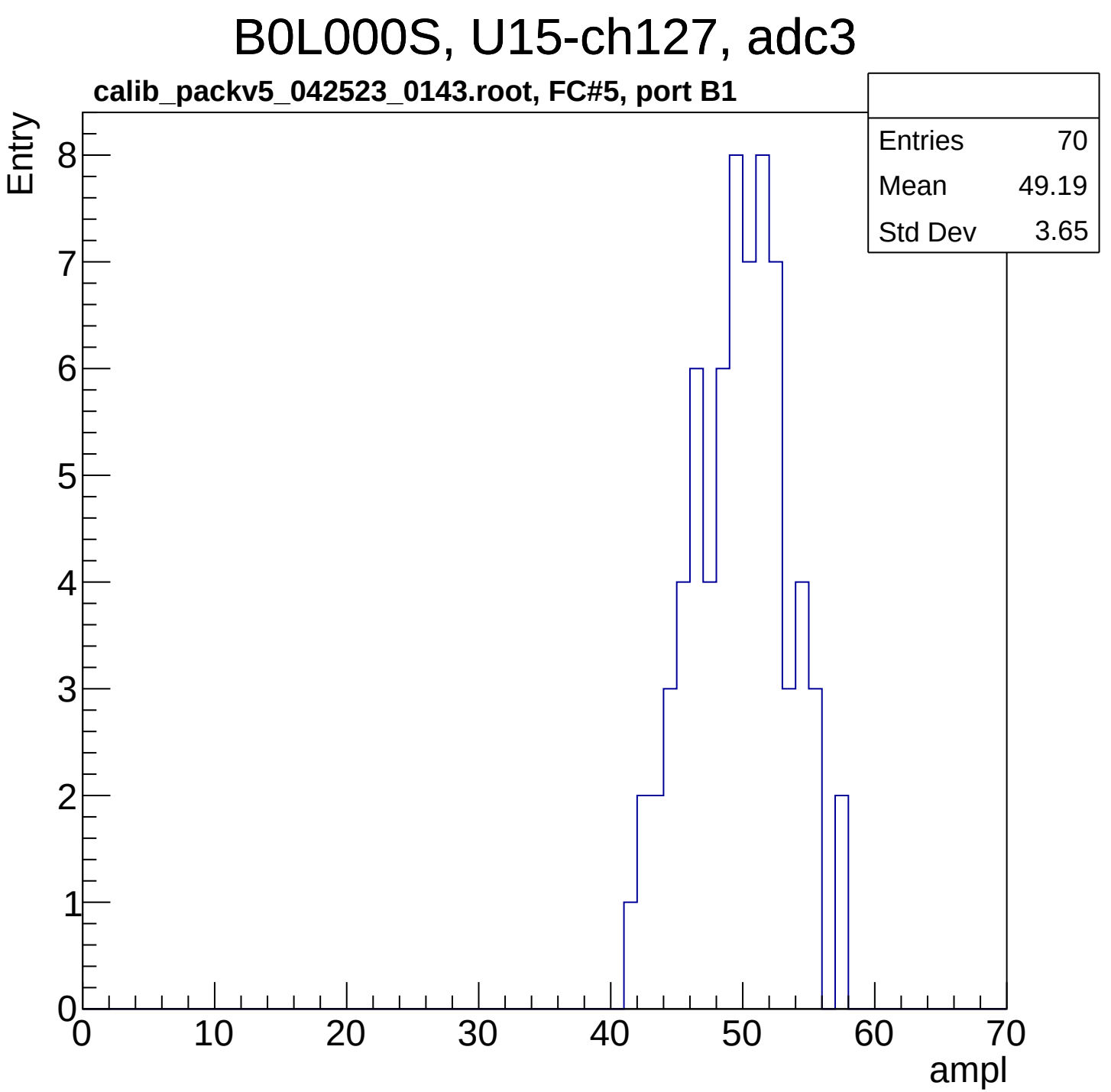
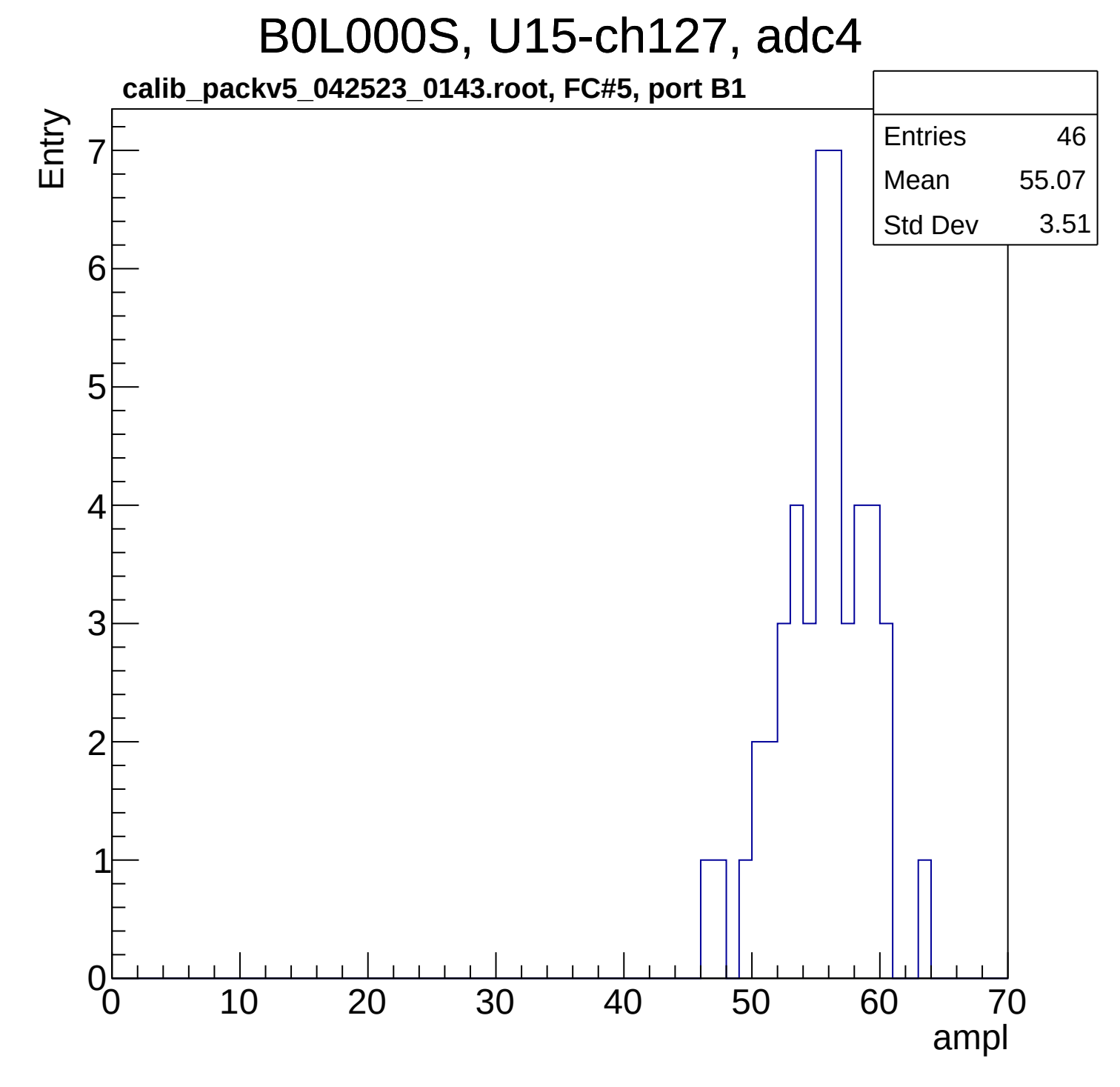
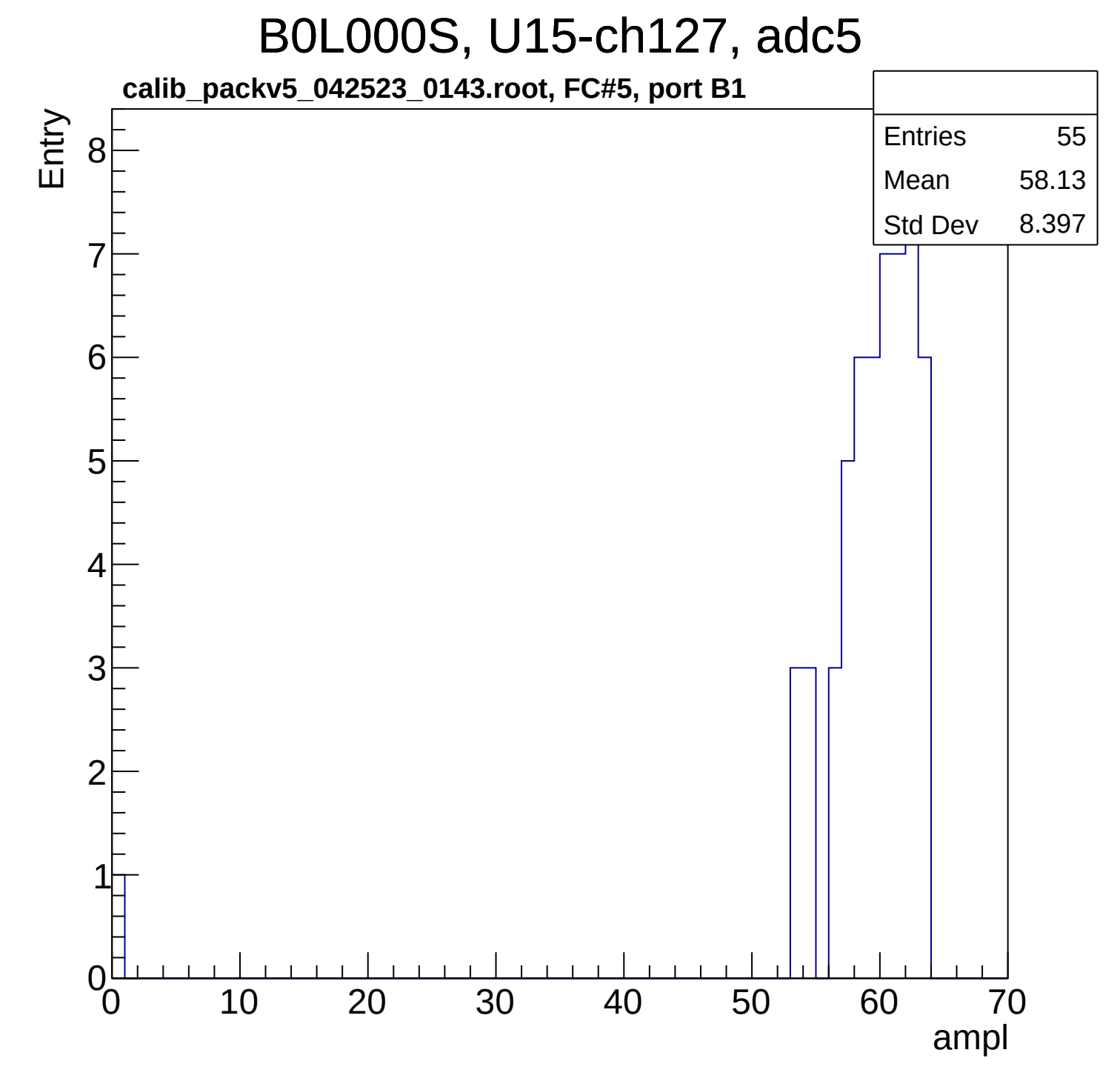
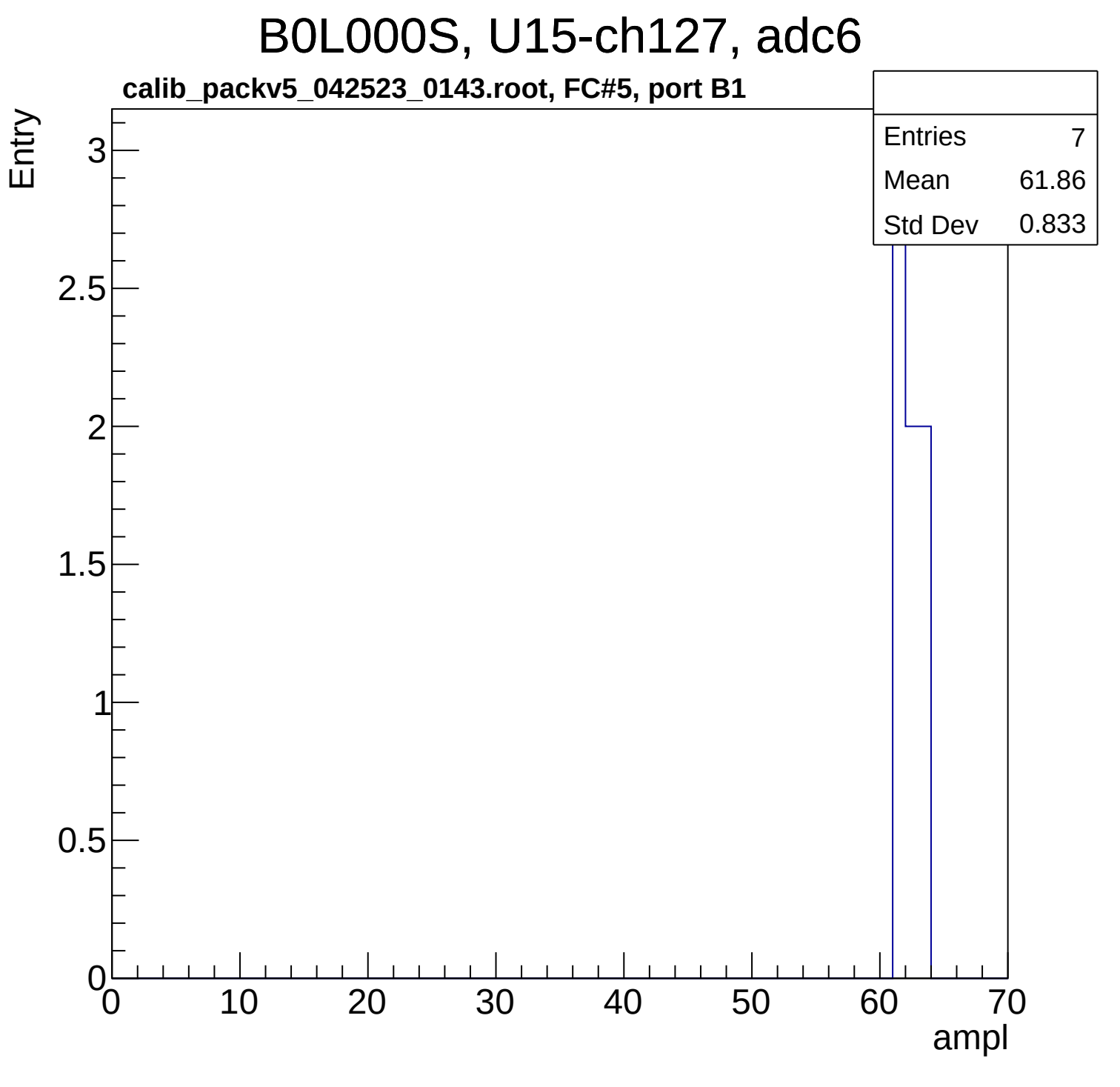
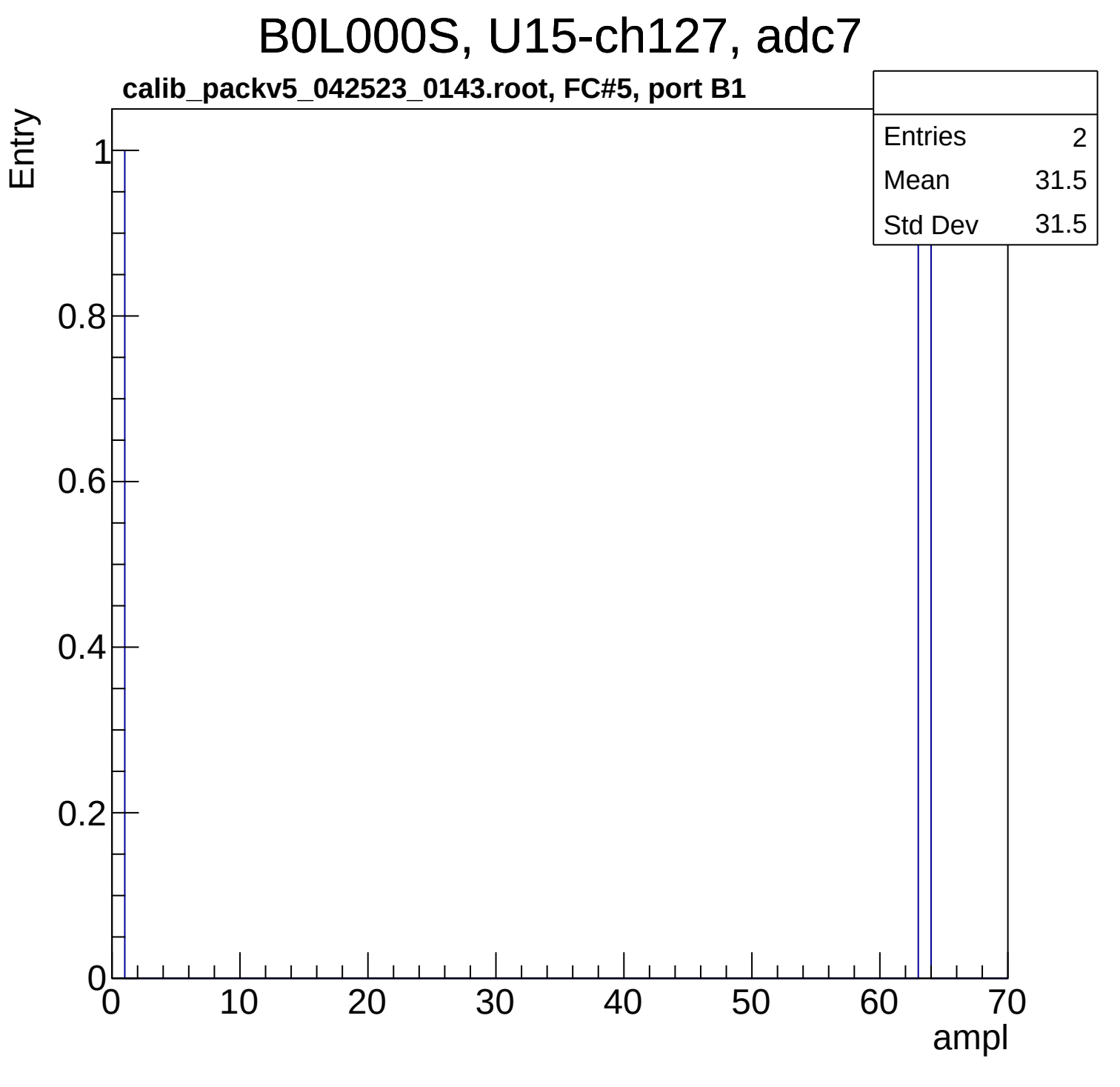

B0L000S, U15-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.54
Std Dev 3.766
Gaus mean : 42.8821
Gaus Width: 4.0272
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

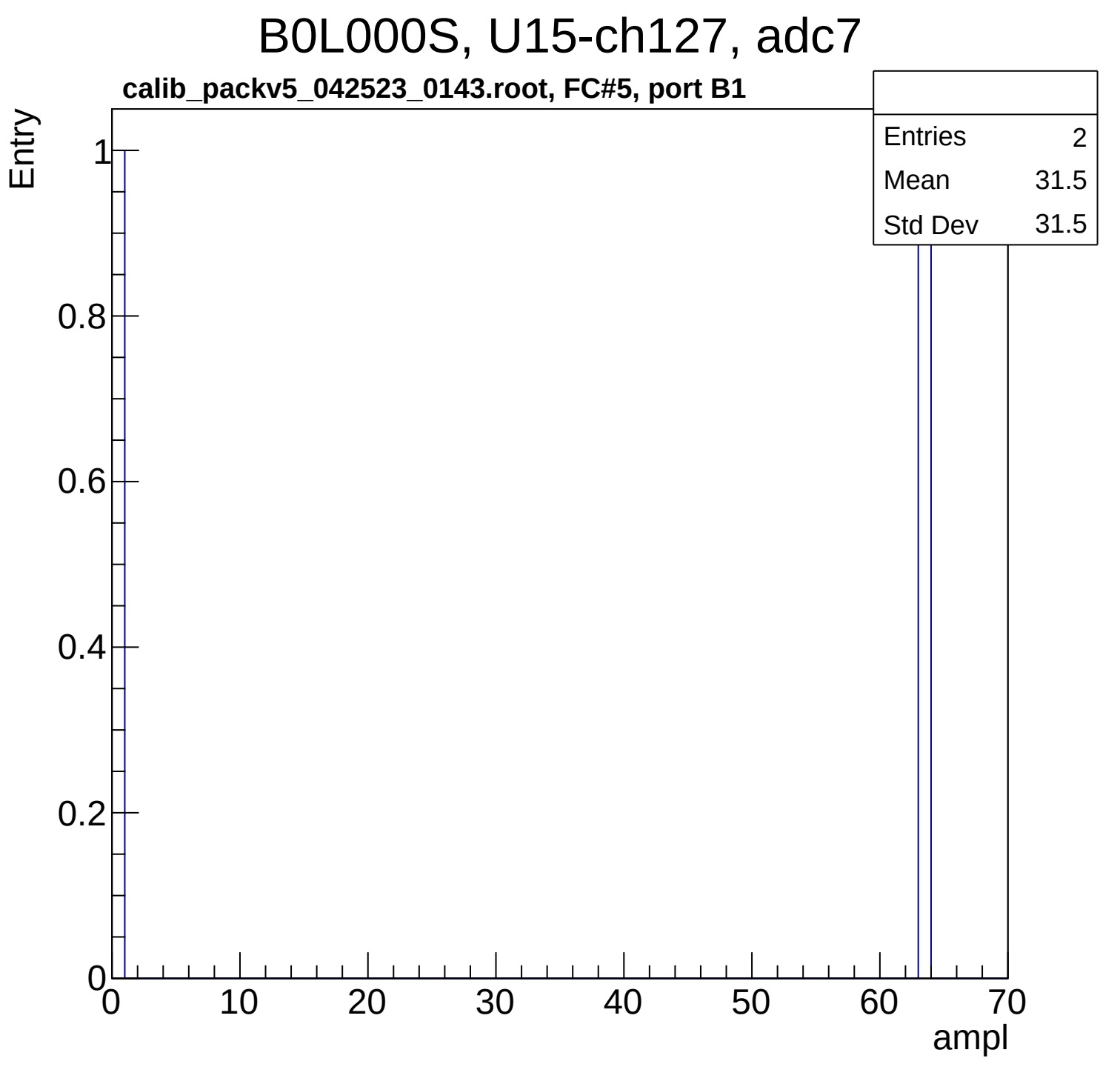

B0L000S, U15-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.19
Std Dev 3.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 55.07
Std Dev 3.51
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U15-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.13
Std Dev 8.397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U15-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.86
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70