

B0L000S, U25-ch0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.8004

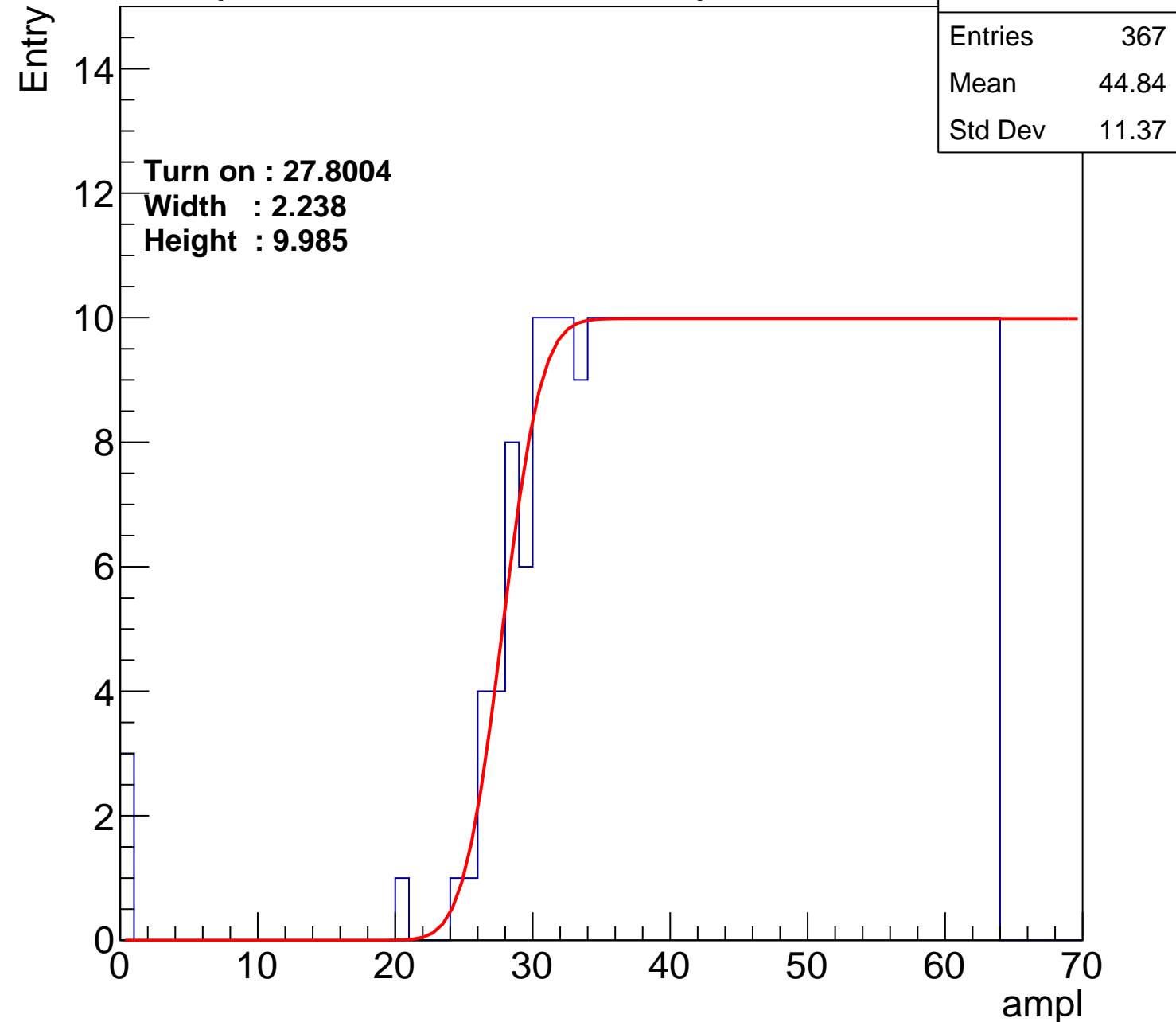
Width : 2.238

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	403
Mean	43.15
Std Dev	12.08

Turn on : 23.9351

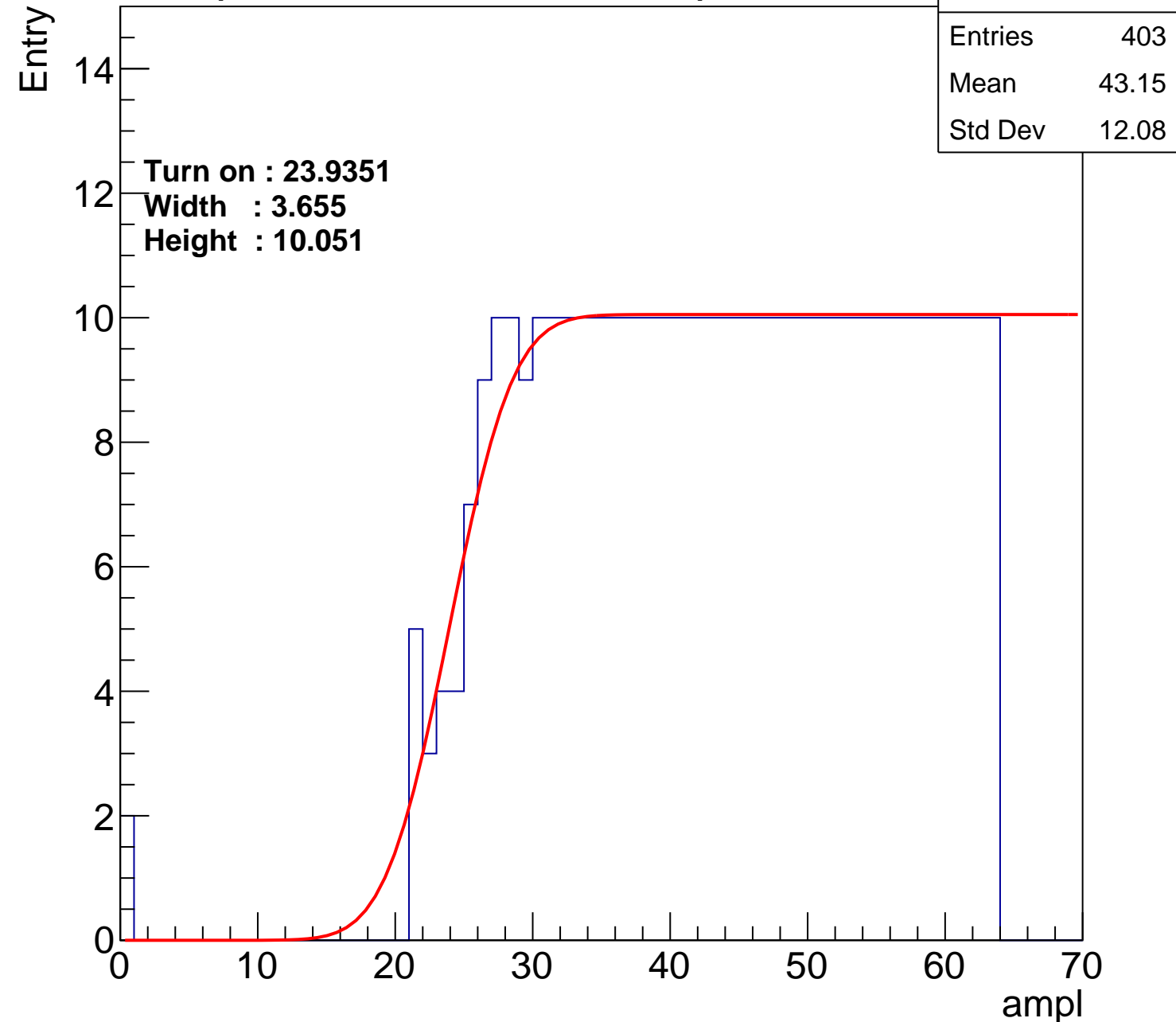
Width : 3.655

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch2

calib_packv5_042523_0143.root, FC#5, port B1

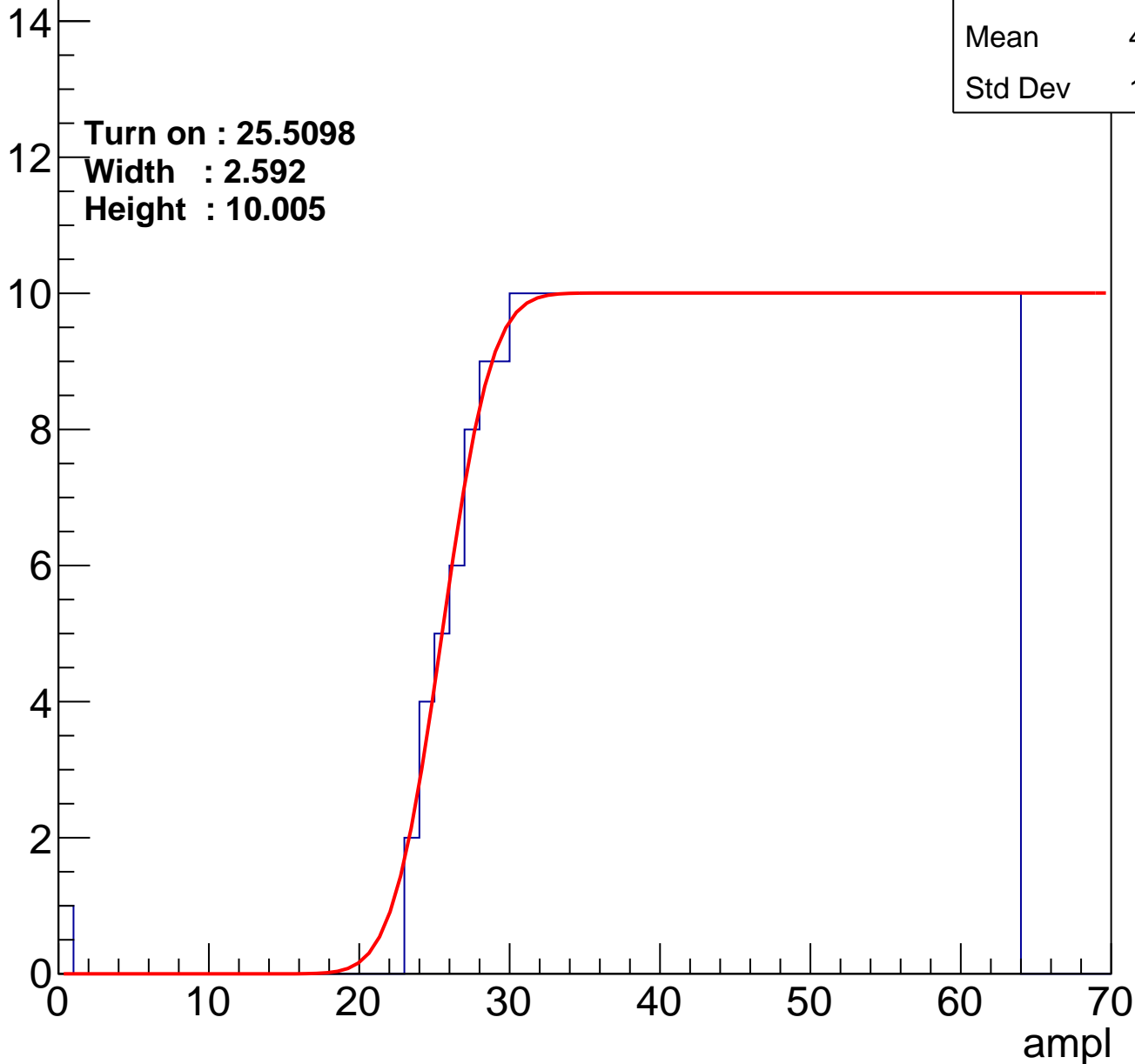
Entry

Entries	384
Mean	44.17
Std Dev	11.37

Turn on : 25.5098

Width : 2.592

Height : 10.005



B0L000S, U25-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 26.9979

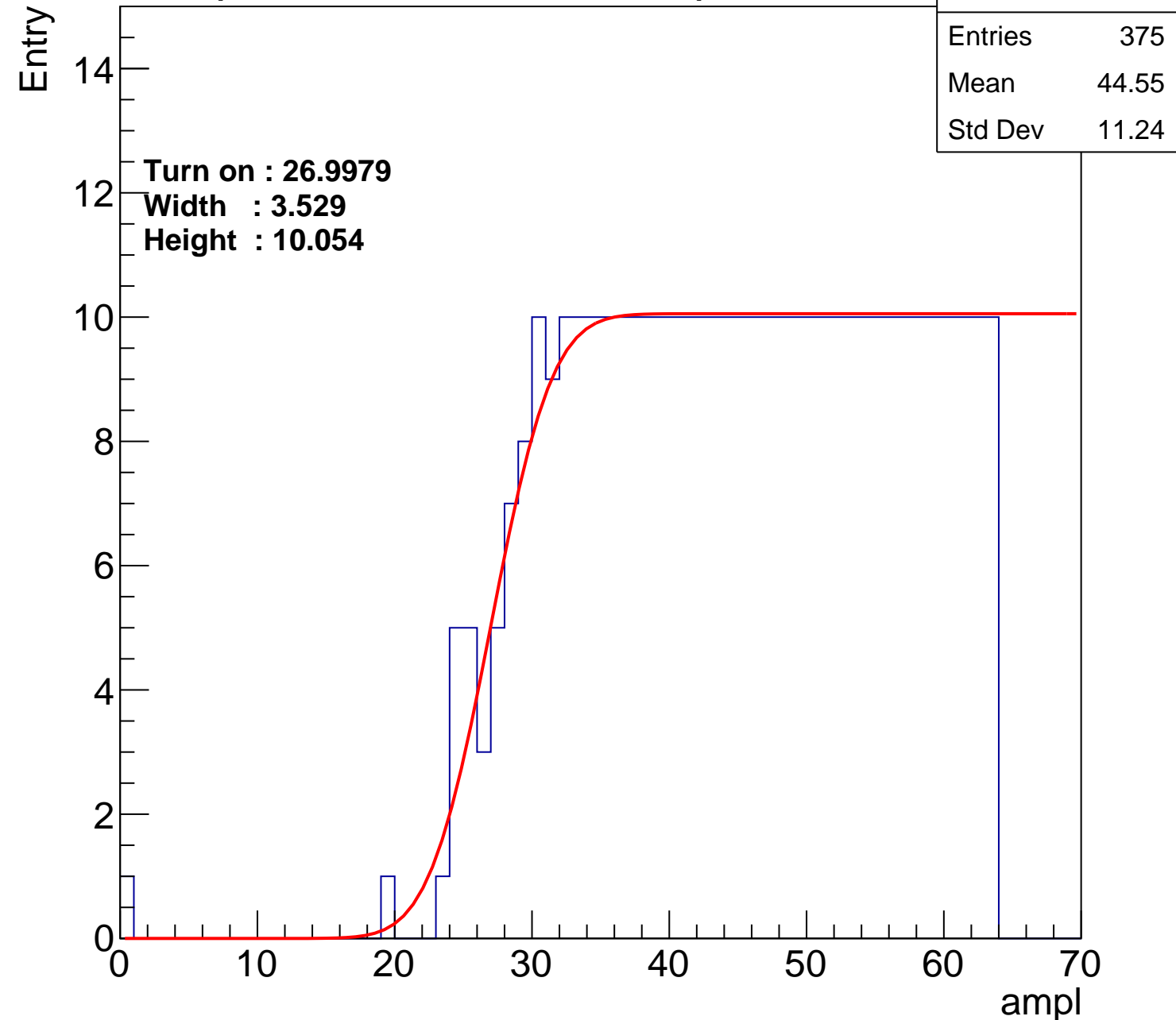
Width : 3.529

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch4

calib_packv5_042523_0143.root, FC#5, port B1

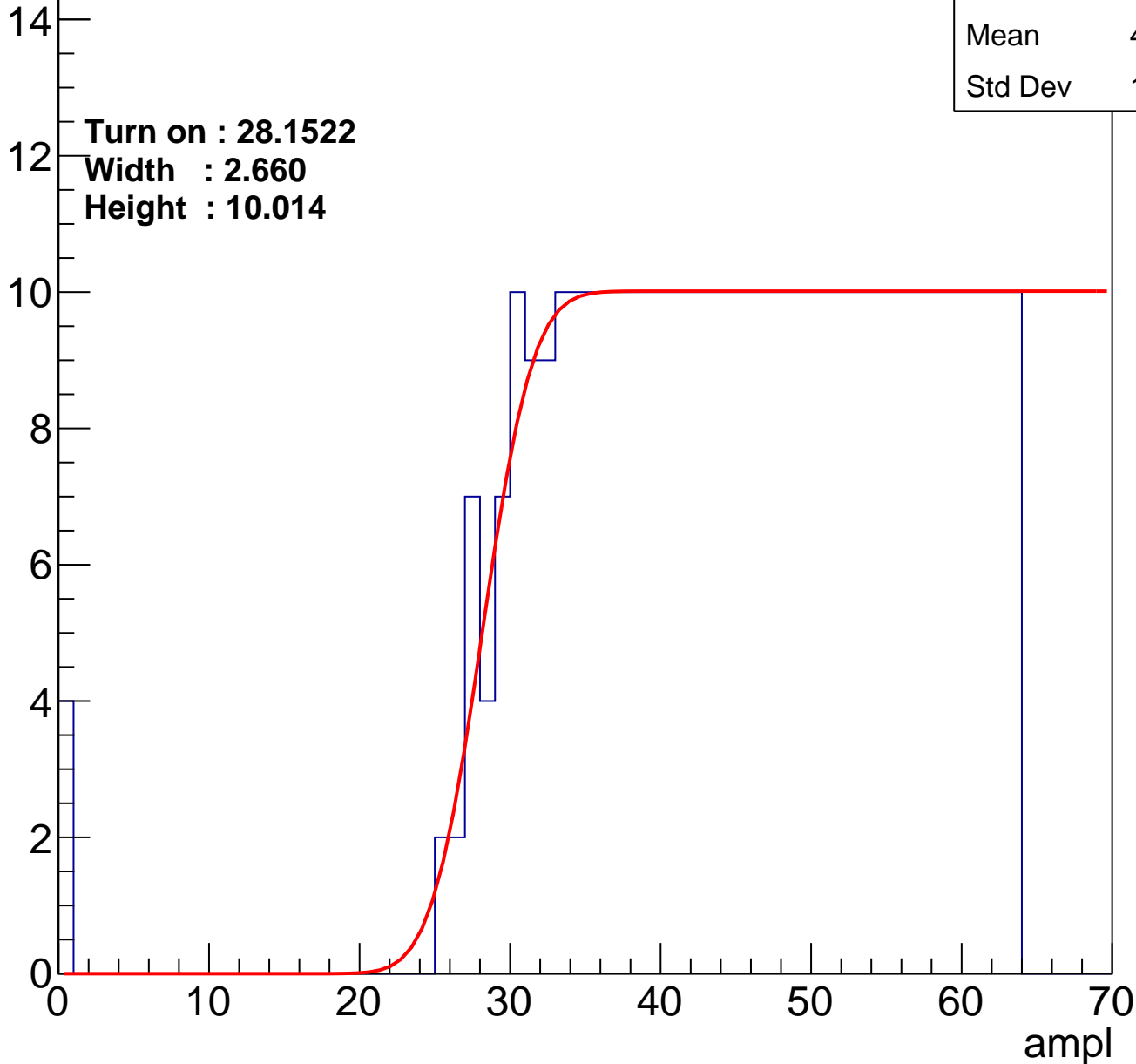
Entry

Entries	364
Mean	44.93
Std Dev	11.48

Turn on : 28.1522

Width : 2.660

Height : 10.014



B0L000S, U25-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.3363

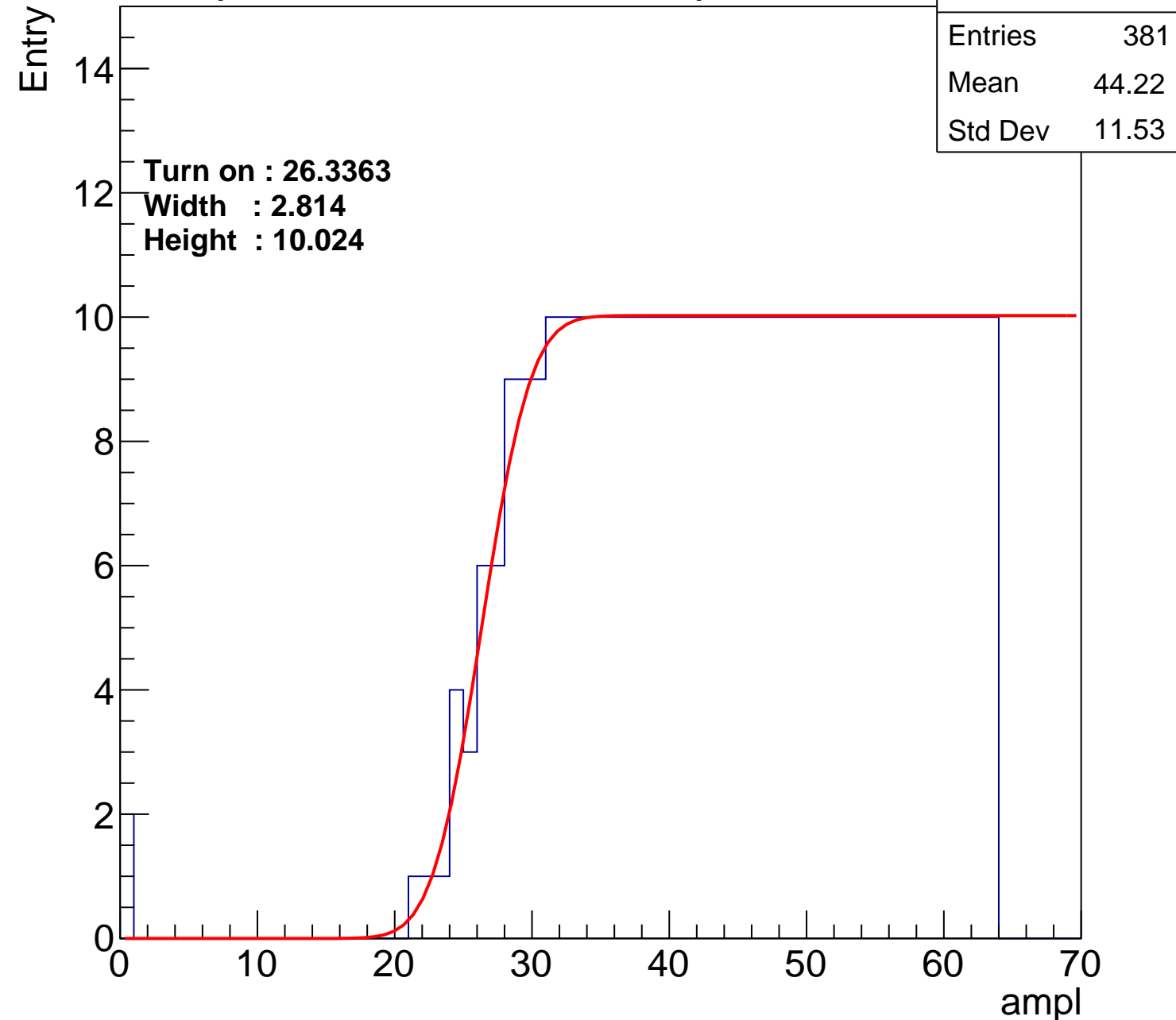
Width : 2.814

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.77
Std Dev	11.05

Turn on : 27.1146

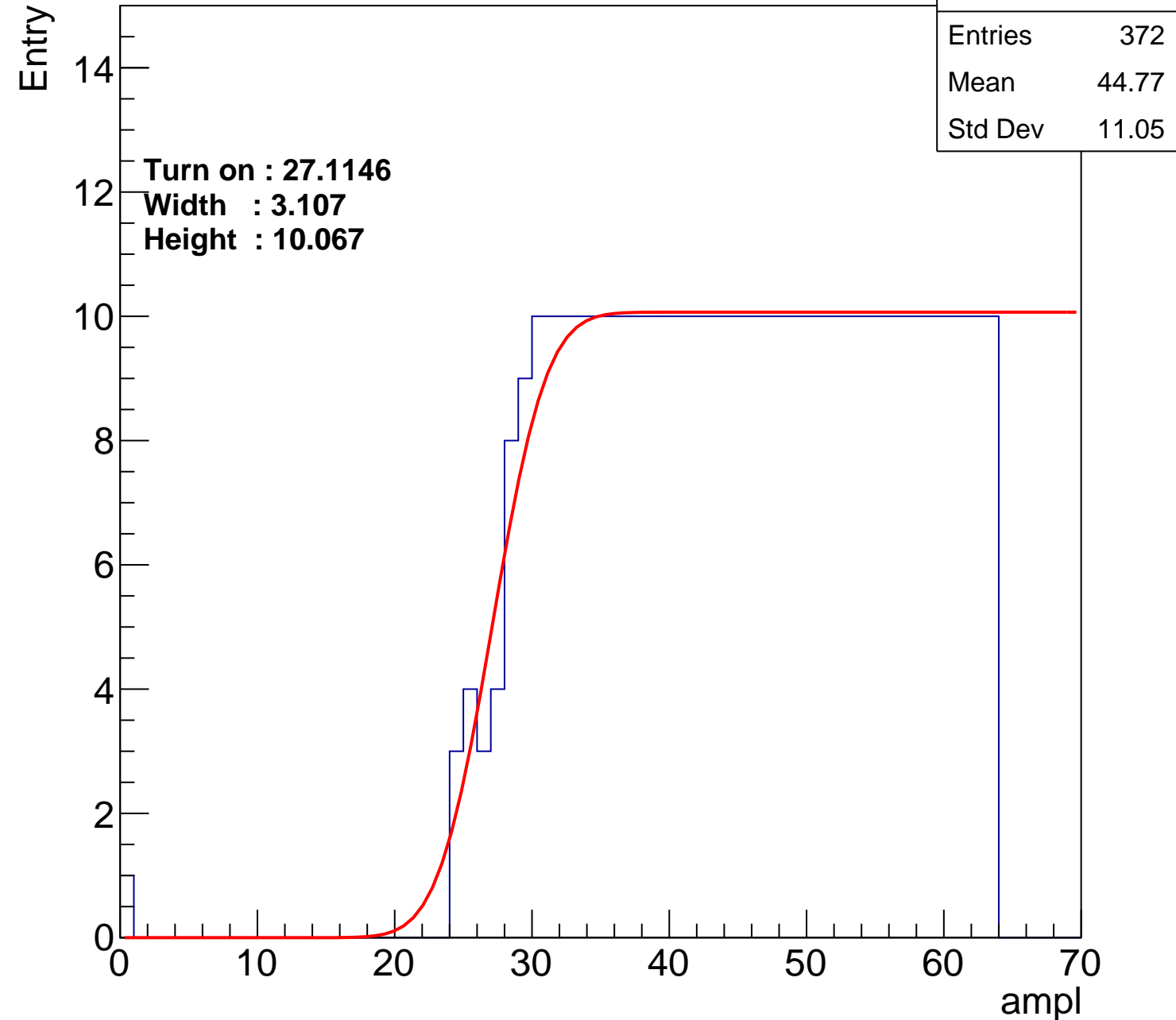
Width : 3.107

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.76
Std Dev	10.55

Turn on : 28.9885

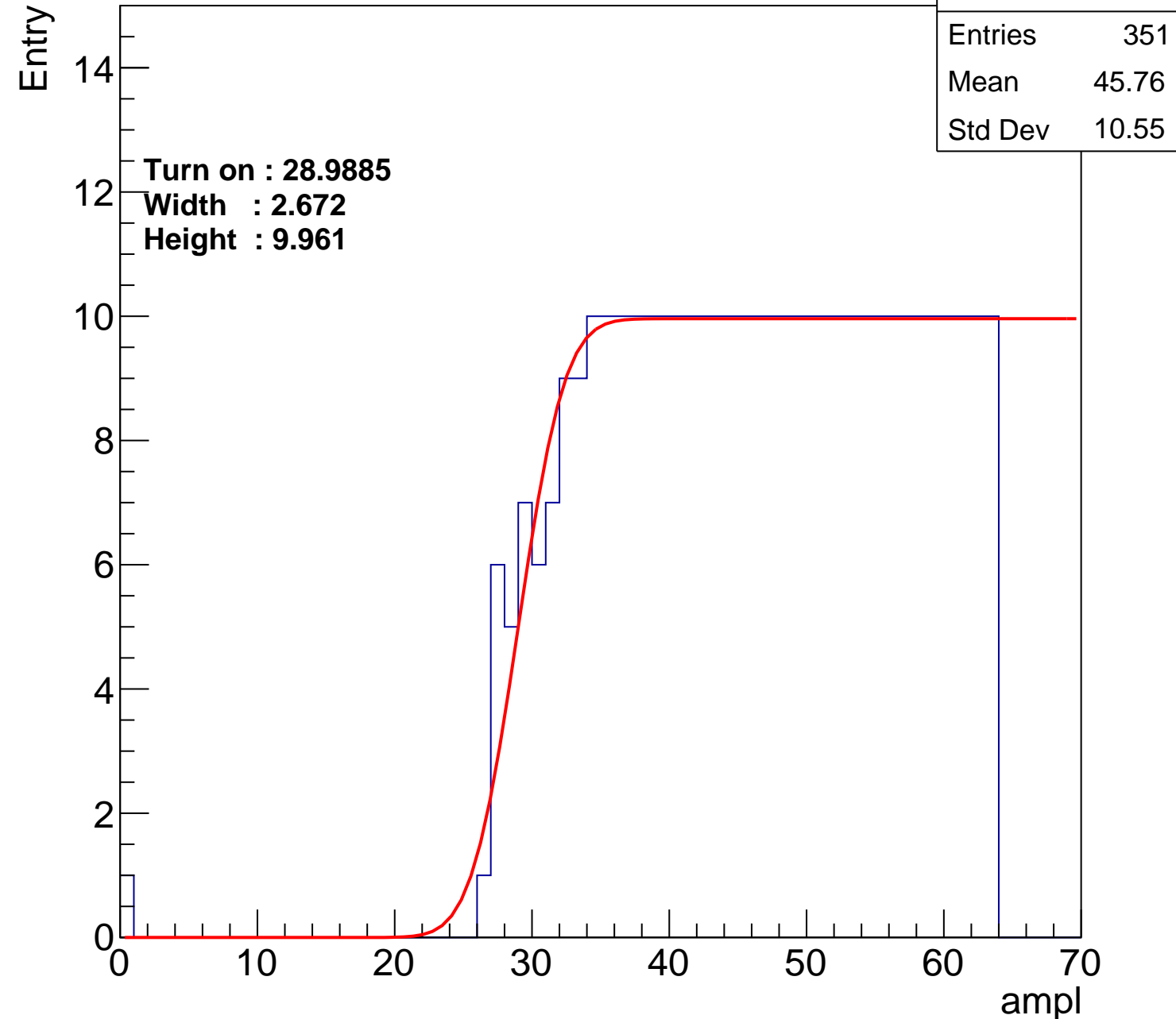
Width : 2.672

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.61
Std Dev	10.88

Turn on : 29.5150

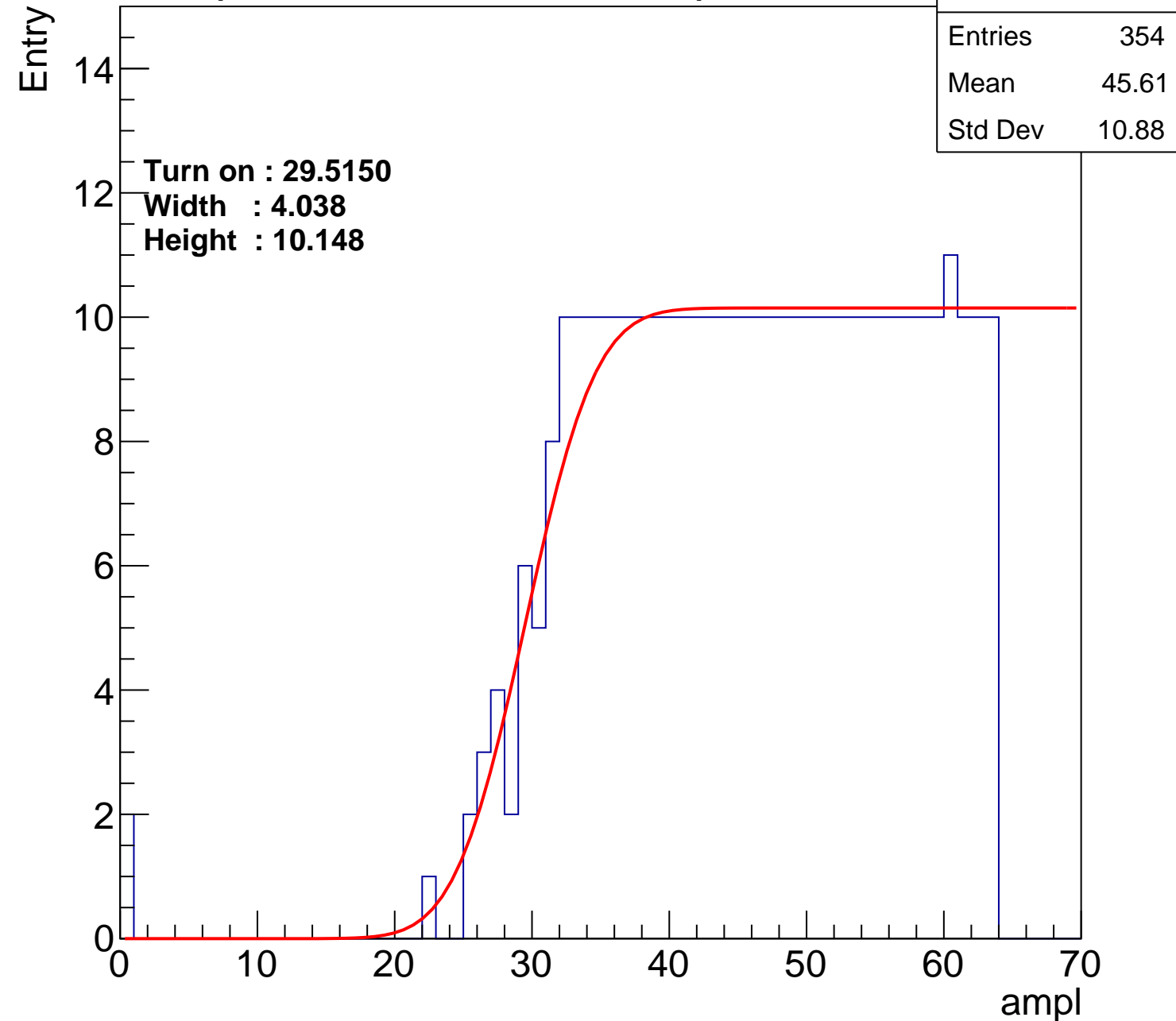
Width : 4.038

Height : 10.148

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 27.0559

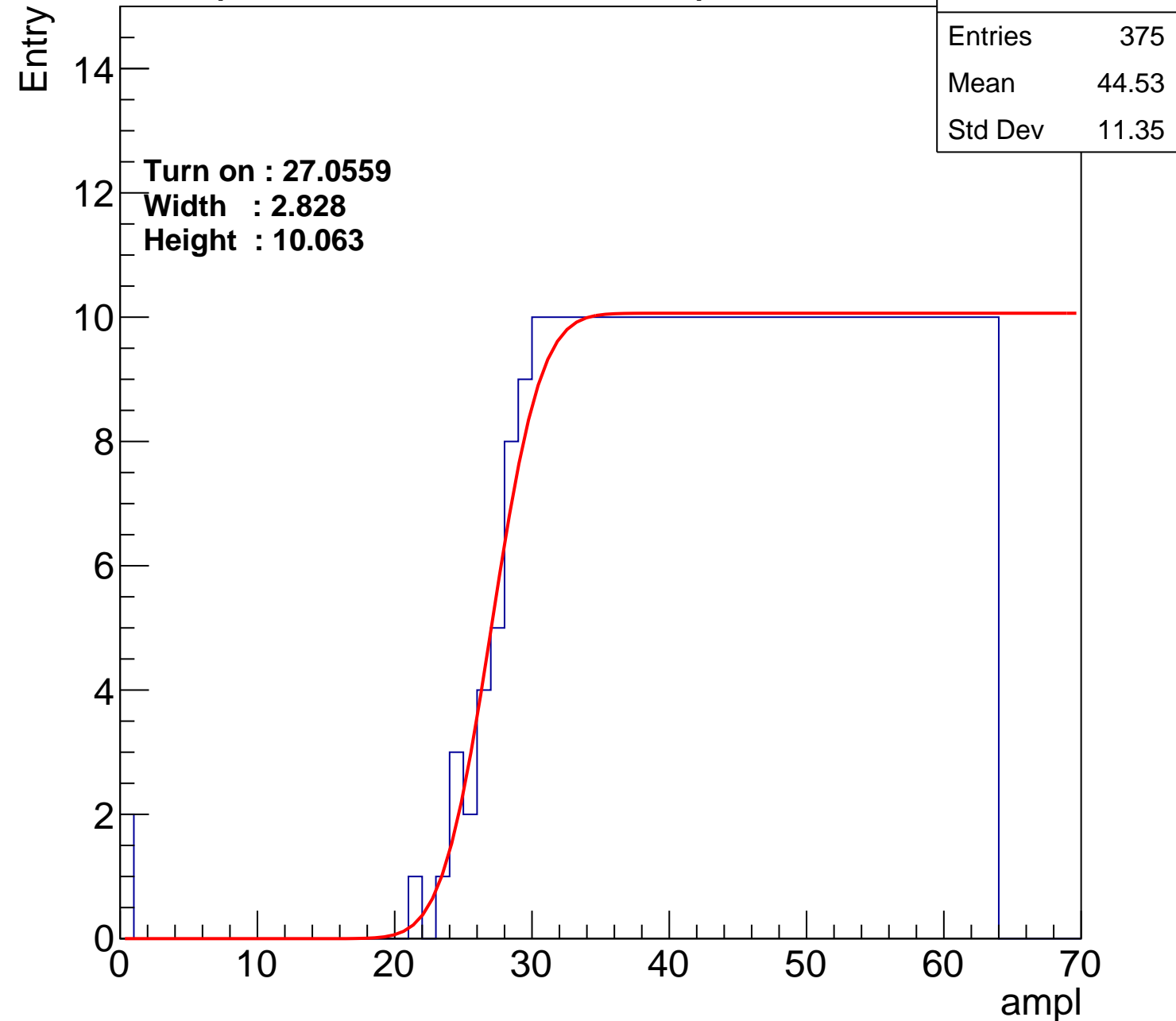
Width : 2.828

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch10

calib_packv5_042523_0143.root, FC#5, port B1

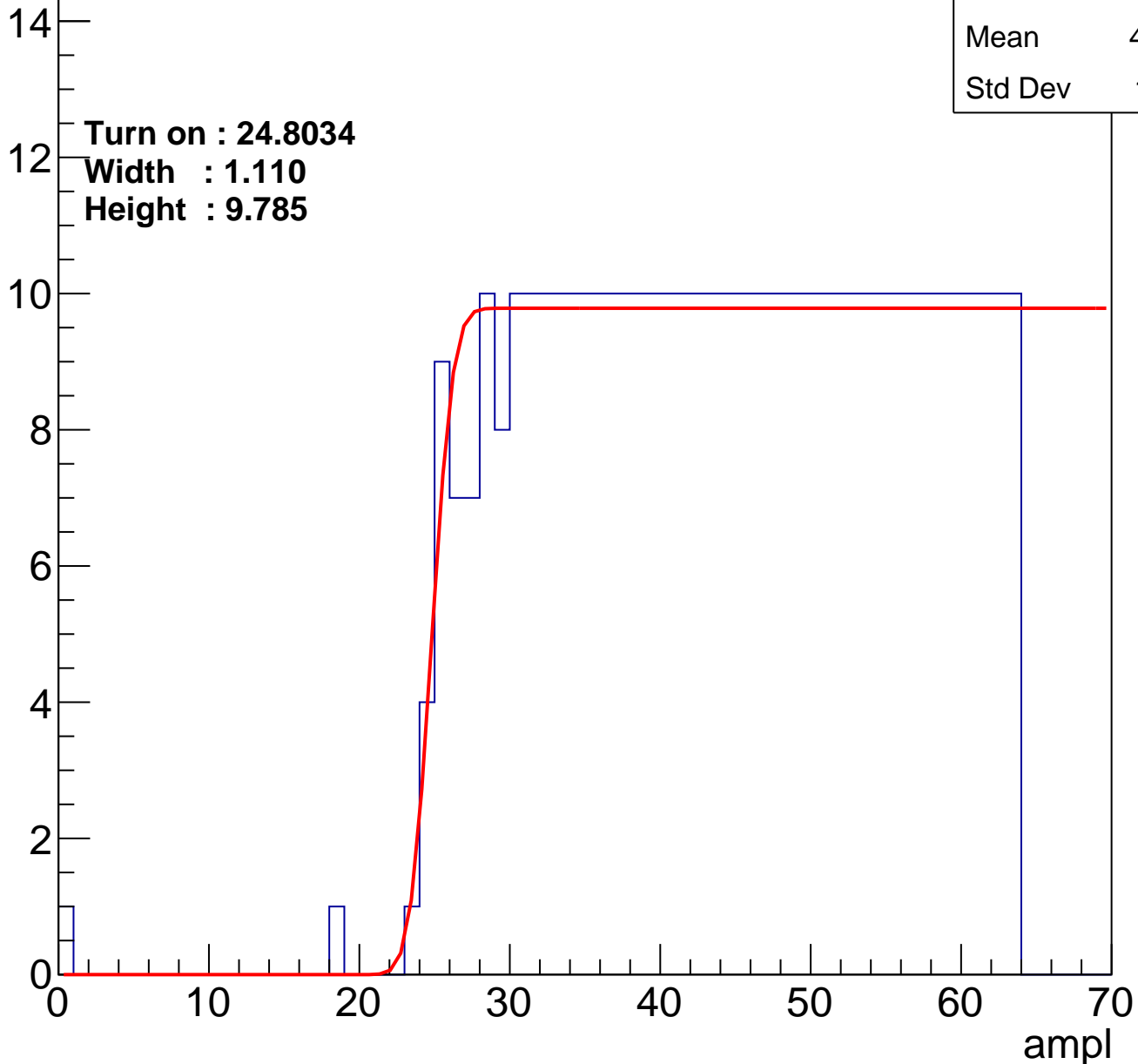
Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 24.8034

Width : 1.110

Height : 9.785

Entry



B0L000S, U25-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.3992

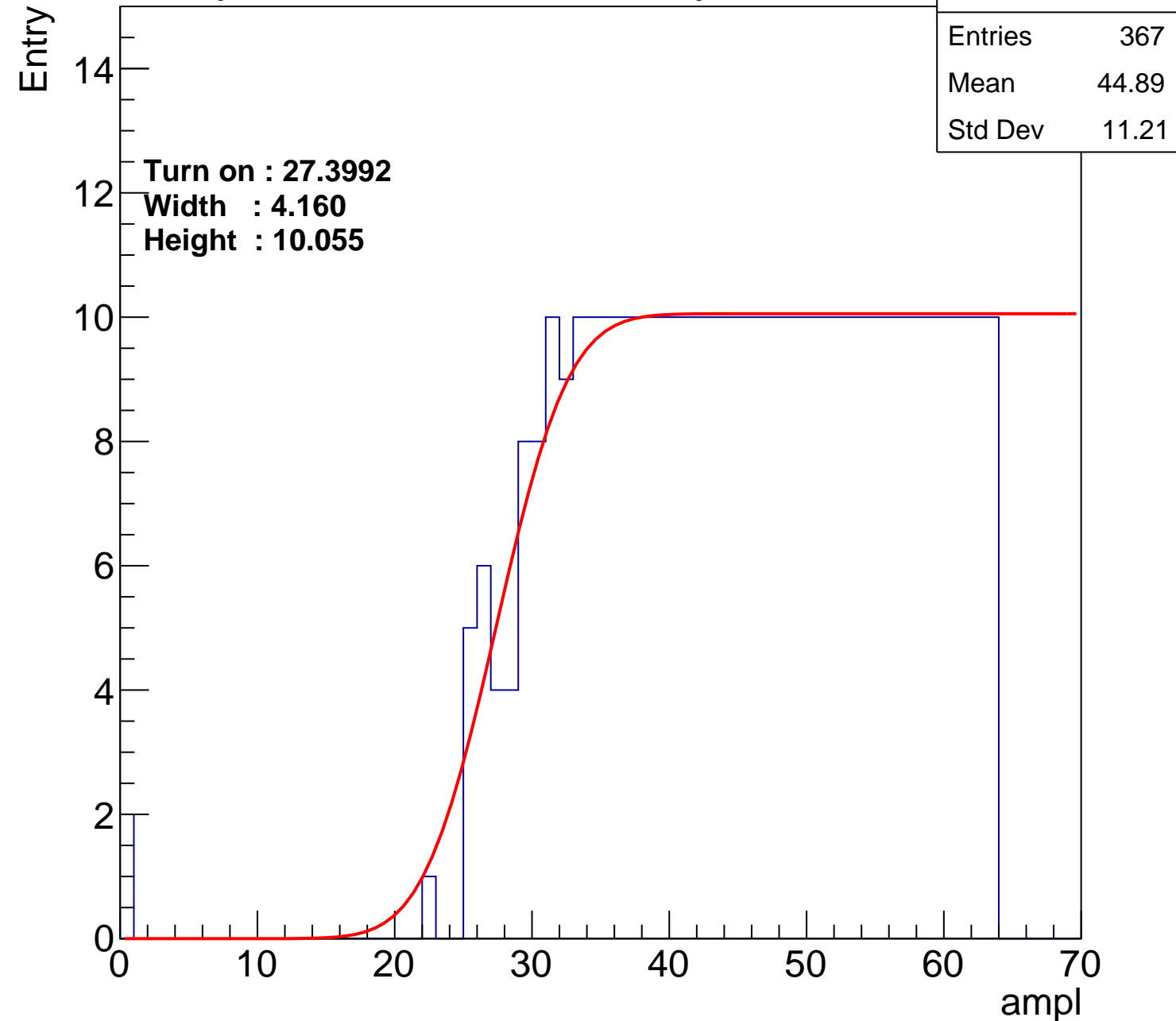
Width : 4.160

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	44.04
Std Dev	11.6

Turn on : 26.0289

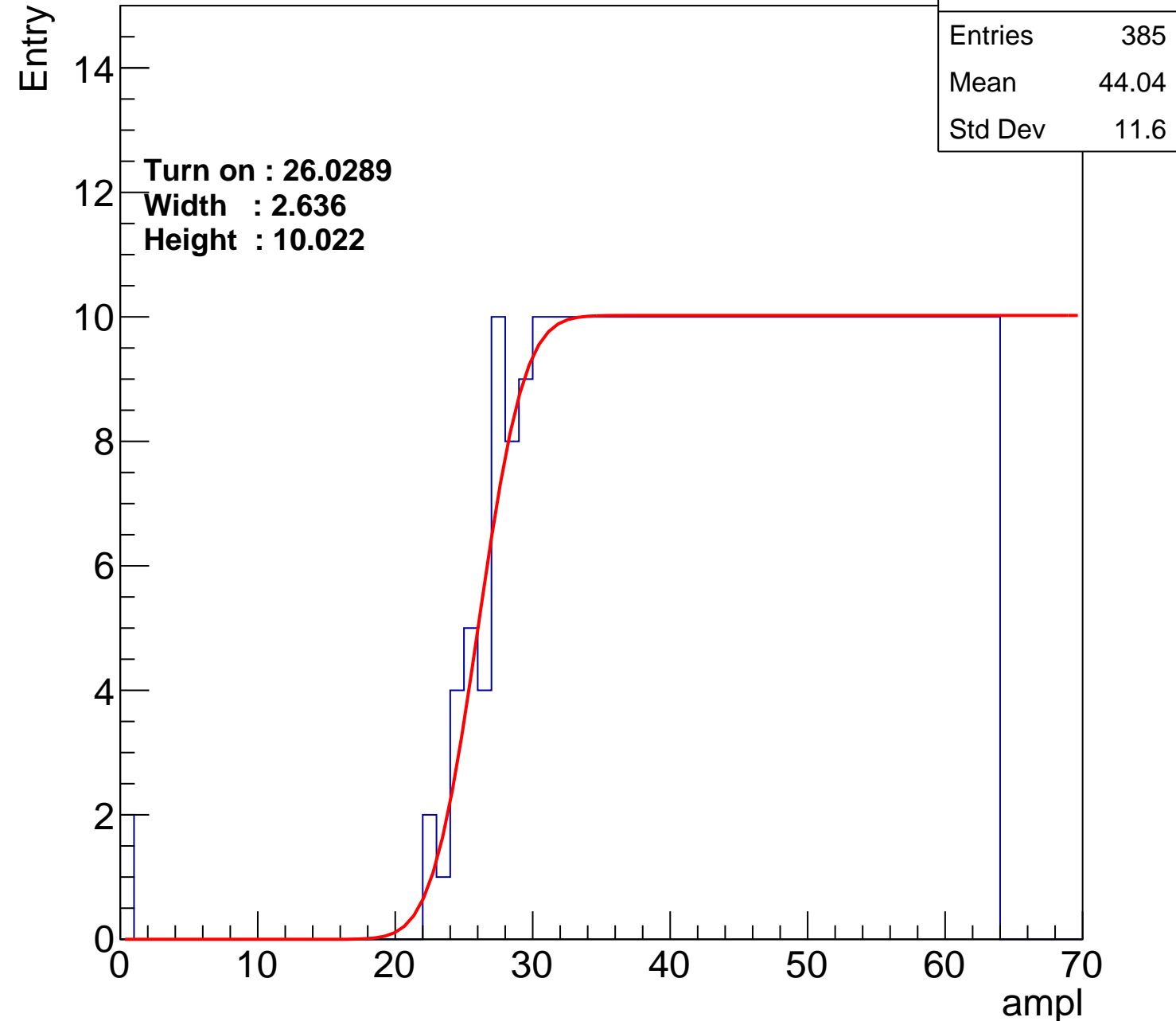
Width : 2.636

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl

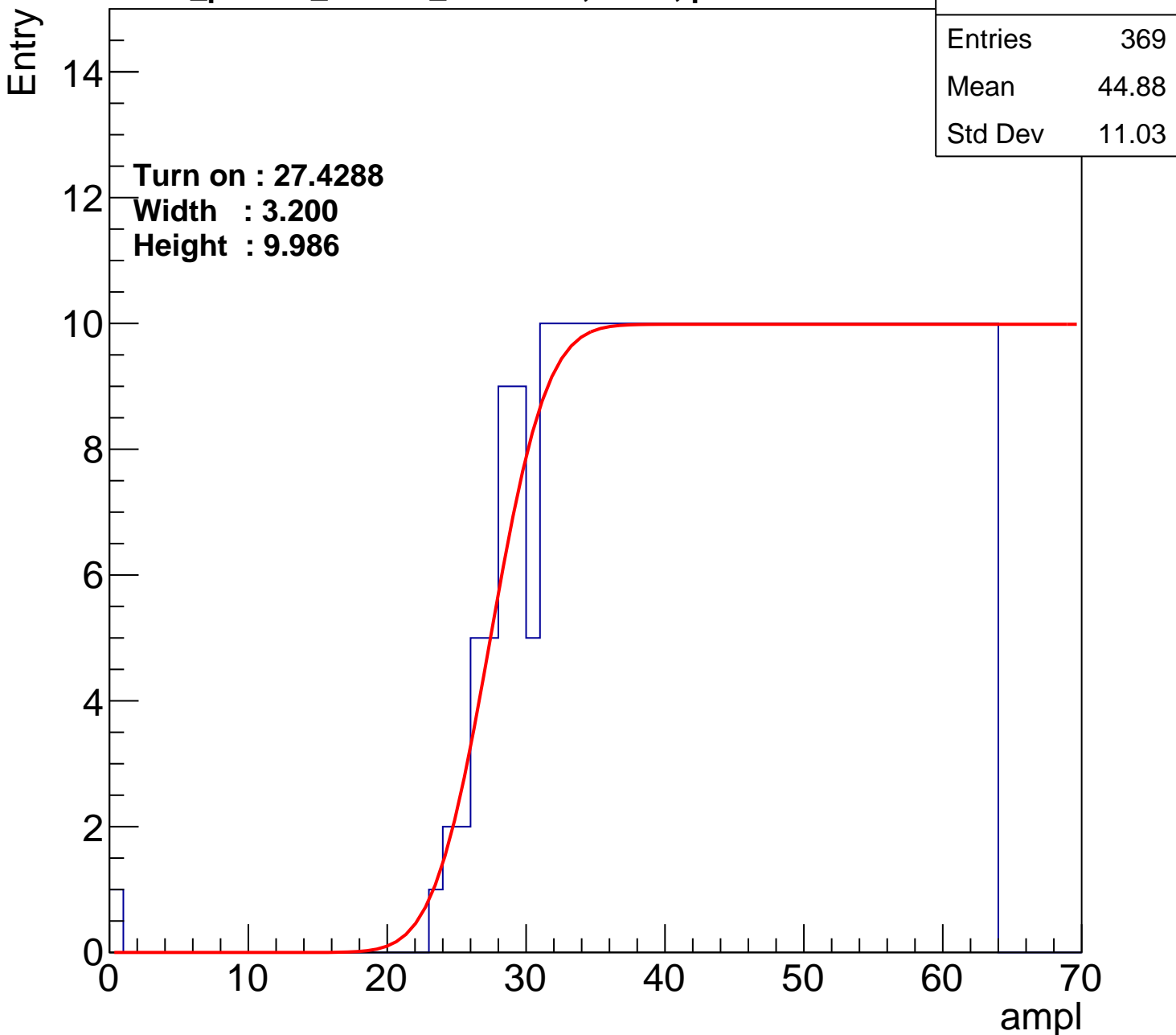


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.88
Std Dev	11.03

Height : 9.986



B0L000S, U25-ch14

calib_packv5_042523_0143.root, FC#5, port B1

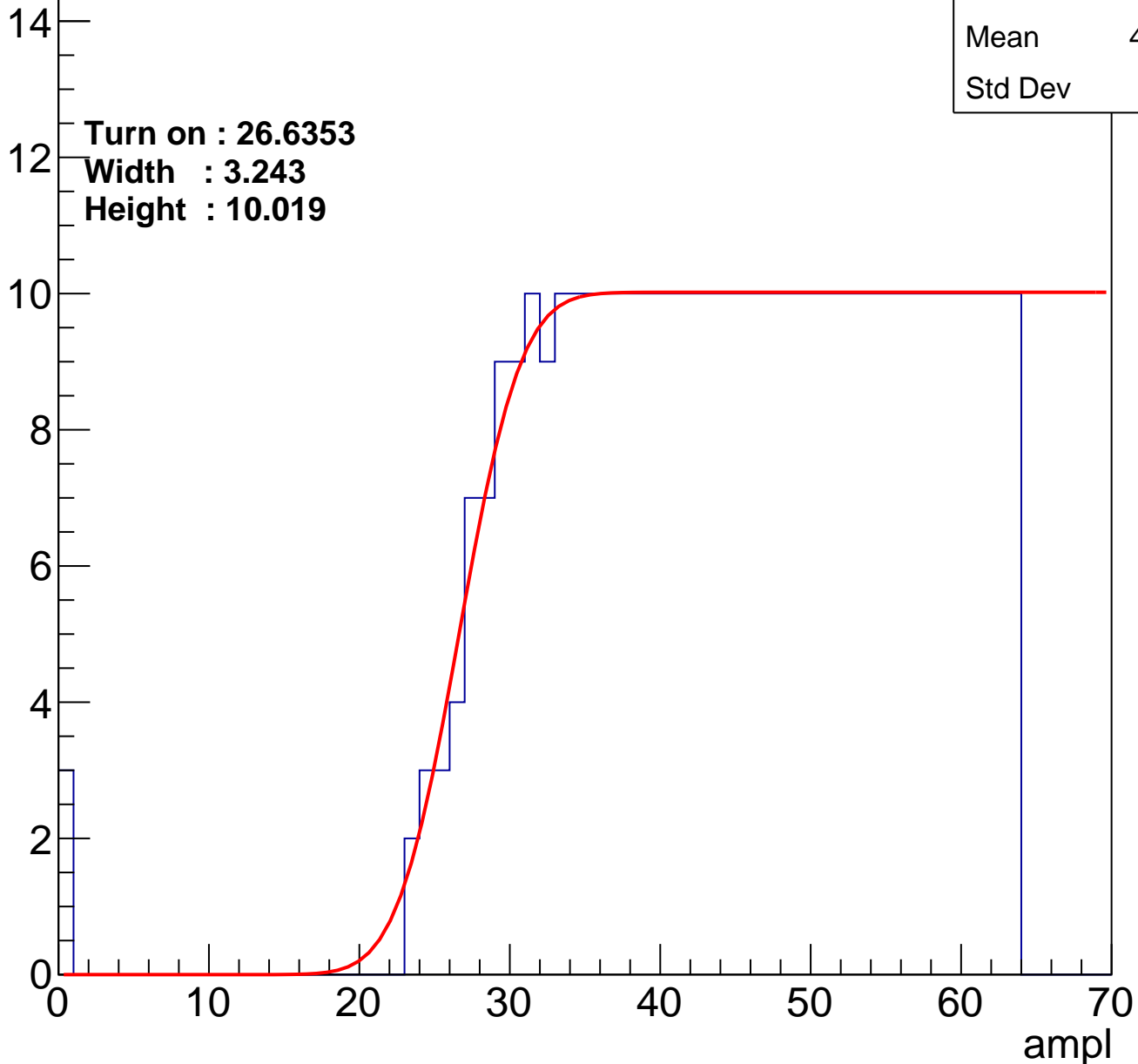
Entries	376
Mean	44.39
Std Dev	11.6

Turn on : 26.6353

Width : 3.243

Height : 10.019

Entry



B0L000S, U25-ch15

calib_packv5_042523_0143.root, FC#5, port B1

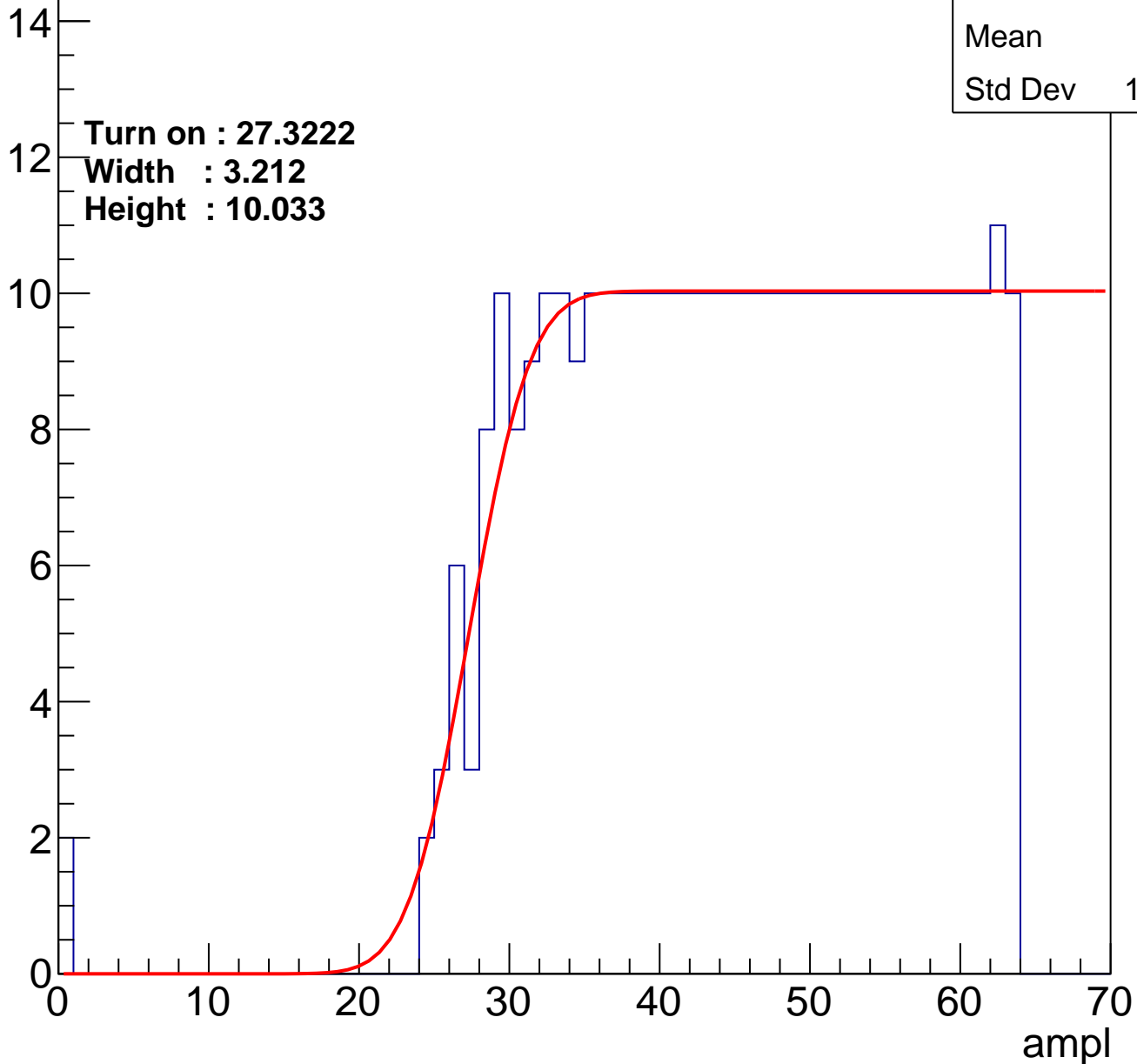
Entries	371
Mean	44.8
Std Dev	11.27

Turn on : 27.3222

Width : 3.212

Height : 10.033

Entry



B0L000S, U25-ch16

calib_packv5_042523_0143.root, FC#5, port B1

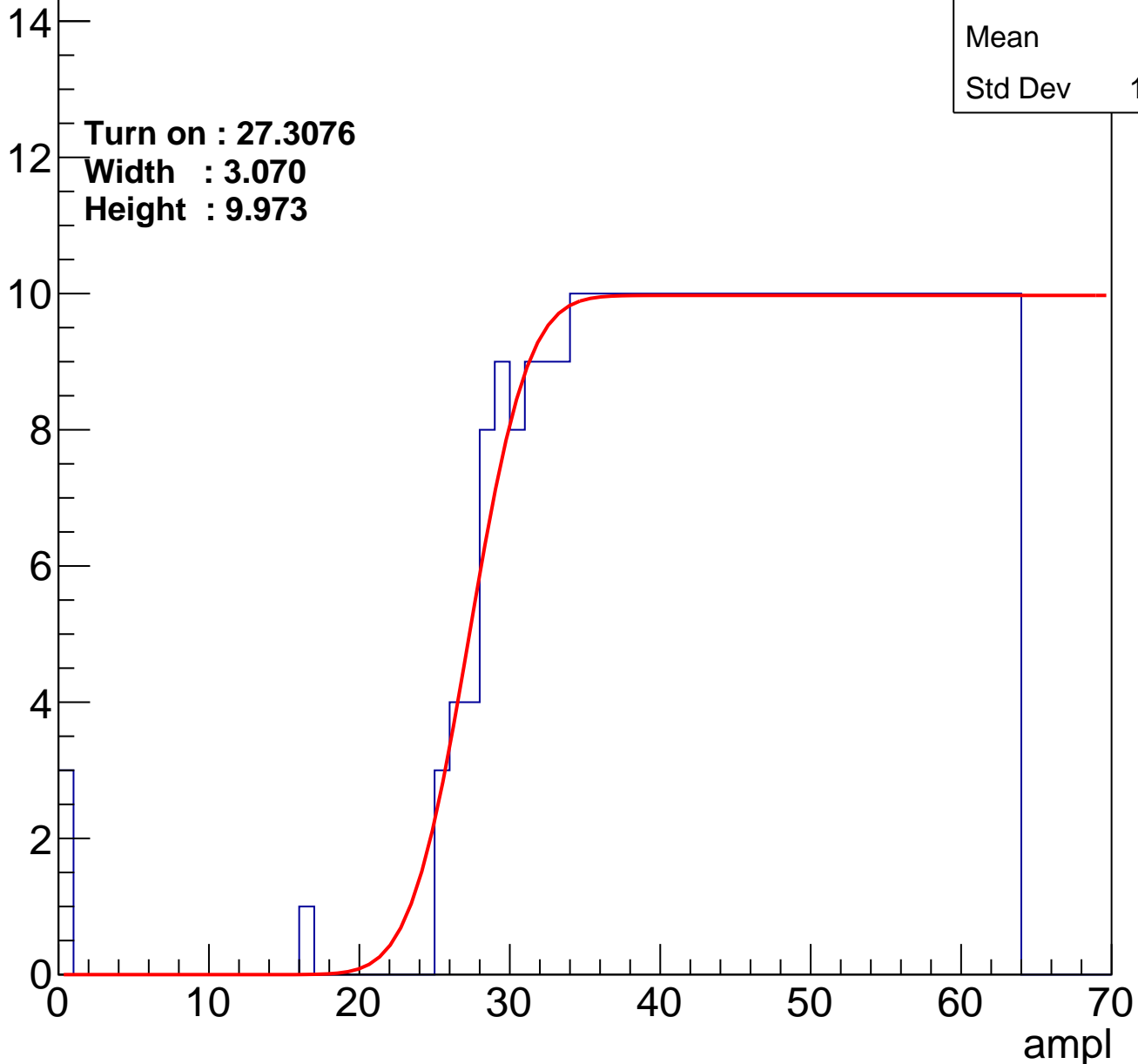
Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 27.3076

Width : 3.070

Height : 9.973

Entry



B0L000S, U25-ch17

calib_packv5_042523_0143.root, FC#5, port B1

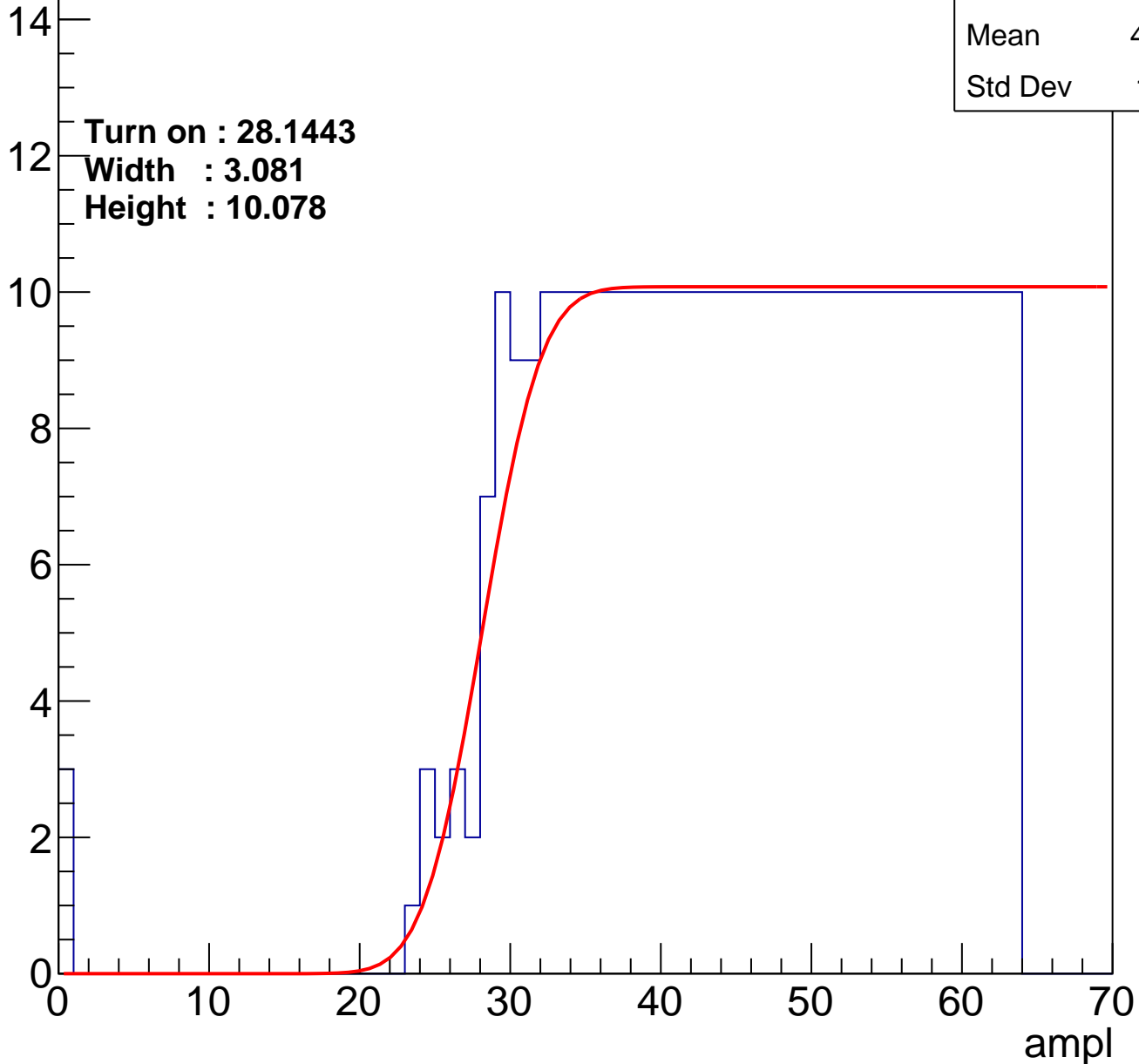
Entries	369
Mean	44.75
Std Dev	11.41

Turn on : 28.1443

Width : 3.081

Height : 10.078

Entry



B0L000S, U25-ch18

calib_packv5_042523_0143.root, FC#5, port B1

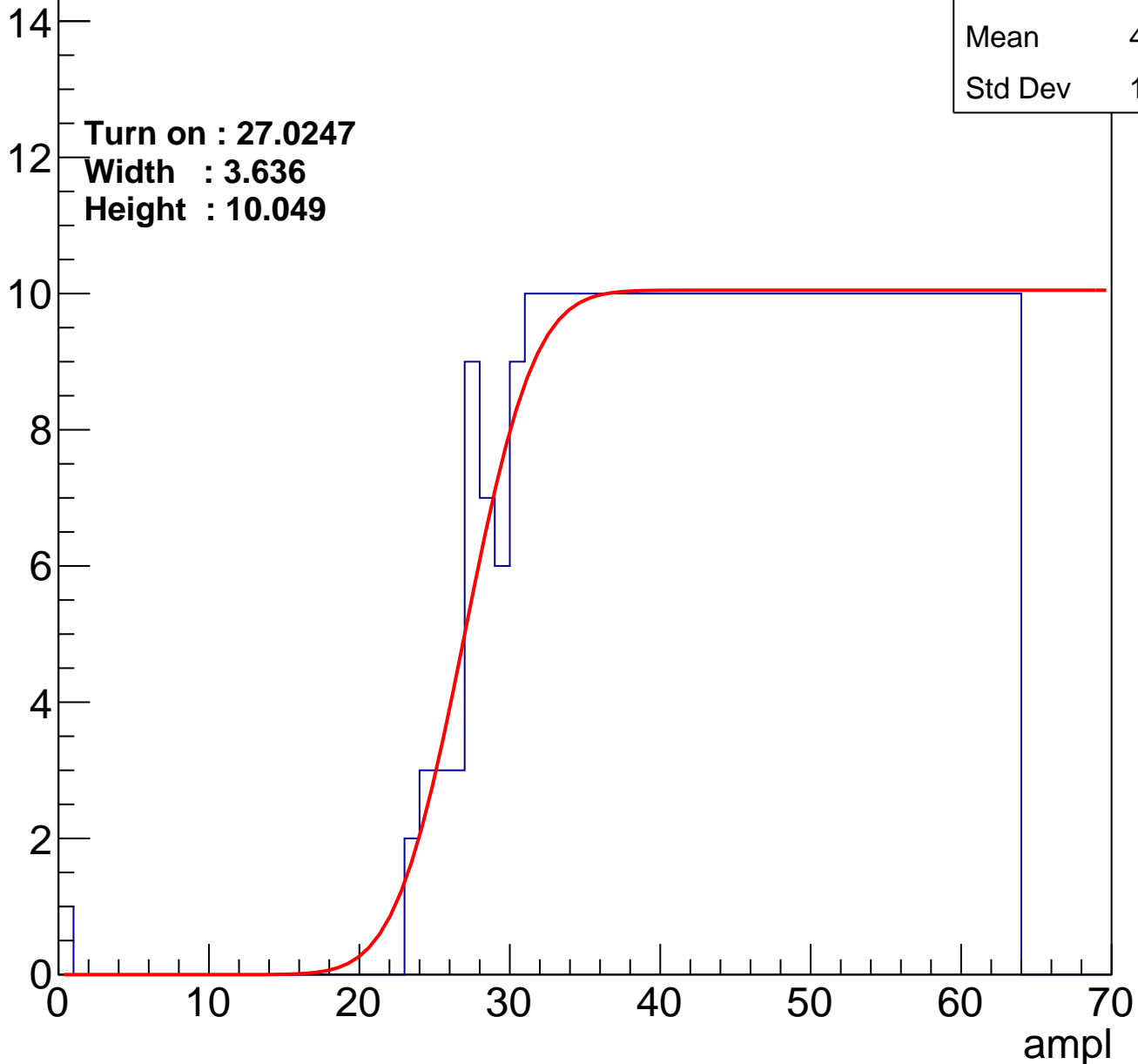
Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 27.0247

Width : 3.636

Height : 10.049

Entry



B0L000S, U25-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 27.8070

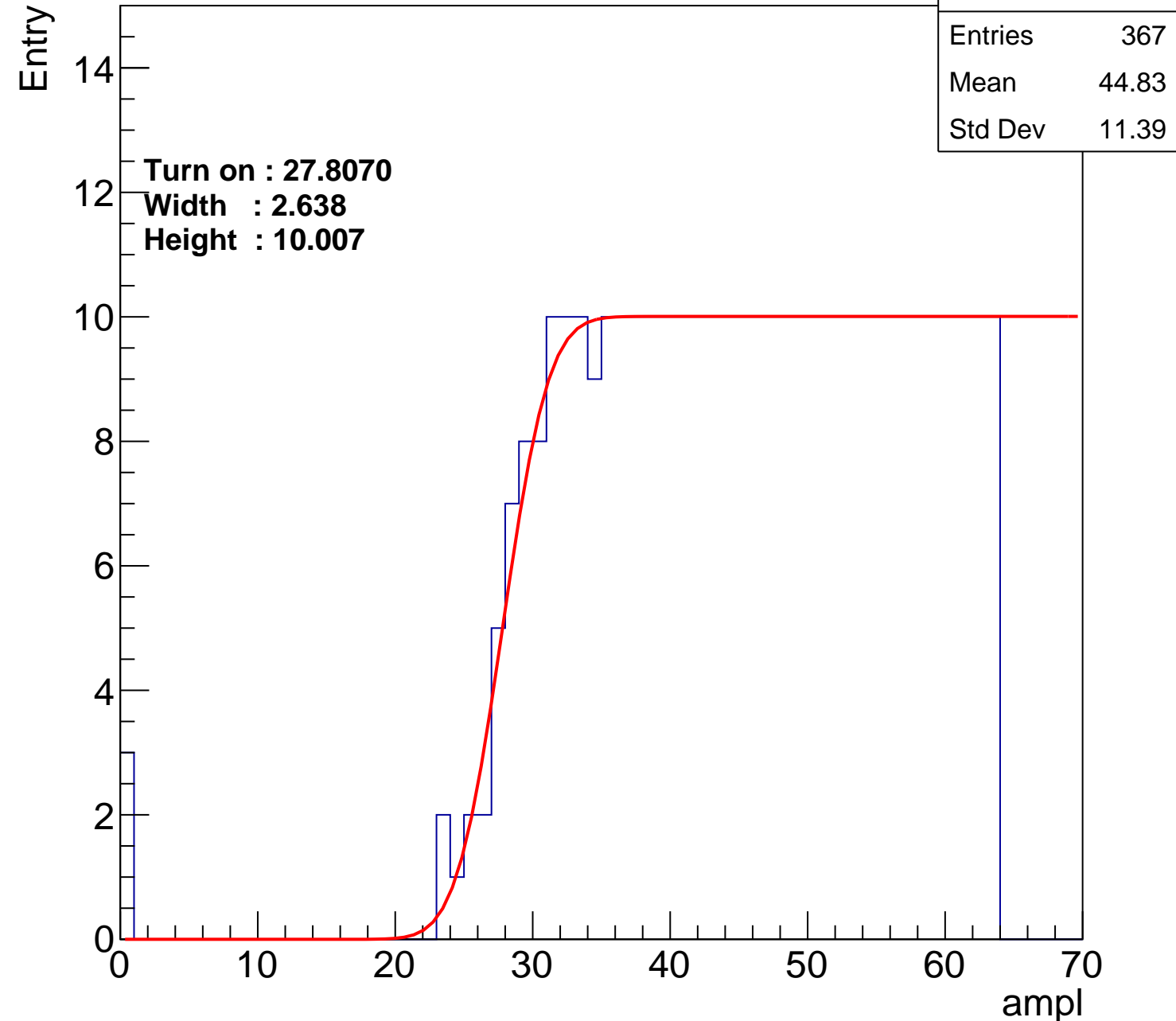
Width : 2.638

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.78
Std Dev	11.55

Turn on : 27.8650

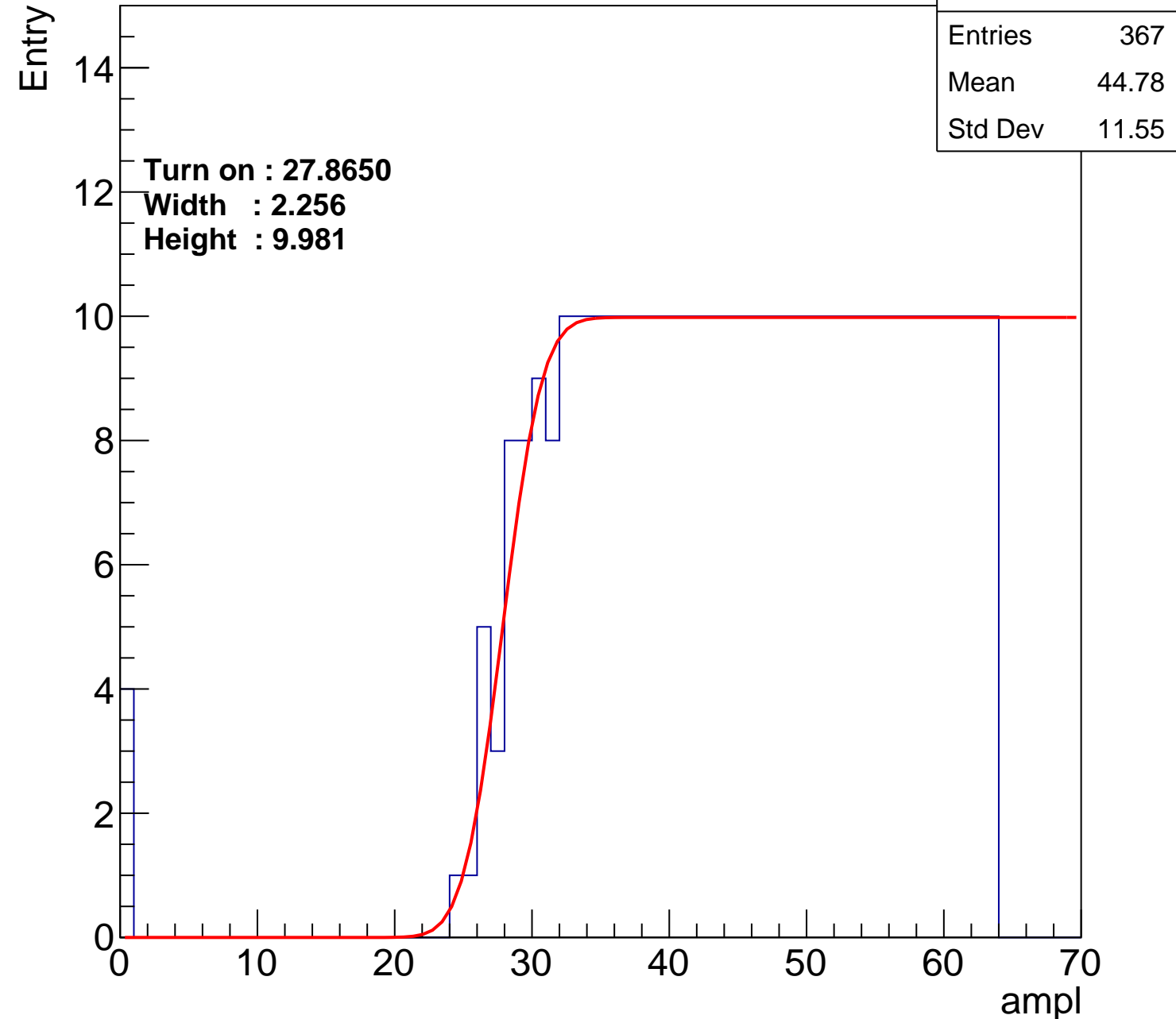
Width : 2.256

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.23
Std Dev	10.91

Turn on : 27.9641

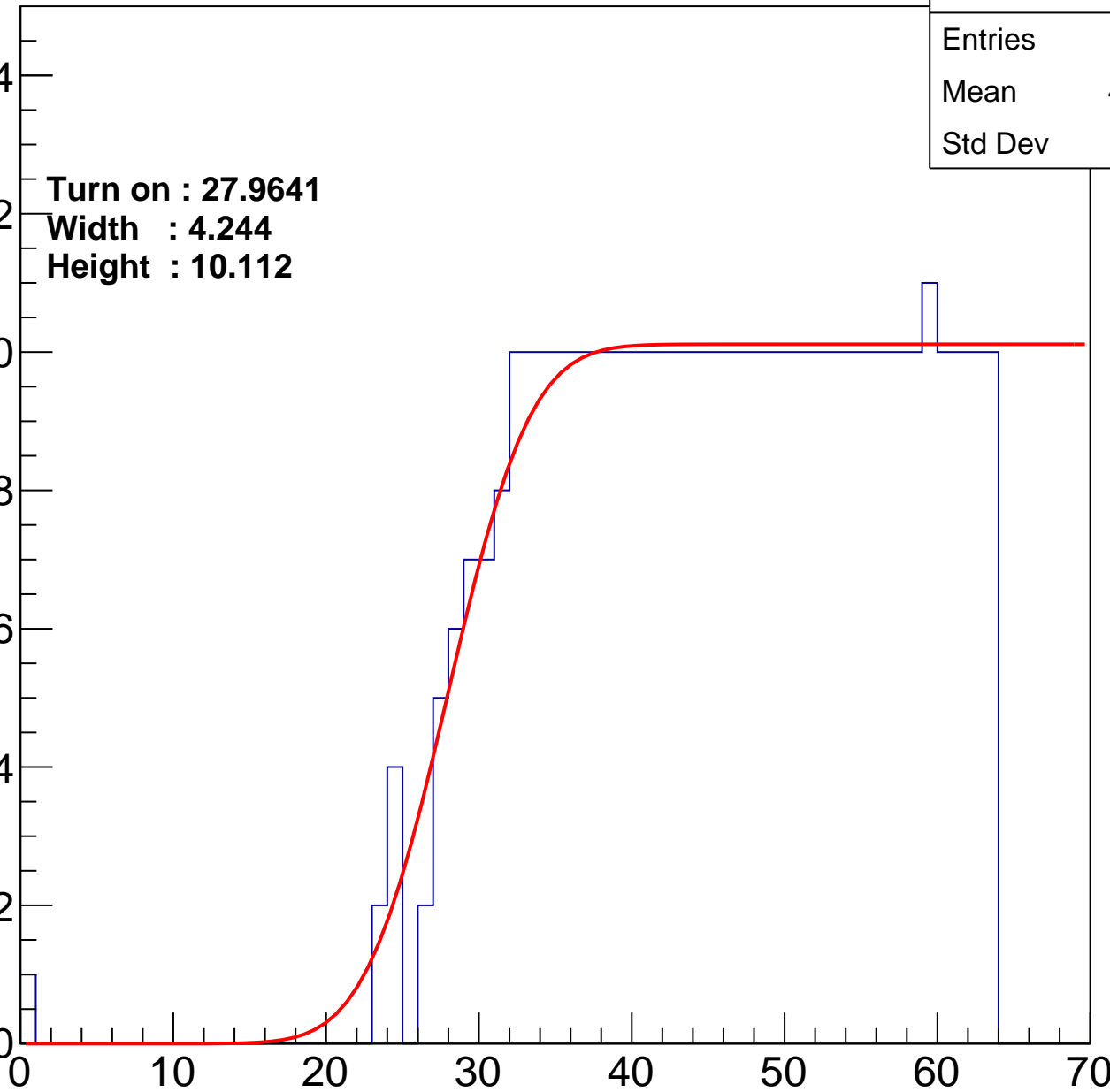
Width : 4.244

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.95
Std Dev	10.96

Turn on : 27.2972

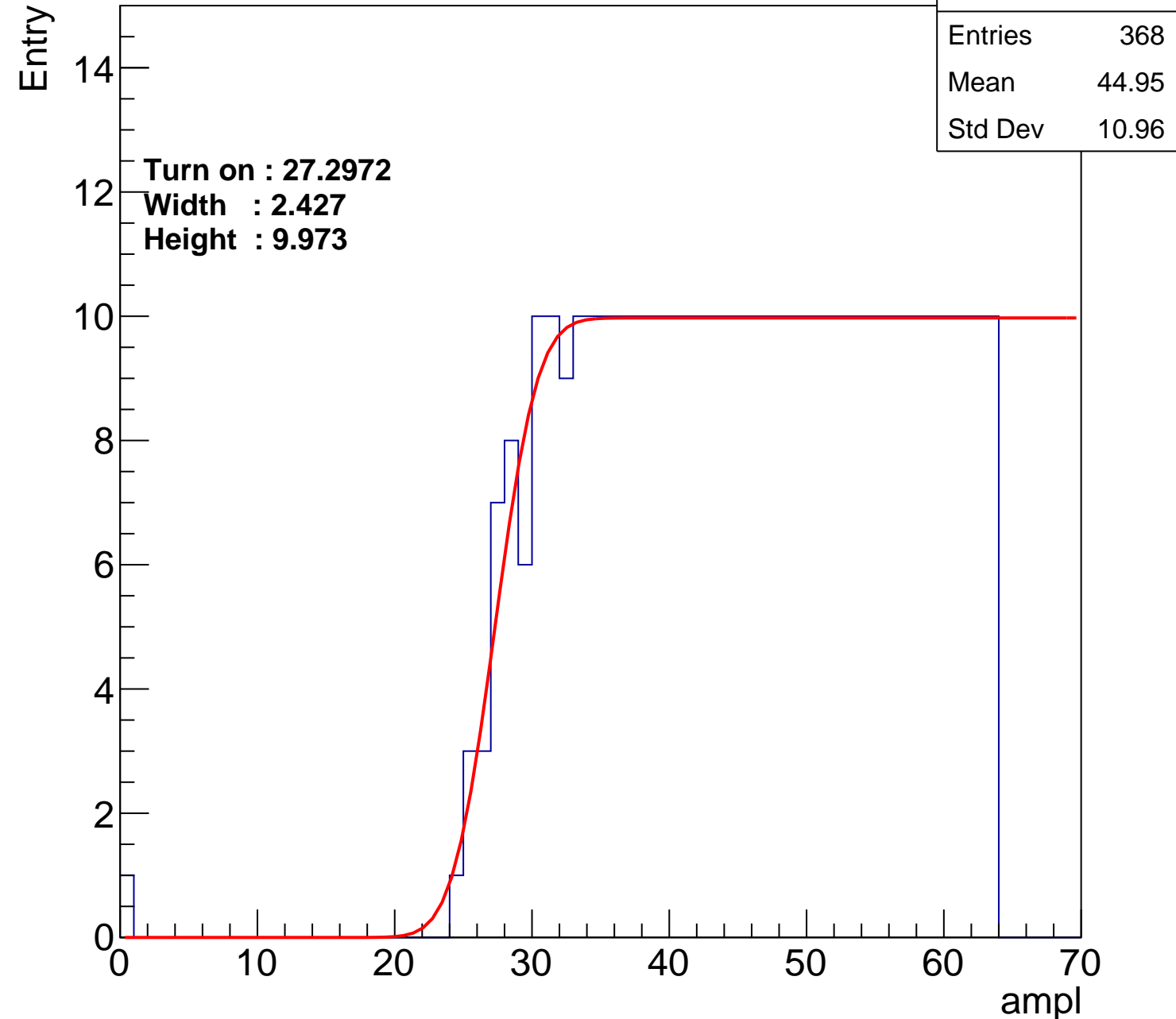
Width : 2.427

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	347
Mean	45.97
Std Dev	10.44

Turn on : 29.7381

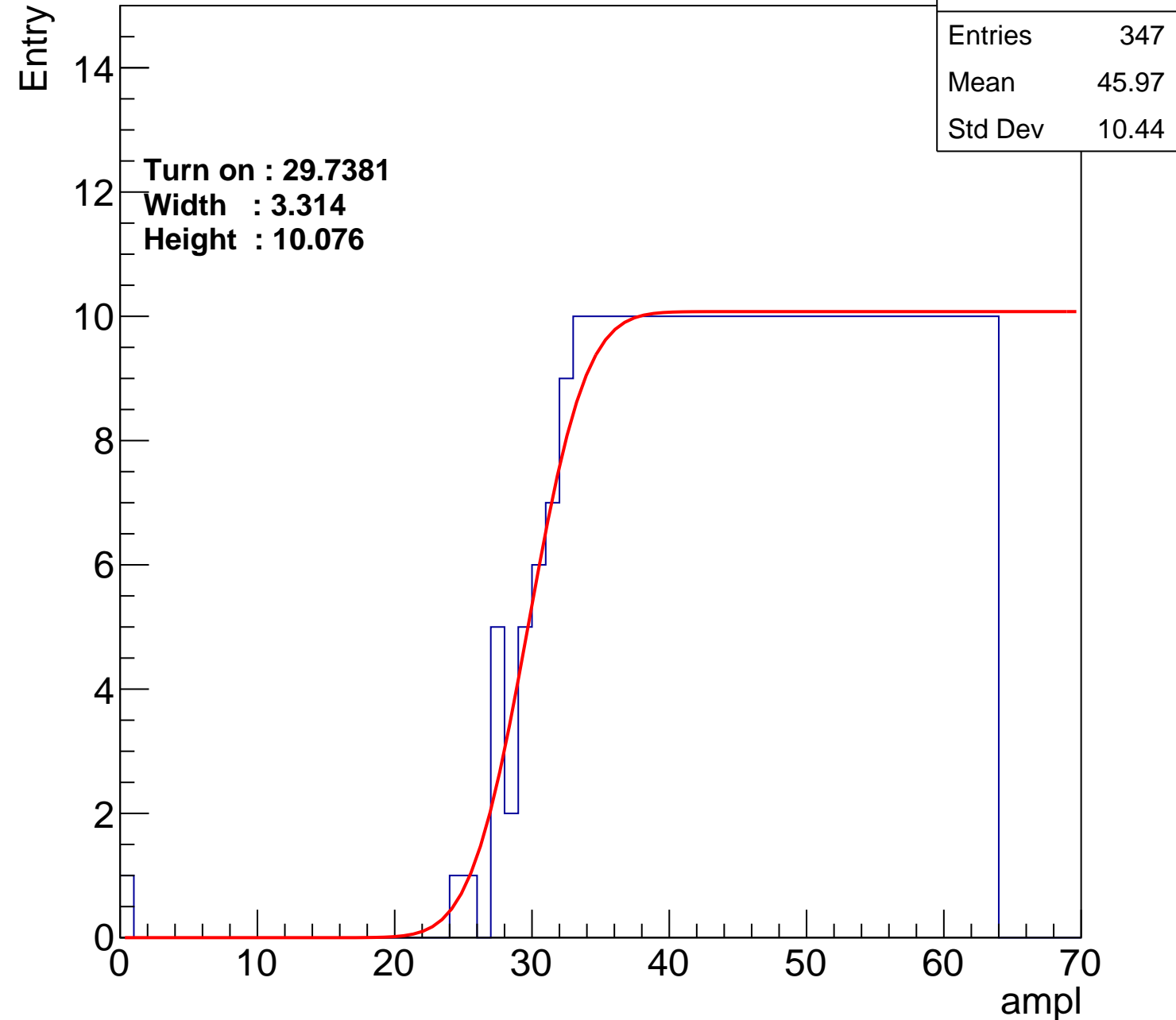
Width : 3.314

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch24

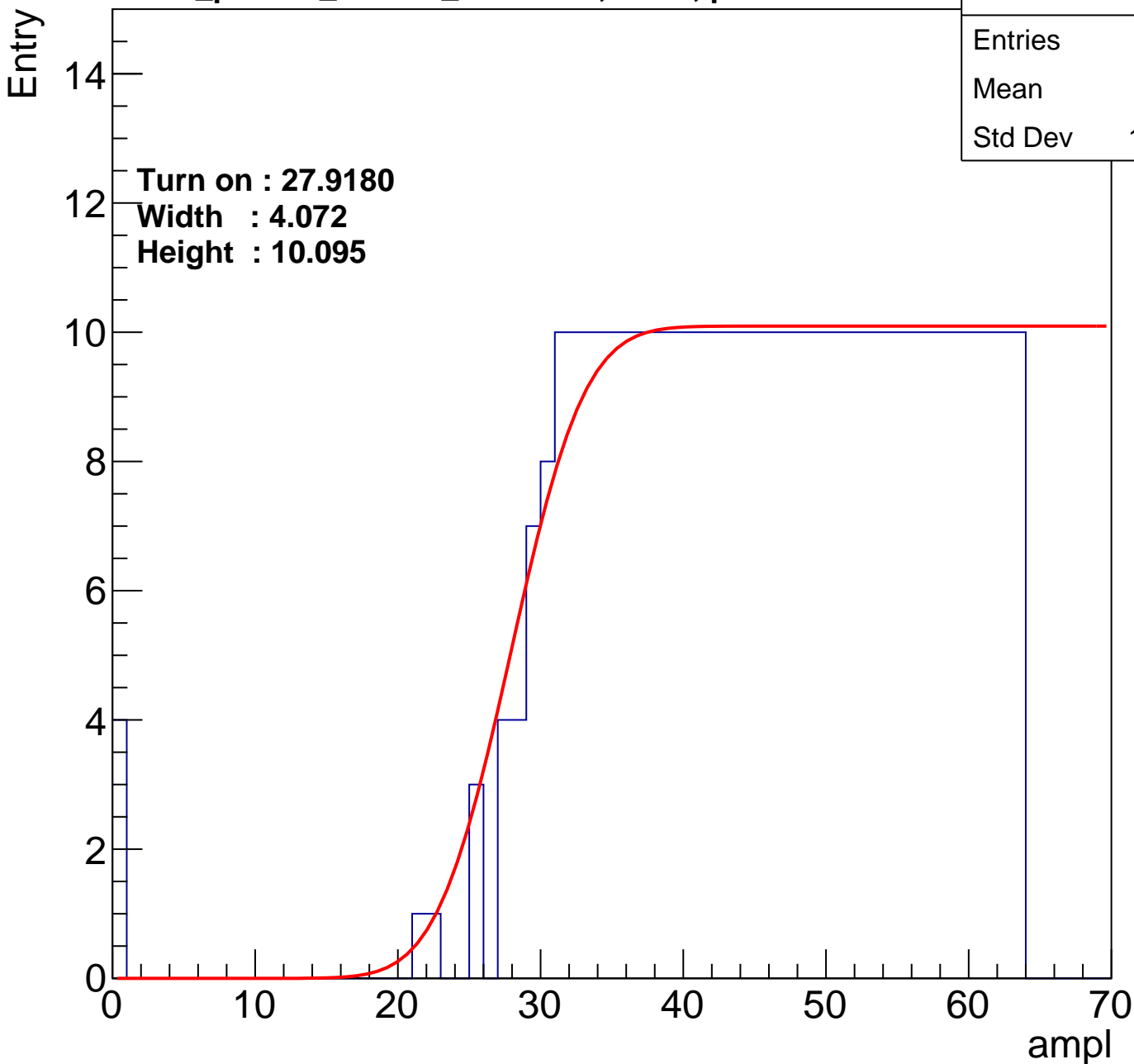
calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45
Std Dev	11.48

Turn on : 27.9180

Width : 4.072

Height : 10.095



B0L000S, U25-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 25.4710

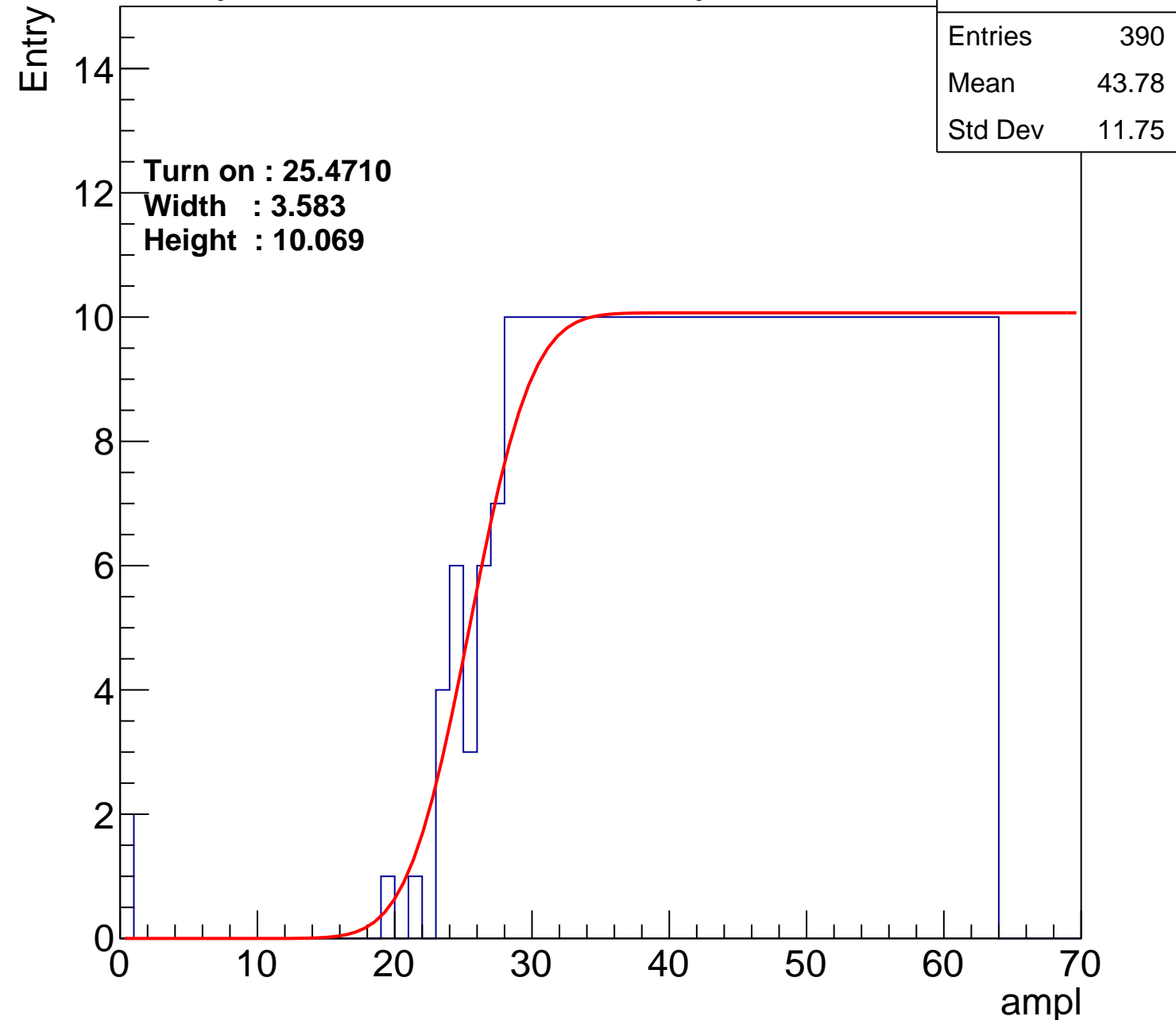
Width : 3.583

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl

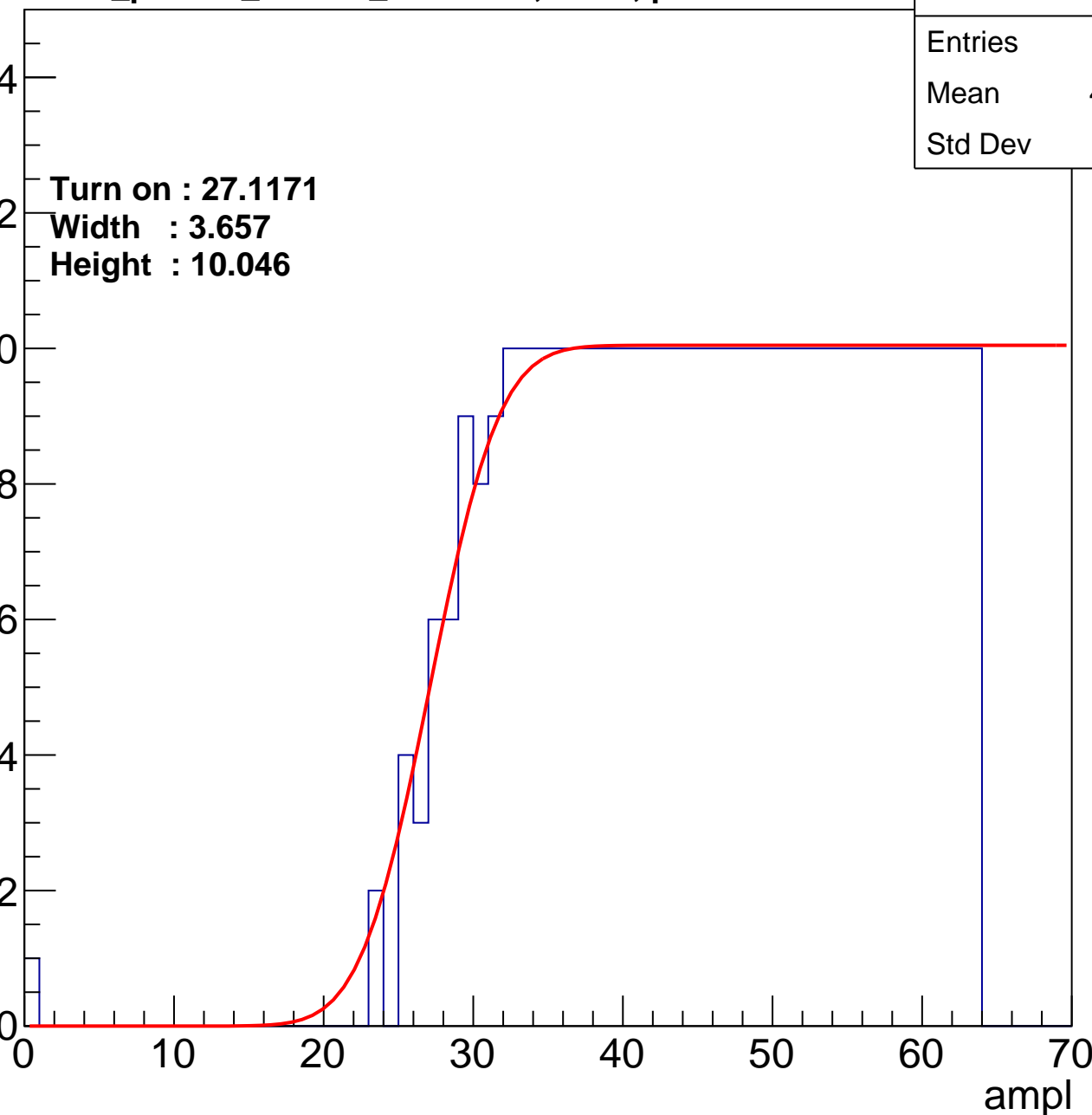


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.93
Std Dev	11

Height : 10.046



B0L000S, U25-ch27

calib_packv5_042523_0143.root, FC#5, port B1

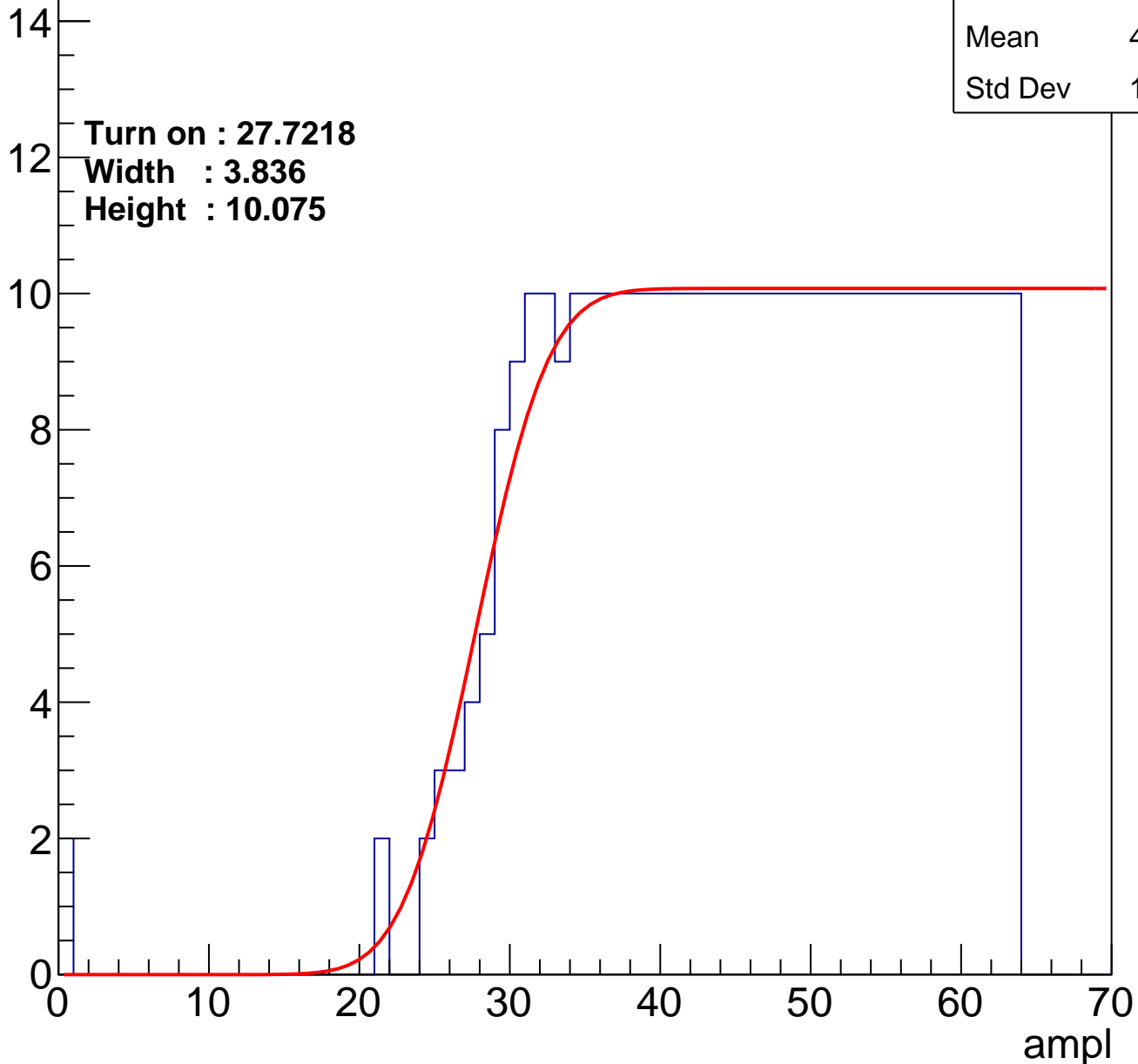
Entries	367
Mean	44.88
Std Dev	11.23

Turn on : 27.7218

Width : 3.836

Height : 10.075

Entry



B0L000S, U25-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.3379

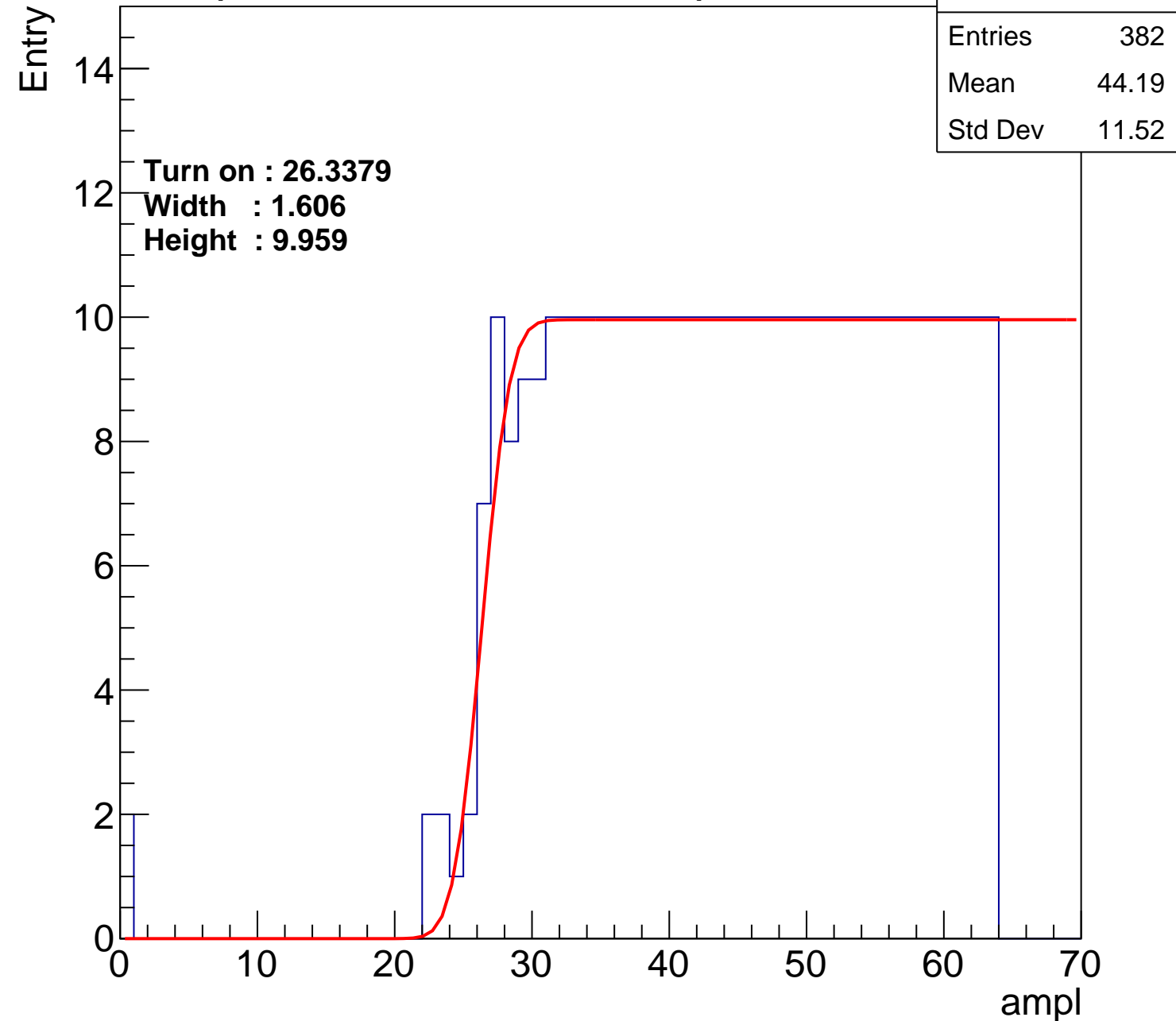
Width : 1.606

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch29

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 25.8168

Width : 2.817

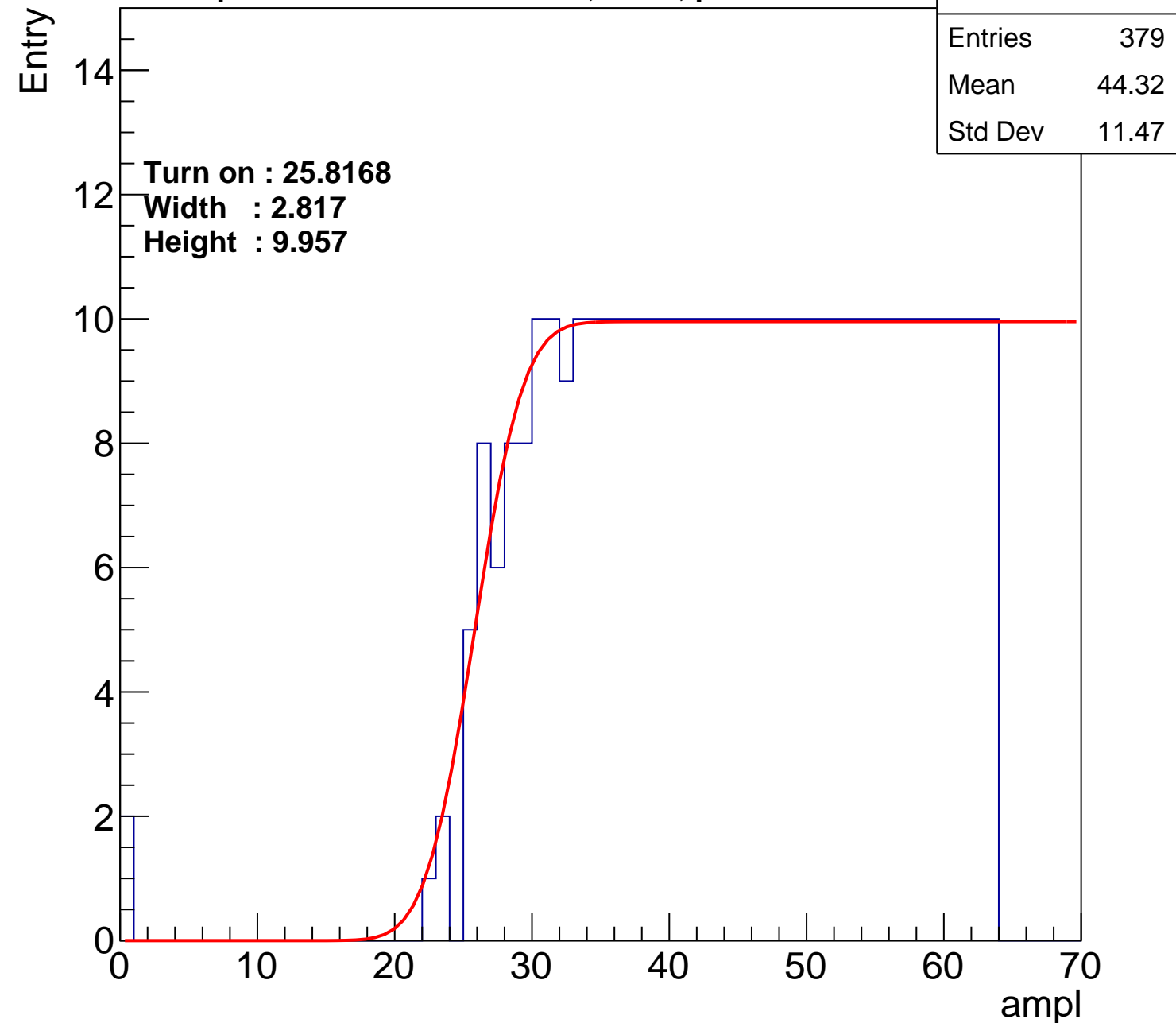
Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U25-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.11
Std Dev	10.87

Turn on : 27.8375

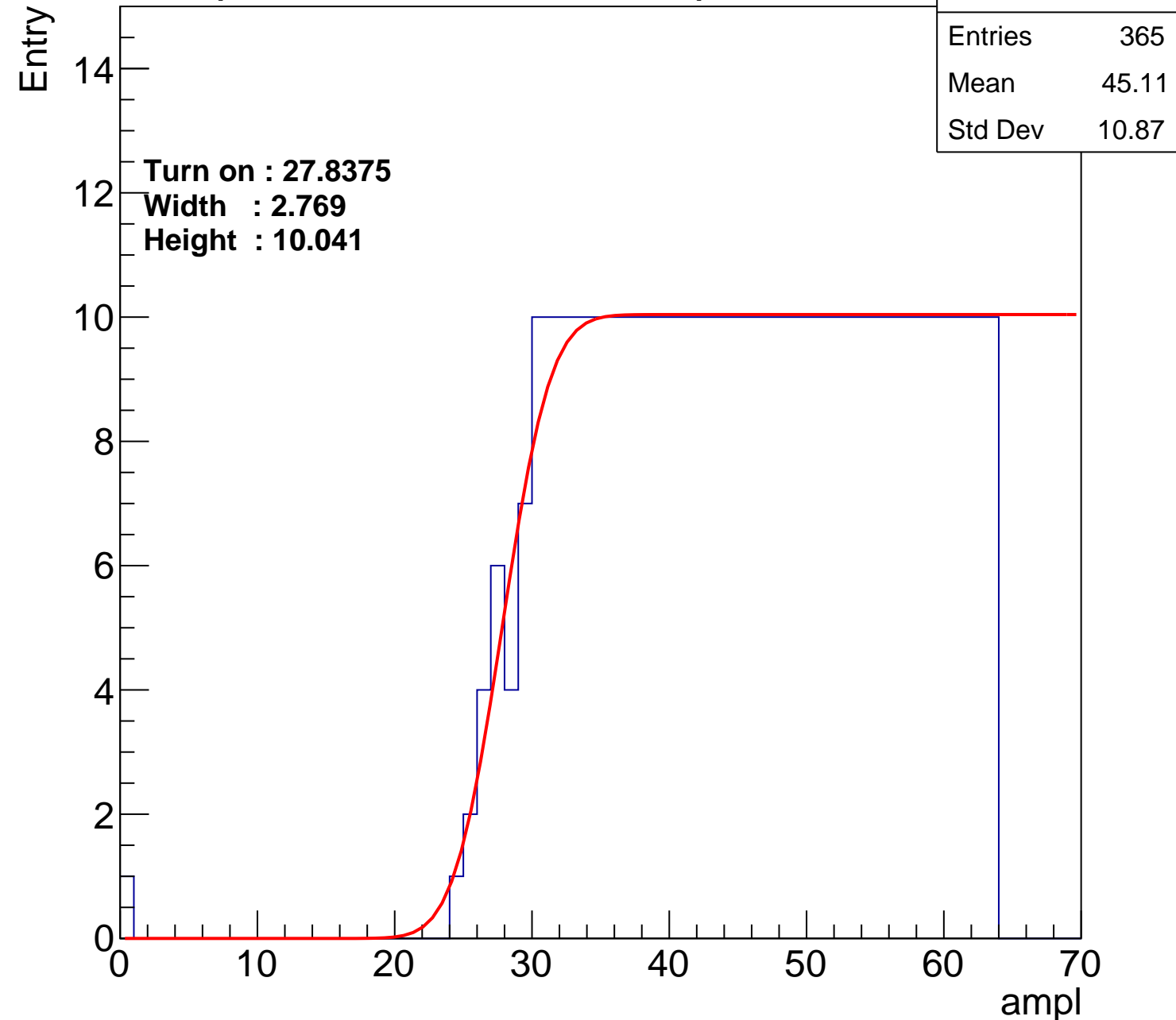
Width : 2.769

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.49
Std Dev	12.31

Turn on : 28.3031

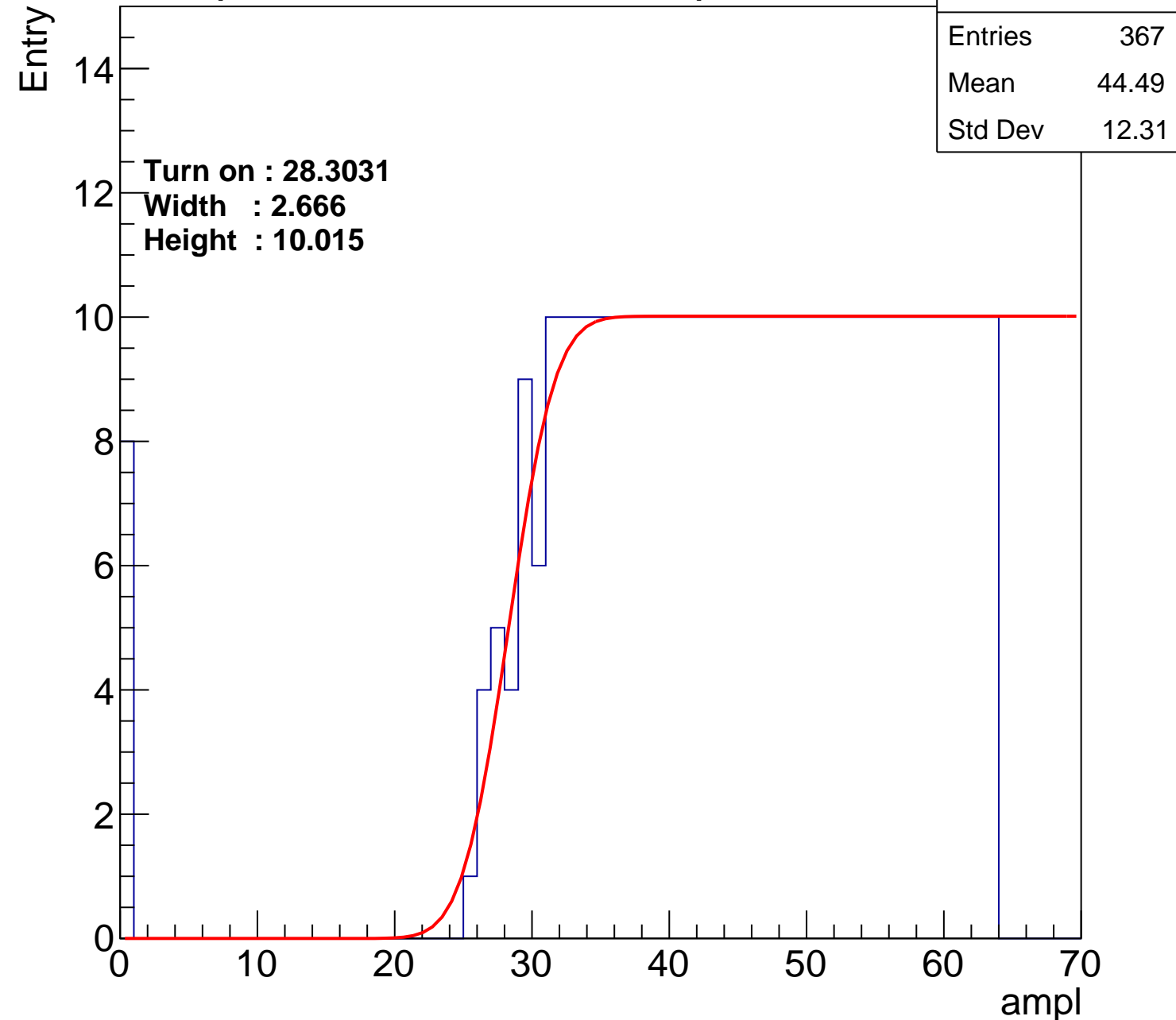
Width : 2.666

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch32

calib_packv5_042523_0143.root, FC#5, port B1

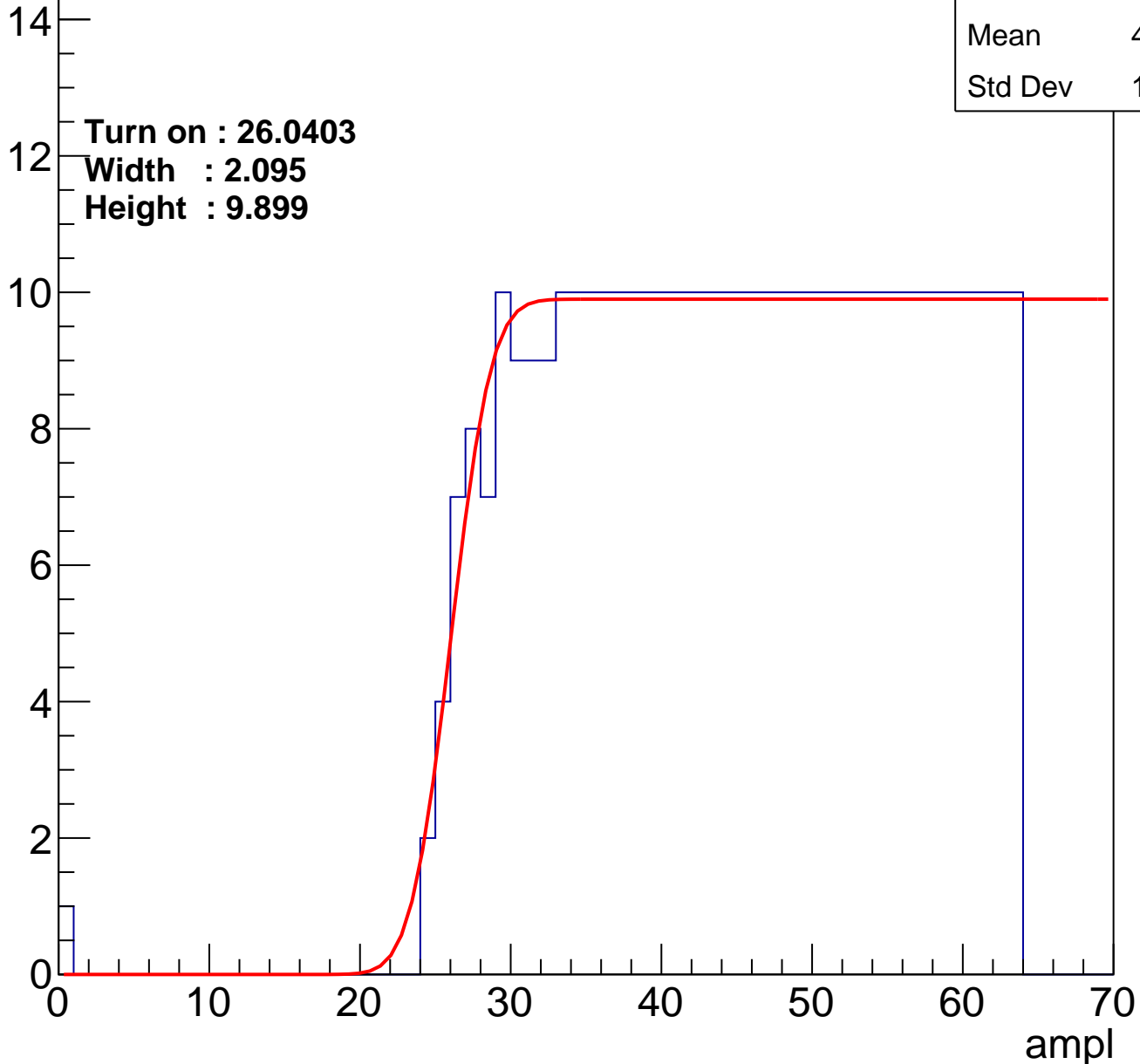
Entries	376
Mean	44.55
Std Dev	11.18

Turn on : 26.0403

Width : 2.095

Height : 9.899

Entry



B0L000S, U25-ch33

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.12
Std Dev	11.24

Turn on : 28.4147

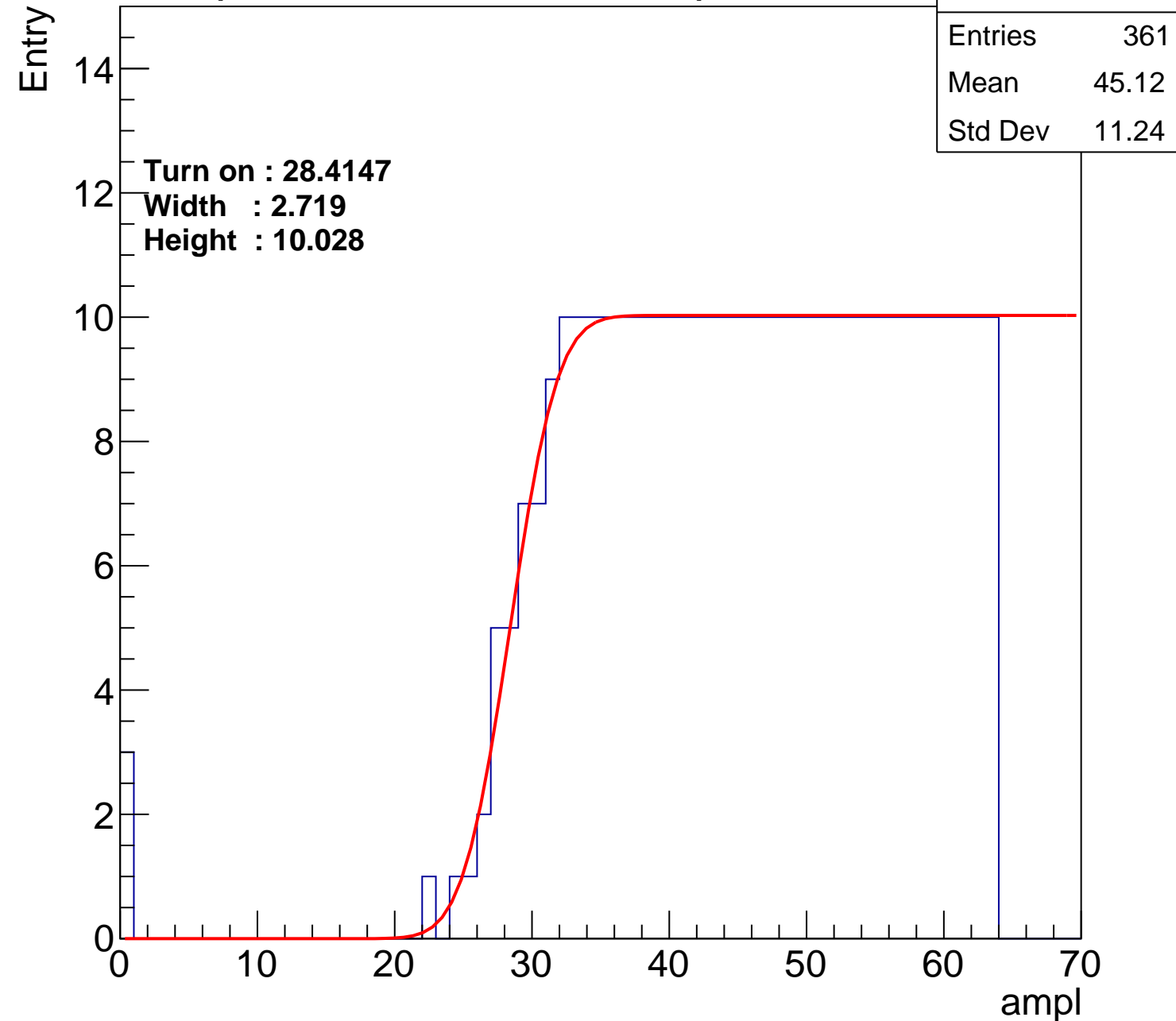
Width : 2.719

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.15
Std Dev	10.91

Turn on : 28.6595

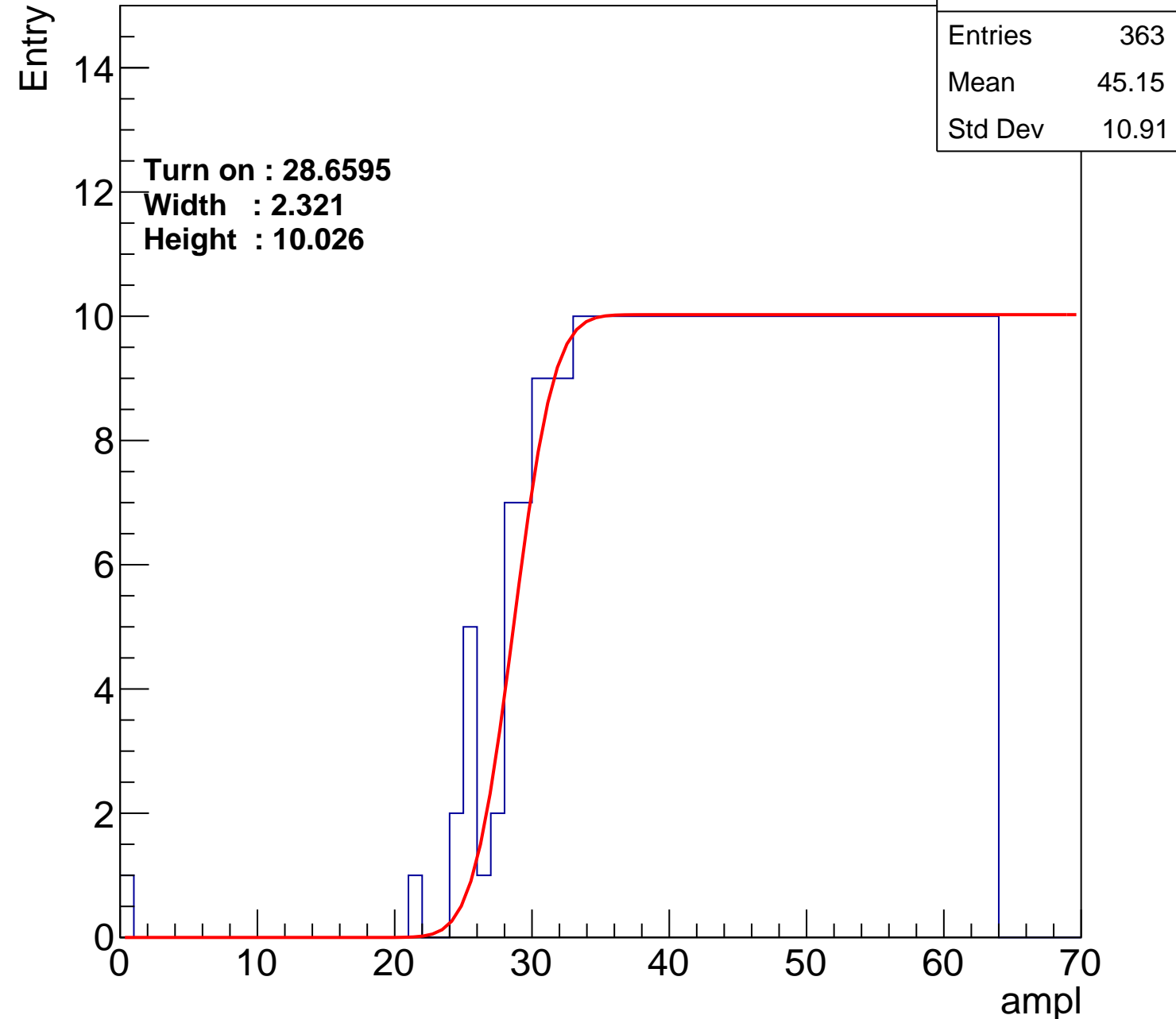
Width : 2.321

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch35

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.97
Std Dev	10.99

Turn on : 27.7838

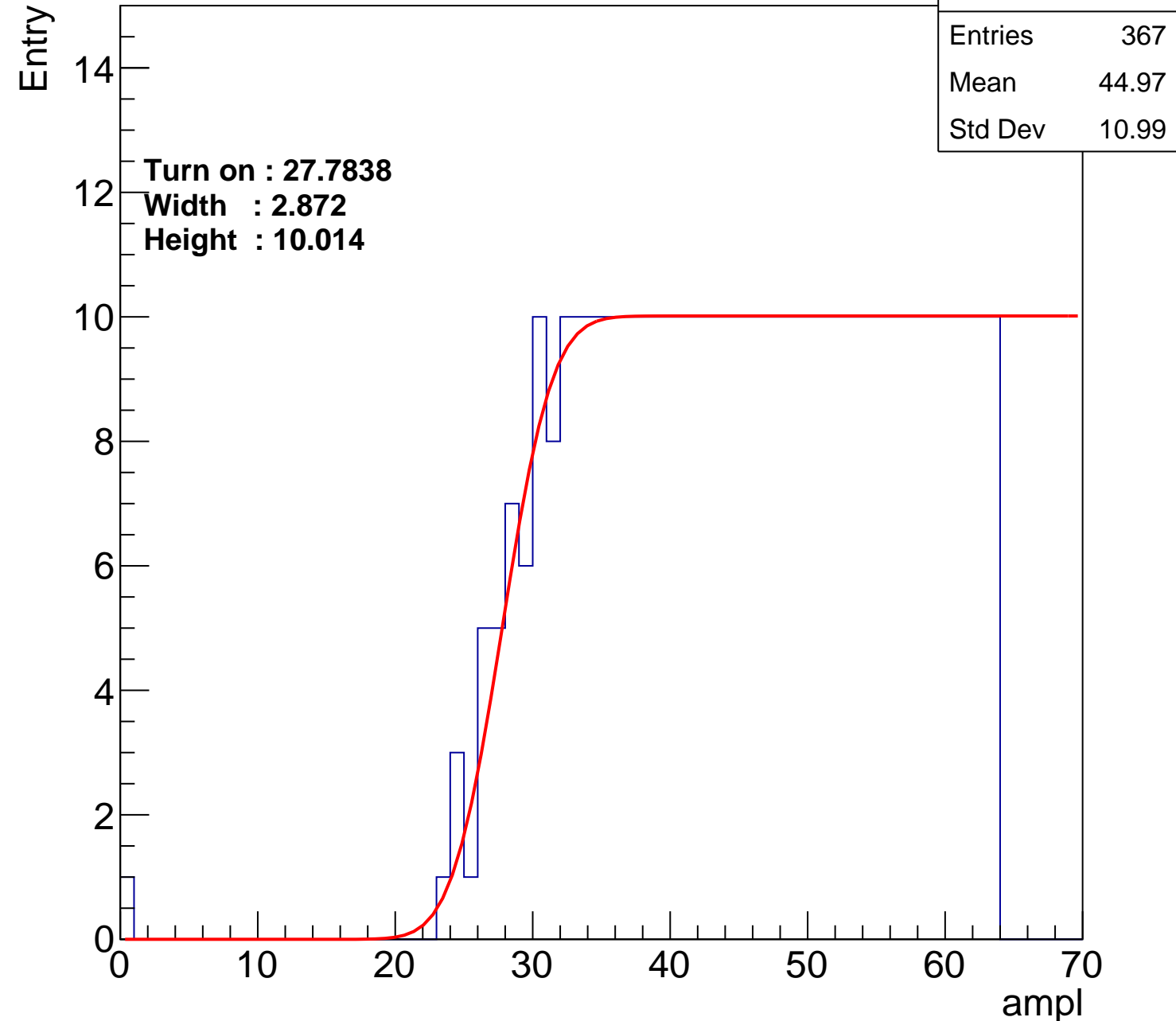
Width : 2.872

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch36

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	43.98
Std Dev	11.94

Turn on : 26.0824

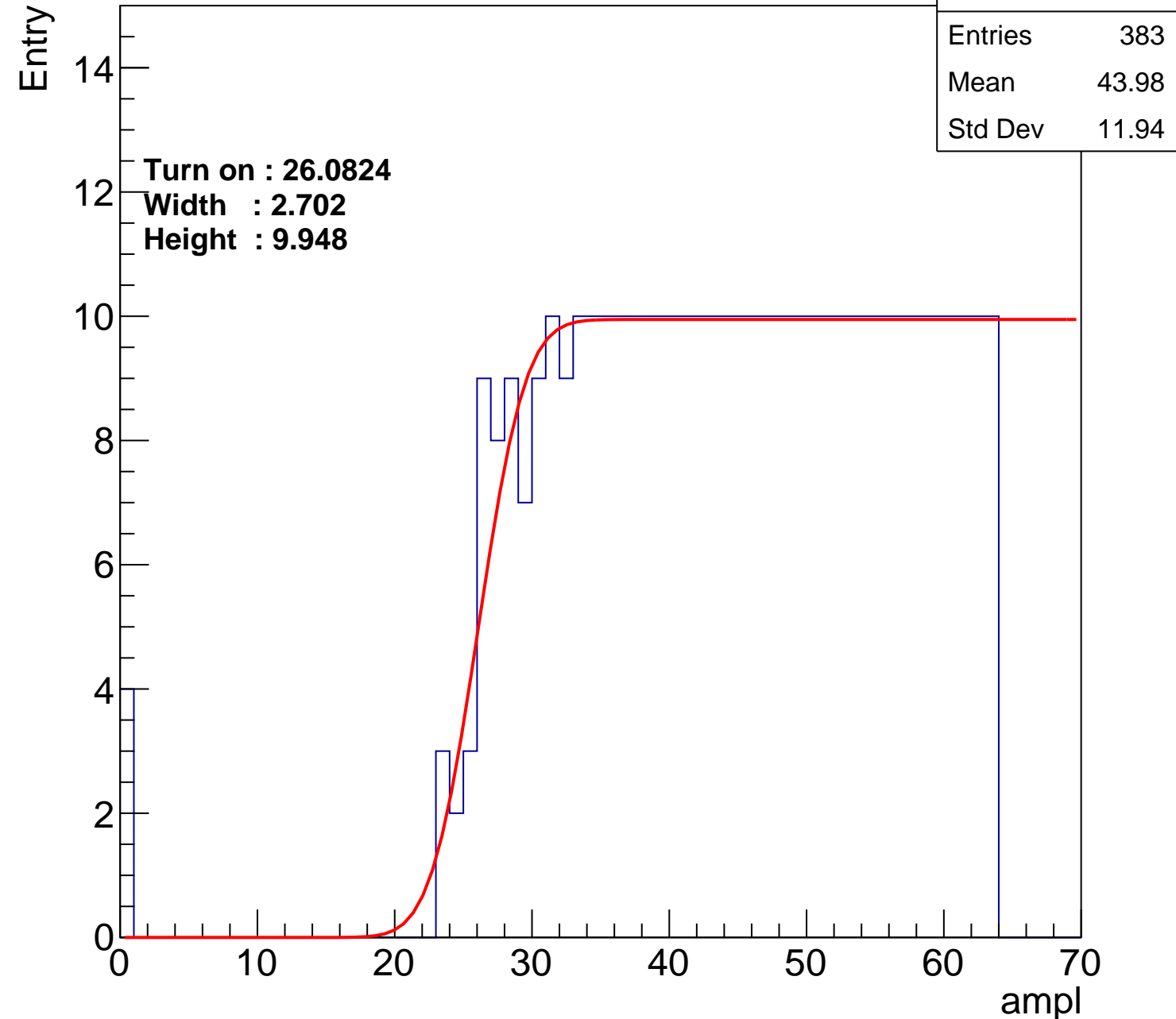
Width : 2.702

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.39
Std Dev	11.6

Turn on : 26.9233

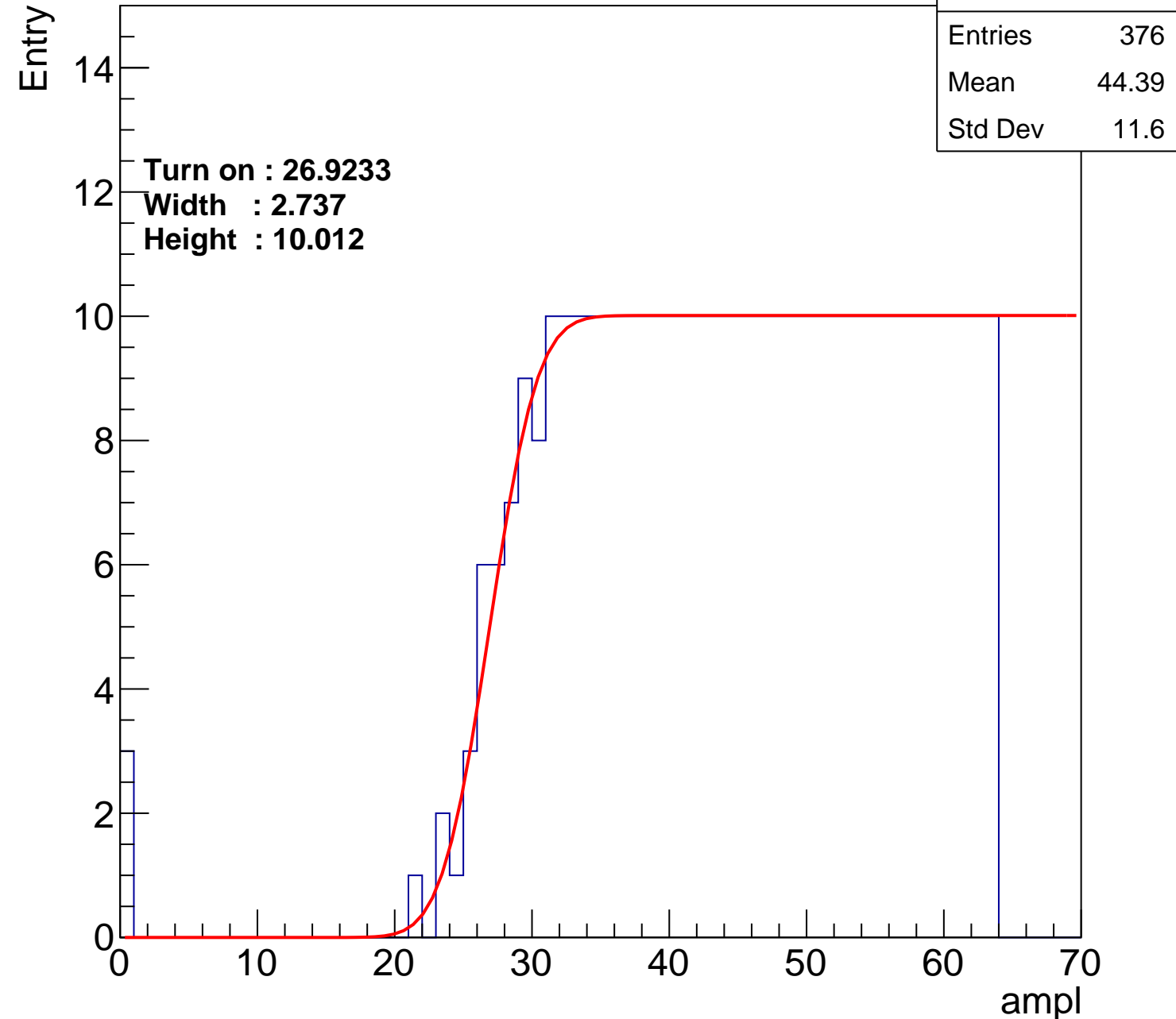
Width : 2.737

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch38

calib_packv5_042523_0143.root, FC#5, port B1

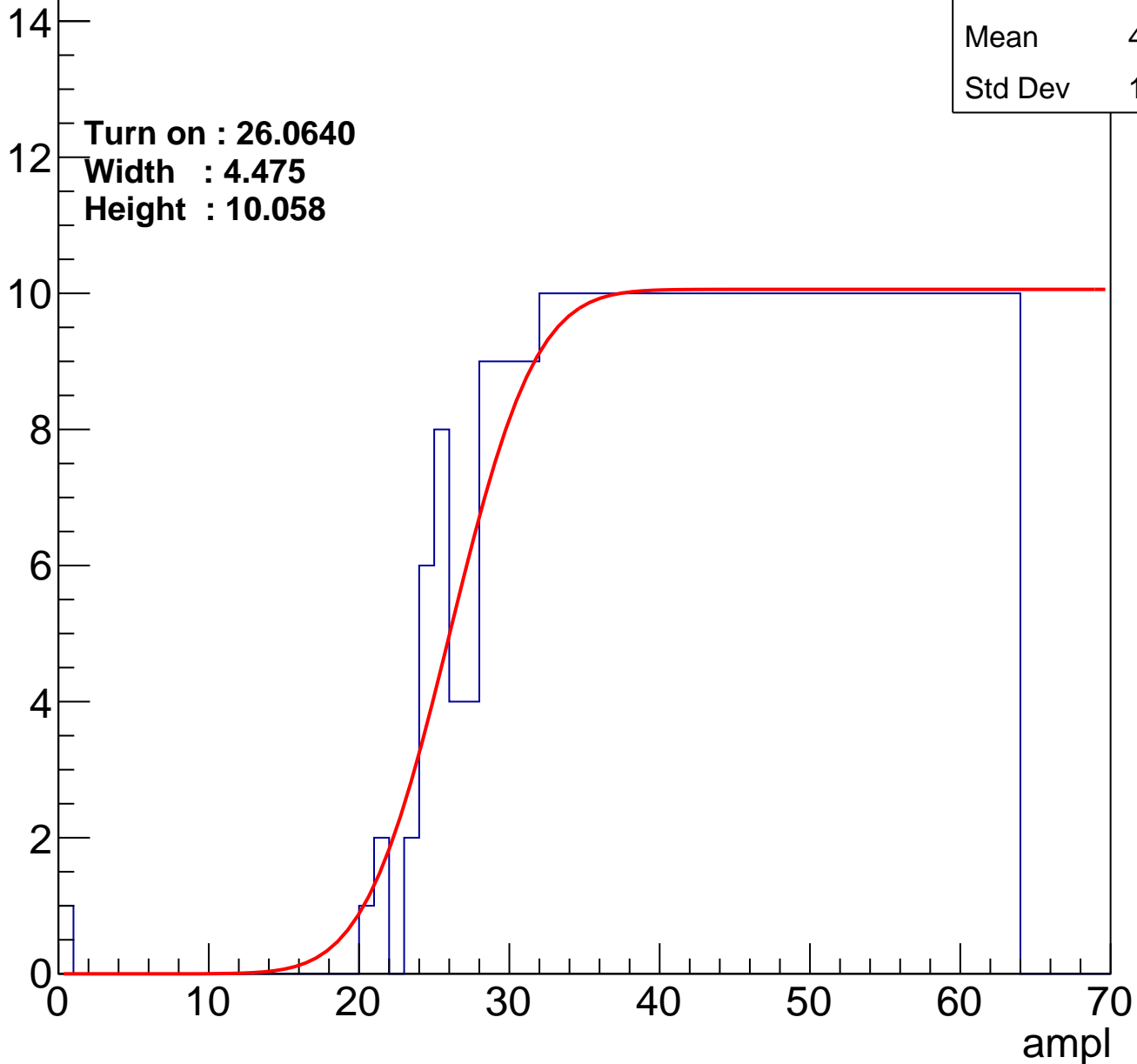
Entries	384
Mean	44.08
Std Dev	11.52

Turn on : 26.0640

Width : 4.475

Height : 10.058

Entry



B0L000S, U25-ch39

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.97
Std Dev	11.34

Turn on : 28.0965

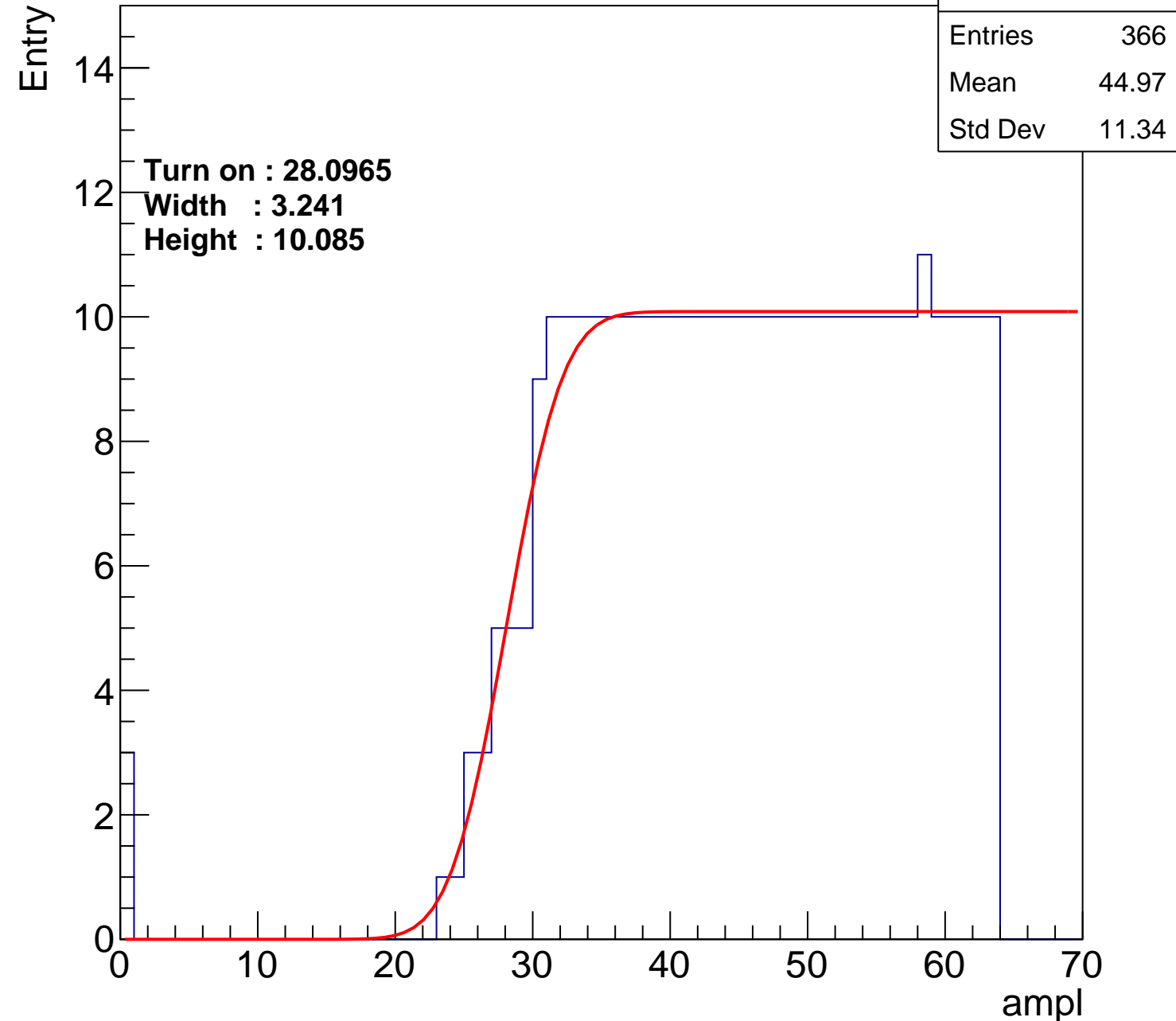
Width : 3.241

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch40

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.95
Std Dev	11.31

Turn on : 27.7651

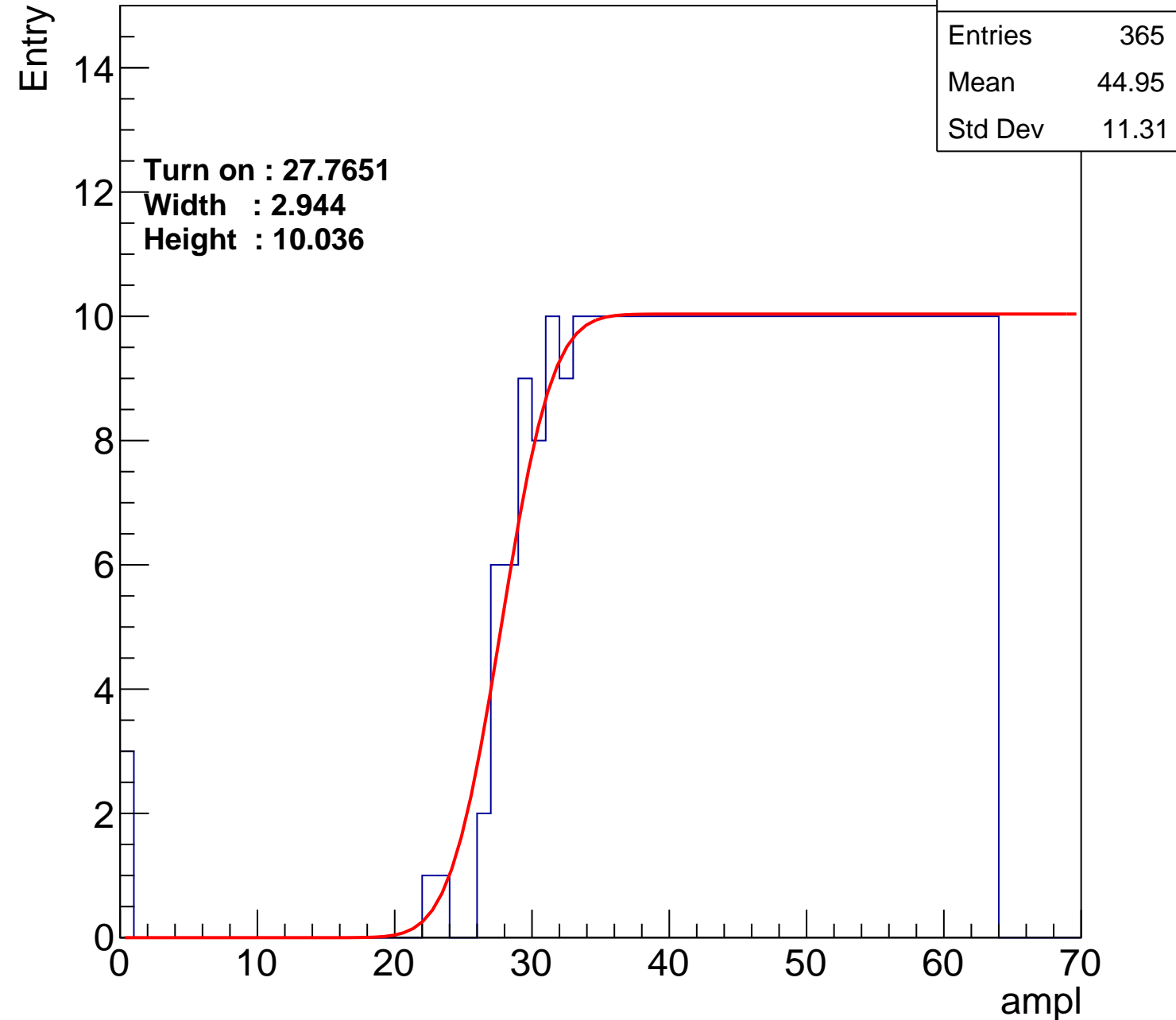
Width : 2.944

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.58
Std Dev	11.7

Turn on : 27.7106

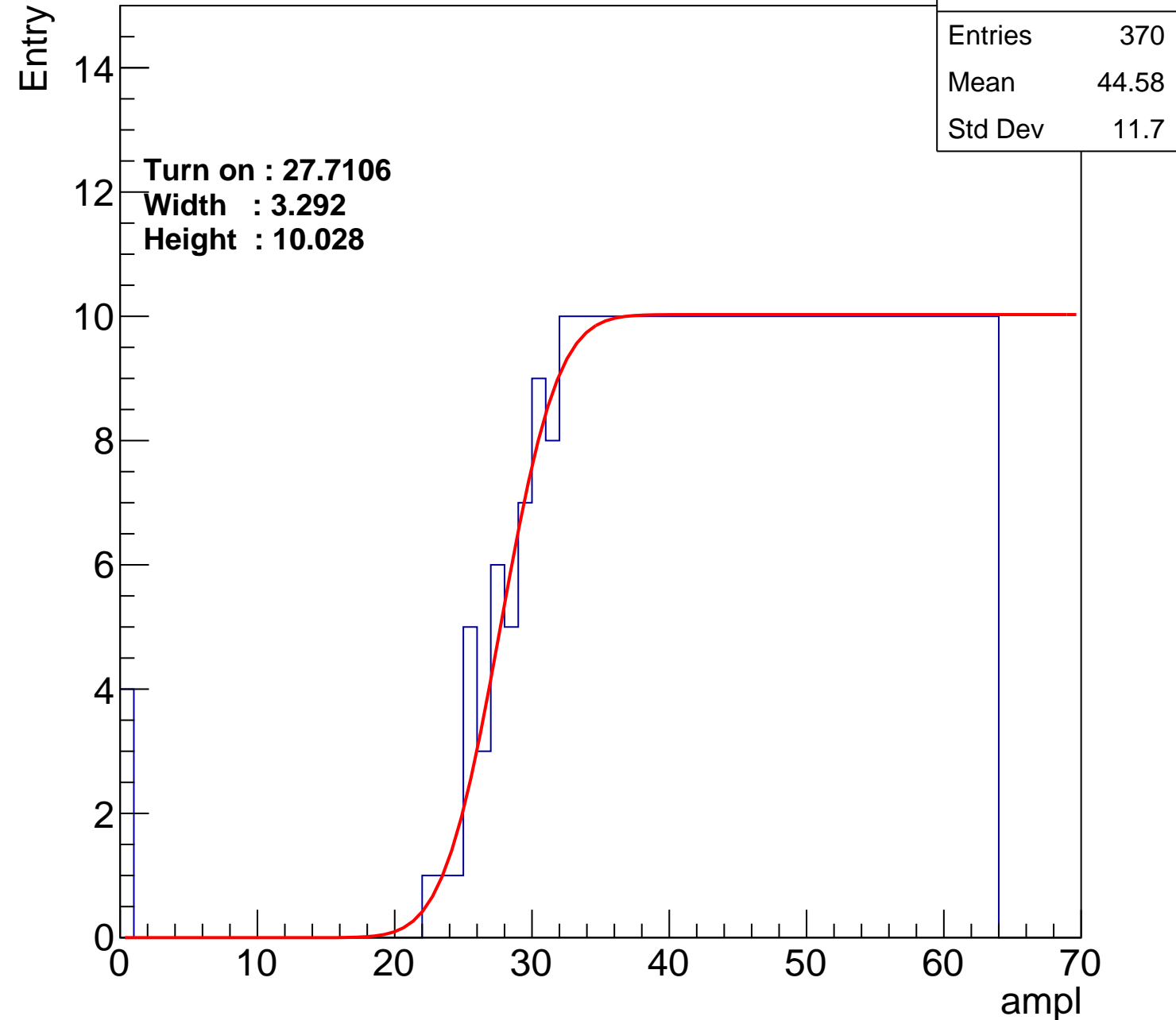
Width : 3.292

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.93
Std Dev	11.48

Turn on : 27.0453

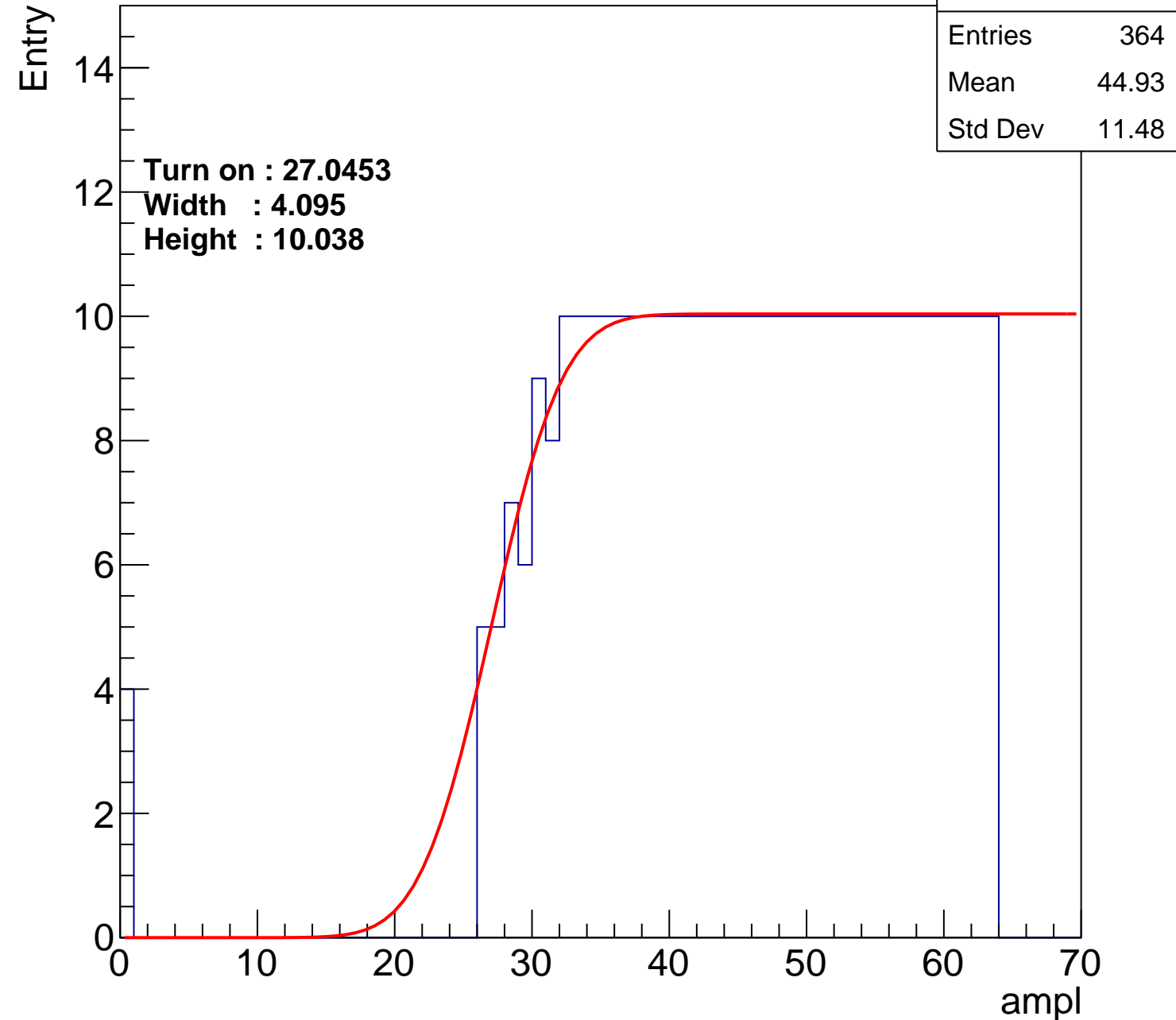
Width : 4.095

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.97
Std Dev	11.05

Turn on : 27.9605

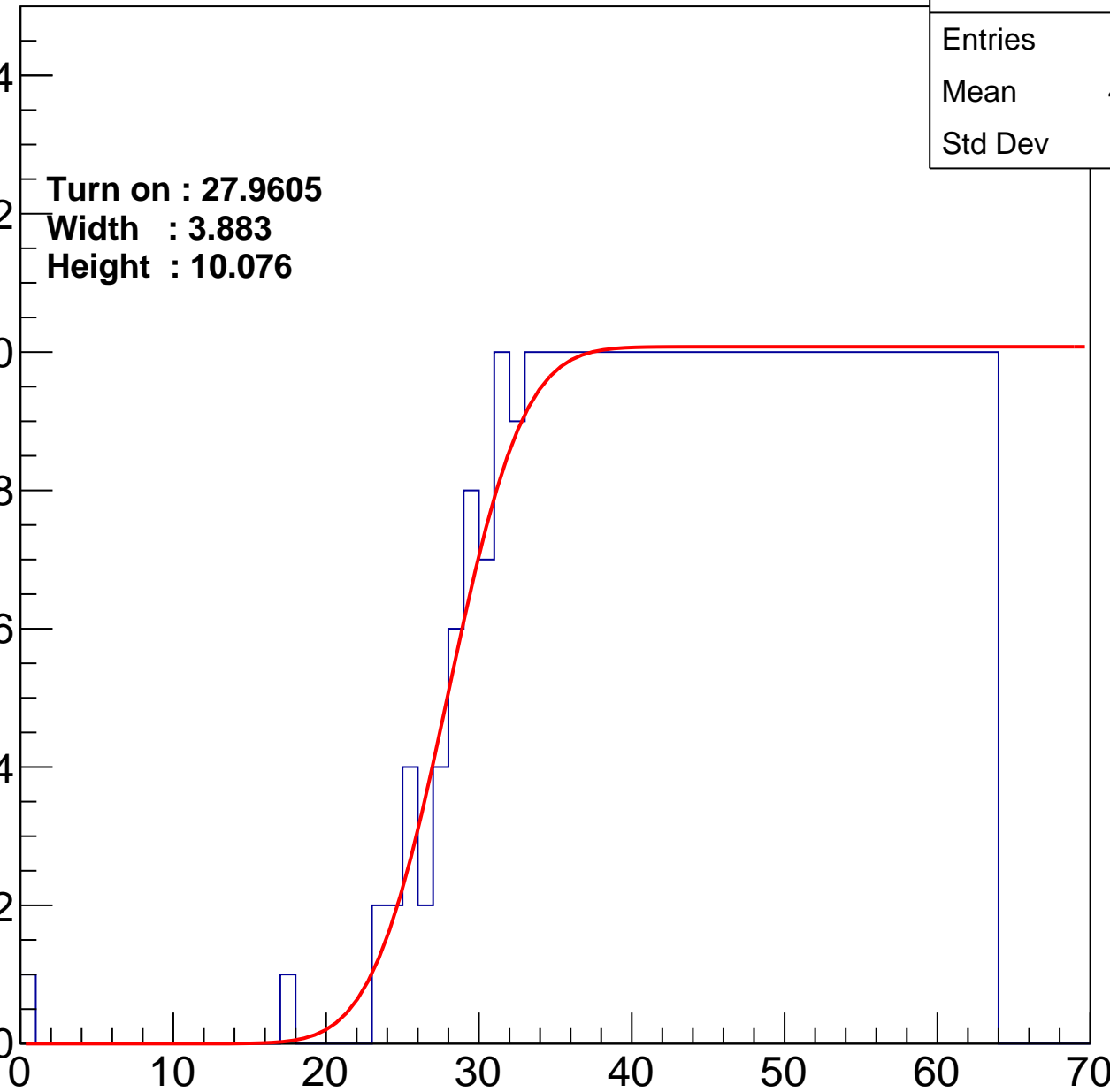
Width : 3.883

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch44

calib_packv5_042523_0143.root, FC#5, port B1

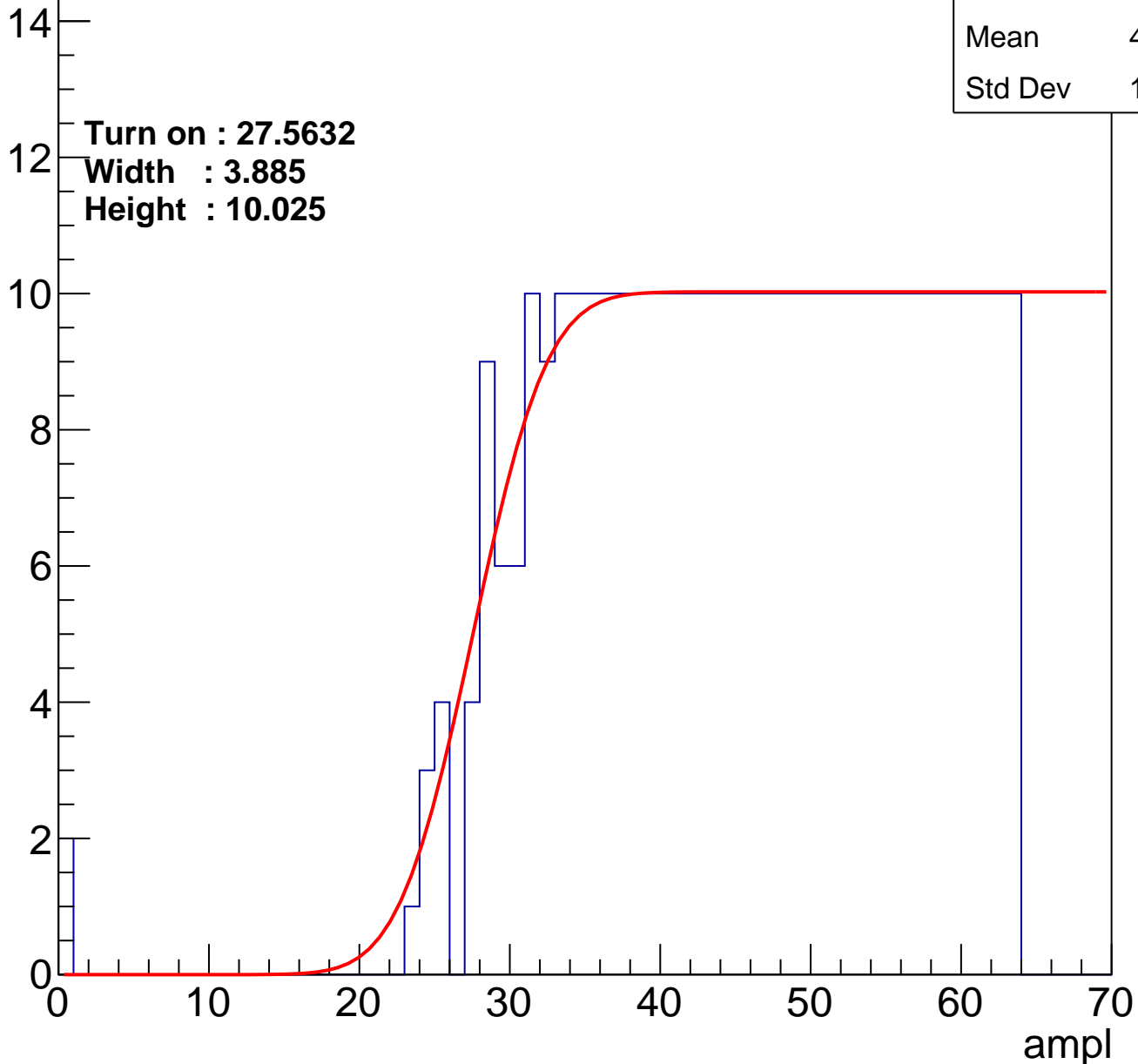
Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 27.5632

Width : 3.885

Height : 10.025

Entry



B0L000S, U25-ch45

calib_packv5_042523_0143.root, FC#5, port B1

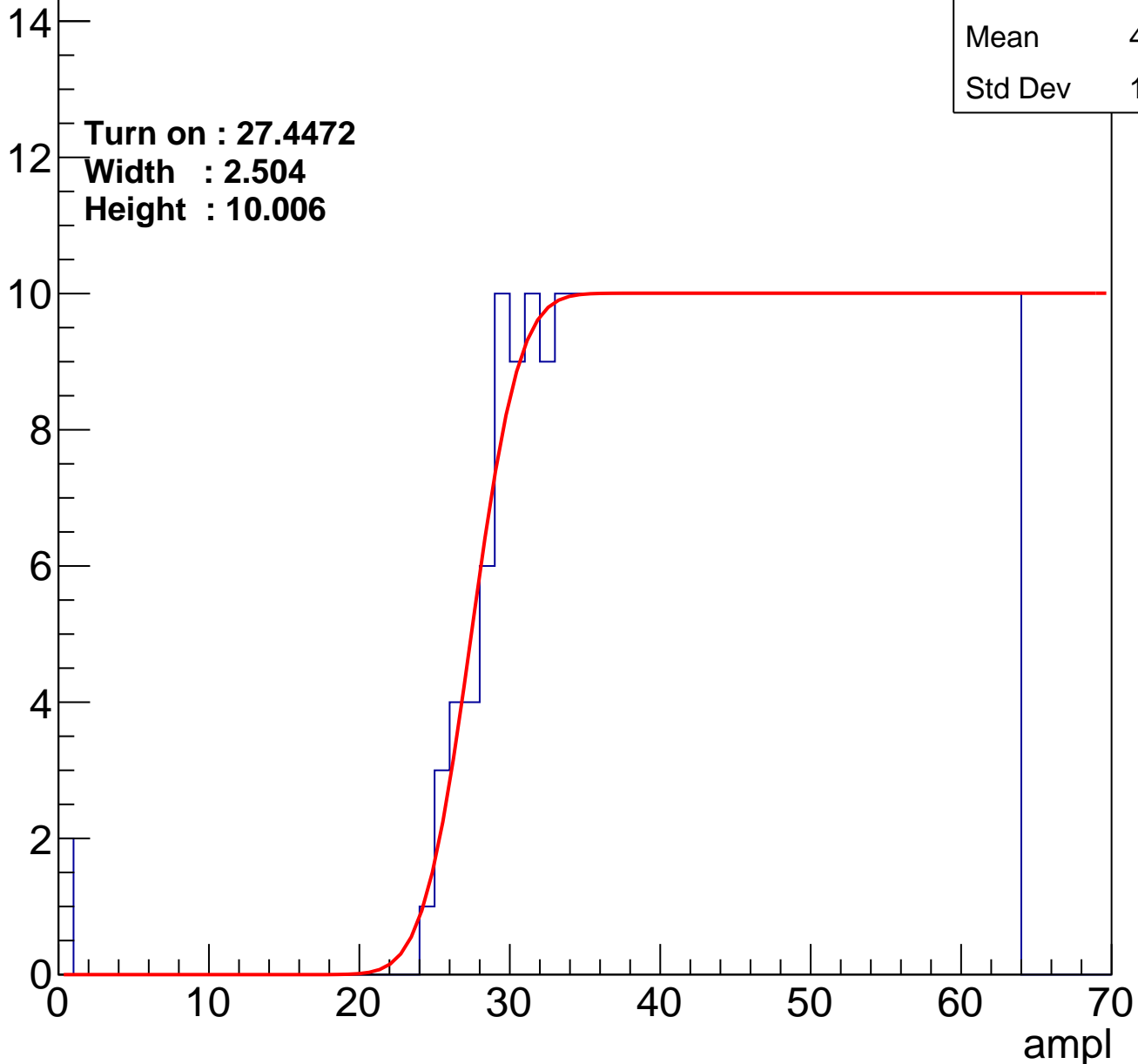
Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.4472

Width : 2.504

Height : 10.006

Entry



B0L000S, U25-ch46

calib_packv5_042523_0143.root, FC#5, port B1

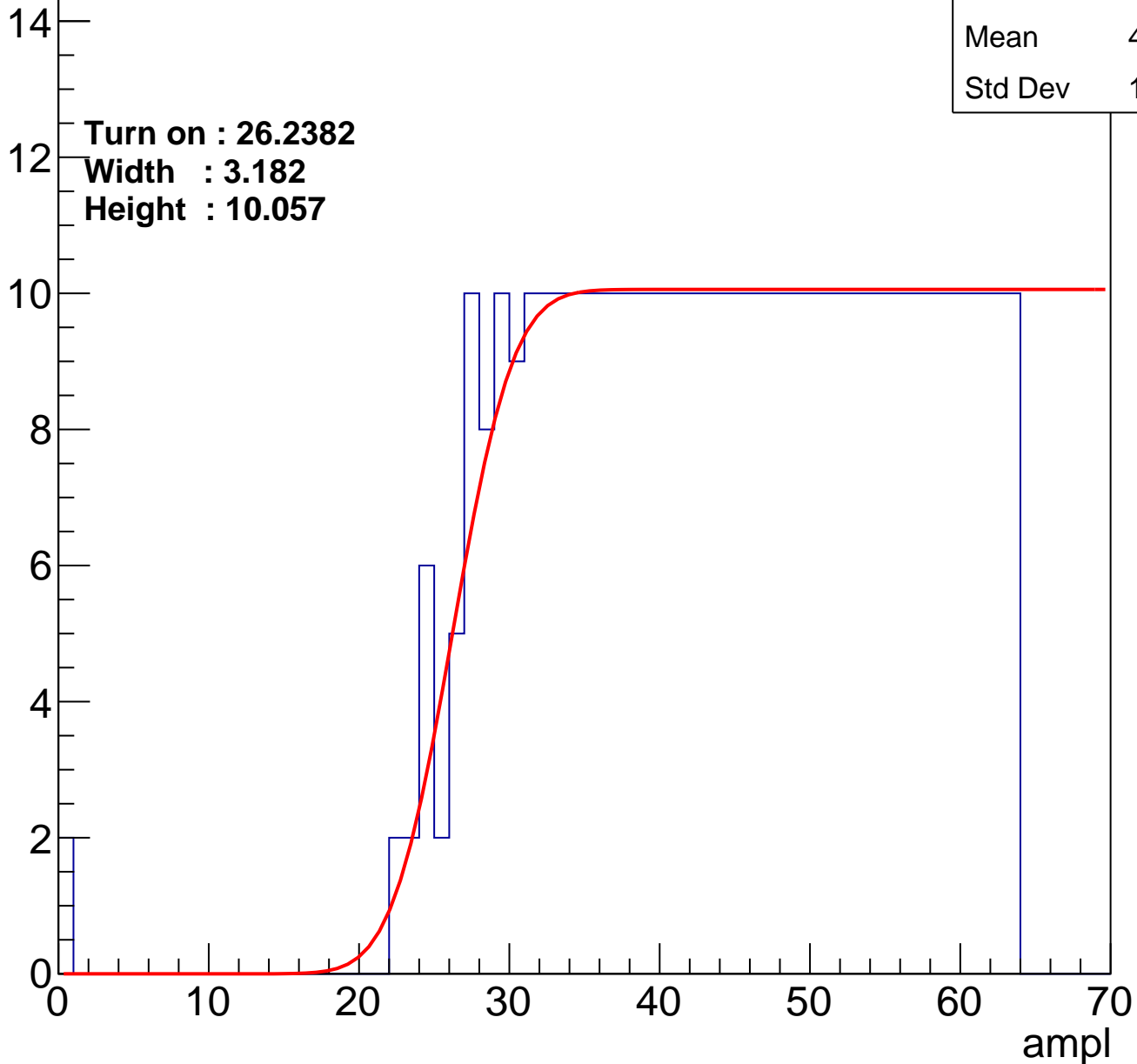
Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 26.2382

Width : 3.182

Height : 10.057

Entry



B0L000S, U25-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.07
Std Dev	11.78

Turn on : 26.7710

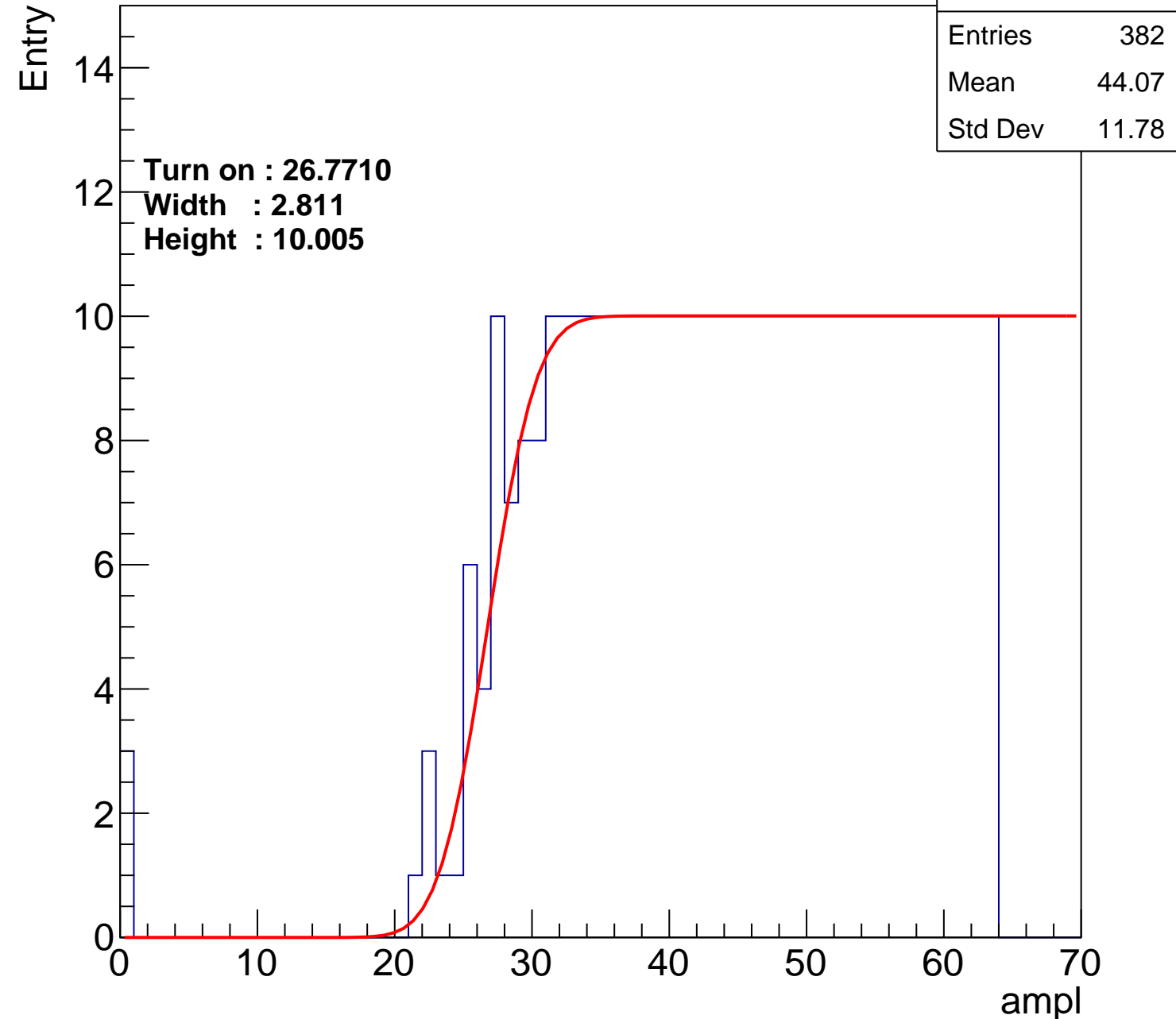
Width : 2.811

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.06
Std Dev	11.76

Turn on : 26.1877

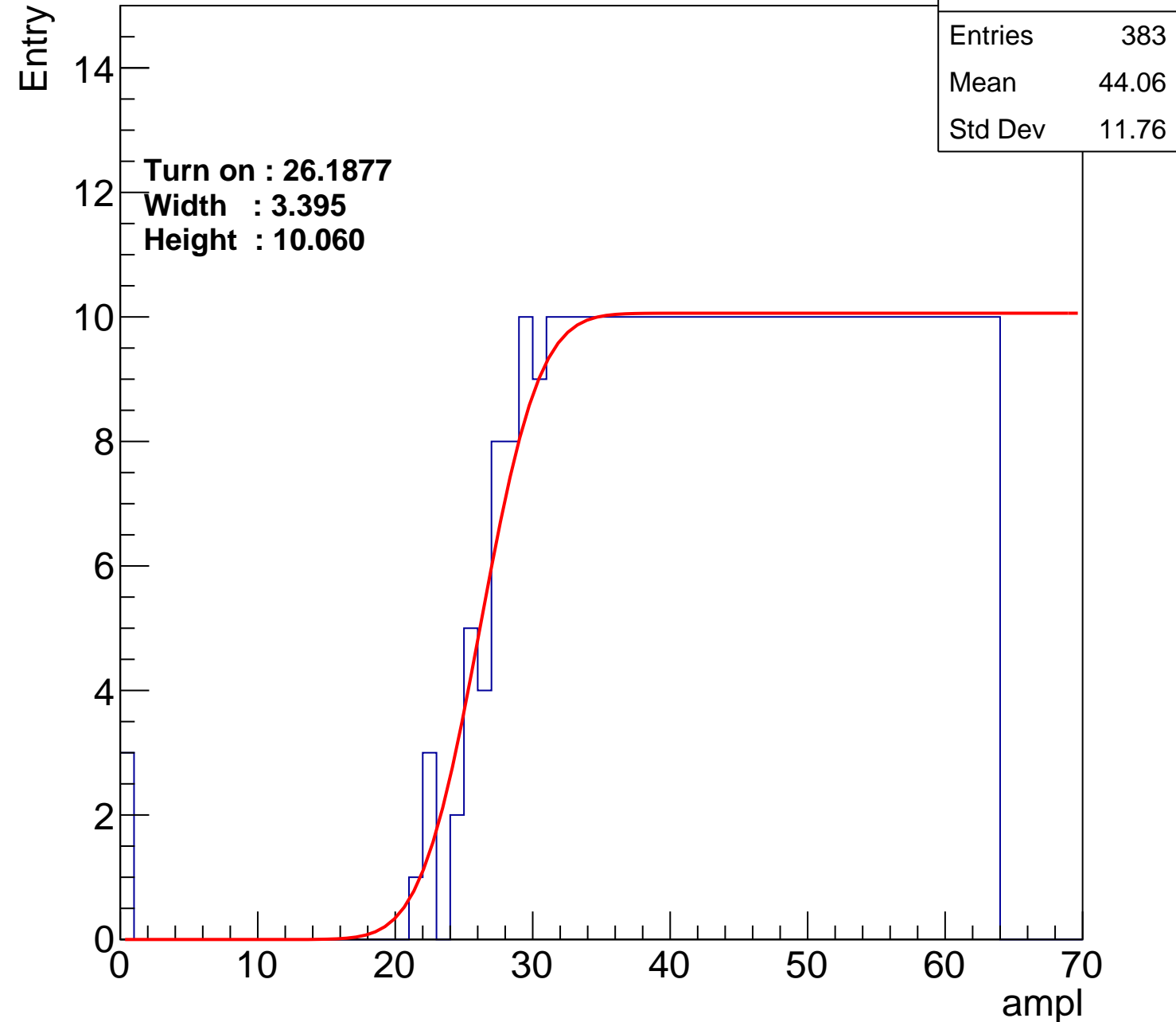
Width : 3.395

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.72
Std Dev	12.21

Turn on : 26.0329

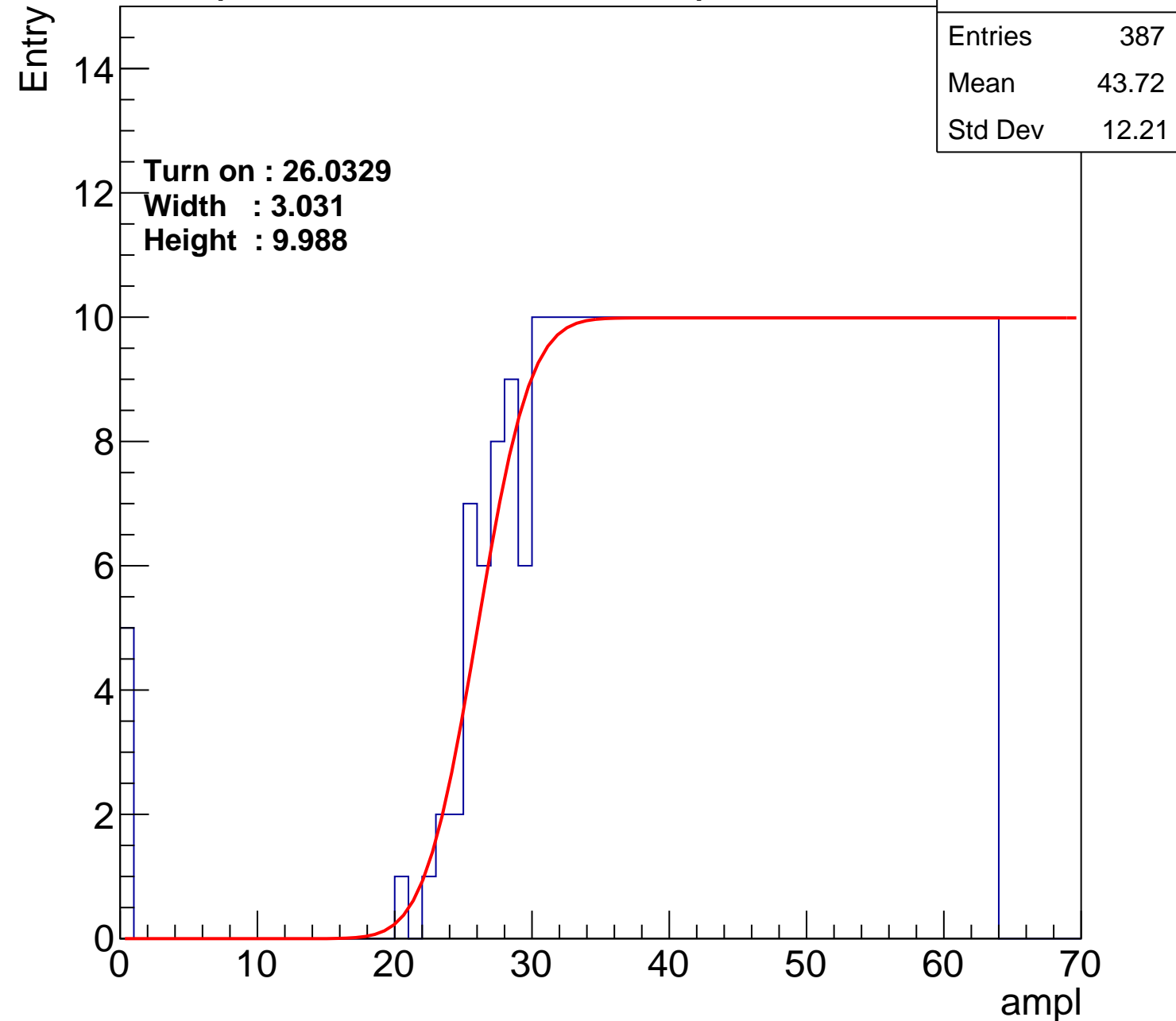
Width : 3.031

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.9951

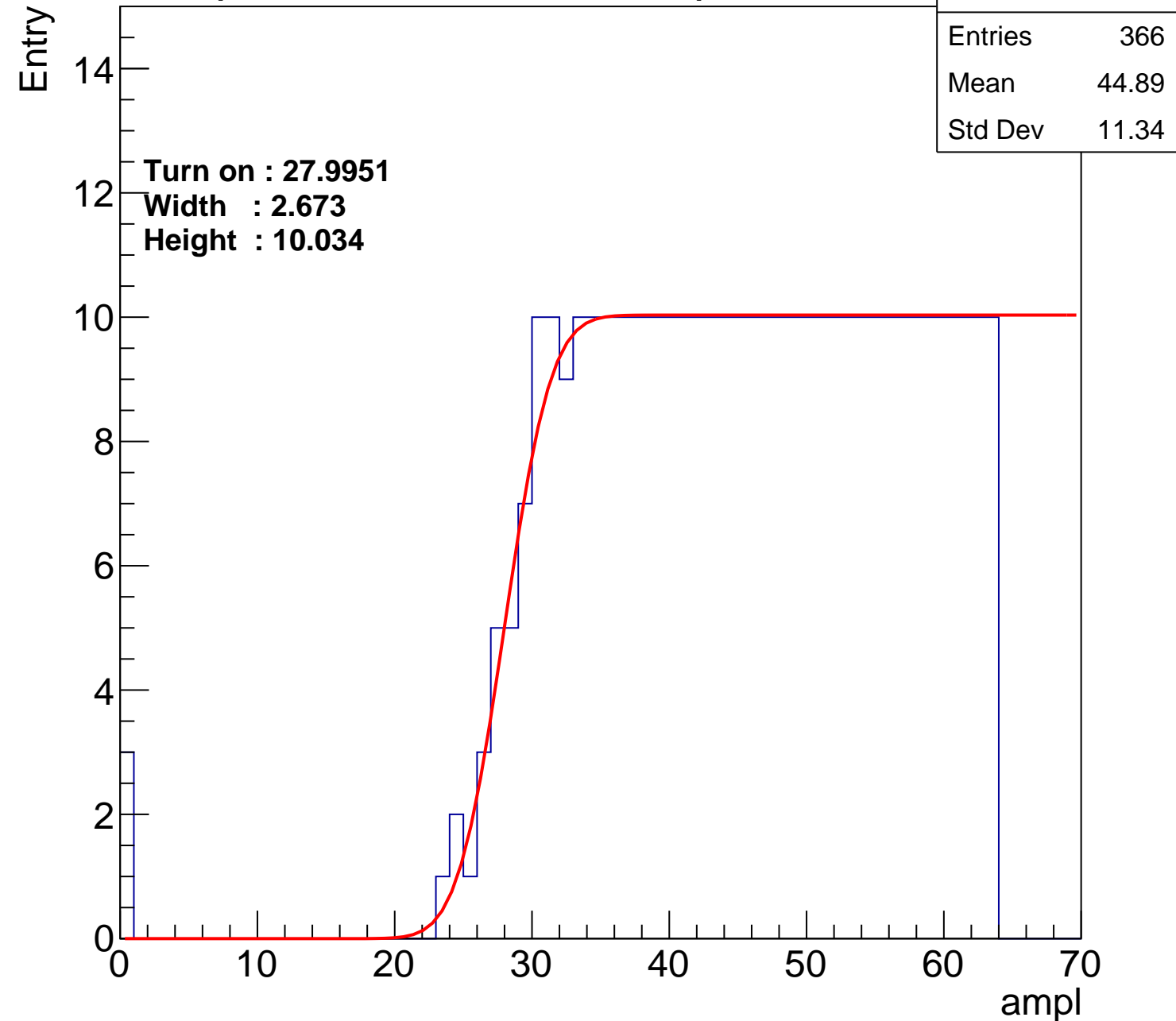
Width : 2.673

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.27
Std Dev	11.01

Turn on : 28.3634

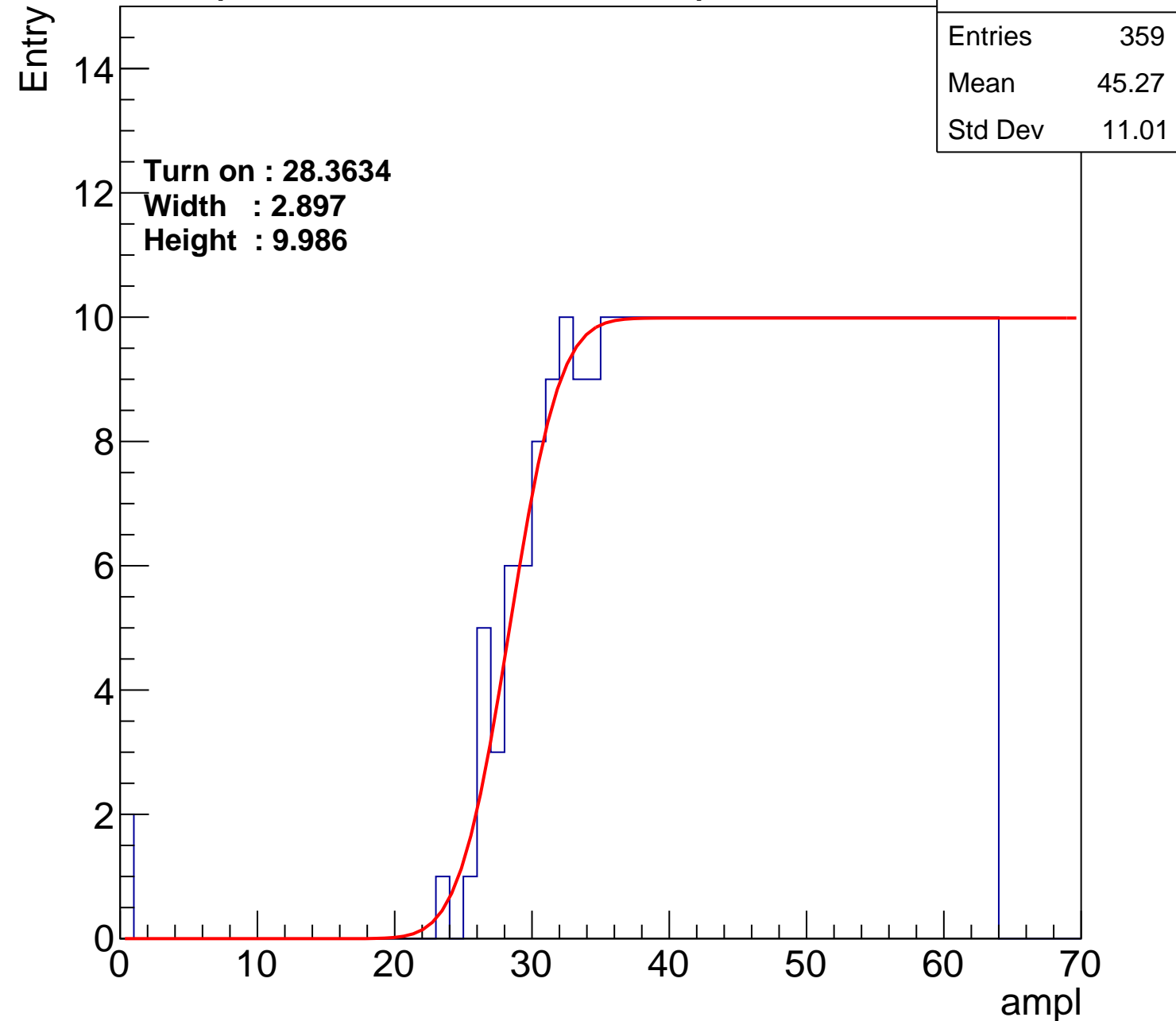
Width : 2.897

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 25.9040

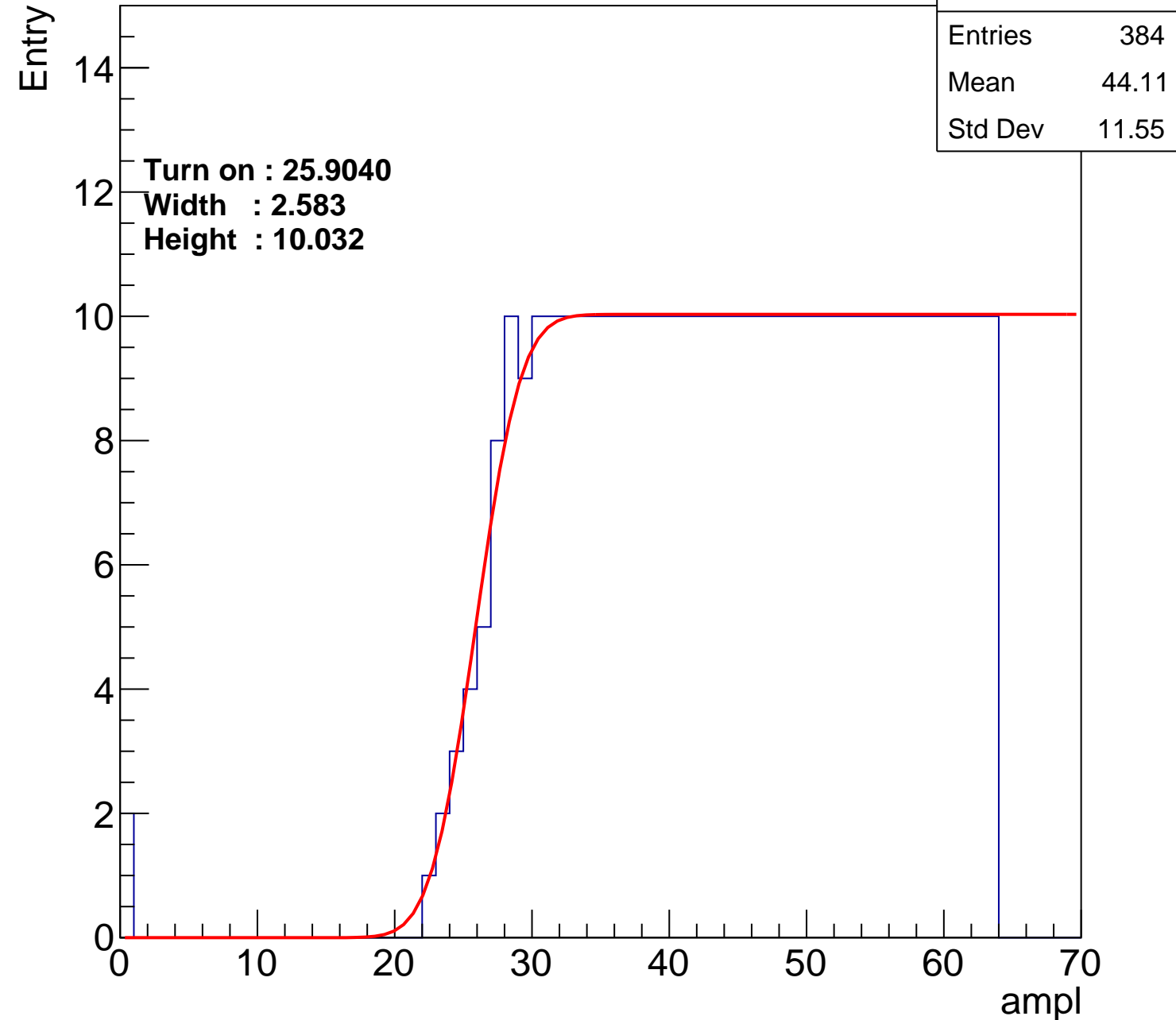
Width : 2.583

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.09
Std Dev	11.15

Turn on : 27.9847

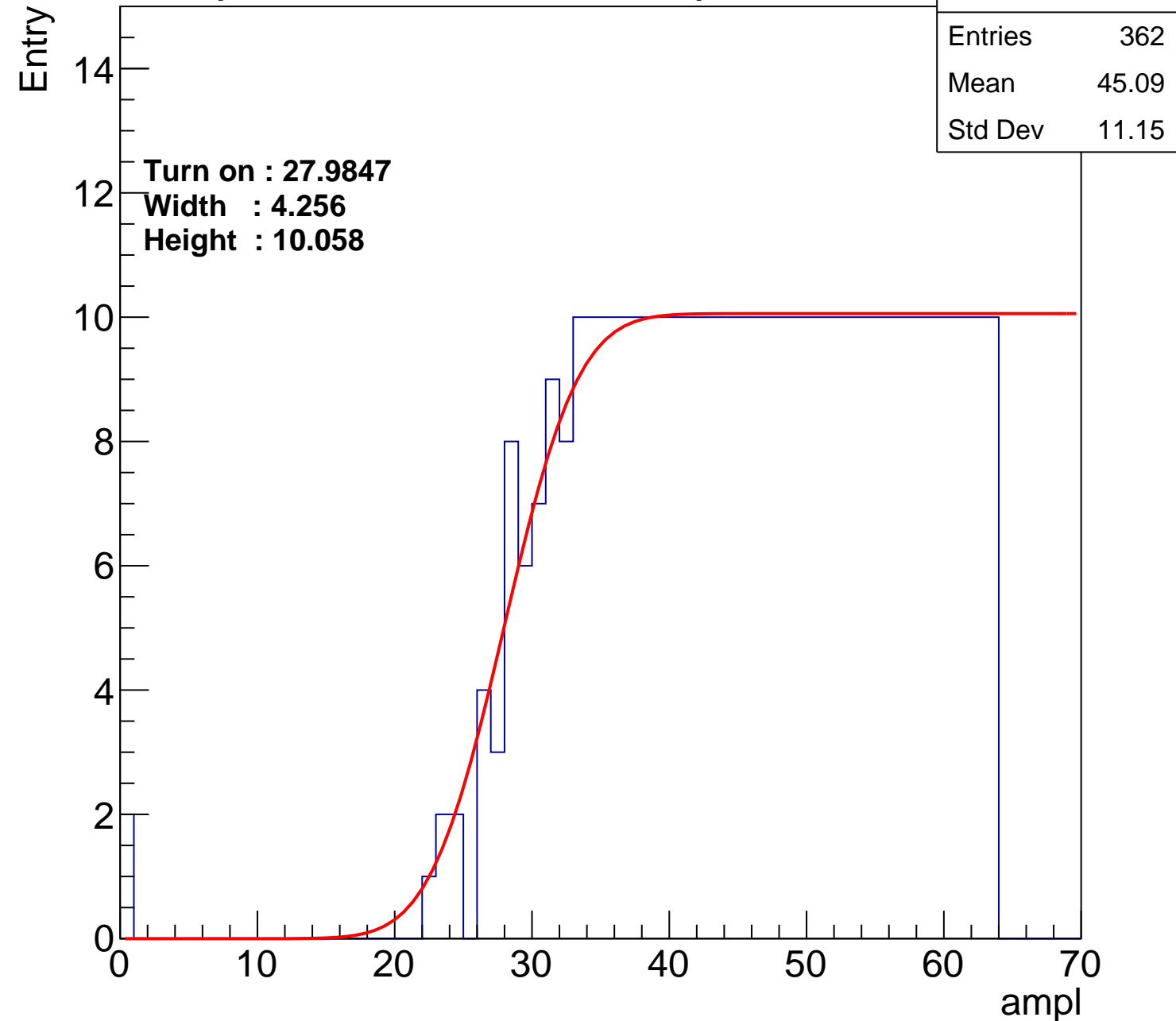
Width : 4.256

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch54

calib_packv5_042523_0143.root, FC#5, port B1

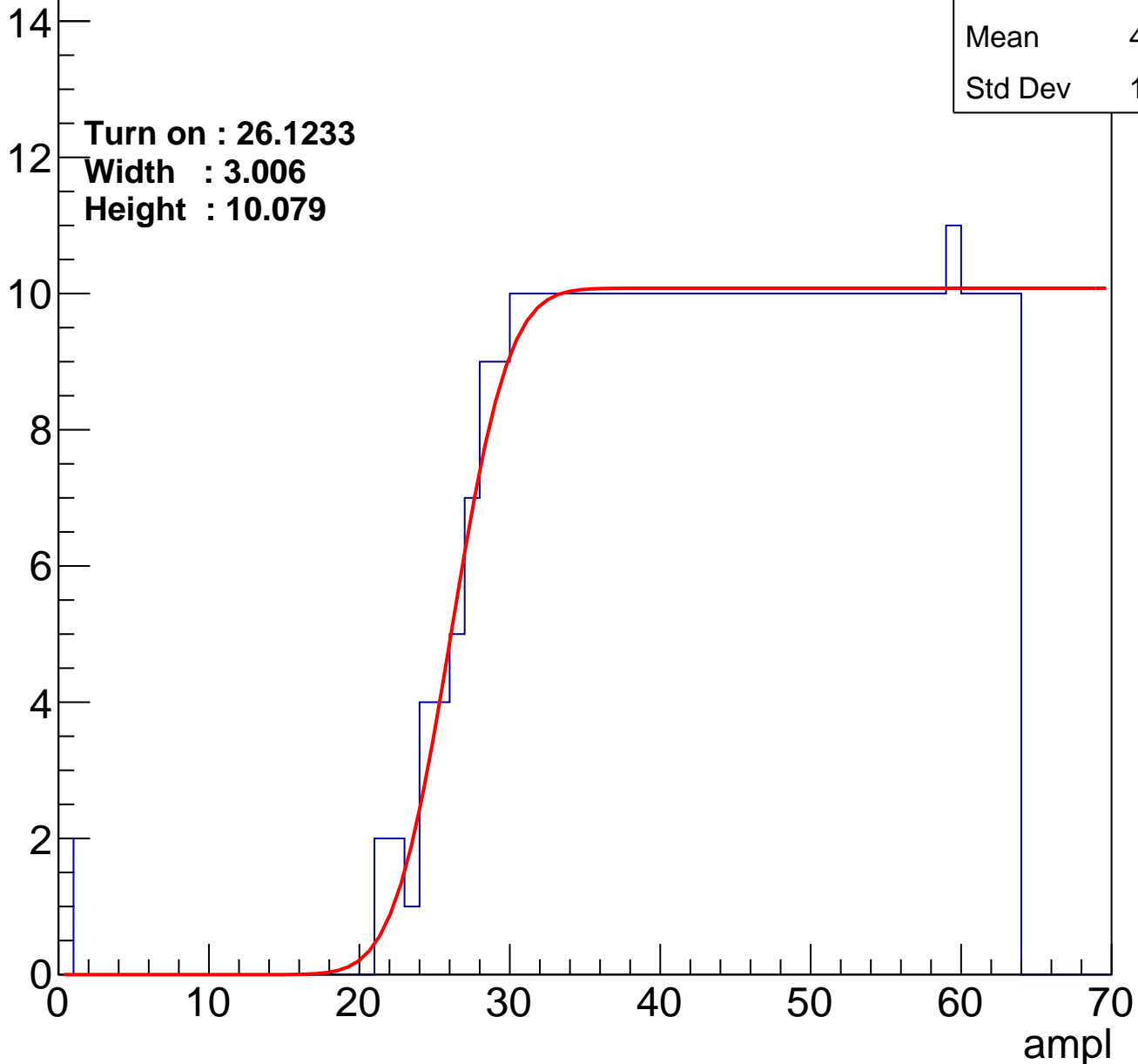
Entries	386
Mean	44.06
Std Dev	11.65

Turn on : 26.1233

Width : 3.006

Height : 10.079

Entry



B0L000S, U25-ch55

calib_packv5_042523_0143.root, FC#5, port B1

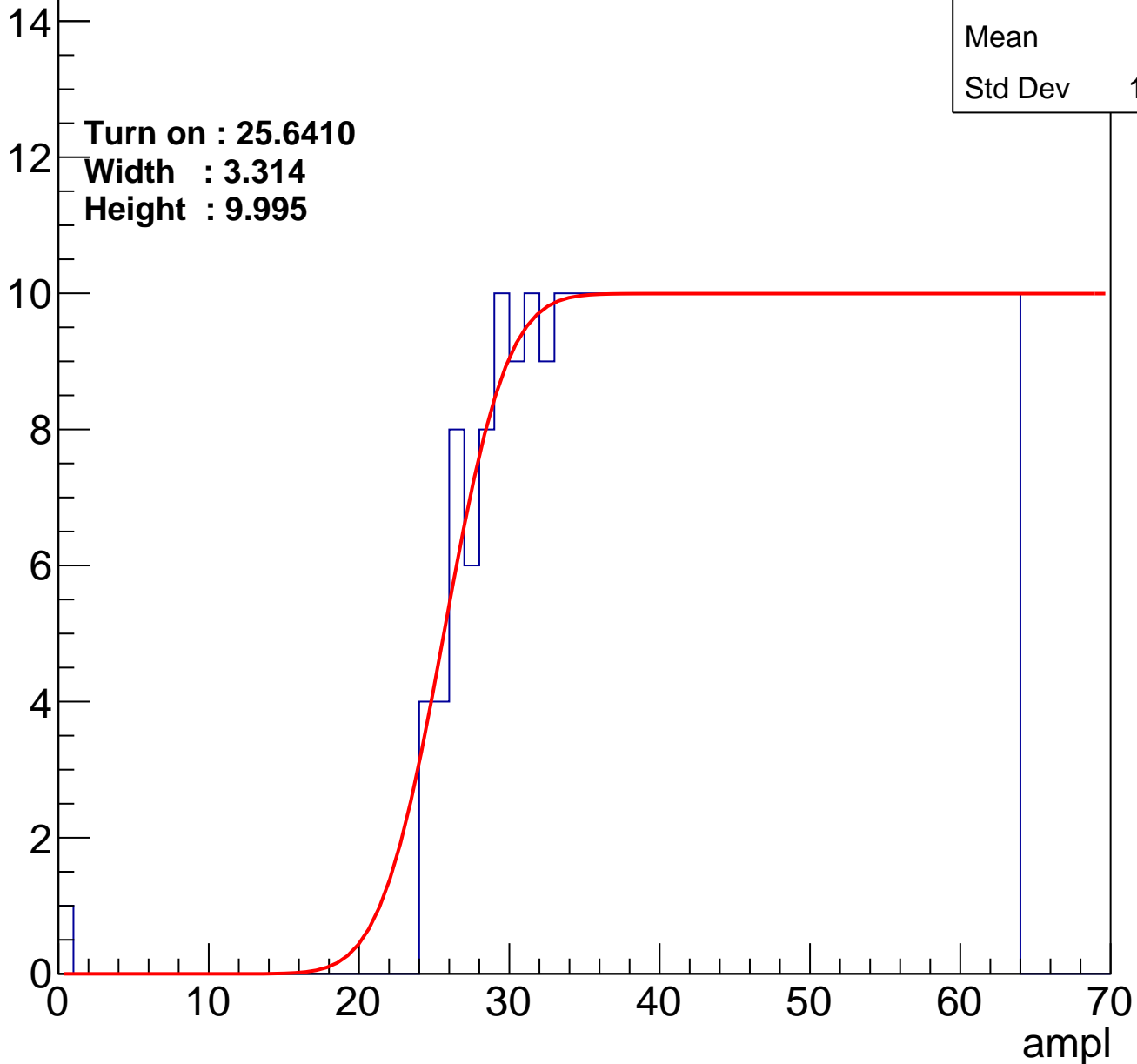
Entries	379
Mean	44.4
Std Dev	11.26

Turn on : 25.6410

Width : 3.314

Height : 9.995

Entry



B0L000S, U25-ch56

calib_packv5_042523_0143.root, FC#5, port B1

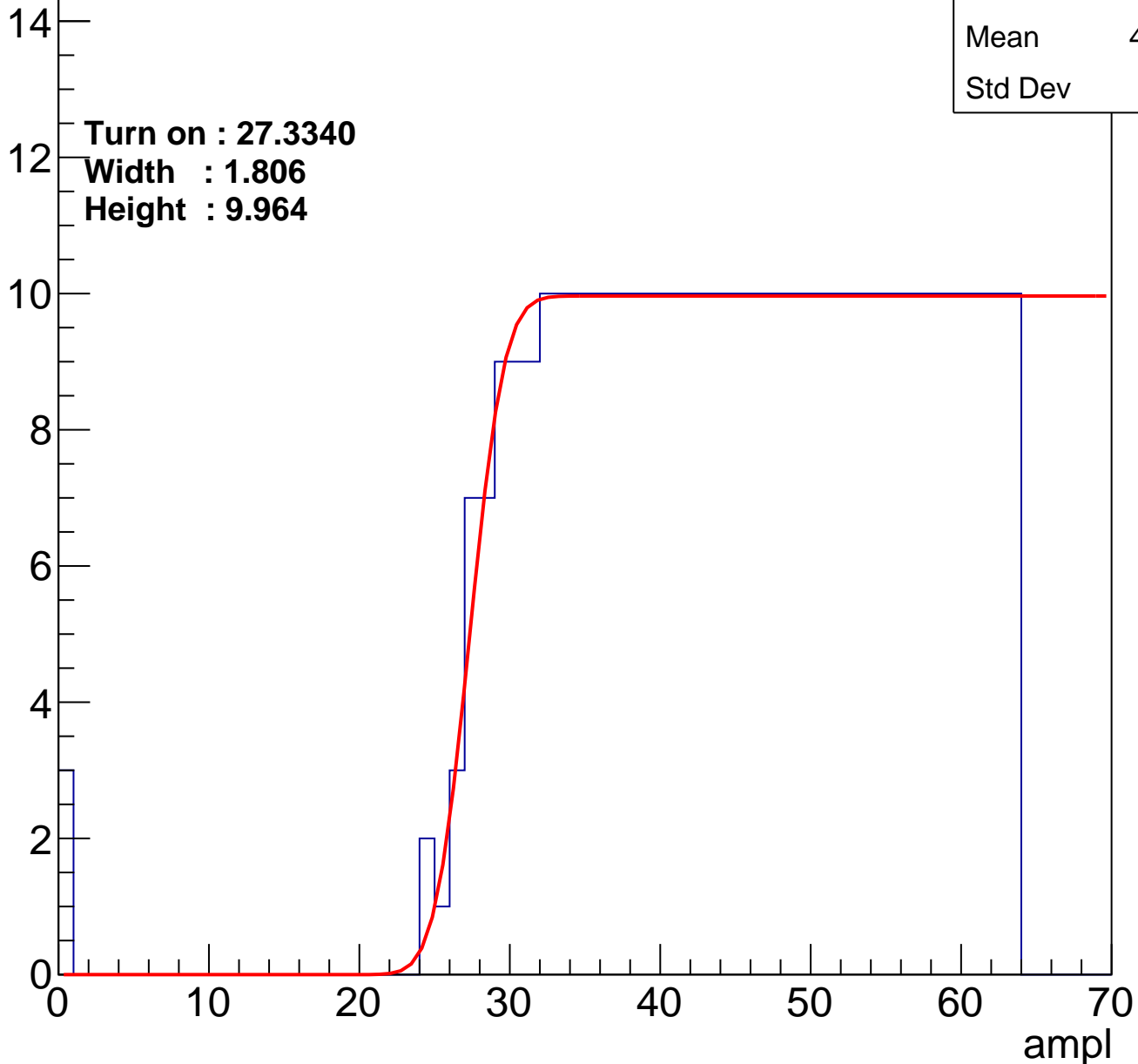
Entries	370
Mean	44.72
Std Dev	11.4

Turn on : 27.3340

Width : 1.806

Height : 9.964

Entry



B0L000S, U25-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.3843

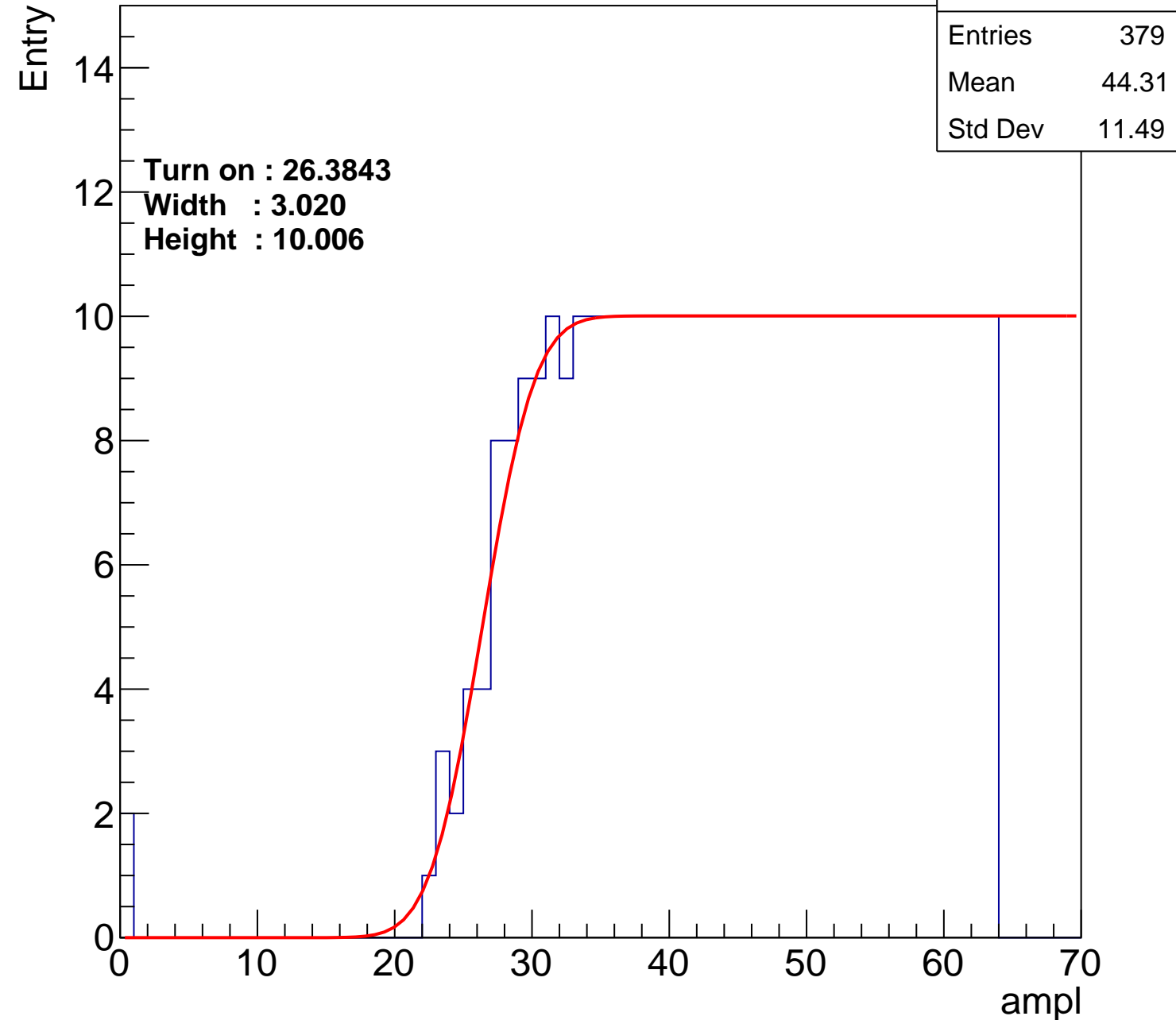
Width : 3.020

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch58

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.7539

Width : 3.030

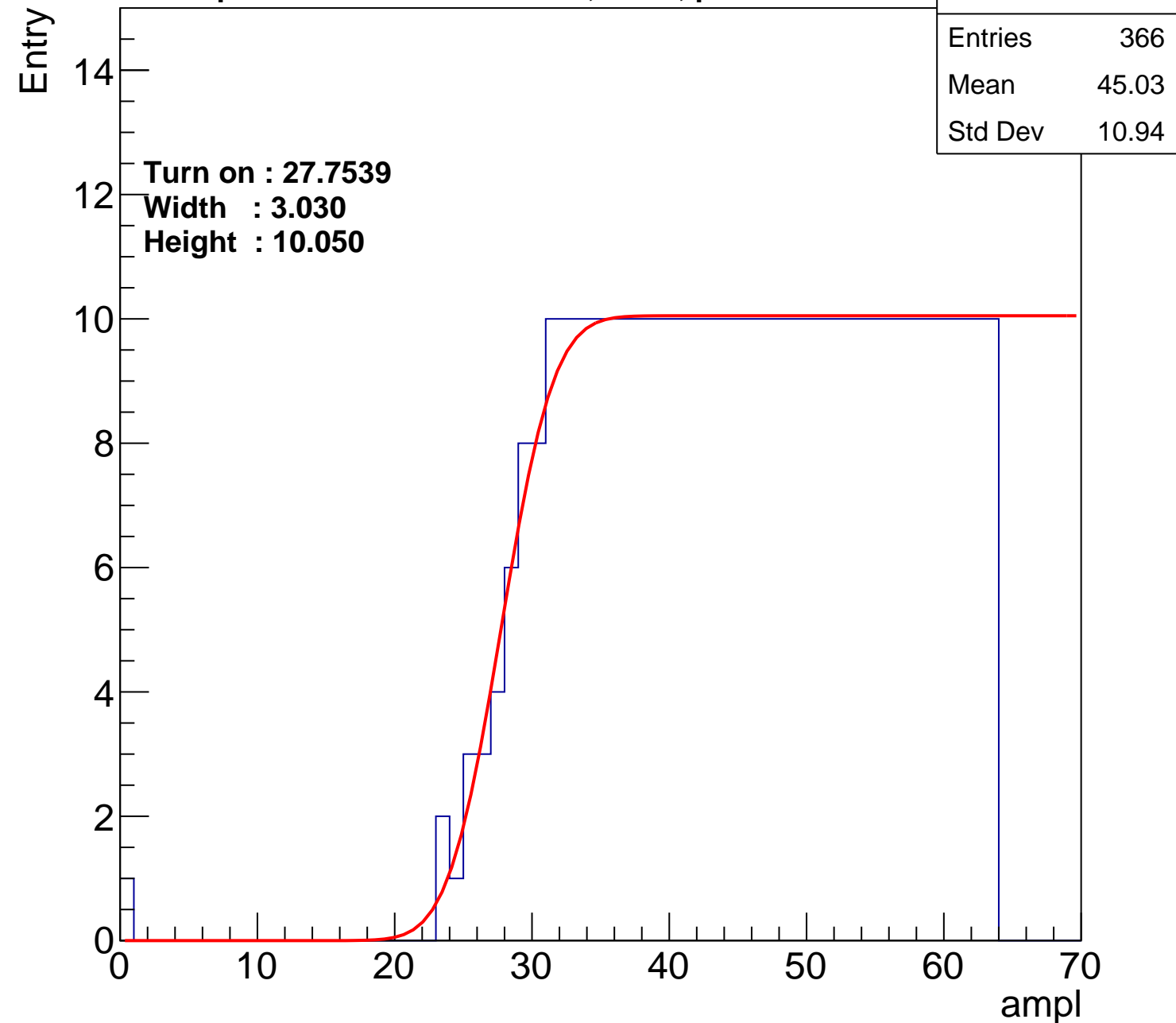
Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U25-ch59

calib_packv5_042523_0143.root, FC#5, port B1

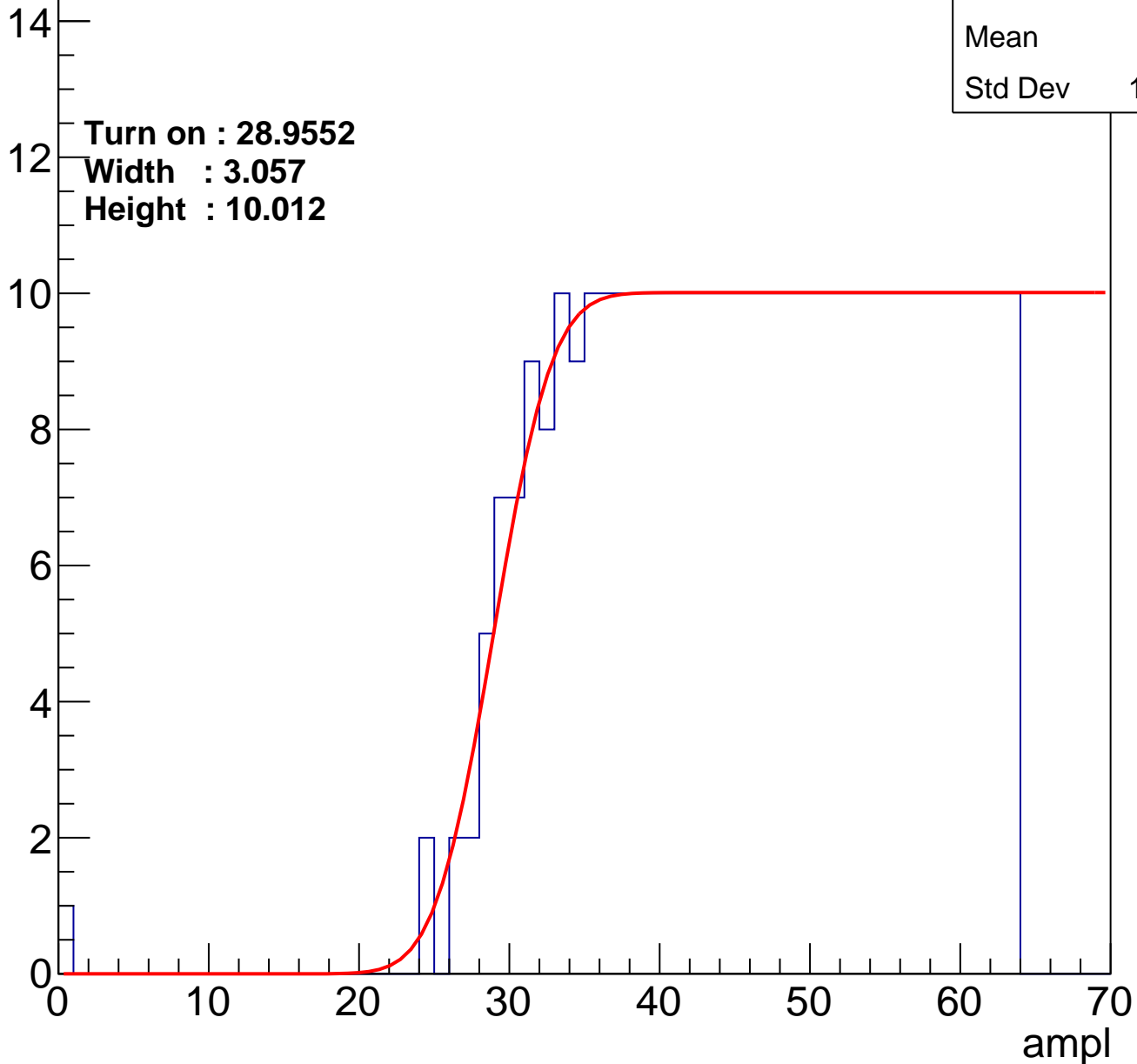
Entries	352
Mean	45.7
Std Dev	10.59

Turn on : 28.9552

Width : 3.057

Height : 10.012

Entry



B0L000S, U25-ch60

calib_packv5_042523_0143.root, FC#5, port B1

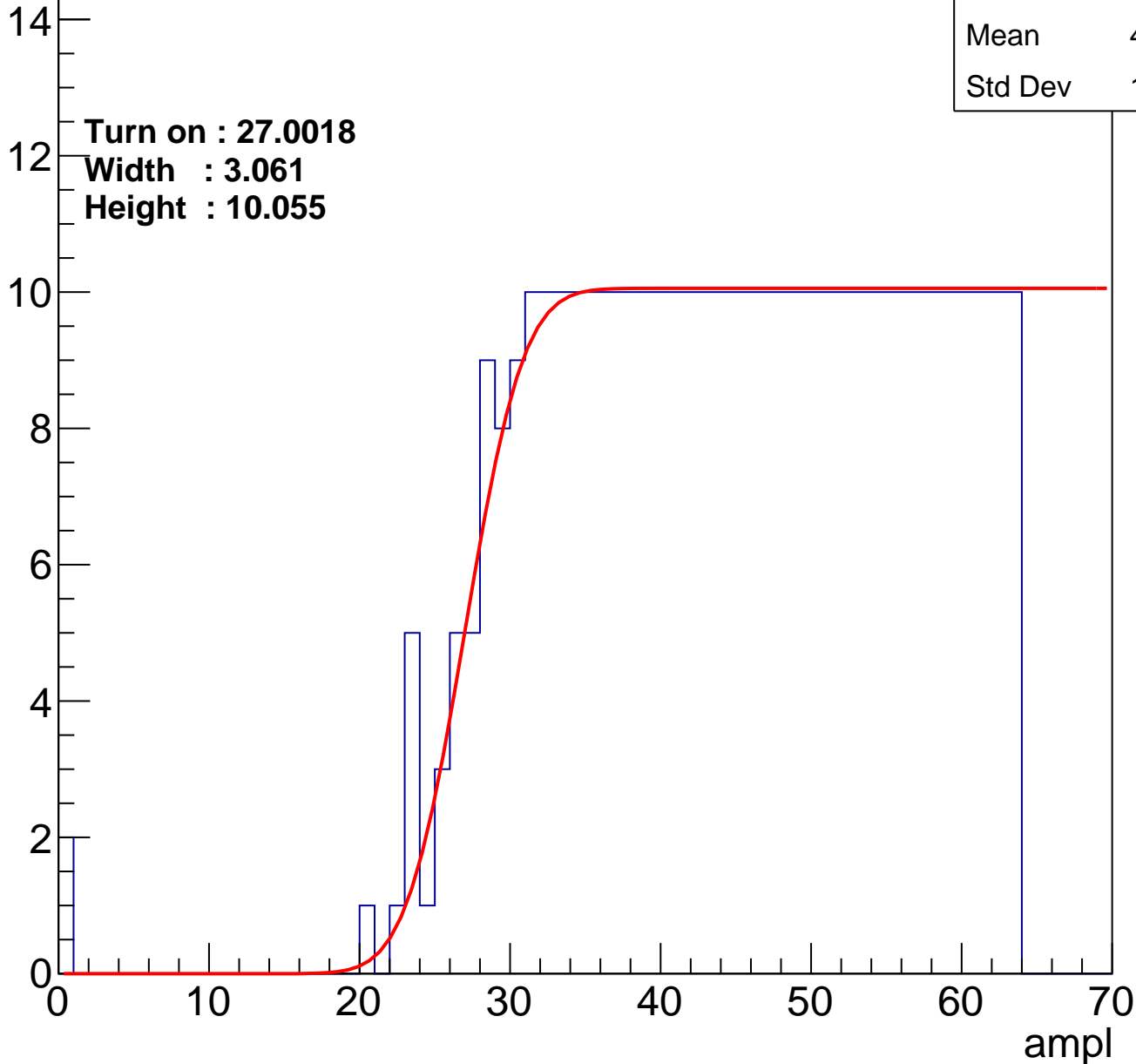
Entries	379
Mean	44.29
Std Dev	11.53

Turn on : 27.0018

Width : 3.061

Height : 10.055

Entry



B0L000S, U25-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.45
Std Dev	11.41

Turn on : 25.6249

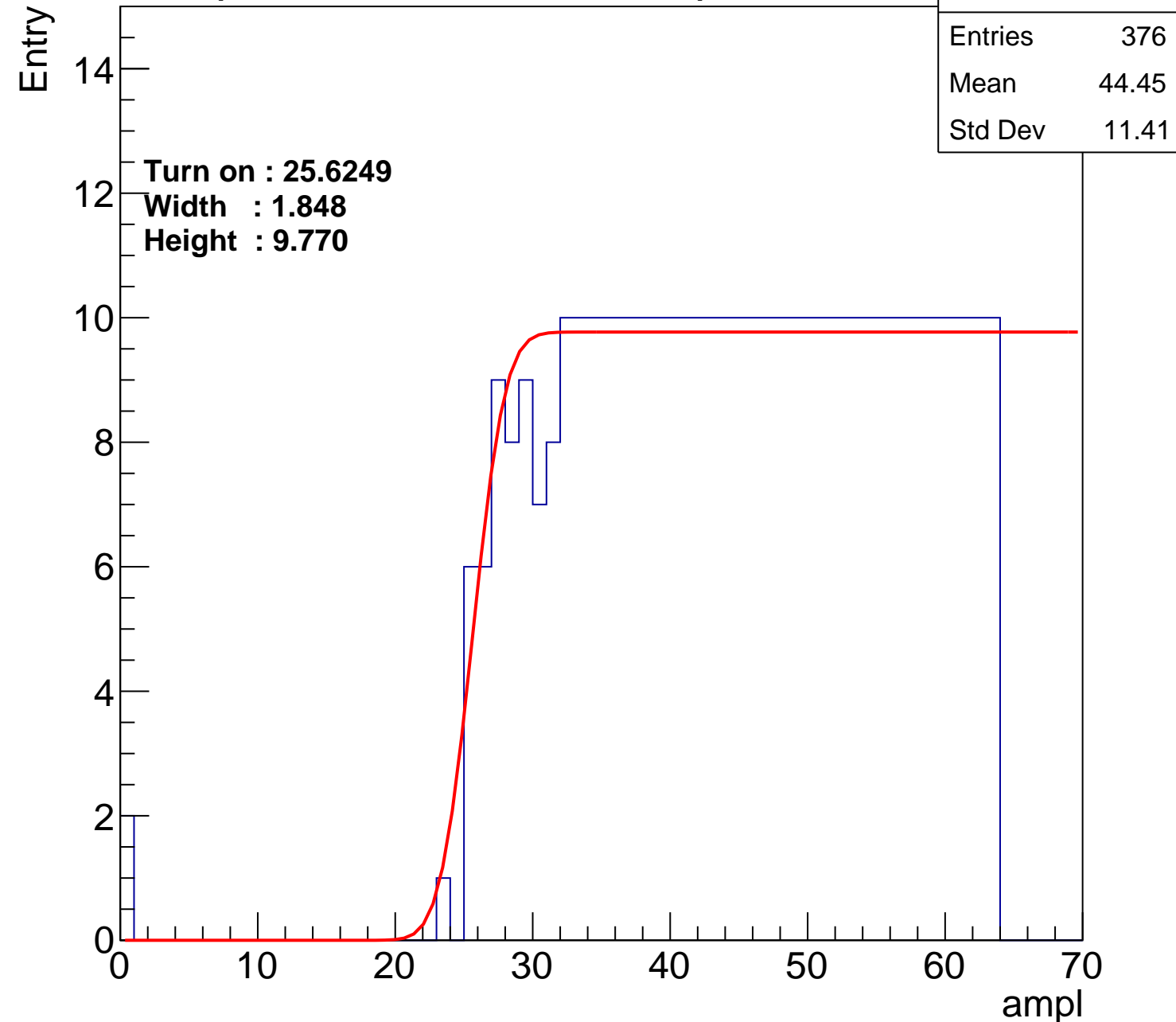
Width : 1.848

Height : 9.770

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 27.4057

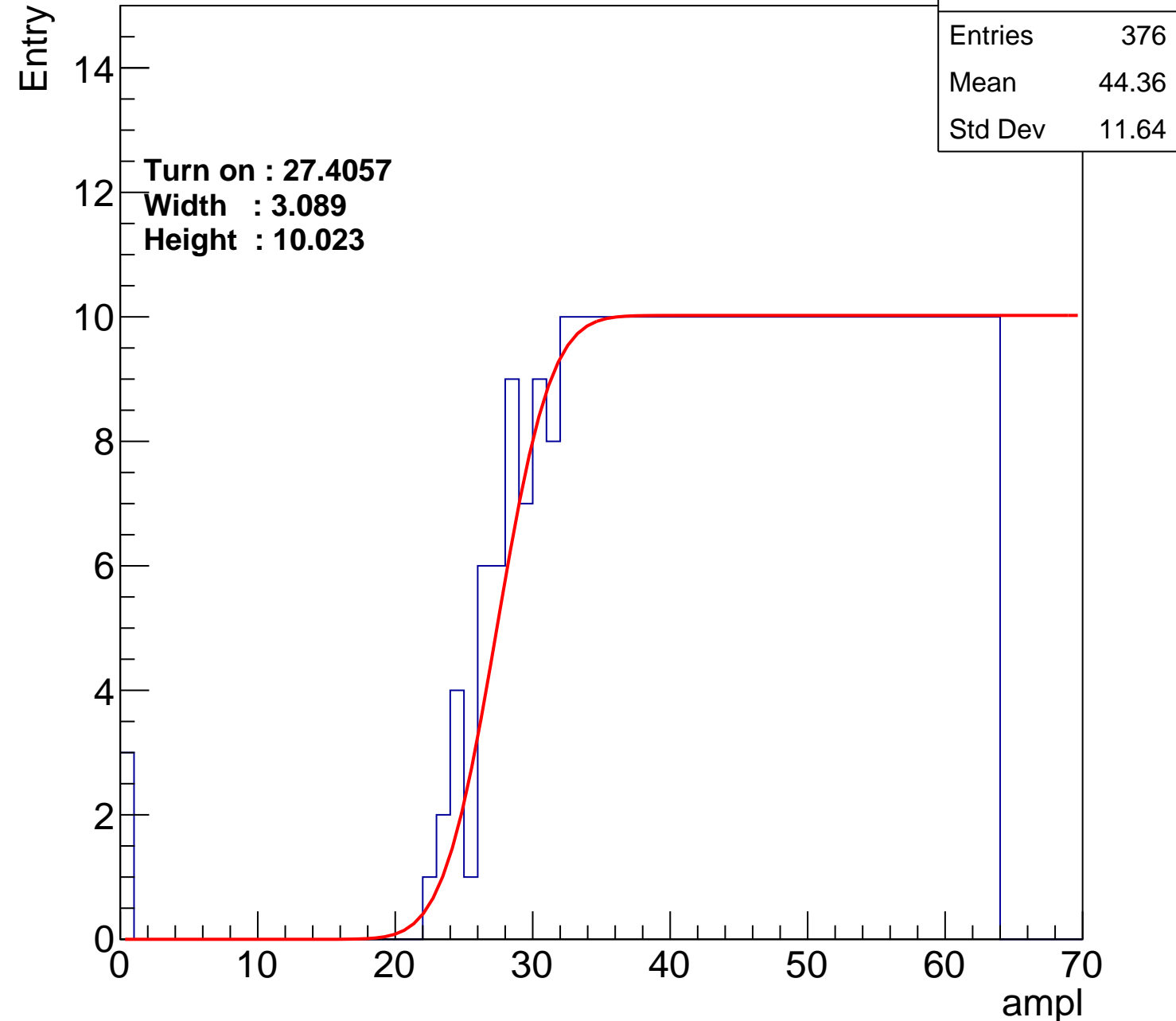
Width : 3.089

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.84

Turn on : 26.6925

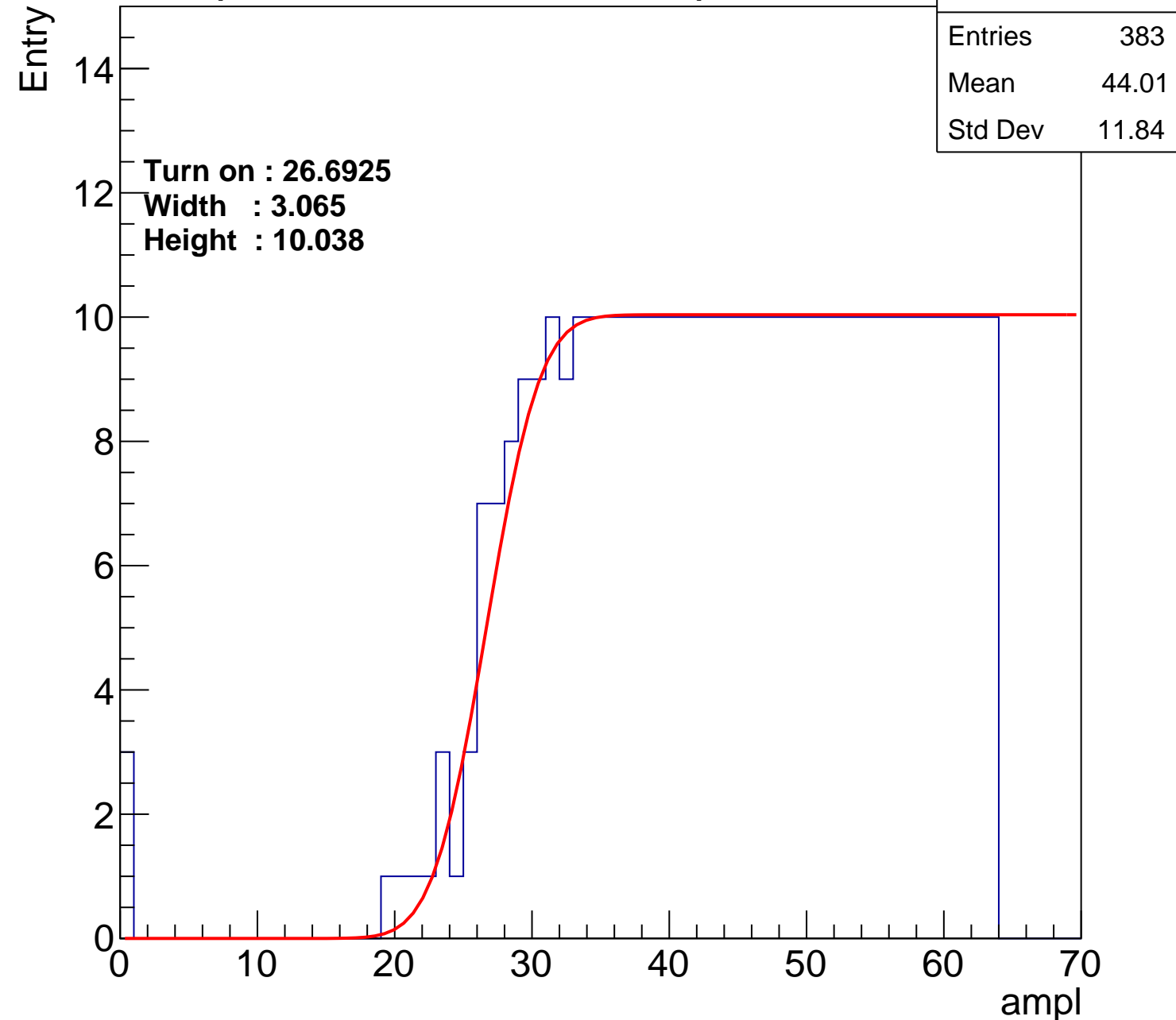
Width : 3.065

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch64

calib_packv5_042523_0143.root, FC#5, port B1

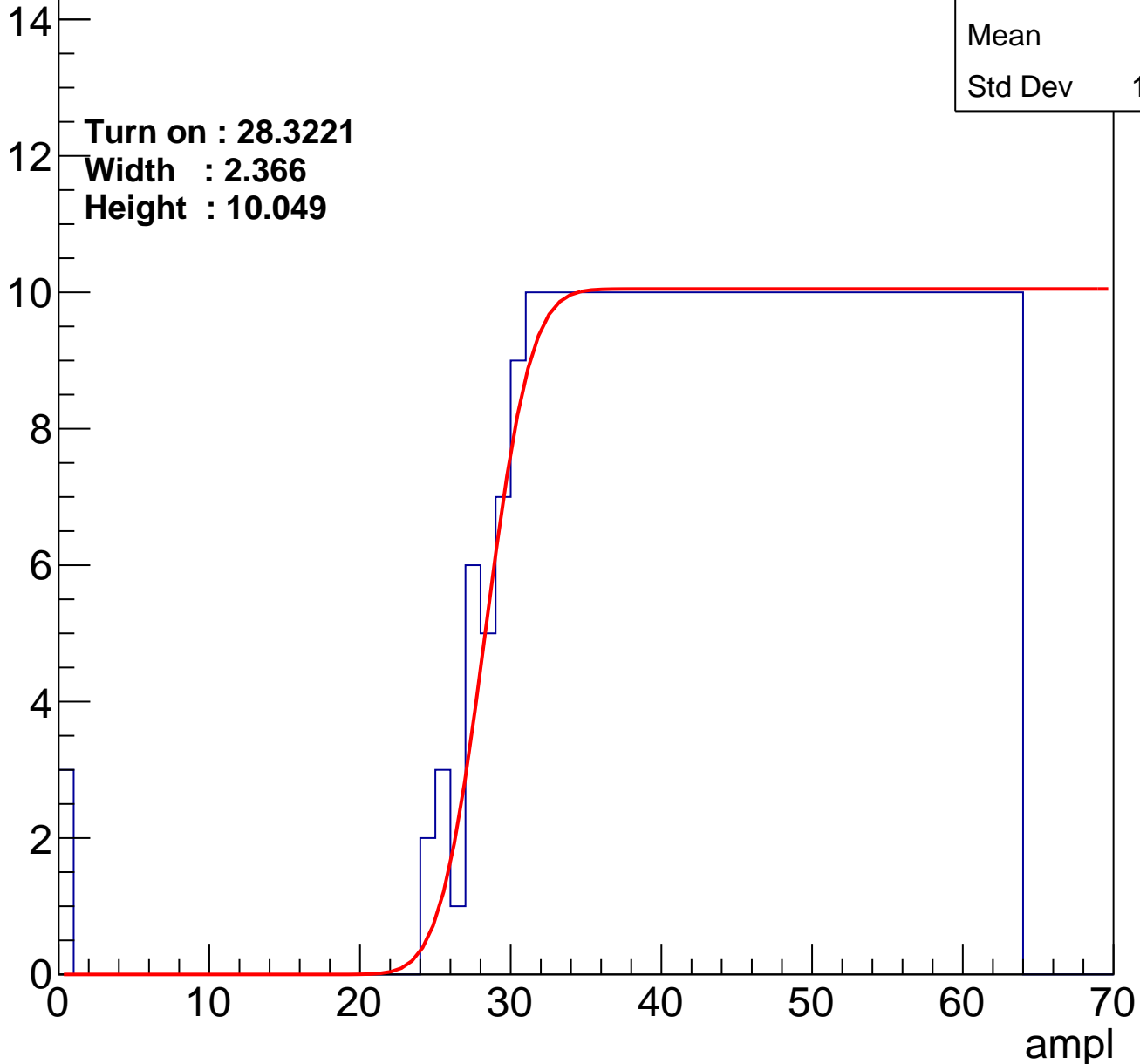
Entries	366
Mean	44.9
Std Dev	11.33

Turn on : 28.3221

Width : 2.366

Height : 10.049

Entry



B0L000S, U25-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.03
Std Dev	11.87

Turn on : 26.1953

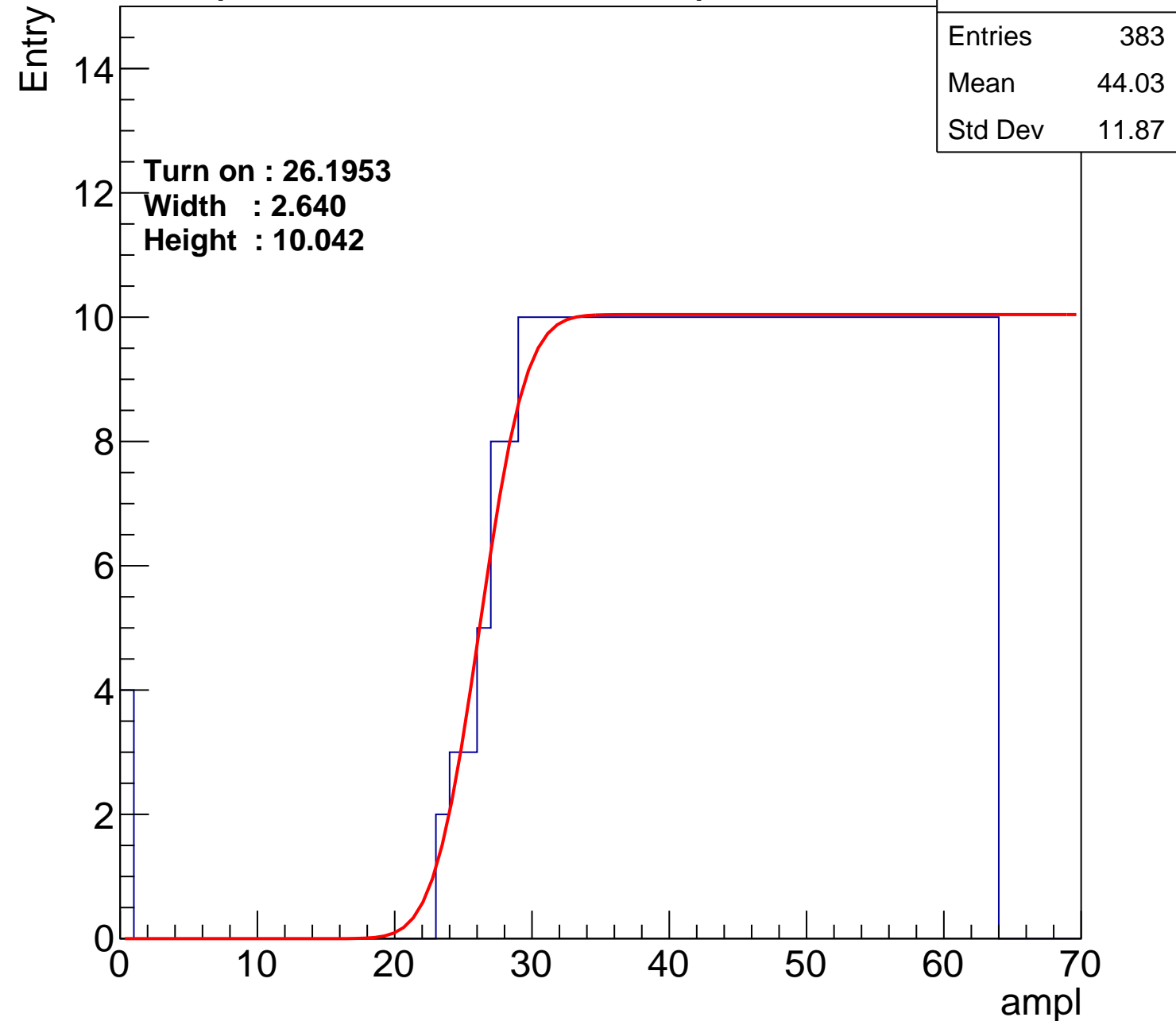
Width : 2.640

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

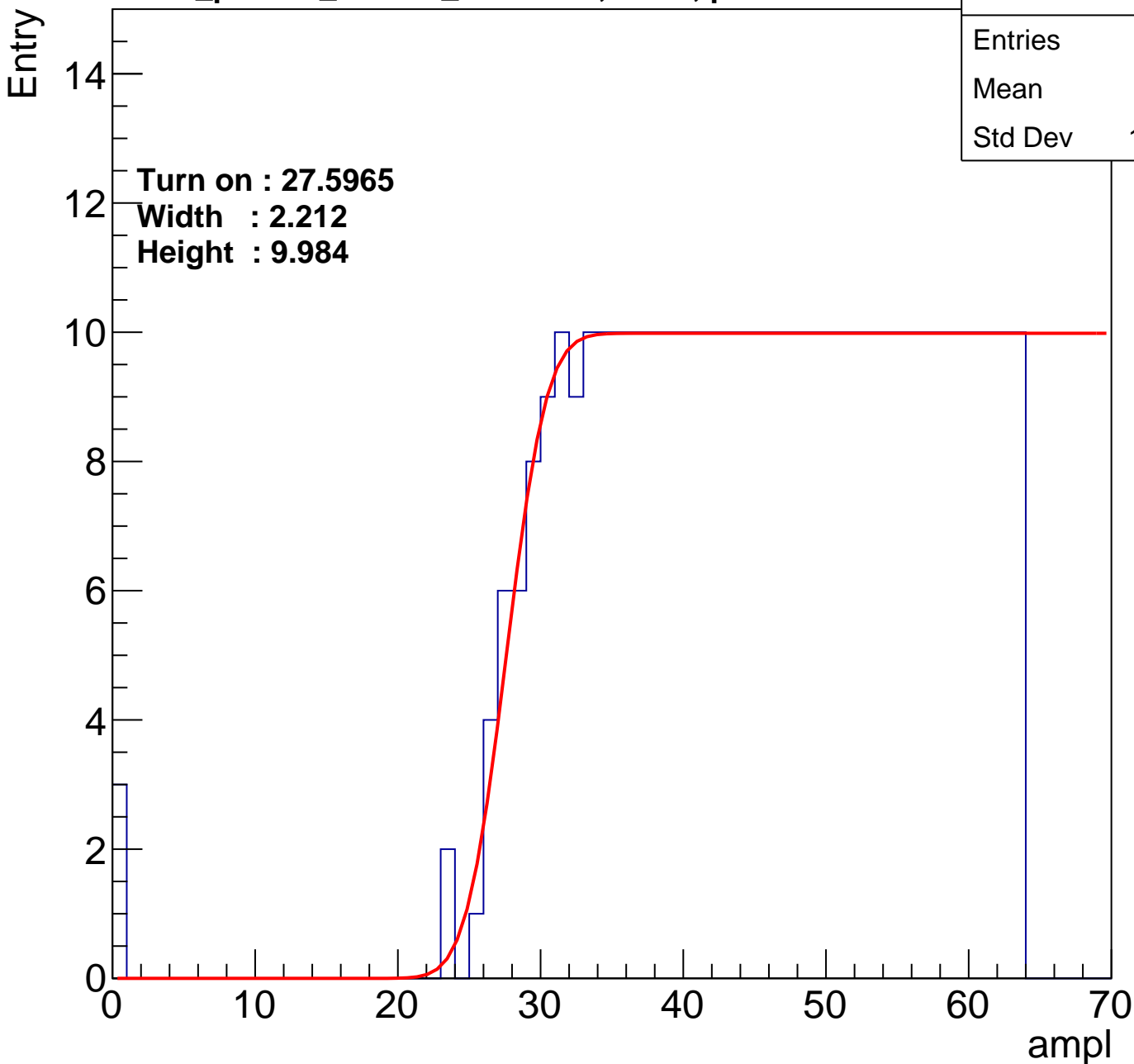
calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.8
Std Dev	11.38

Turn on : 27.5965

Width : 2.212

Height : 9.984



B0L000S, U25-ch67

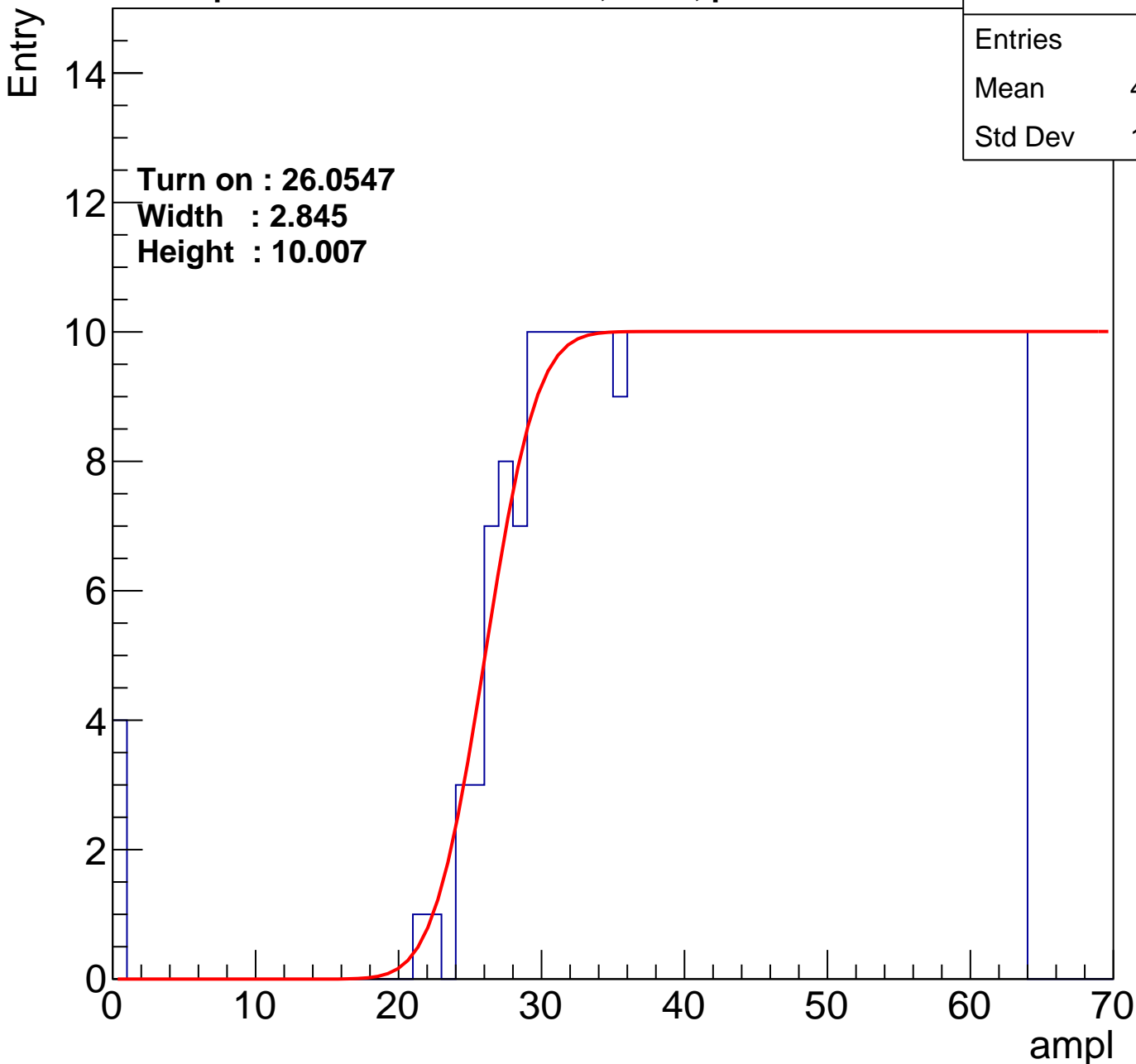
calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	43.99
Std Dev	11.92

Turn on : 26.0547

Width : 2.845

Height : 10.007



B0L000S, U25-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.64
Std Dev	10.79

Turn on : 29.0739

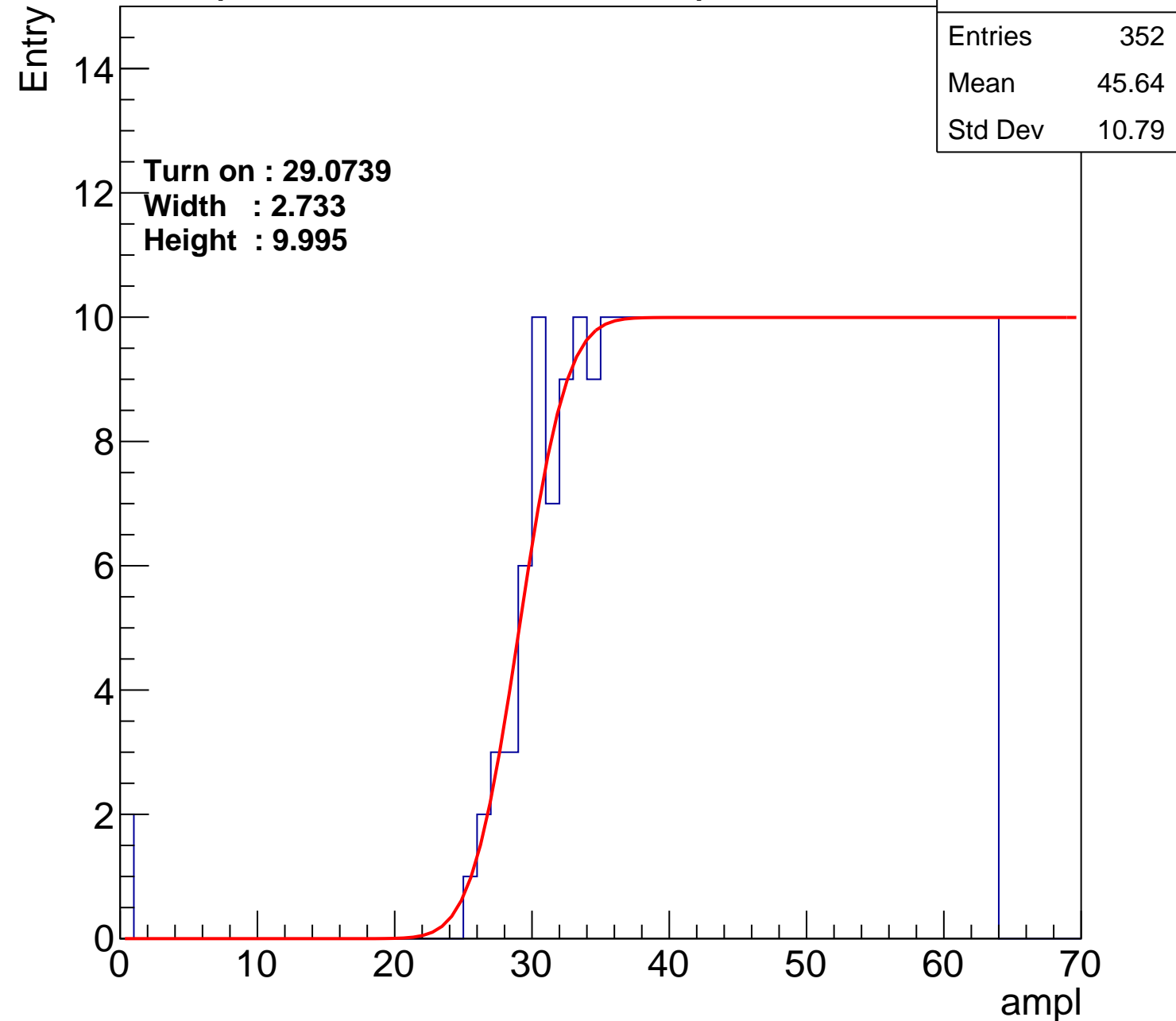
Width : 2.733

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch69

calib_packv5_042523_0143.root, FC#5, port B1

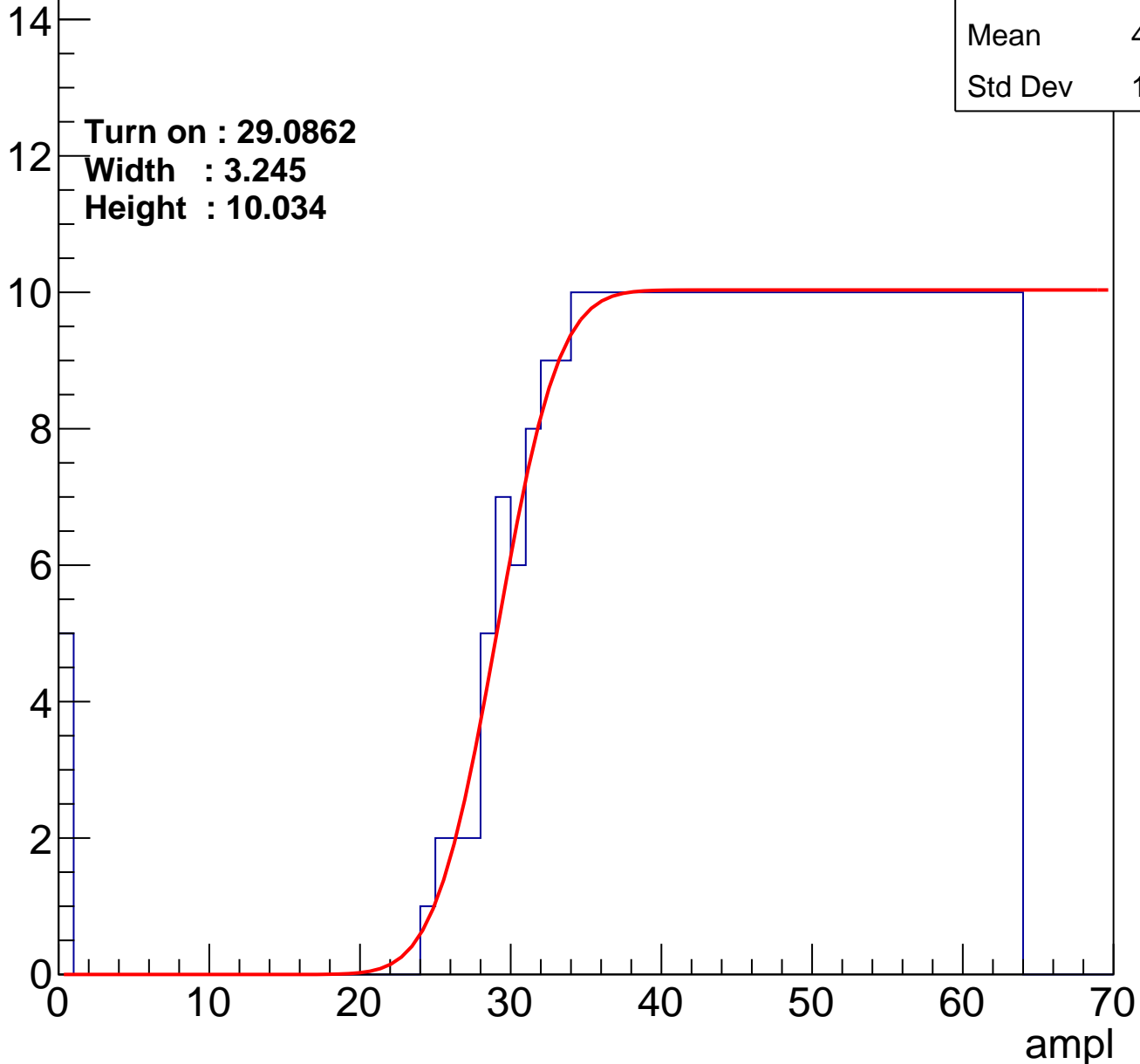
Entries	356
Mean	45.19
Std Dev	11.59

Turn on : 29.0862

Width : 3.245

Height : 10.034

Entry



B0L000S, U25-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.7784

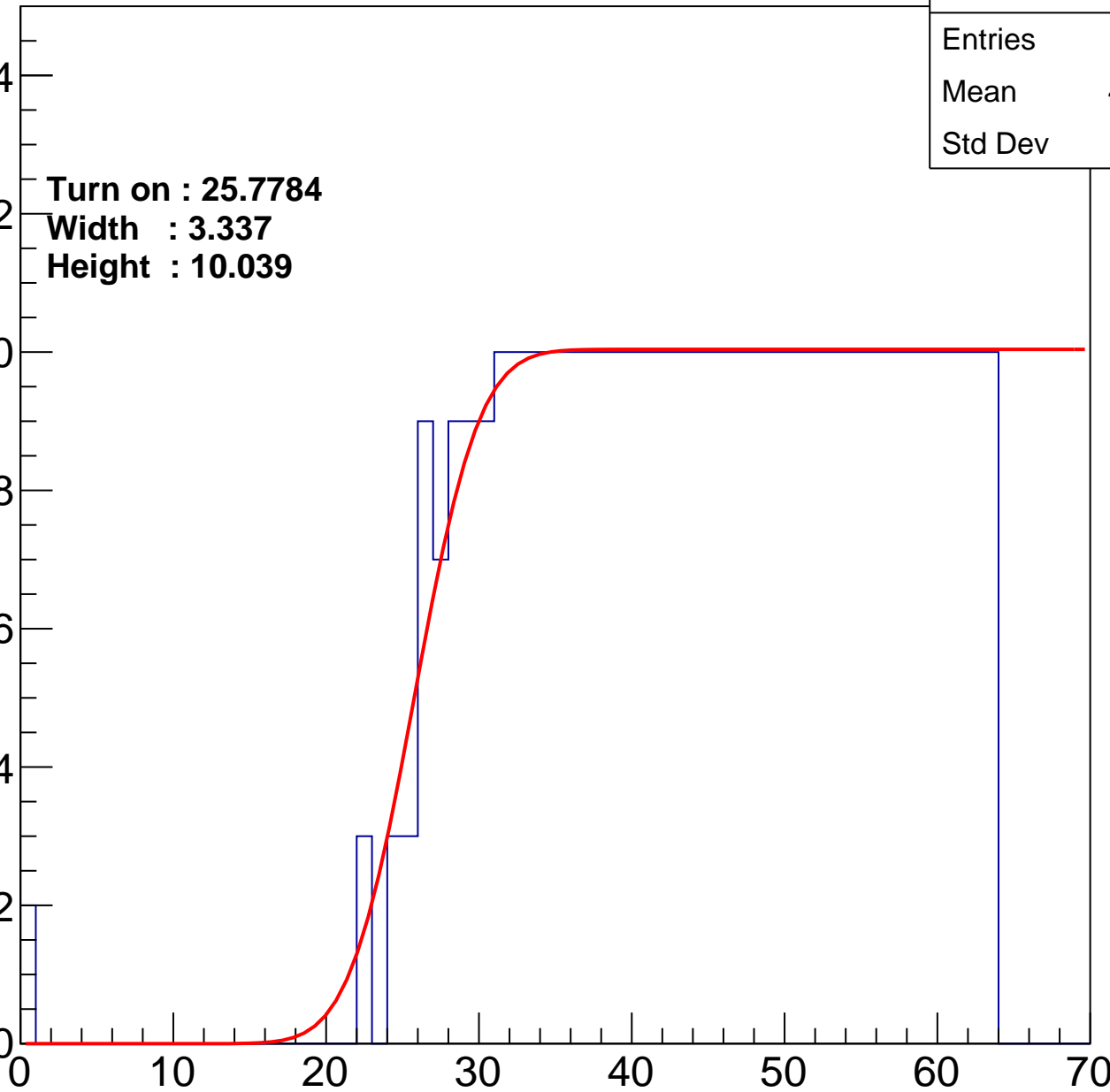
Width : 3.337

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	43.96
Std Dev	12.27

Turn on : 26.7435

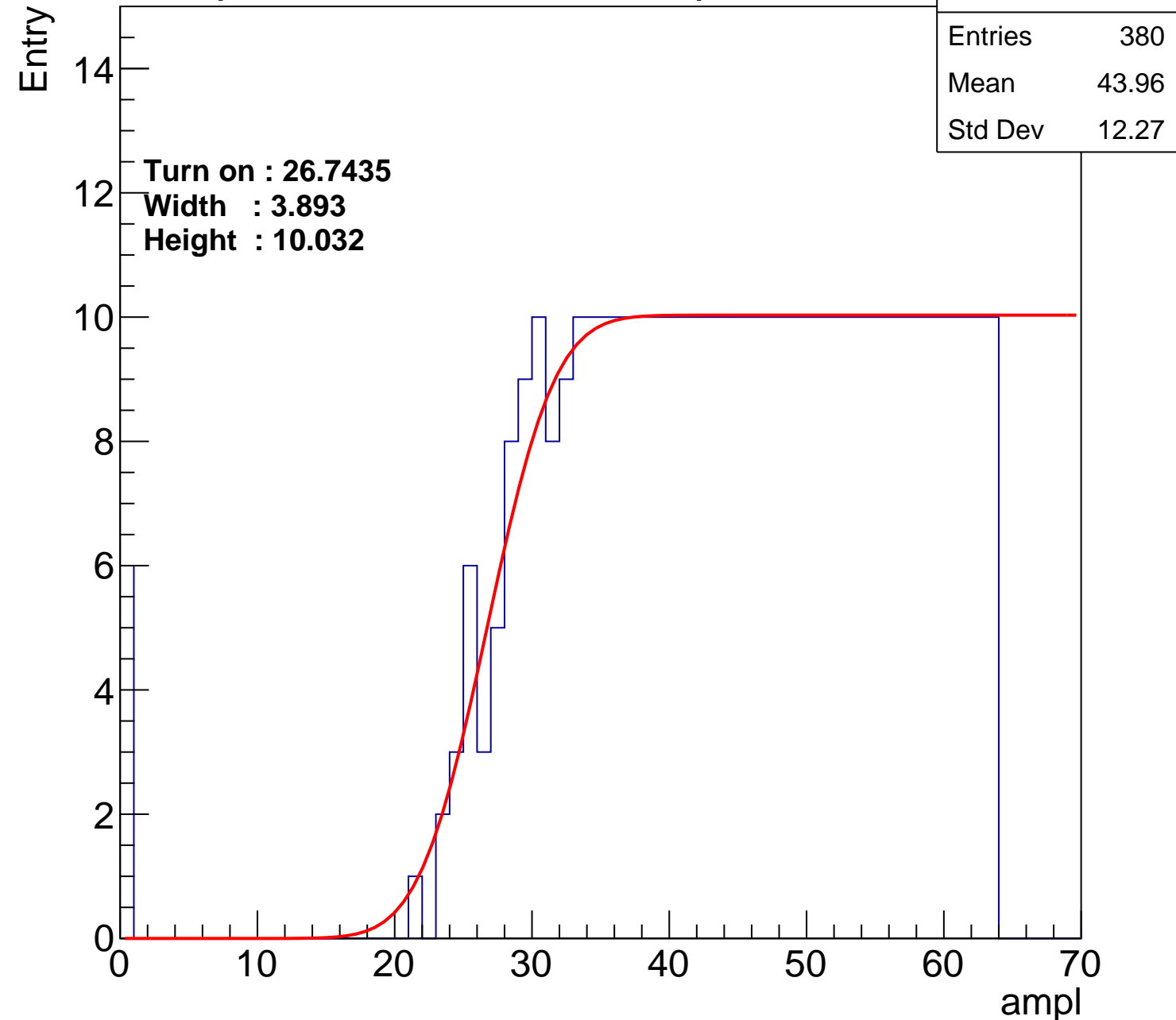
Width : 3.893

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch72

calib_packv5_042523_0143.root, FC#5, port B1

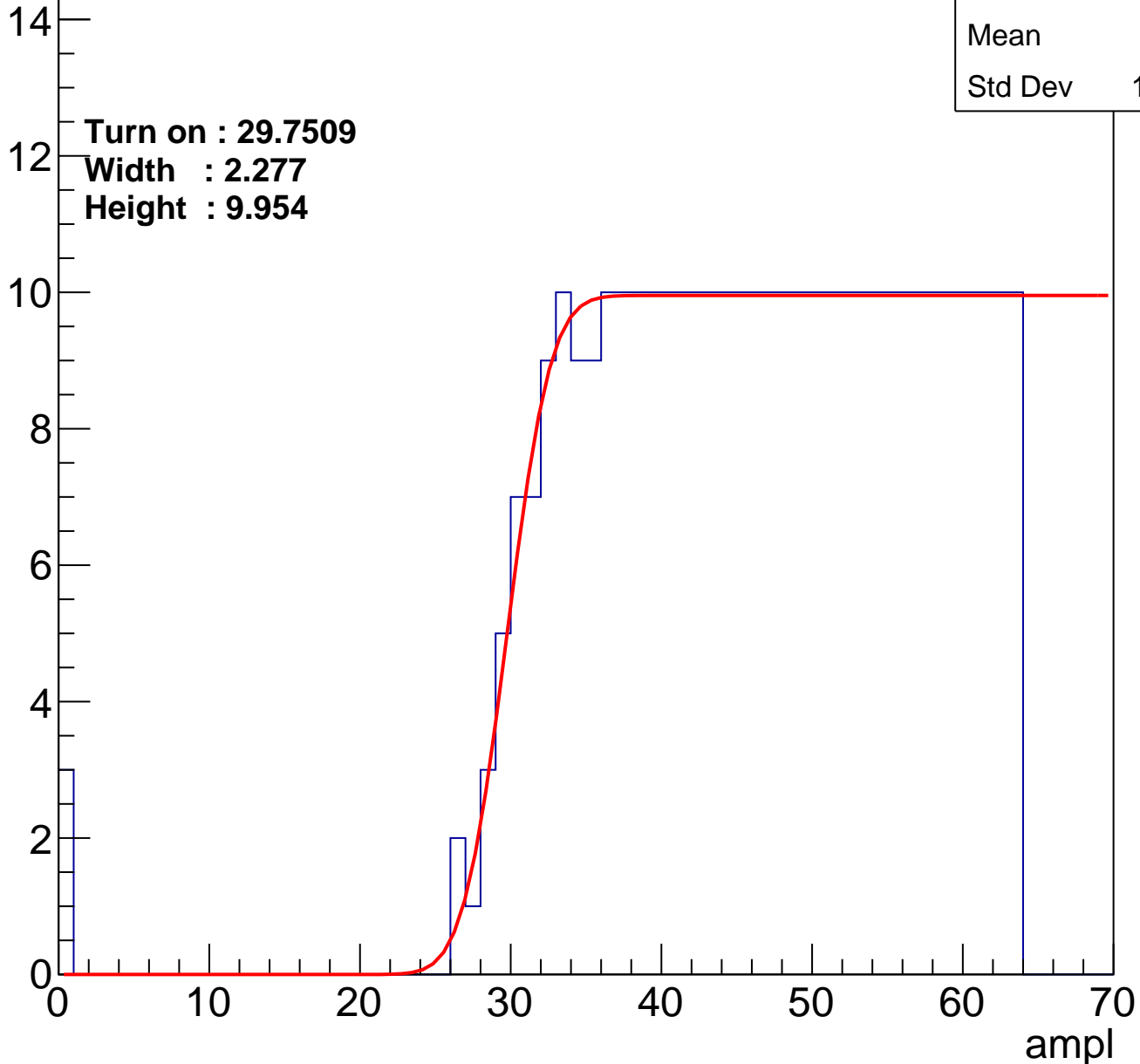
Entries	345
Mean	45.9
Std Dev	10.88

Turn on : 29.7509

Width : 2.277

Height : 9.954

Entry



B0L000S, U25-ch73

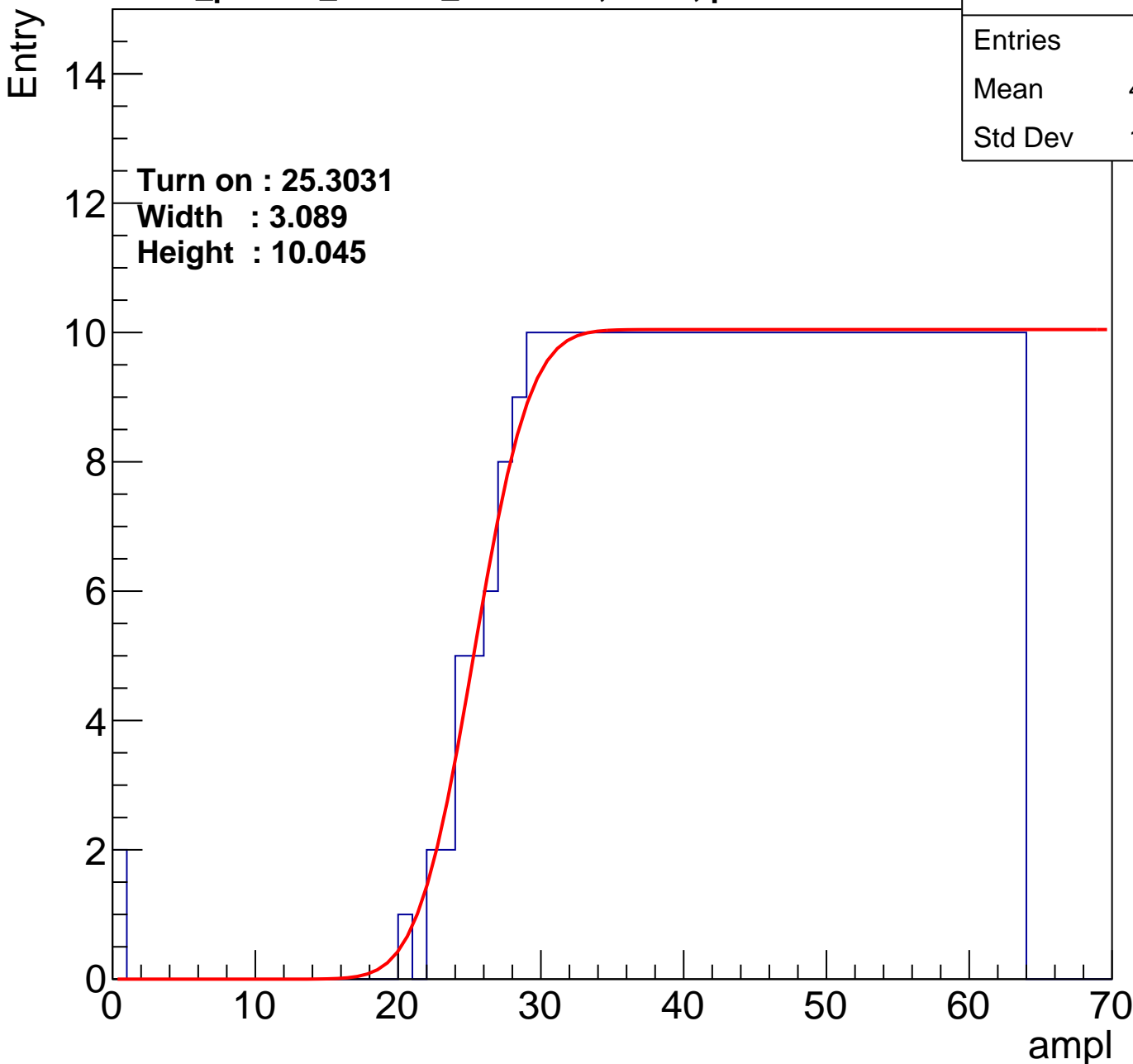
calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.3031

Width : 3.089

Height : 10.045



B0L000S, U25-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.81
Std Dev	11.75

Turn on : 25.2042

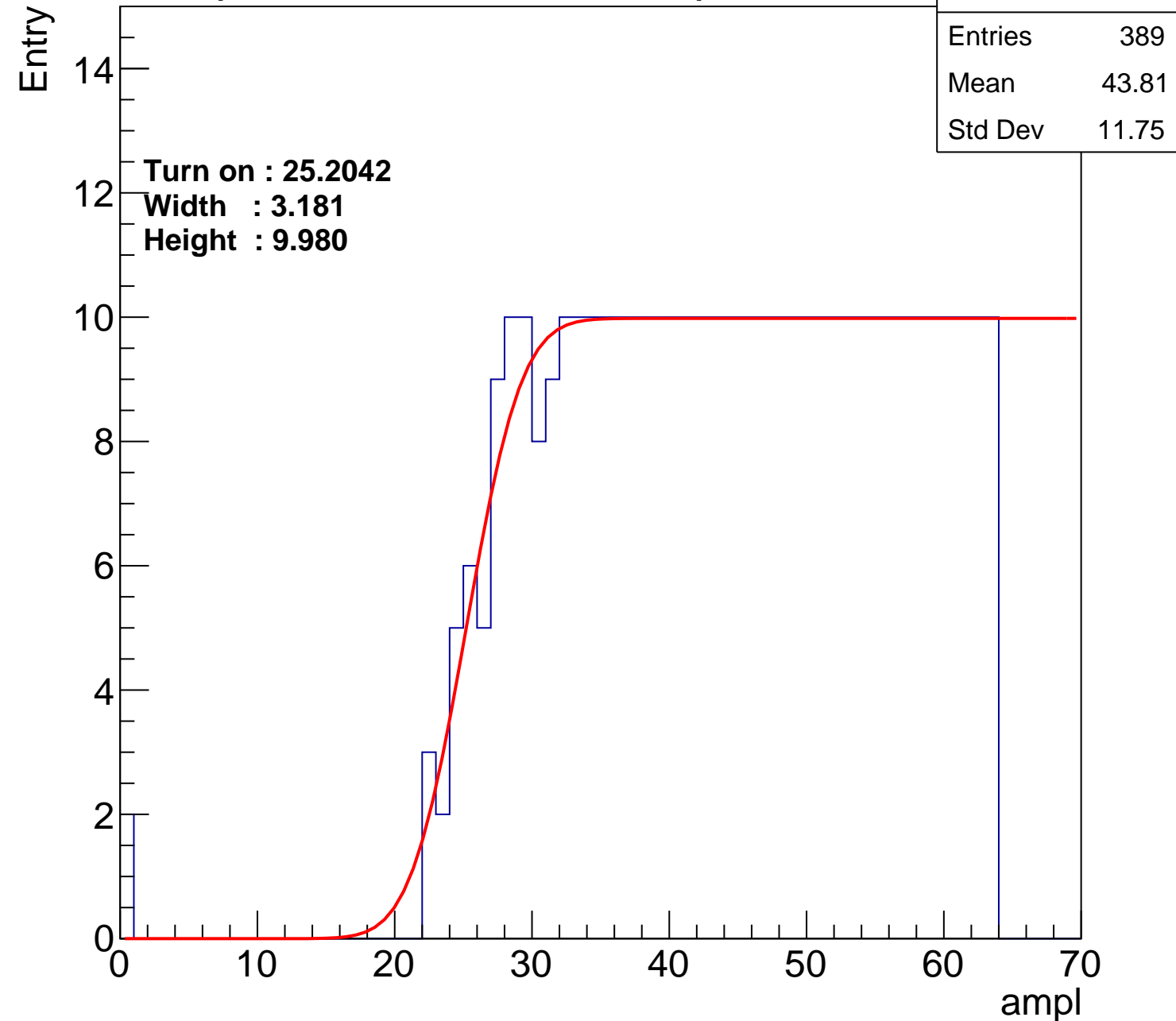
Width : 3.181

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	348
Mean	45.91
Std Dev	10.47

Turn on : 28.8564

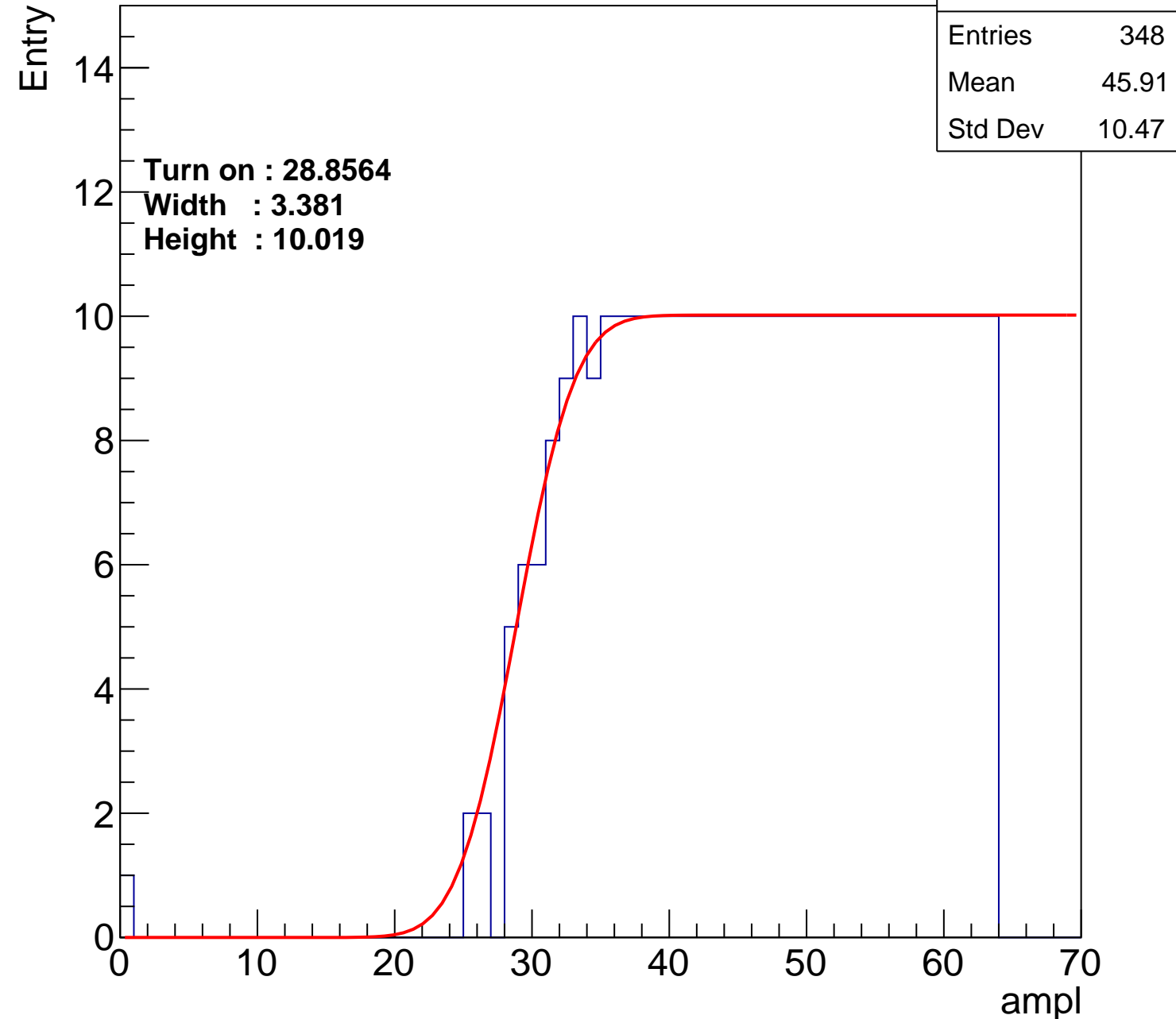
Width : 3.381

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.7
Std Dev	11.44

Mean	44.7
------	------

Std Dev	11.44
---------	-------

Width : 3.004

Height : 10.071



B0L000S, U25-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.5156

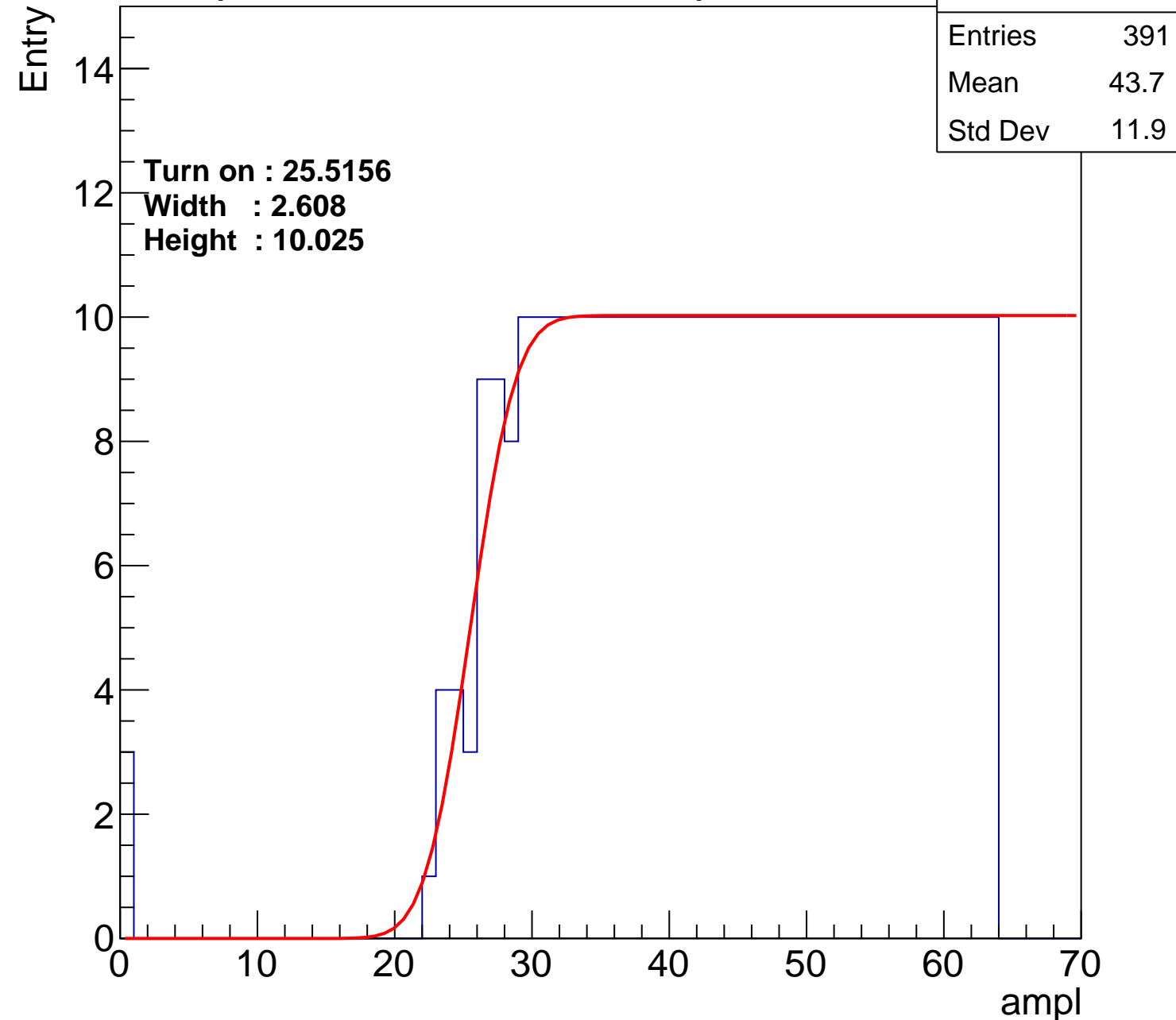
Width : 2.608

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch78

calib_packv5_042523_0143.root, FC#5, port B1

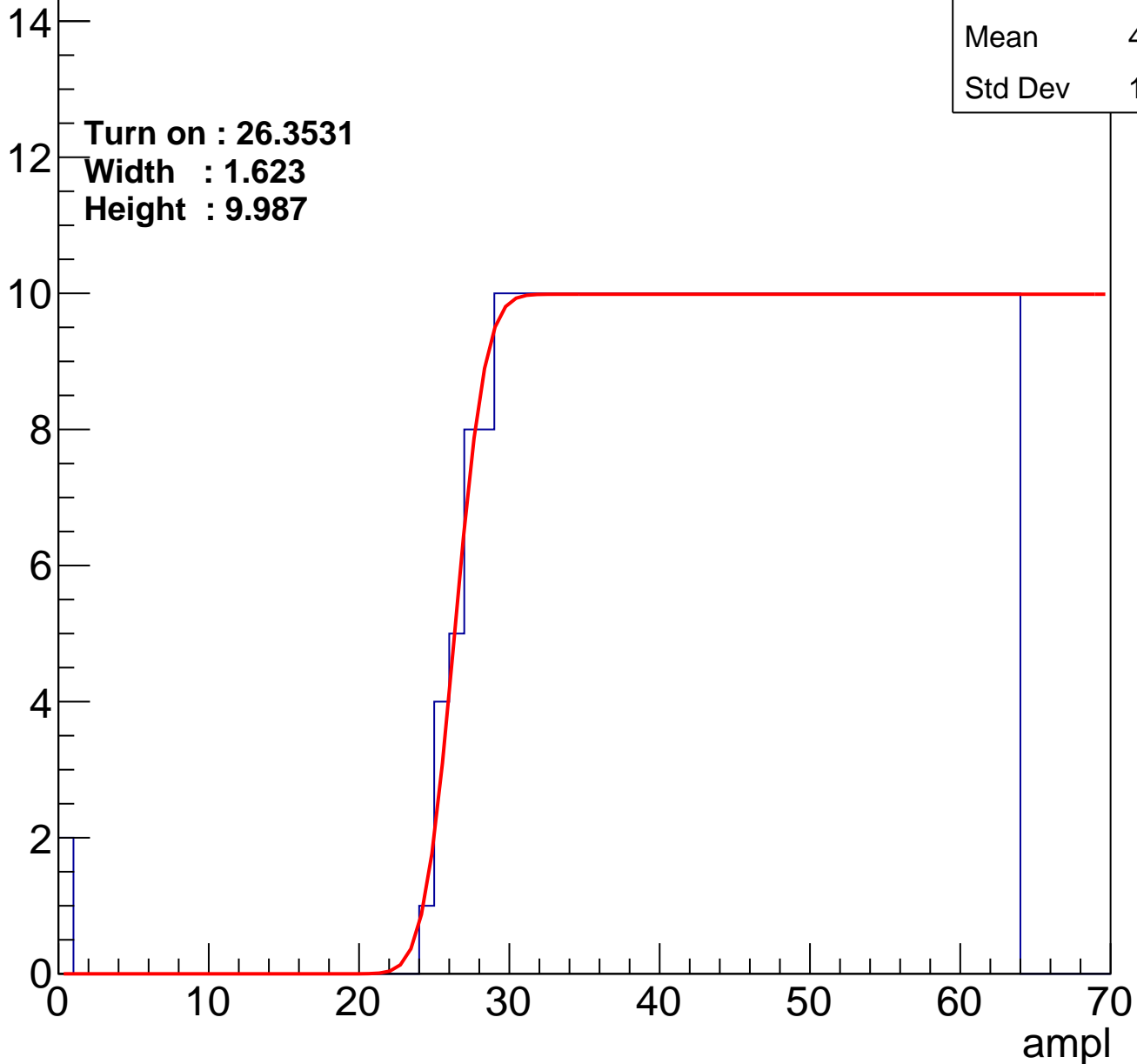
Entries	378
Mean	44.43
Std Dev	11.35

Turn on : 26.3531

Width : 1.623

Height : 9.987

Entry



B0L000S, U25-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 28.0724

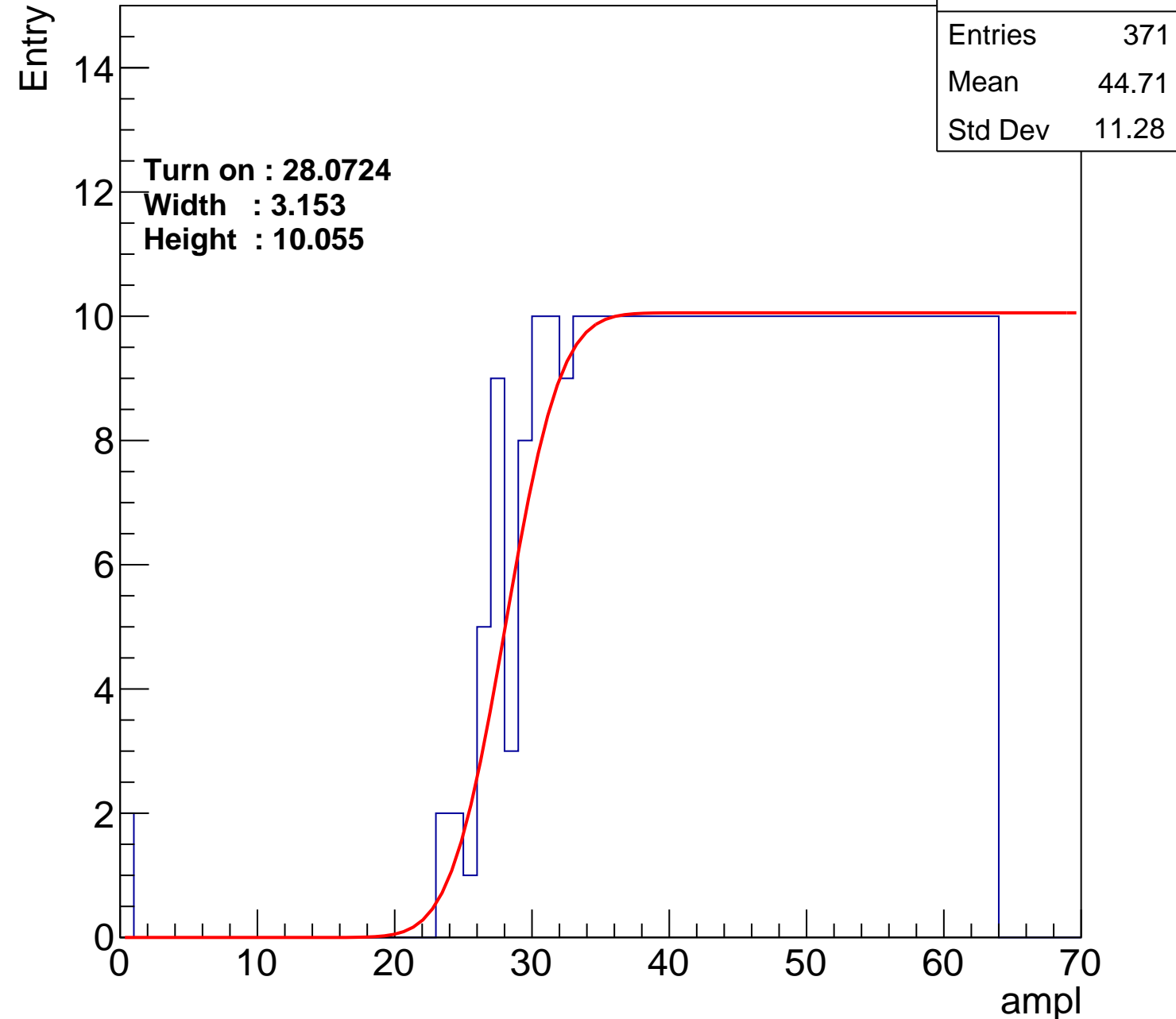
Width : 3.153

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.39
Std Dev	11.48

Turn on : 26.6896

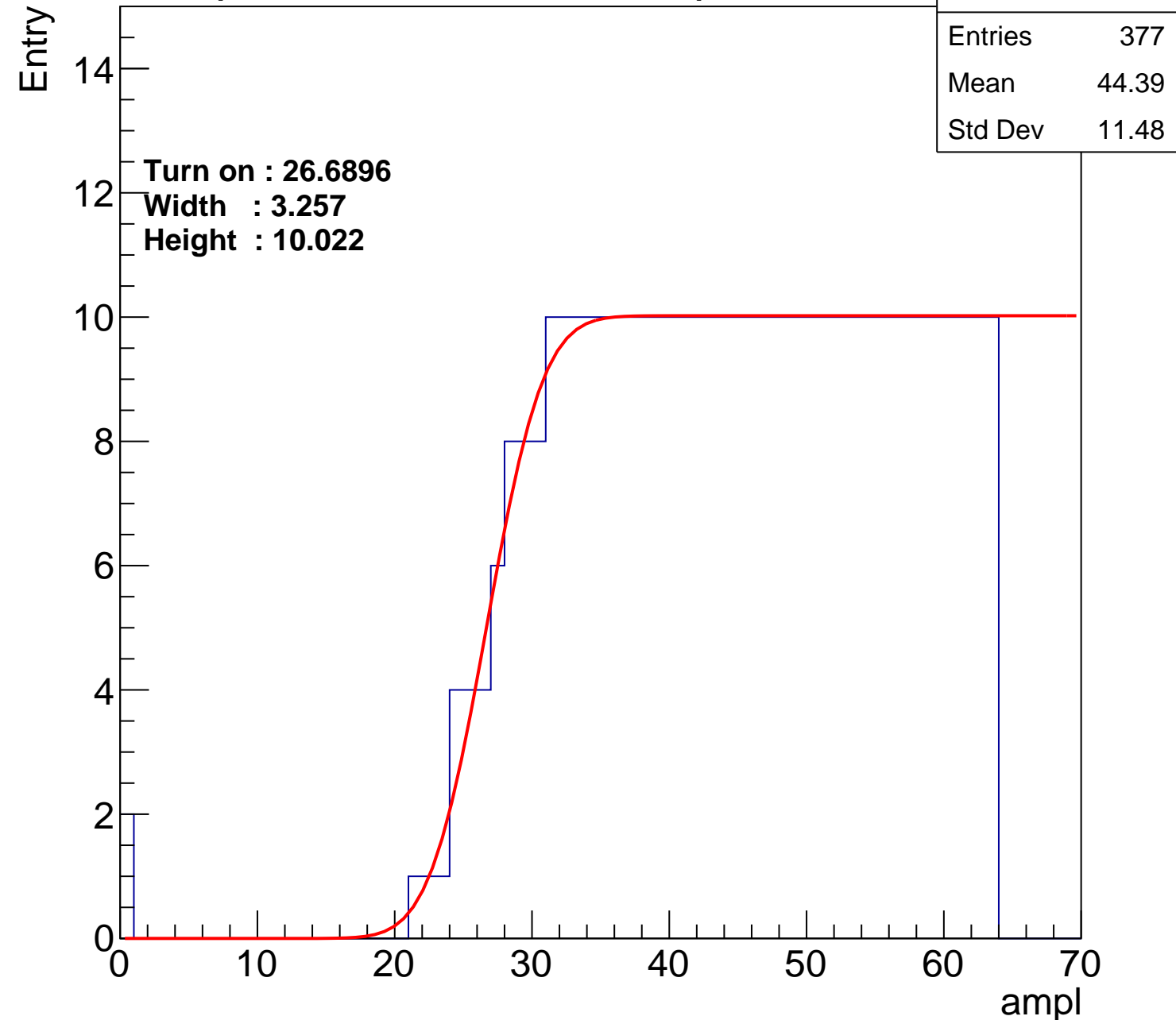
Width : 3.257

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch81

calib_packv5_042523_0143.root, FC#5, port B1

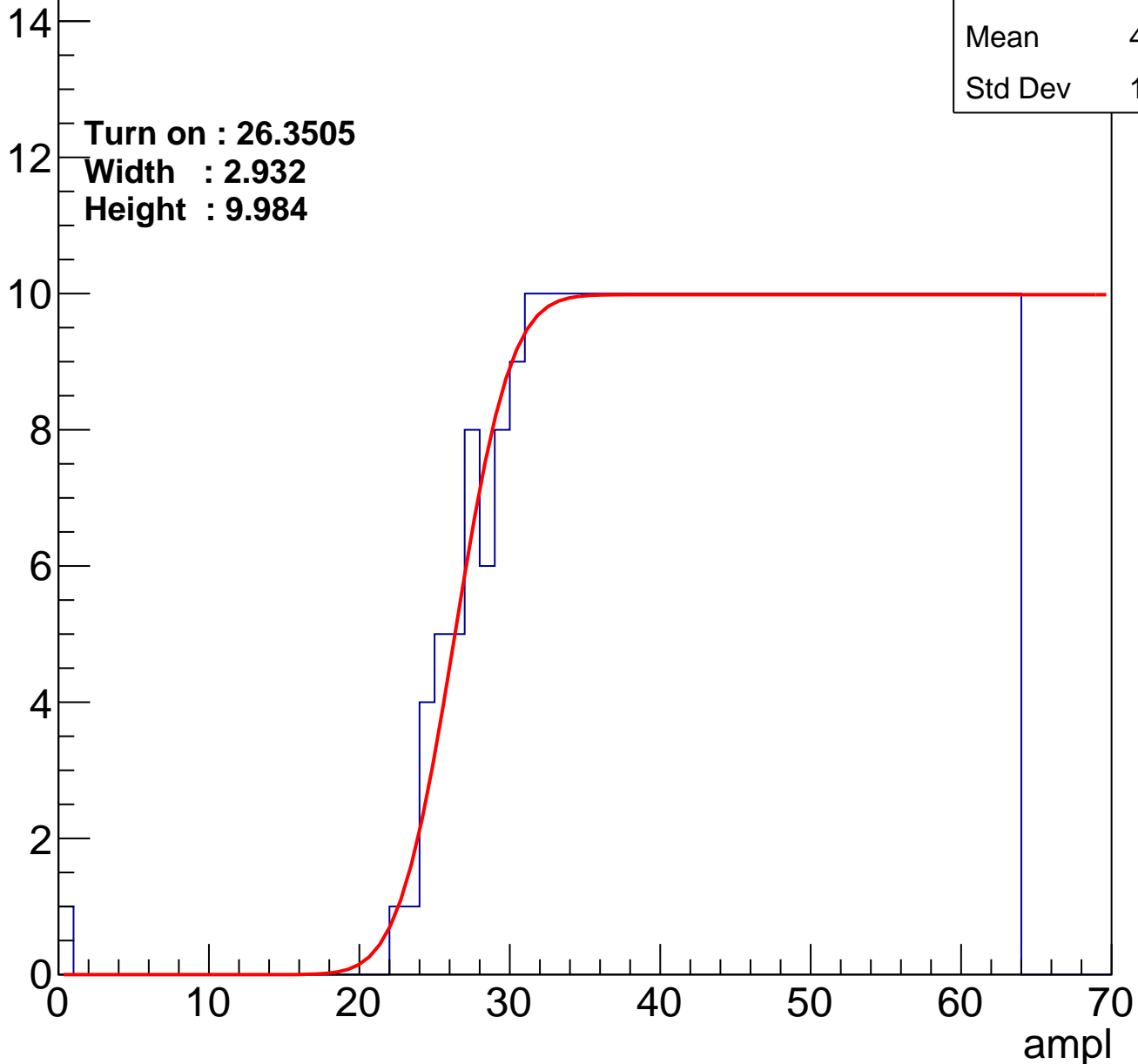
Entries	378
Mean	44.42
Std Dev	11.28

Turn on : 26.3505

Width : 2.932

Height : 9.984

Entry



B0L000S, U25-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.63
Std Dev	11.83

Turn on : 24.7834

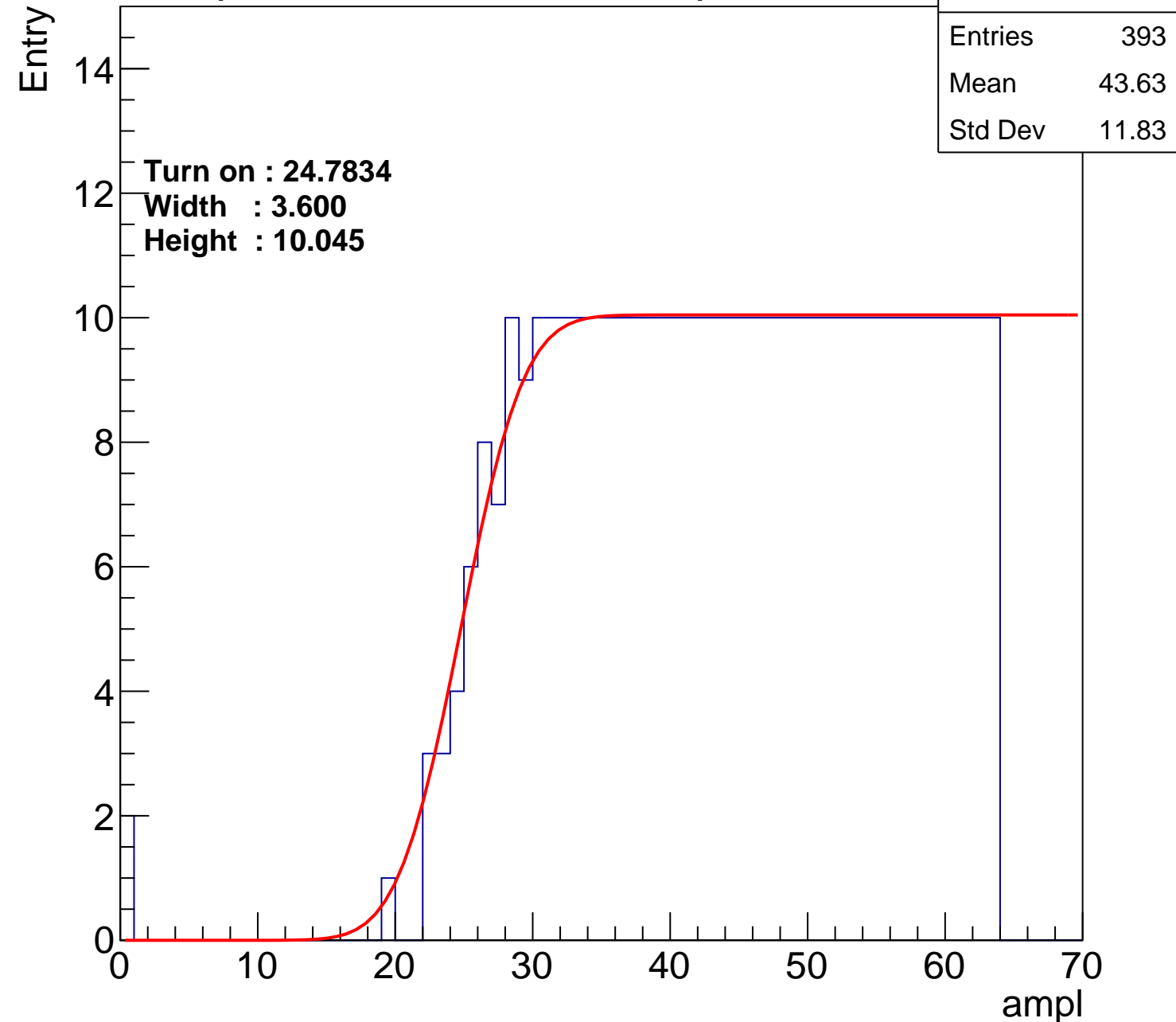
Width : 3.600

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.6121

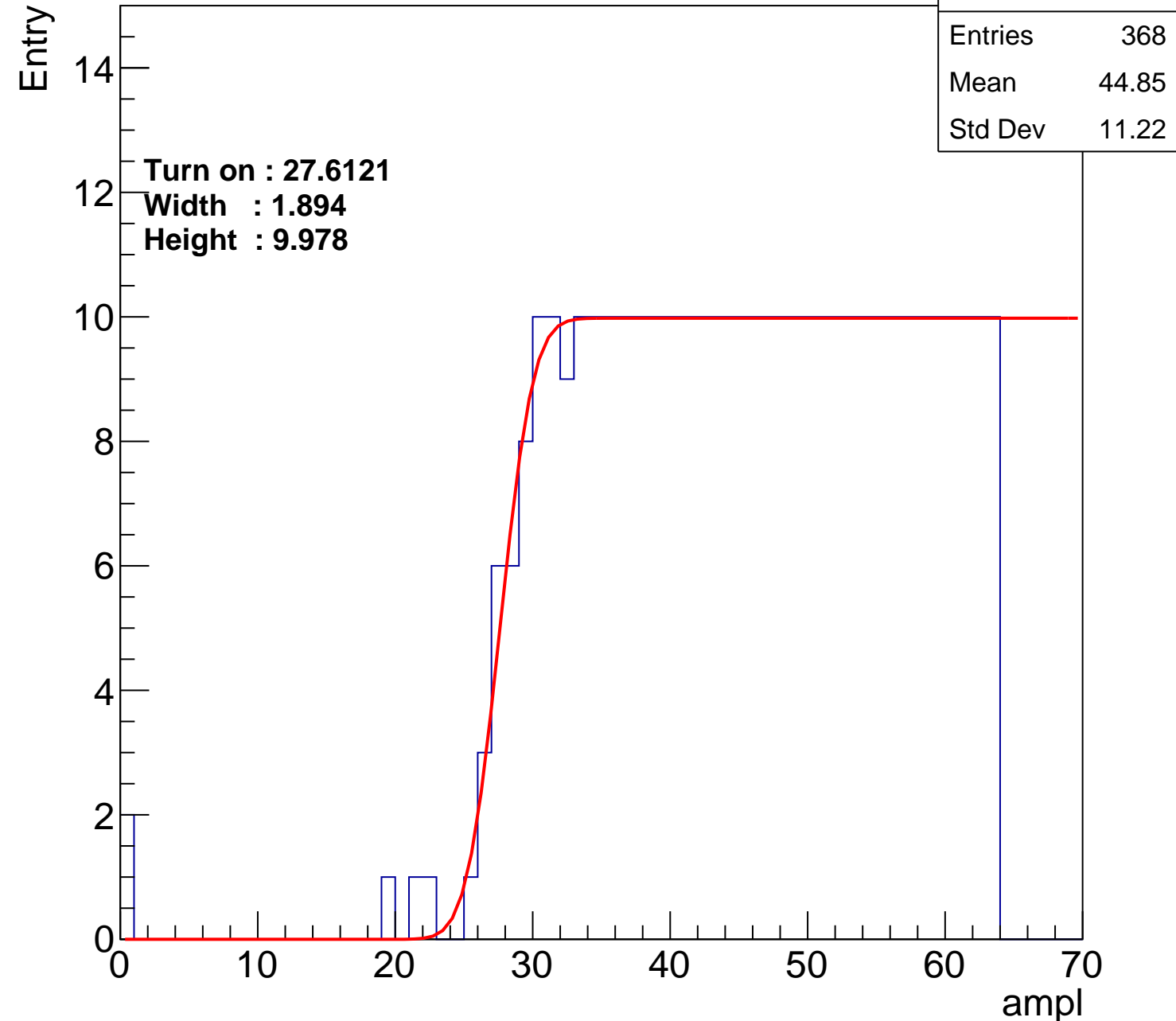
Width : 1.894

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch84

calib_packv5_042523_0143.root, FC#5, port B1

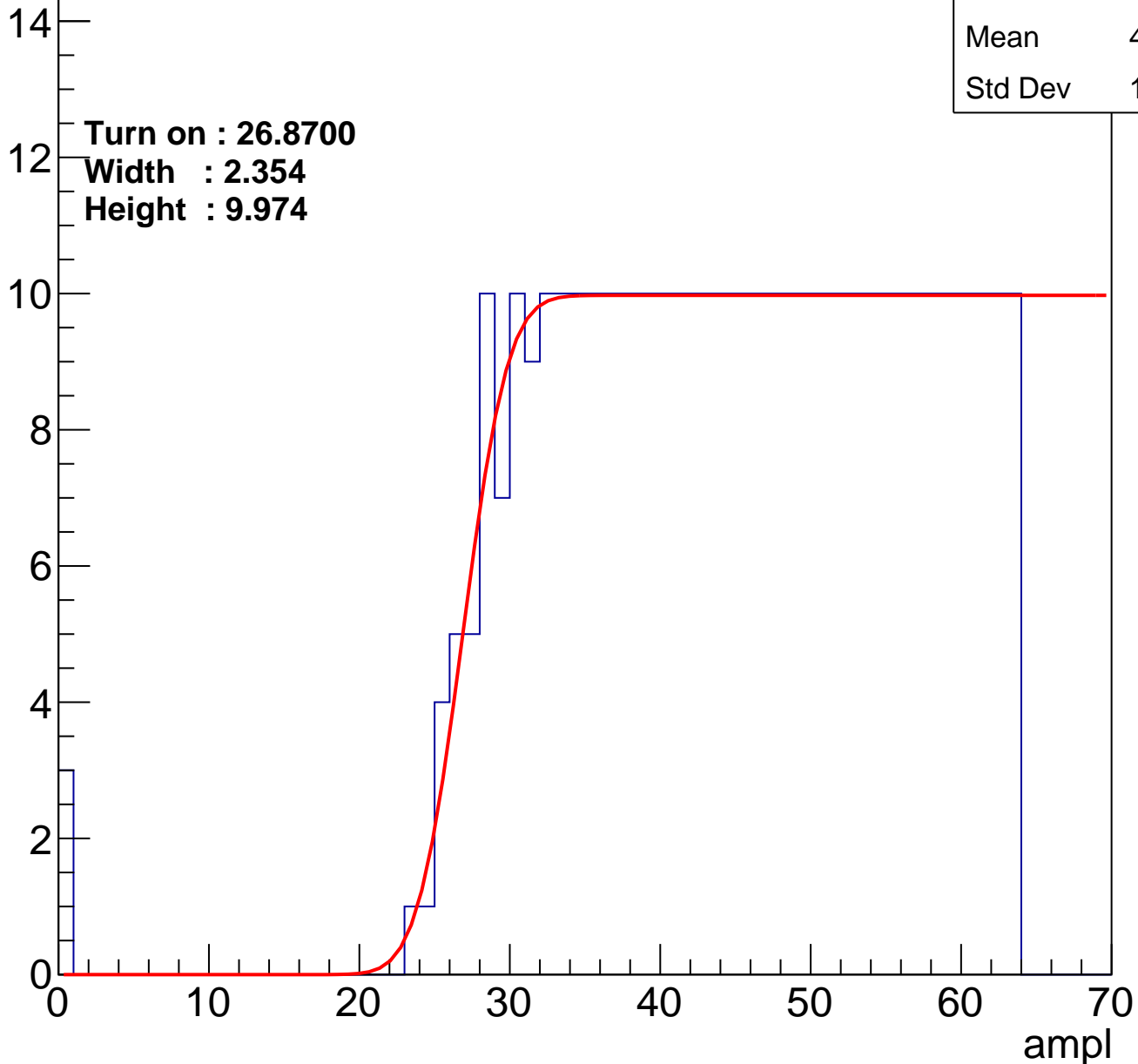
Entries	375
Mean	44.46
Std Dev	11.53

Turn on : 26.8700

Width : 2.354

Height : 9.974

Entry



B0L000S, U25-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.9114

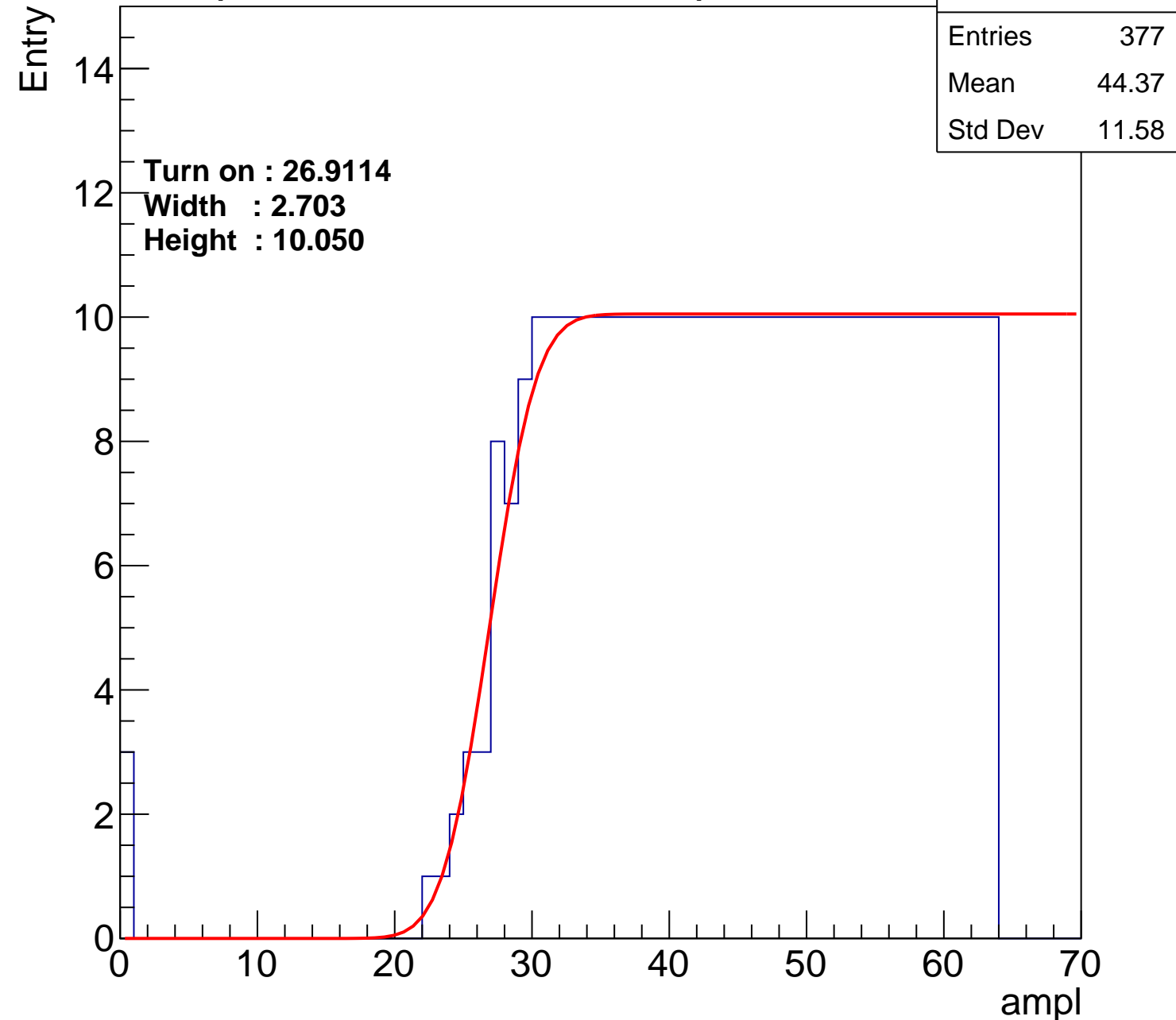
Width : 2.703

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.27
Std Dev	11.94

Turn on : 26.7379

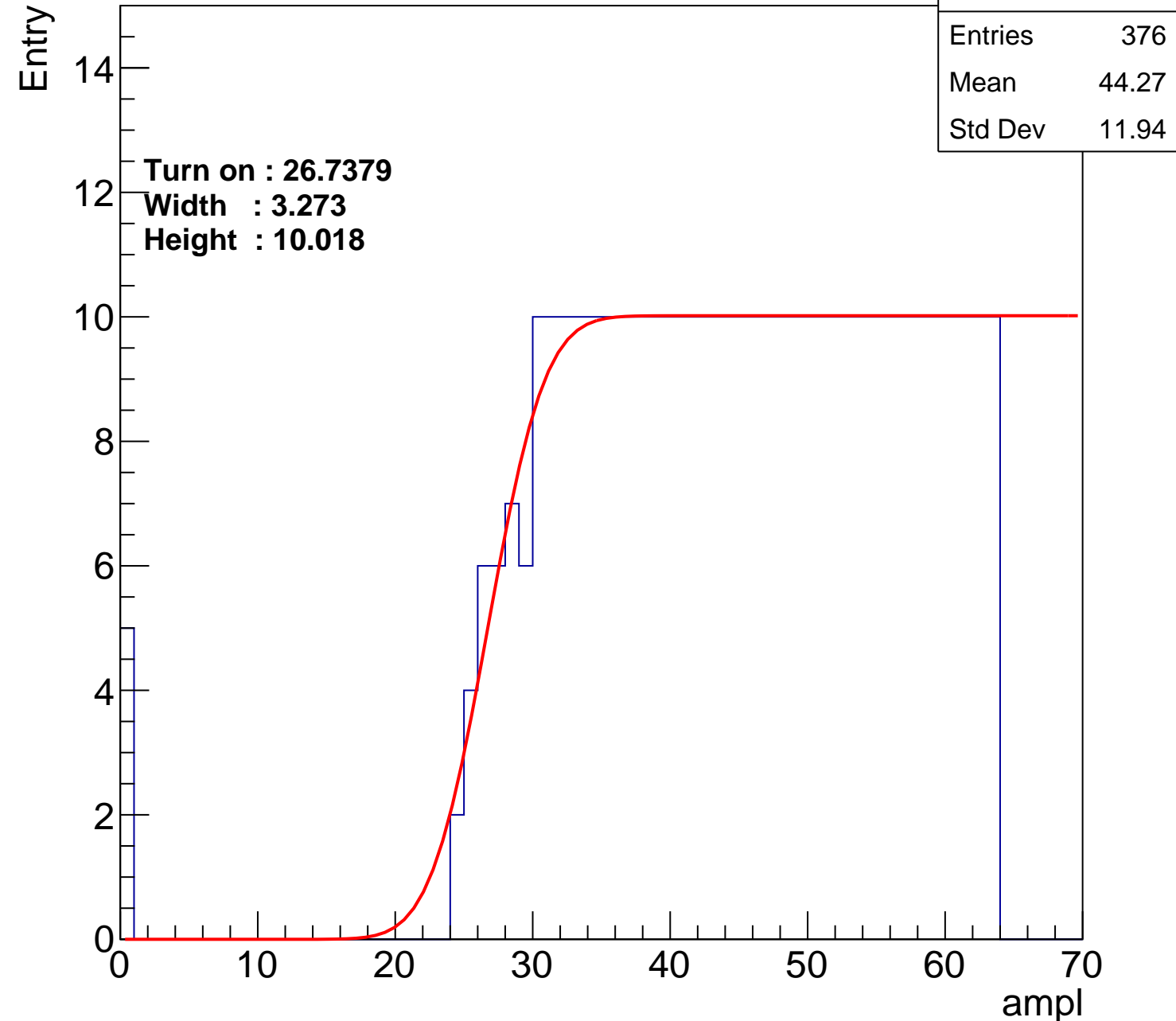
Width : 3.273

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.18
Std Dev	11.06

Turn on : 28.1338

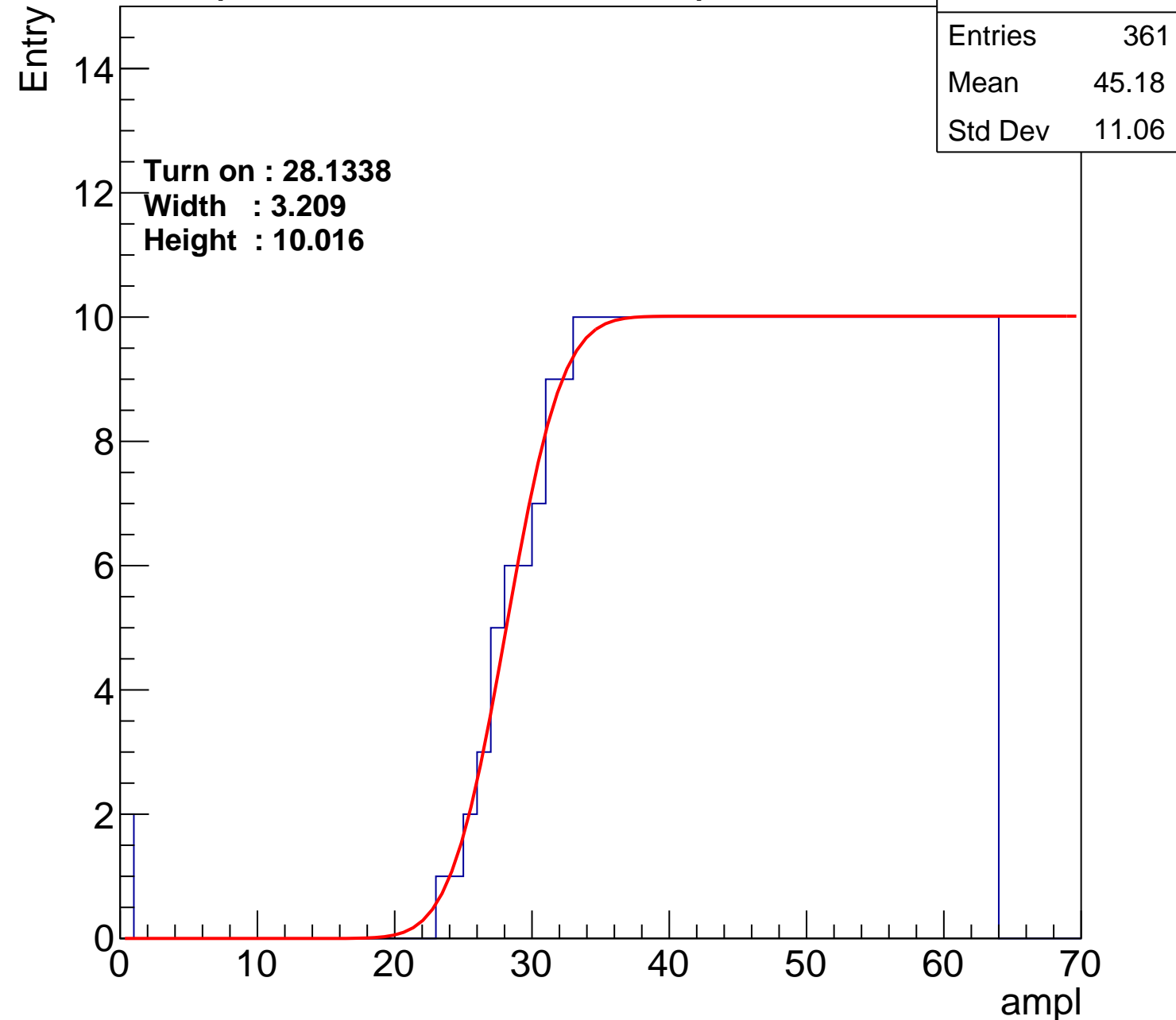
Width : 3.209

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 26.9261

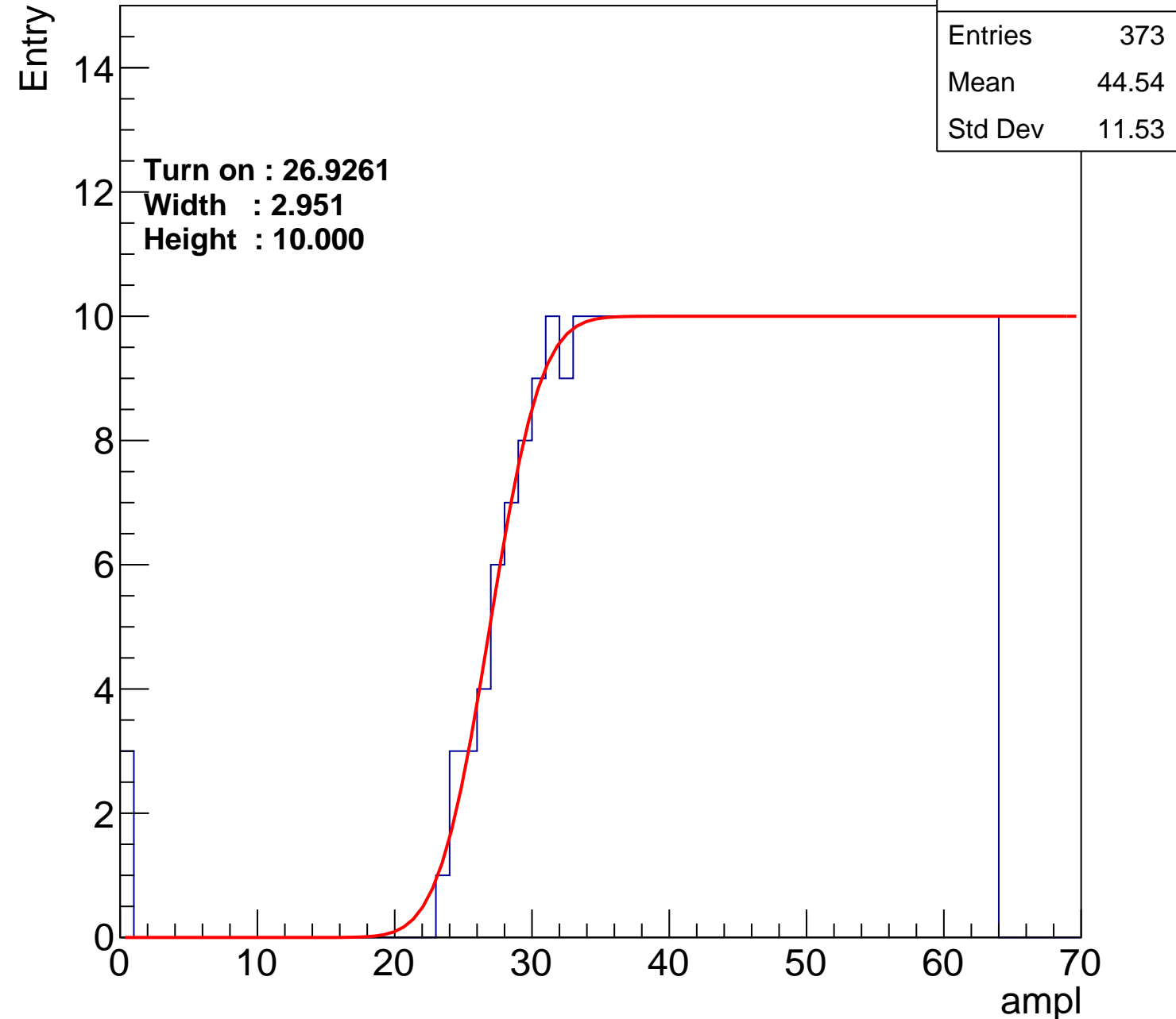
Width : 2.951

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.51
Std Dev	11.57

Turn on : 27.2370

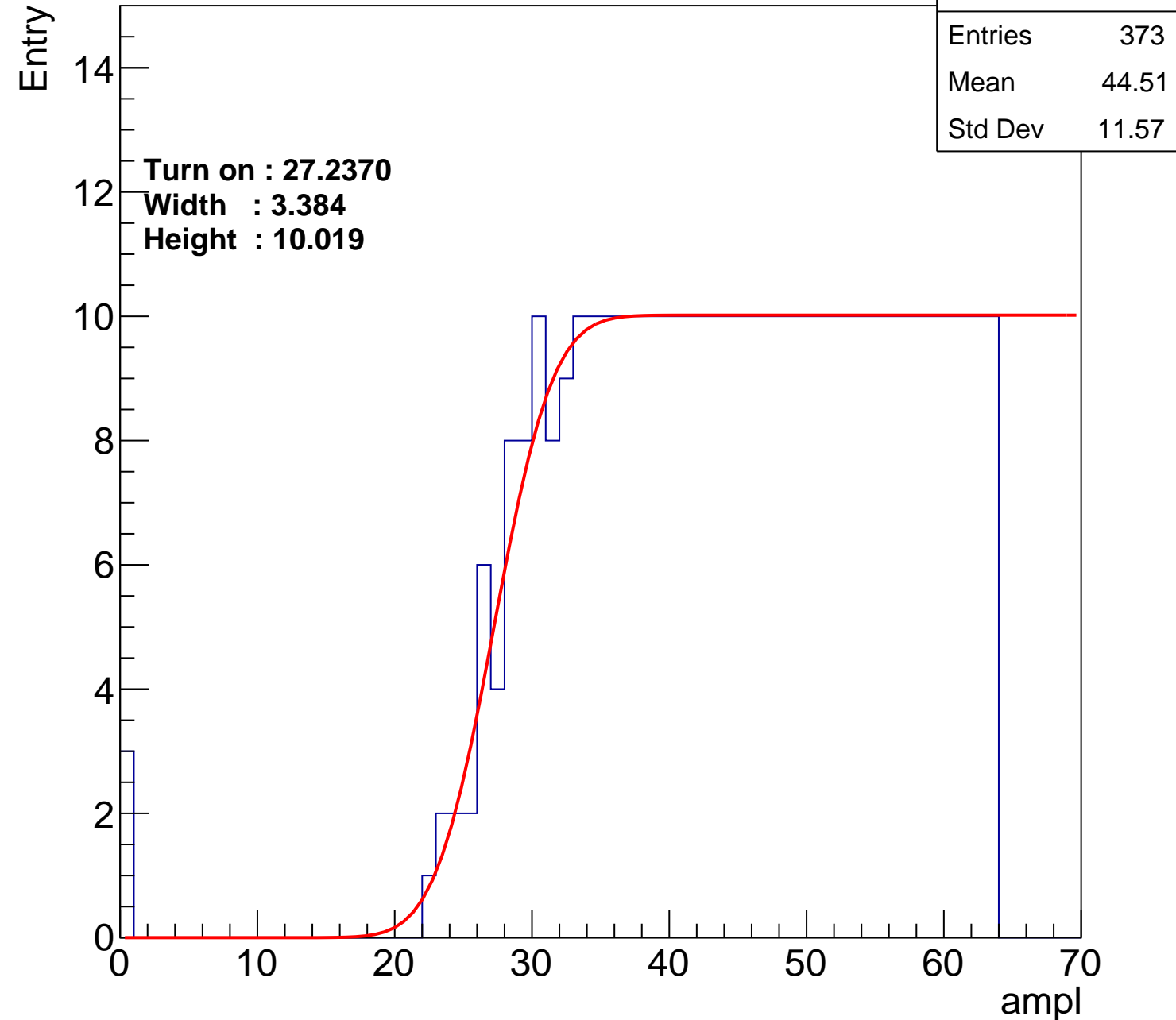
Width : 3.384

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch90

calib_packv5_042523_0143.root, FC#5, port B1

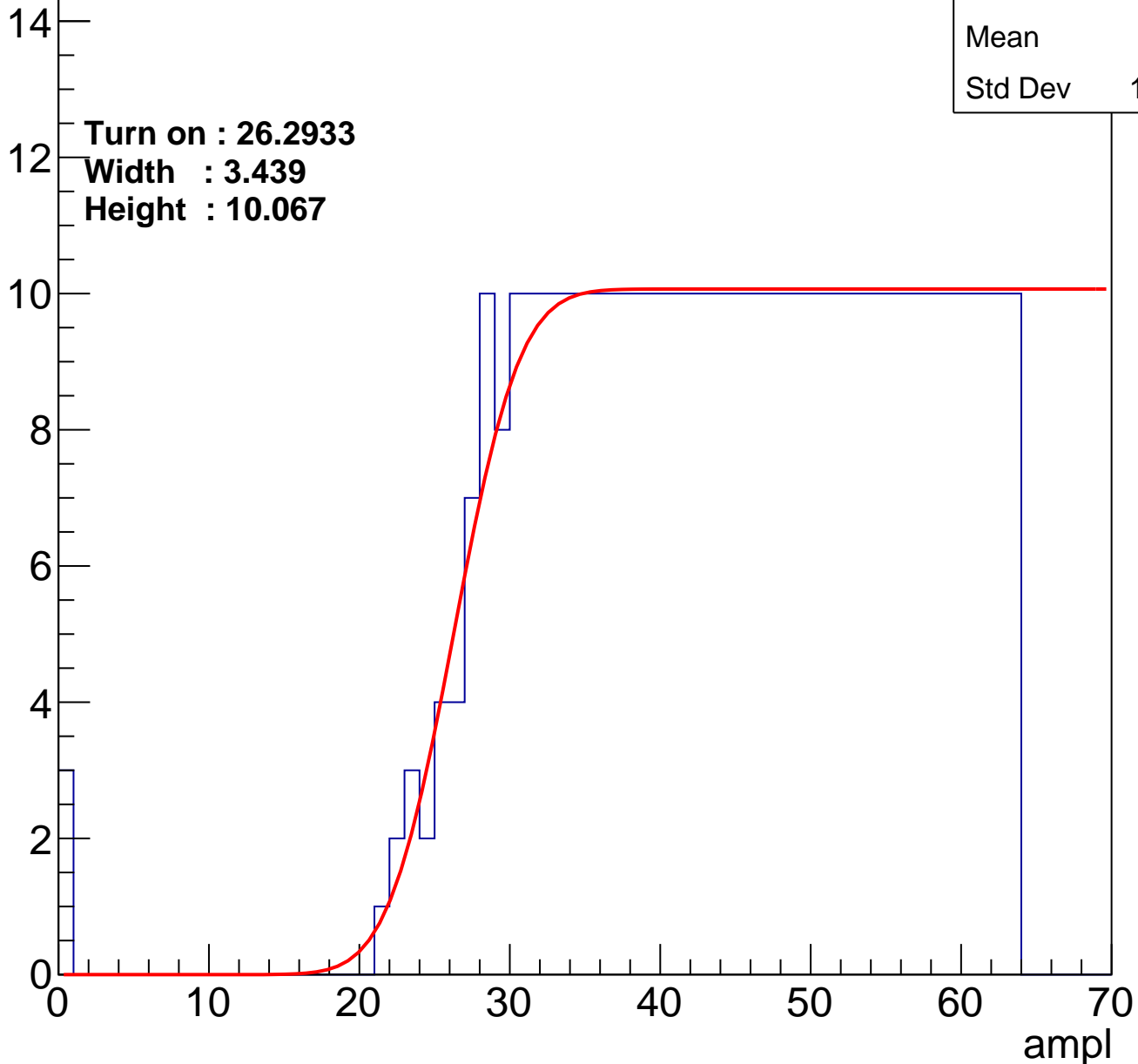
Entries	384
Mean	44
Std Dev	11.79

Turn on : 26.2933

Width : 3.439

Height : 10.067

Entry



B0L000S, U25-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.12
Std Dev	10.96

Turn on : 27.4874

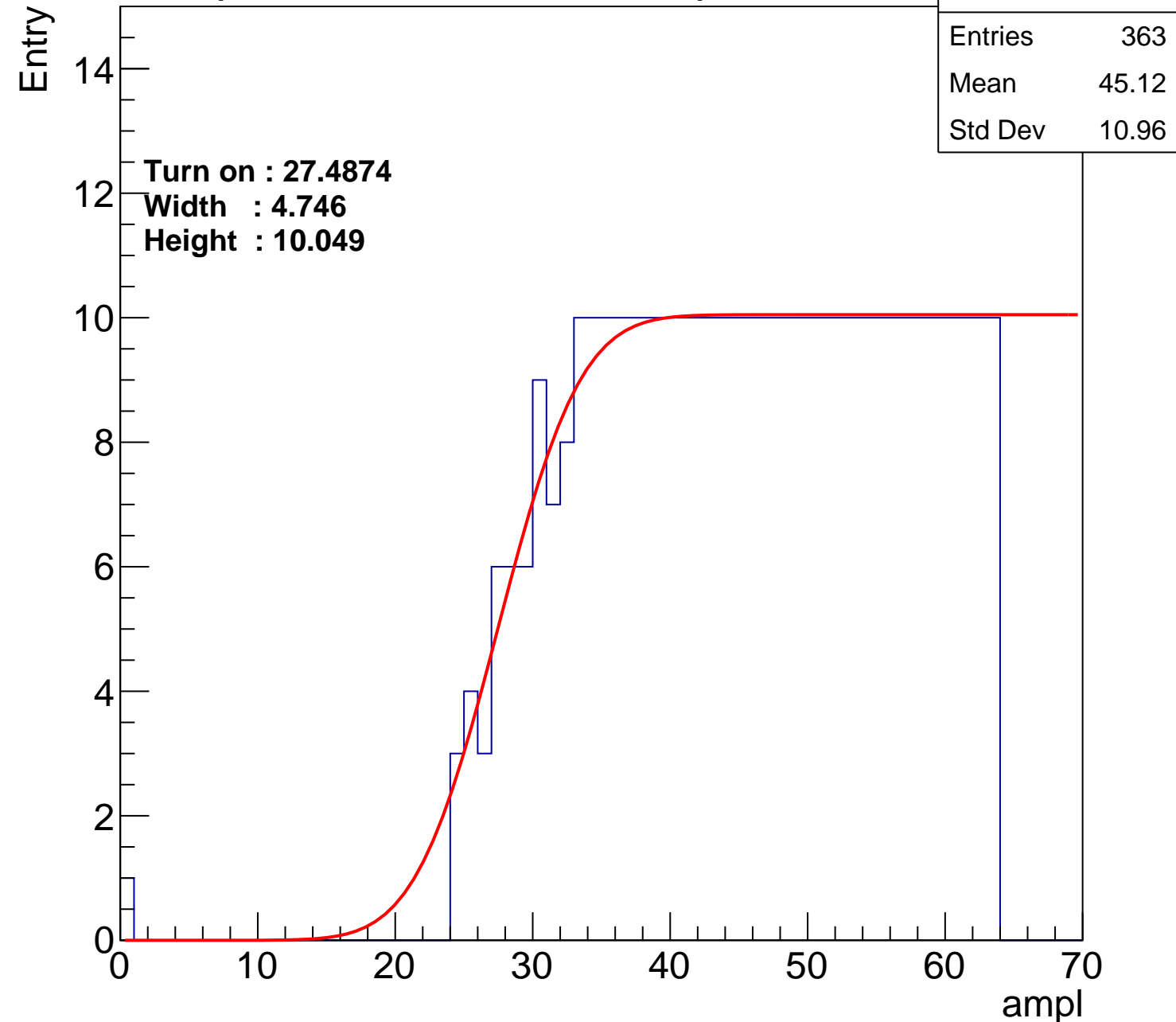
Width : 4.746

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.48
Std Dev	11.24

Turn on : 26.3244

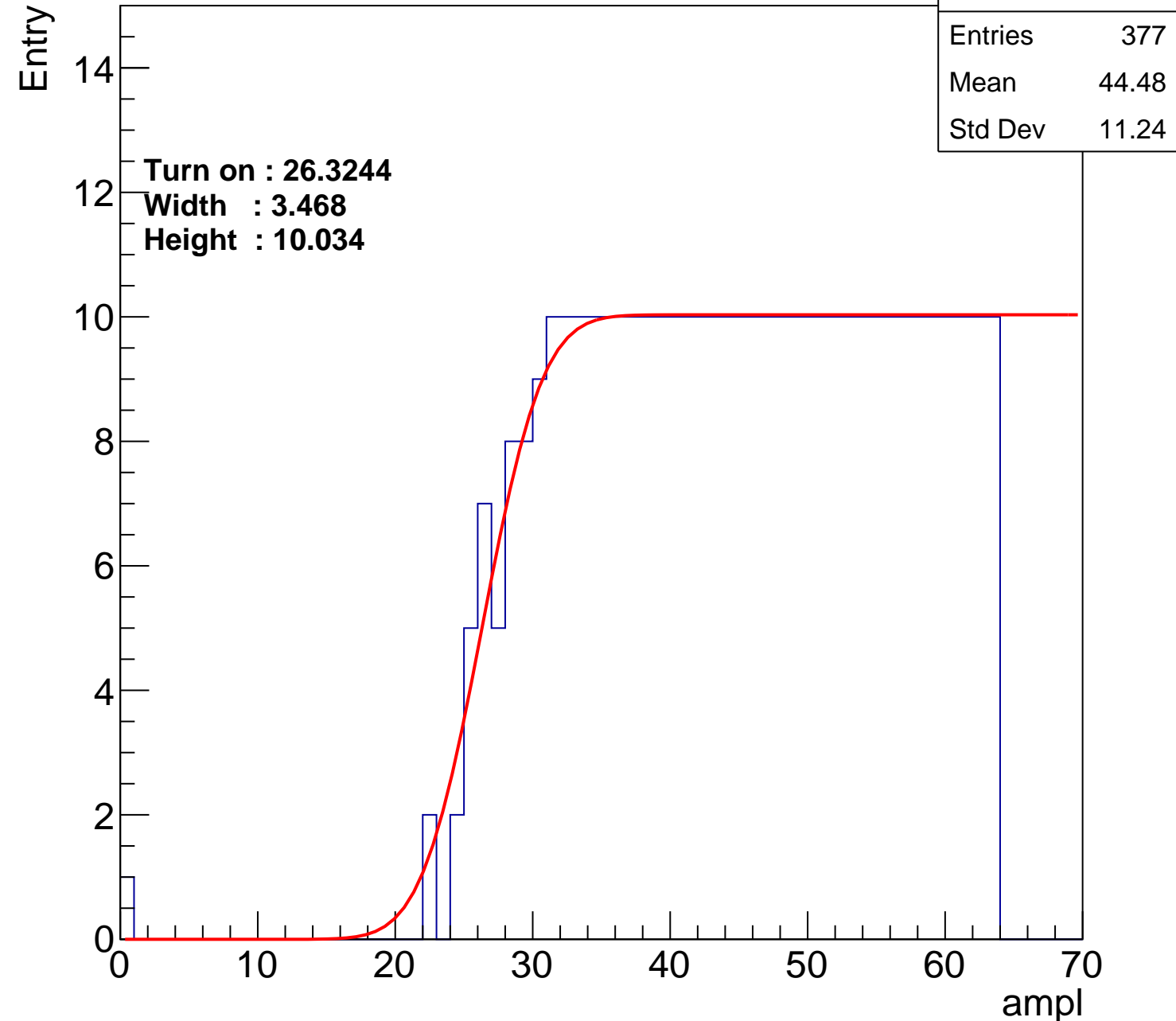
Width : 3.468

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch93

calib_packv5_042523_0143.root, FC#5, port B1

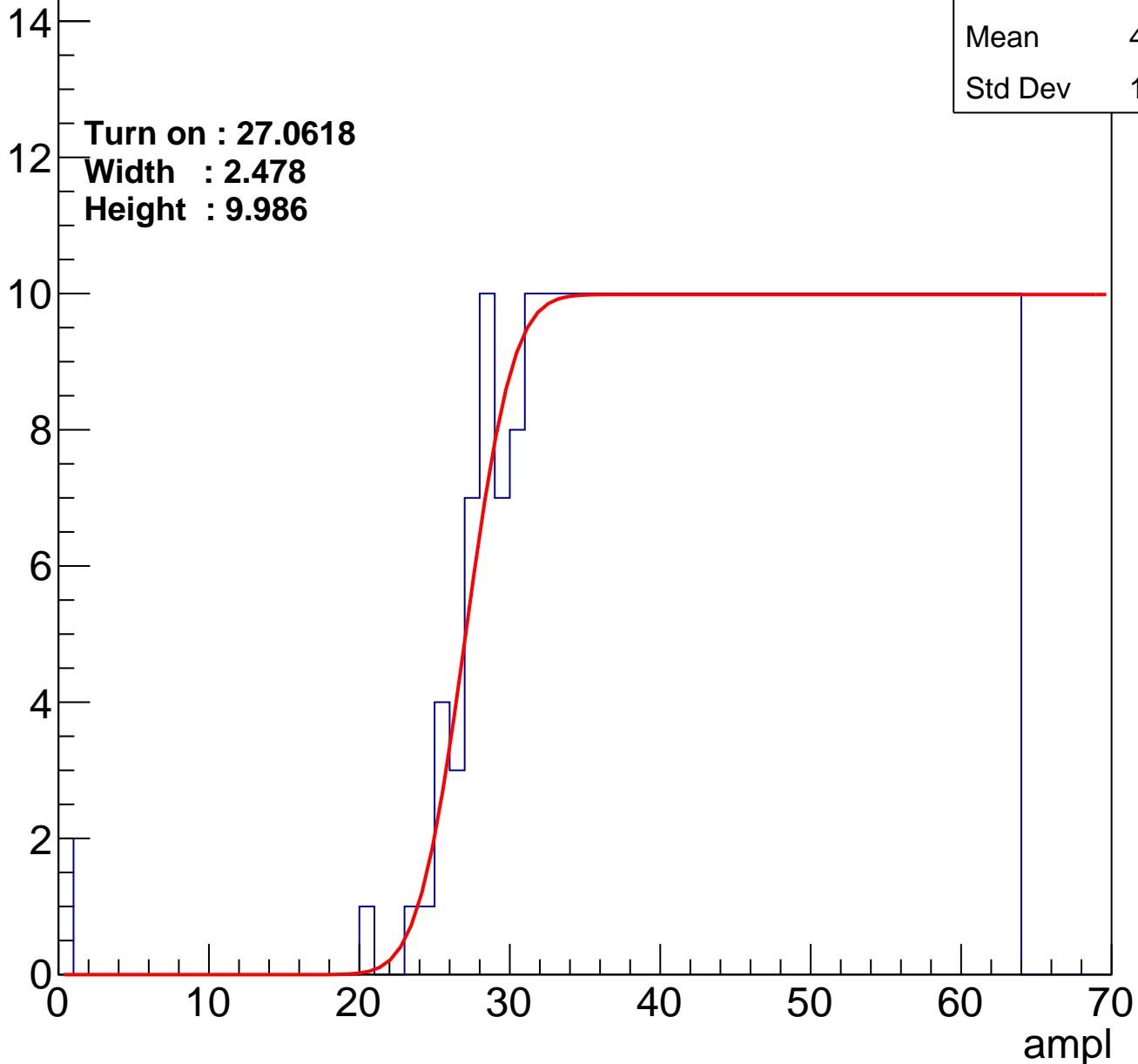
Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 27.0618

Width : 2.478

Height : 9.986

Entry



B0L000S, U25-ch94

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 28.1067

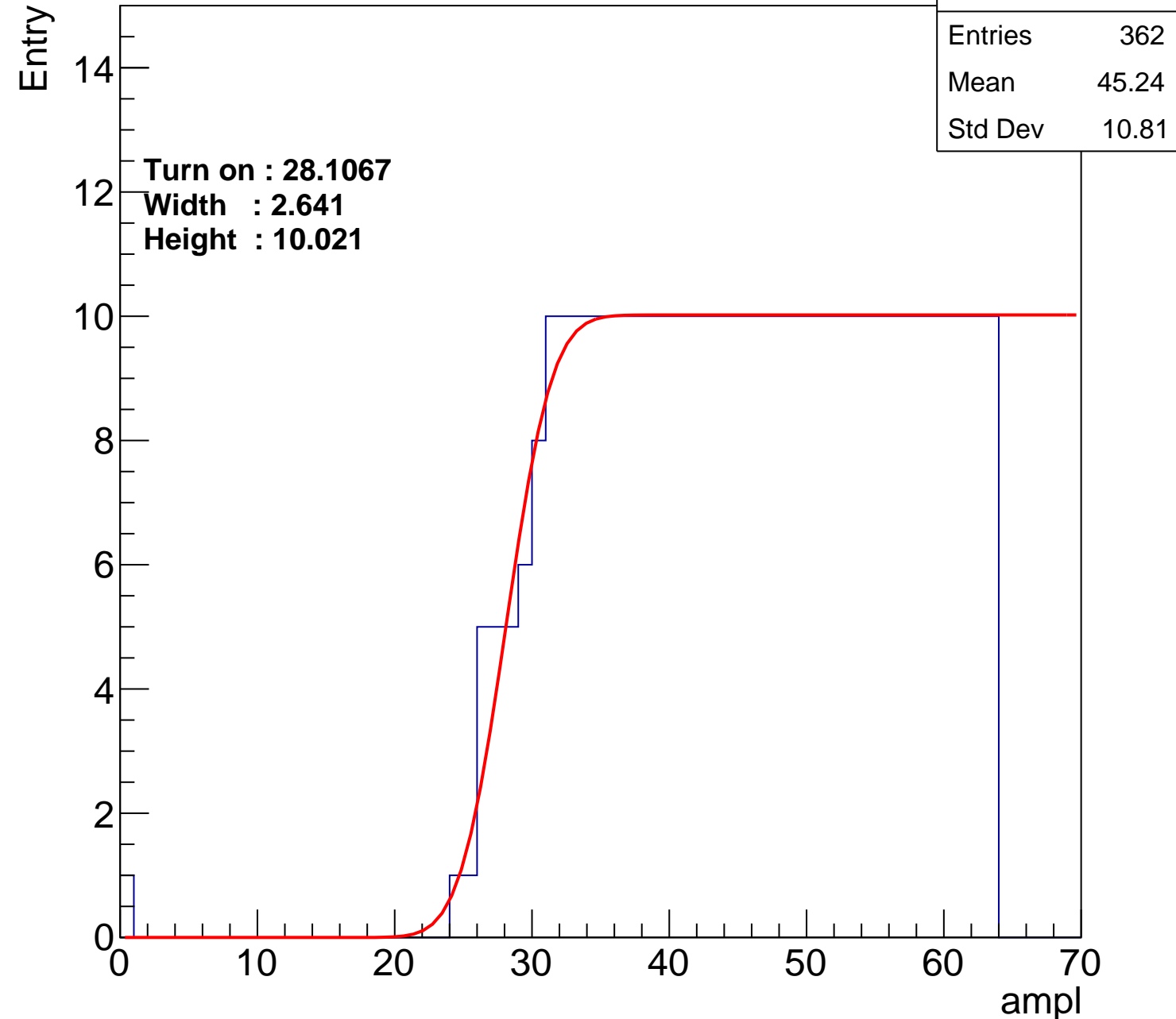
Width : 2.641

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.7962

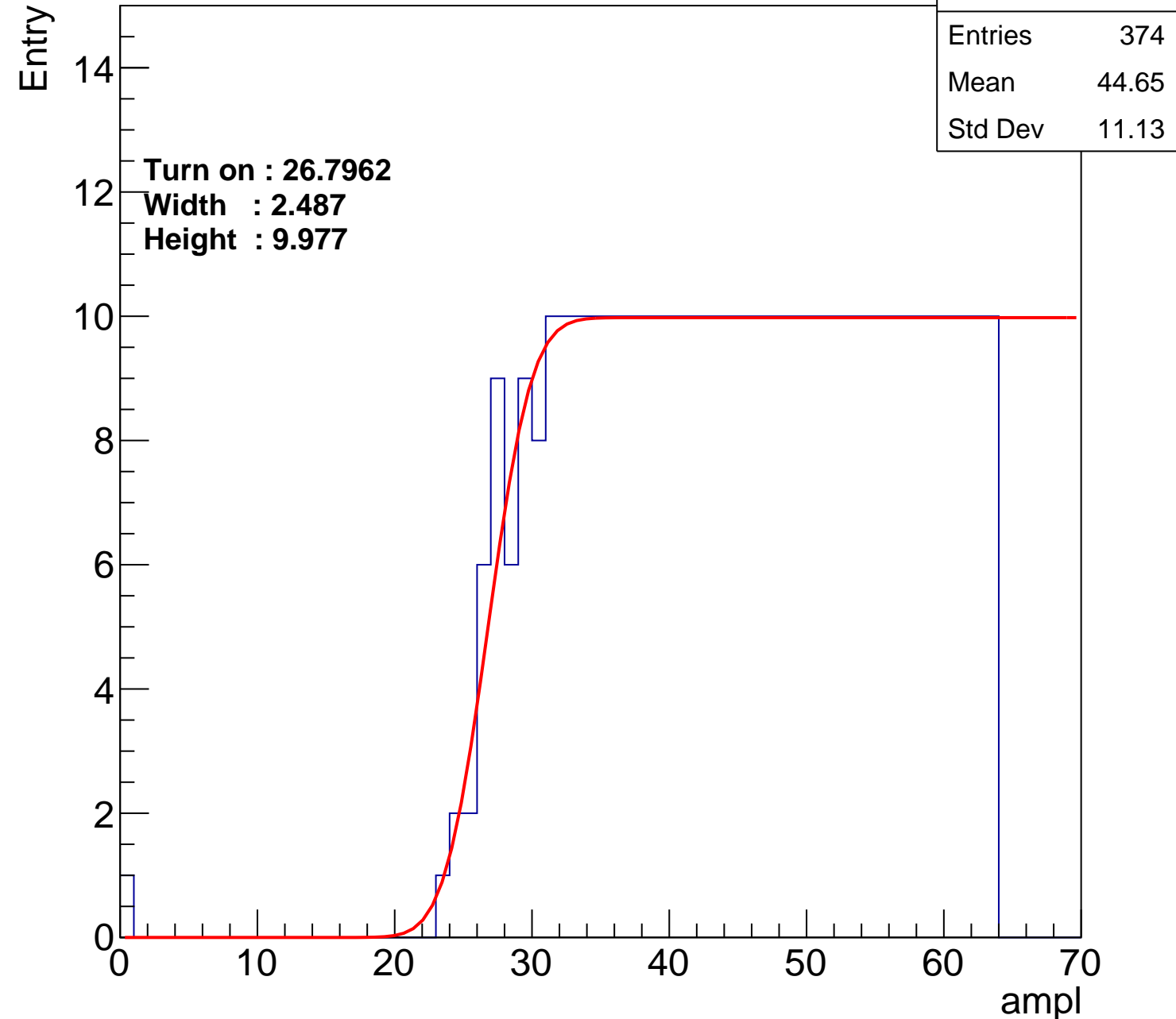
Width : 2.487

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.55
Std Dev	11.37

Turn on : 26.4886

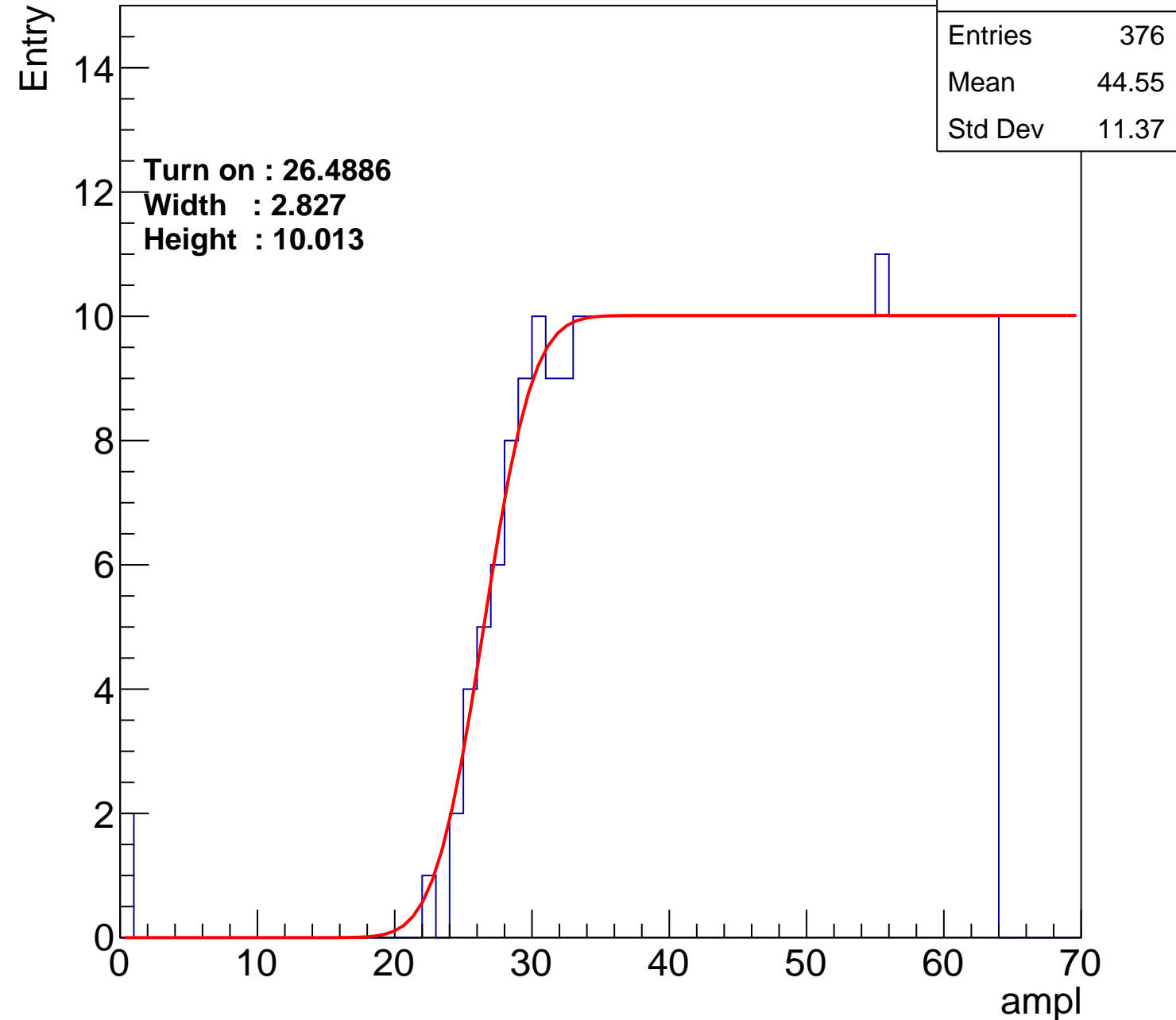
Width : 2.827

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.2763

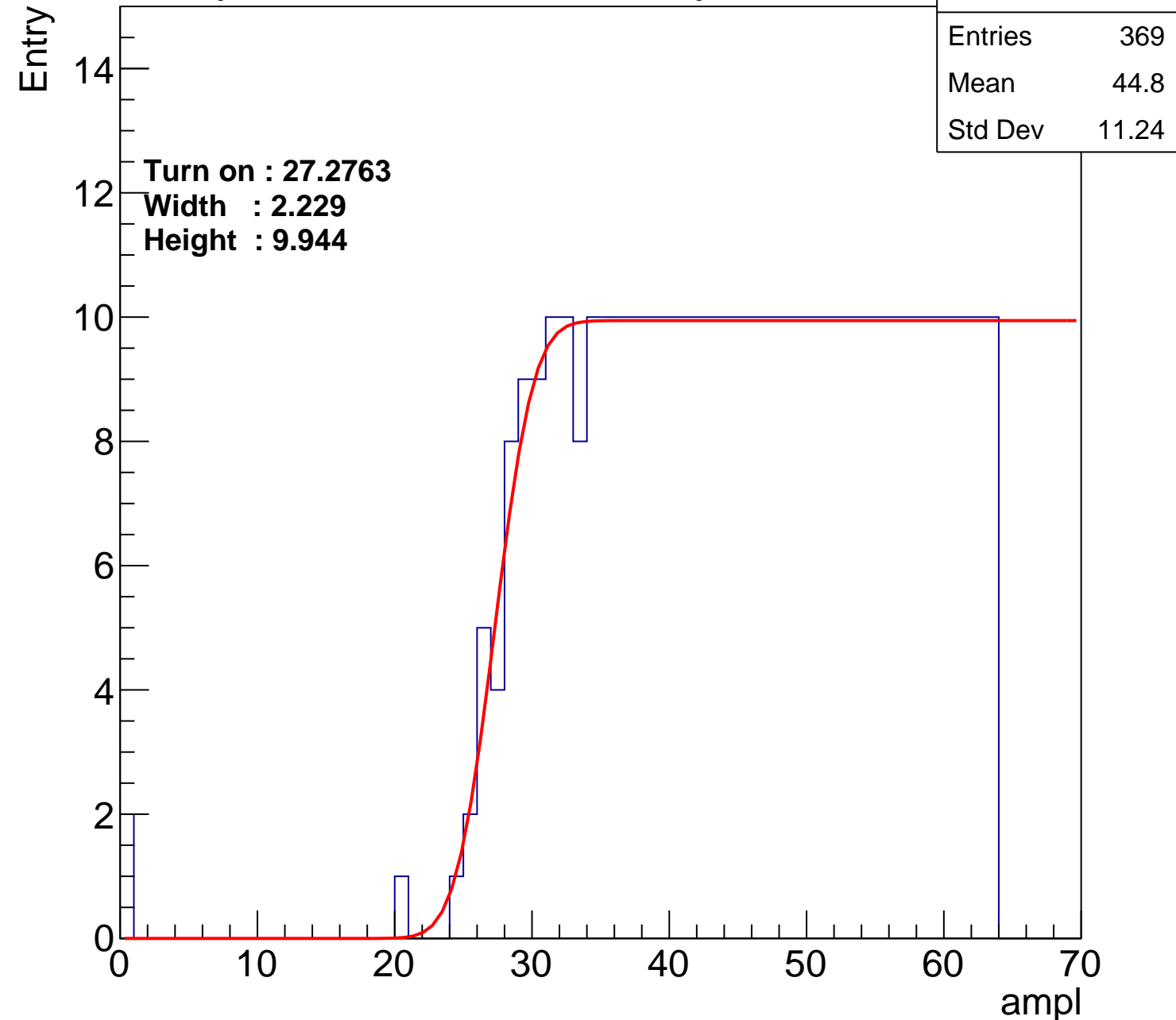
Width : 2.229

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 25.9959

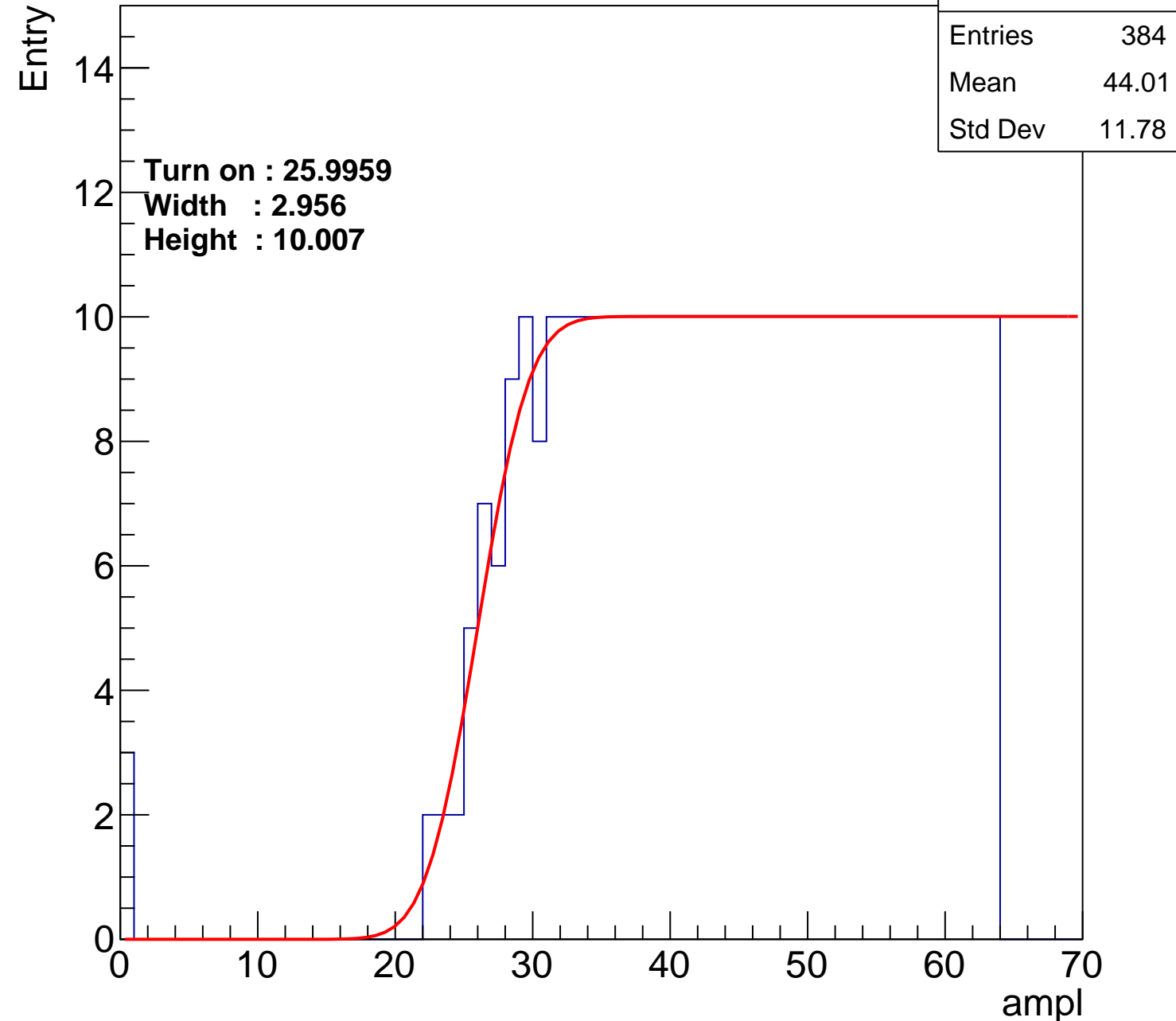
Width : 2.956

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.6083

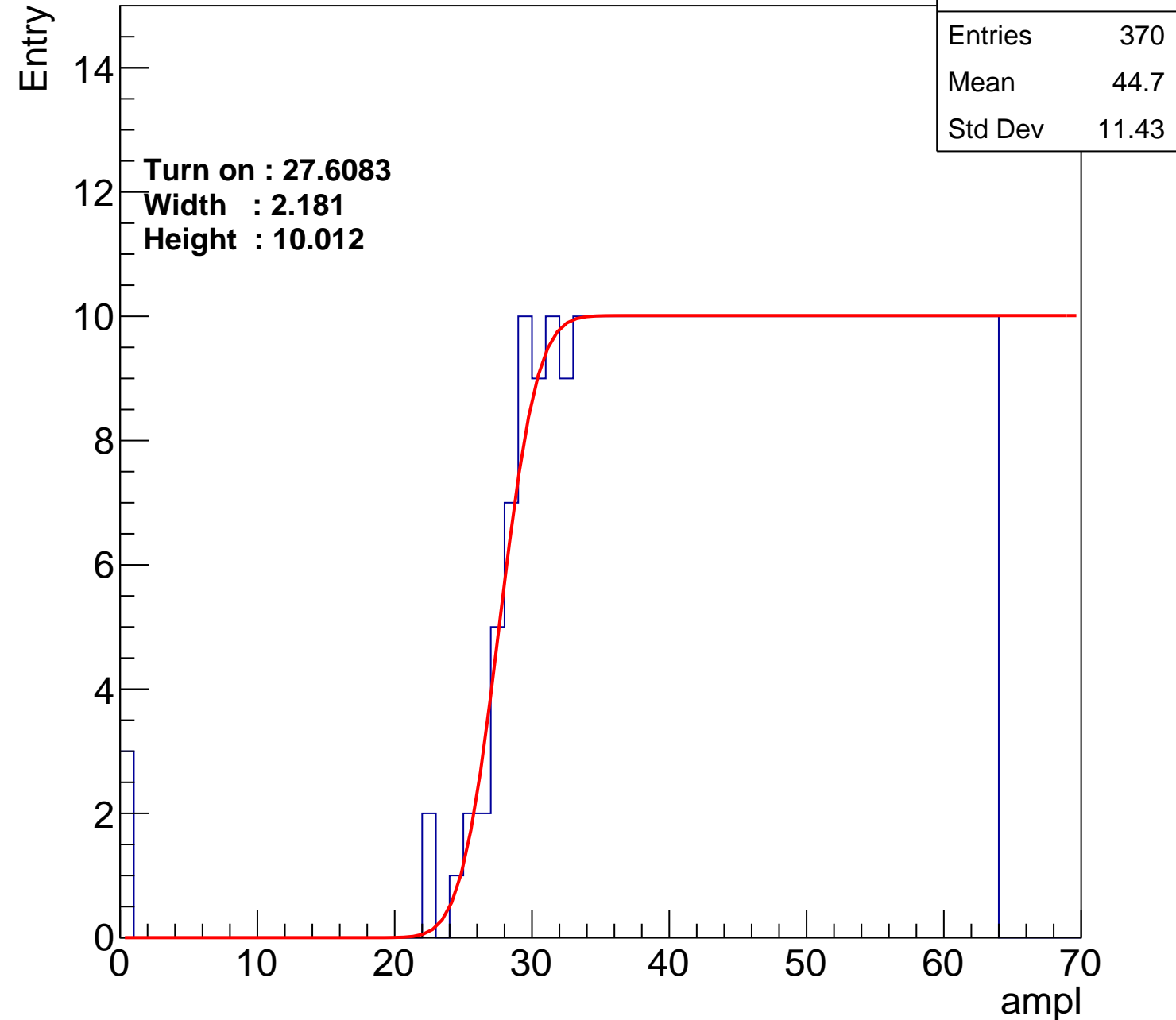
Width : 2.181

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch100

calib_packv5_042523_0143.root, FC#5, port B1

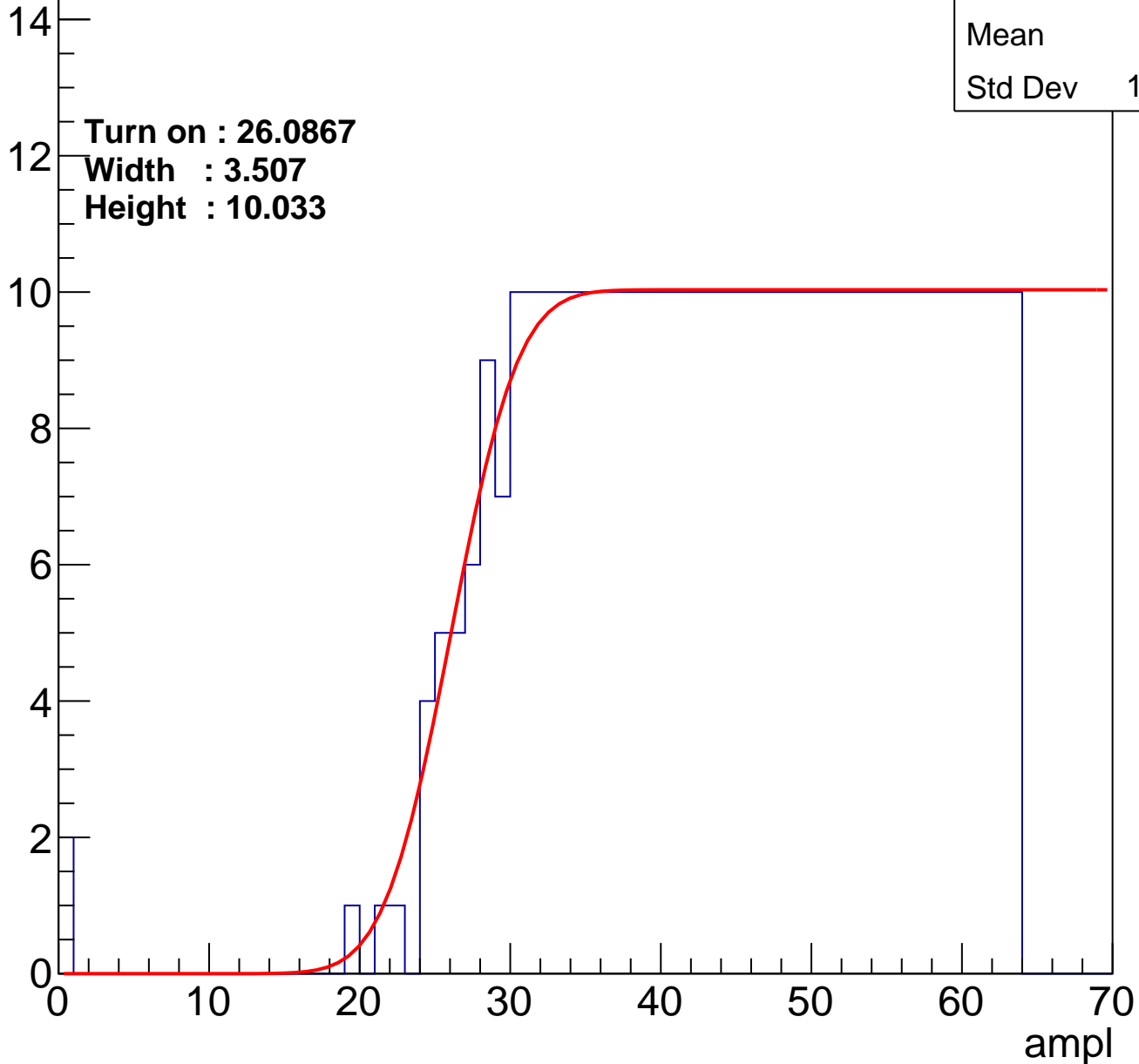
Entries	381
Mean	44.2
Std Dev	11.57

Turn on : 26.0867

Width : 3.507

Height : 10.033

Entry



B0L000S, U25-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.73
Std Dev	11.24

Turn on : 27.1965

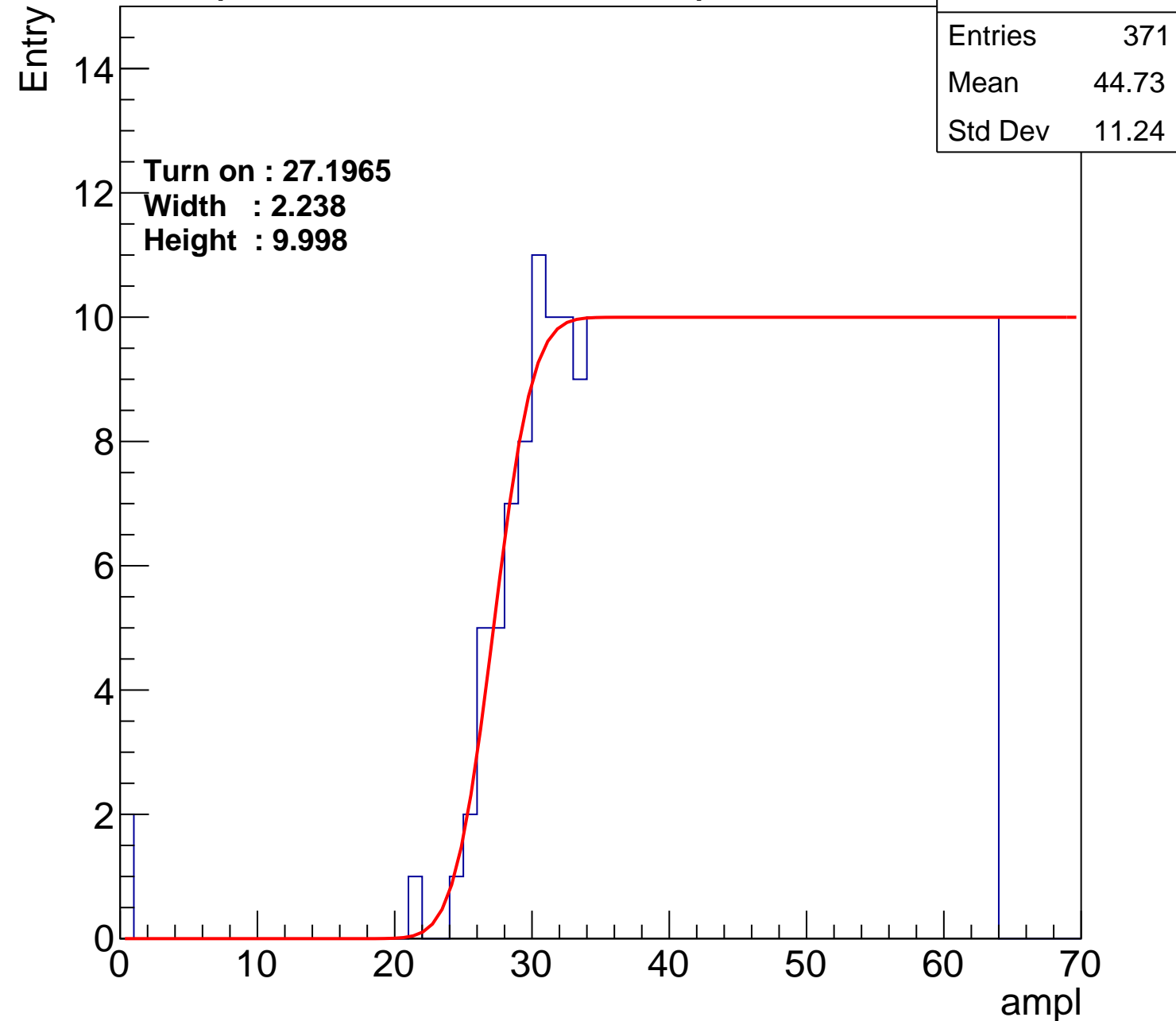
Width : 2.238

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch102

calib_packv5_042523_0143.root, FC#5, port B1

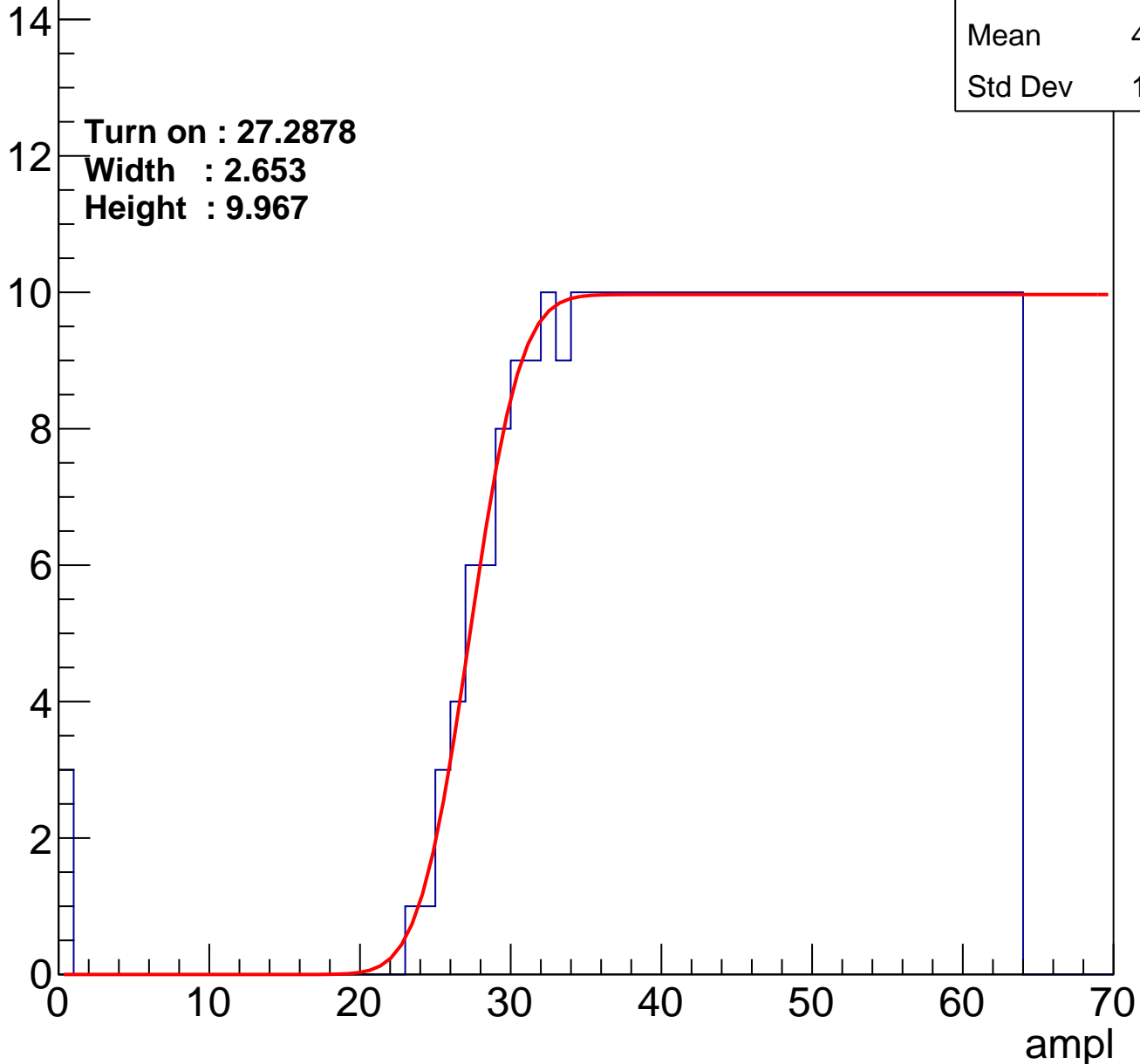
Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.2878

Width : 2.653

Height : 9.967

Entry



B0L000S, U25-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 26.7506

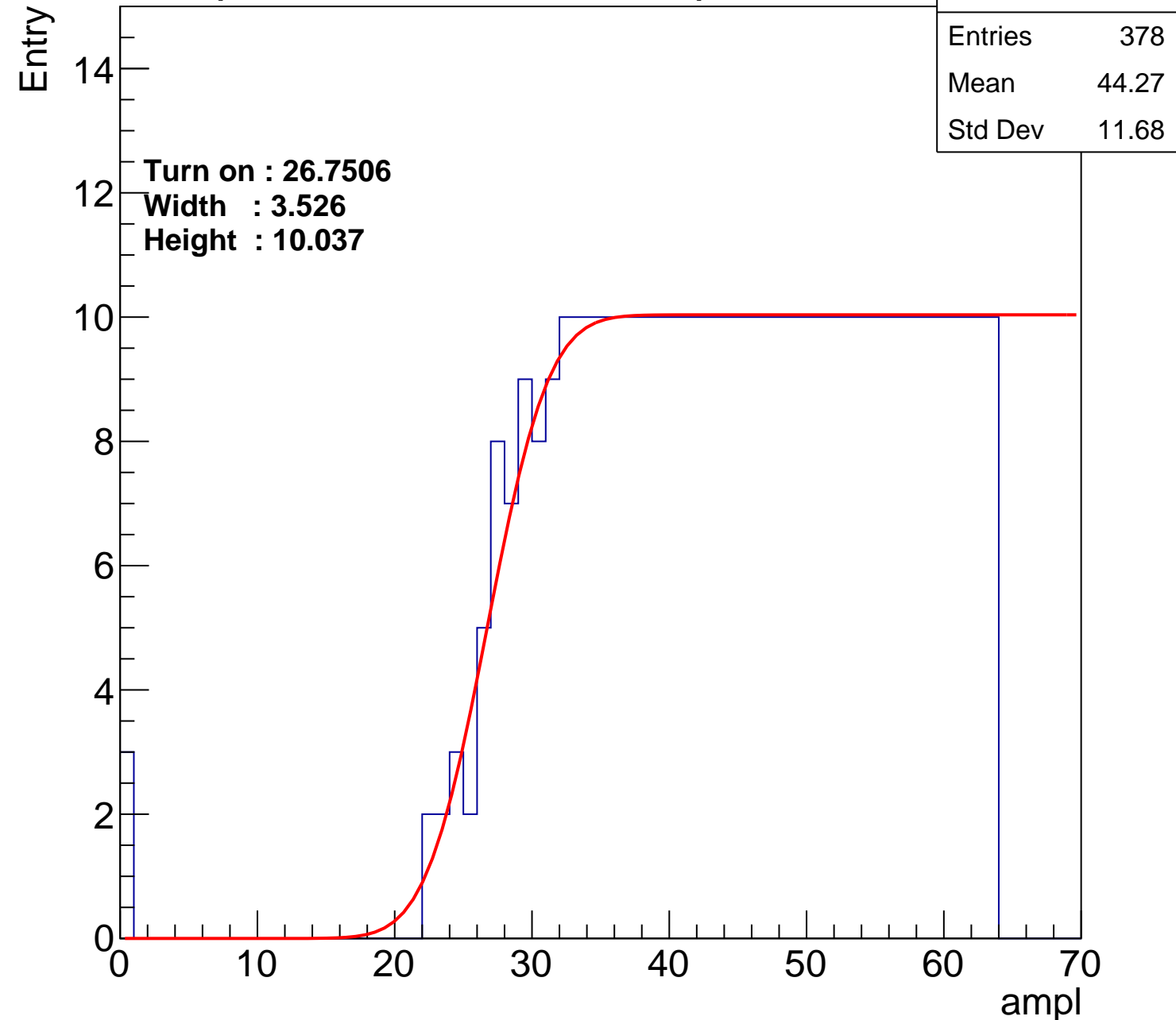
Width : 3.526

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.8316

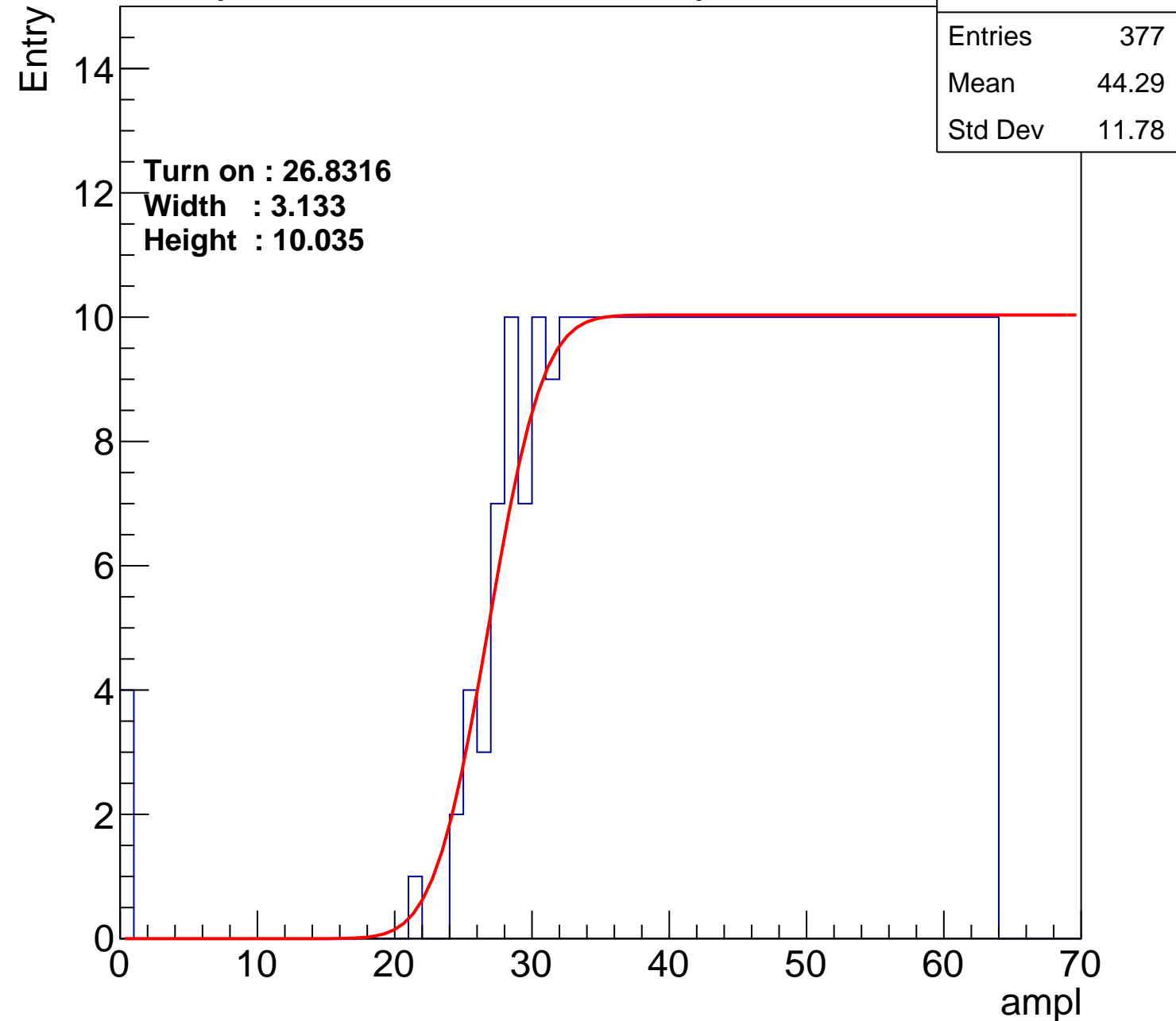
Width : 3.133

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.76
Std Dev	11.98

Turn on : 25.5537

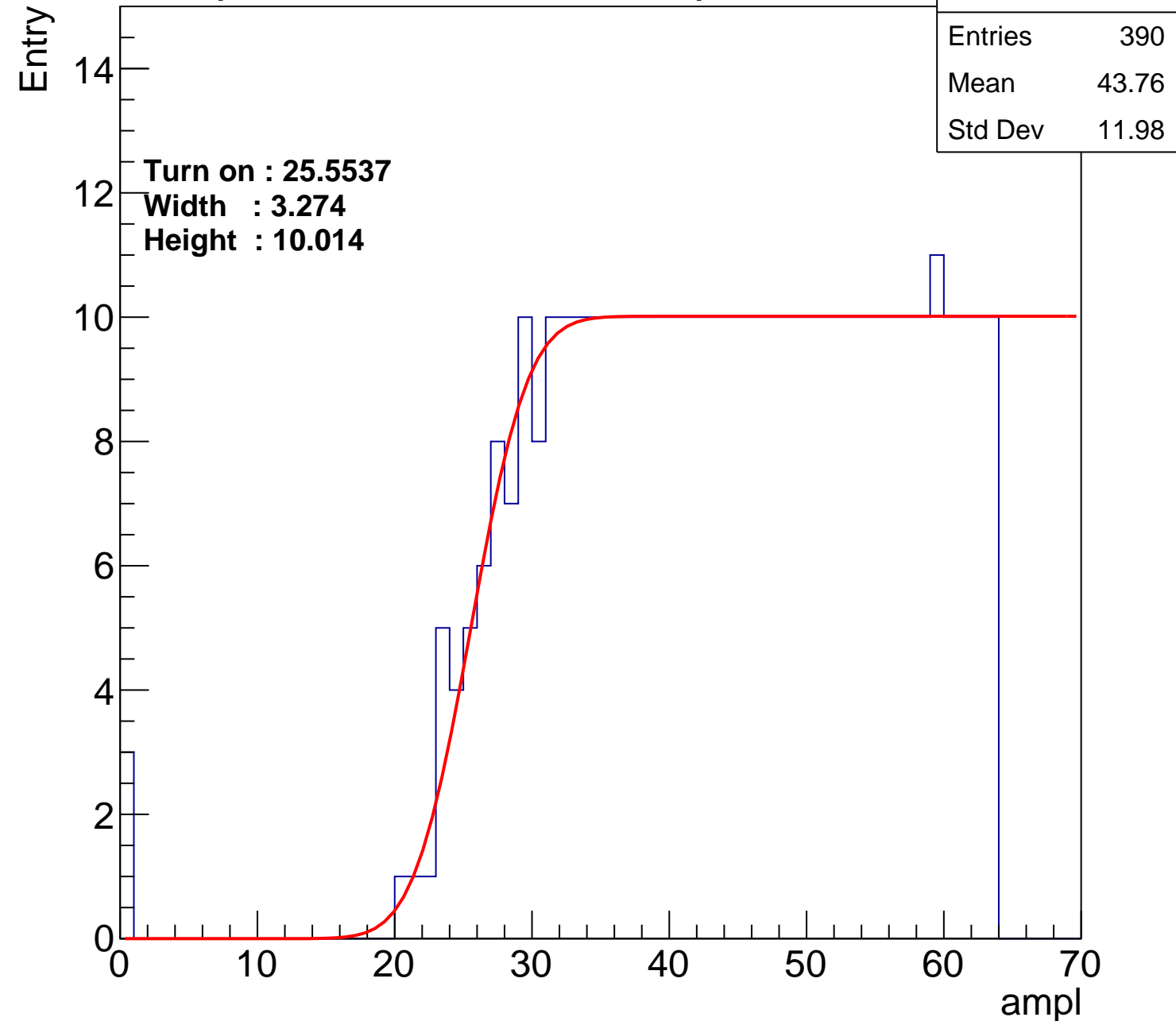
Width : 3.274

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch106

calib_packv5_042523_0143.root, FC#5, port B1

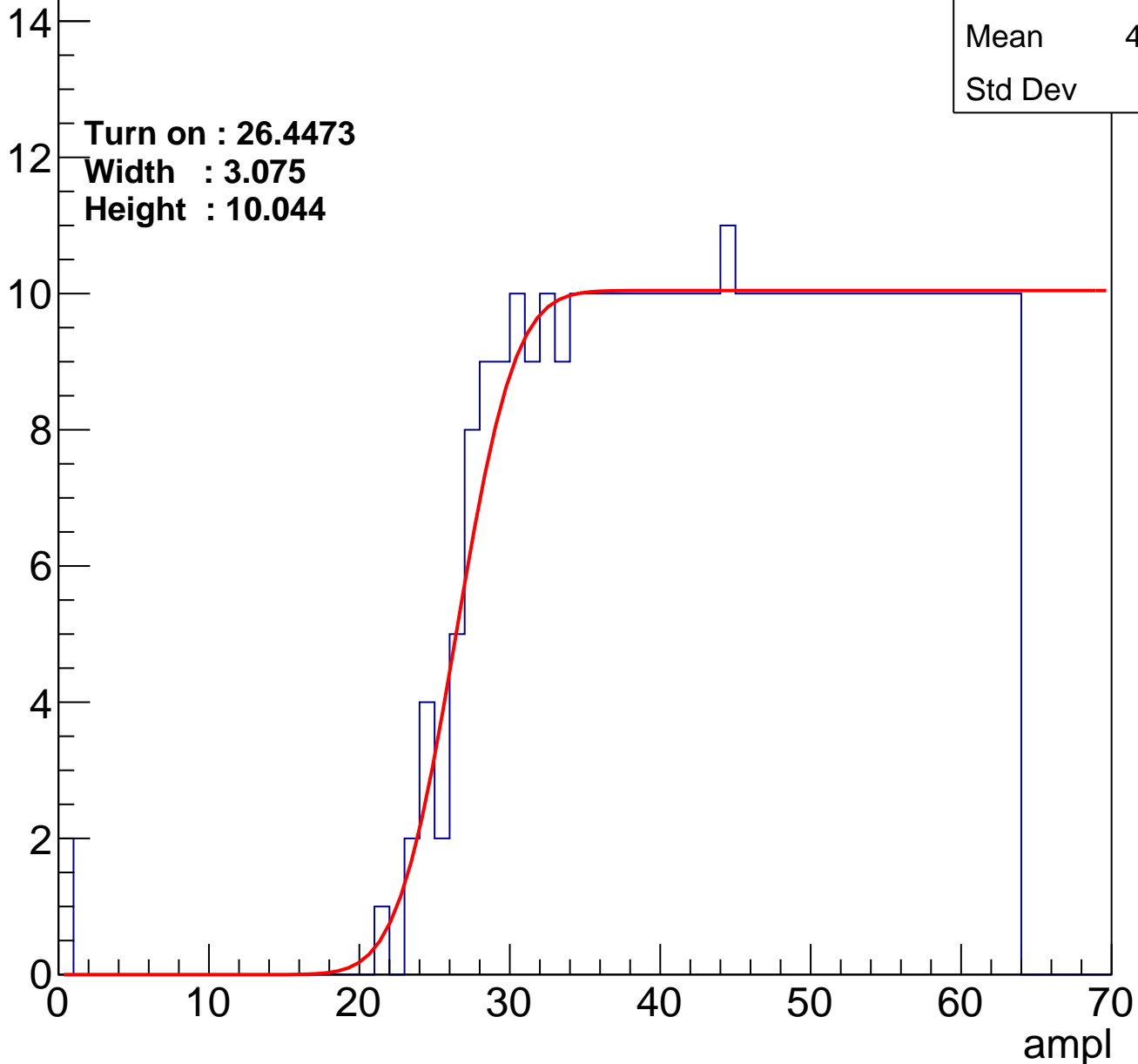
Entries	381
Mean	44.26
Std Dev	11.5

Turn on : 26.4473

Width : 3.075

Height : 10.044

Entry



B0L000S, U25-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.14
Std Dev	11.48

Turn on : 25.8561

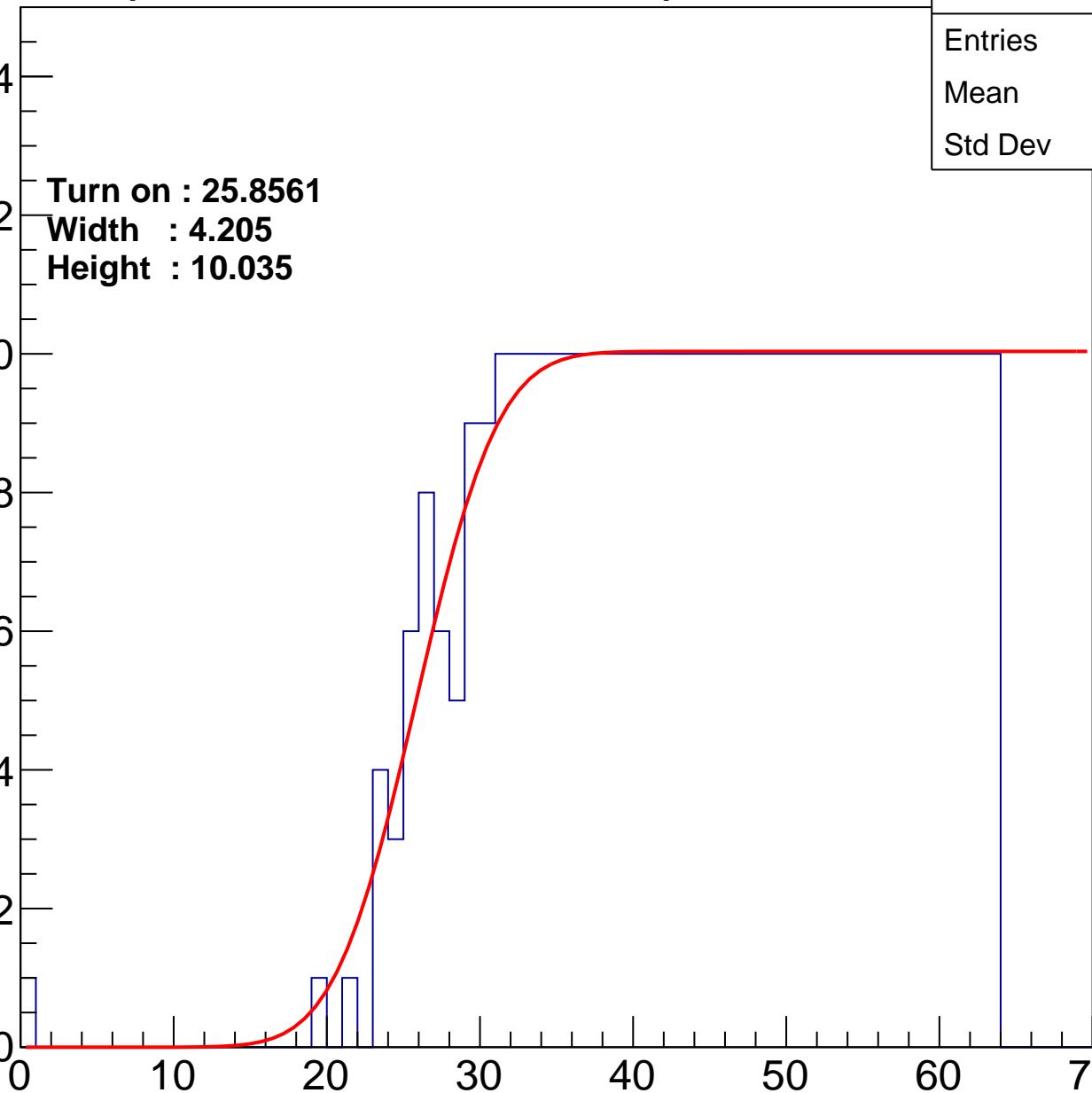
Width : 4.205

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.96
Std Dev	11.73

Turn on : 26.2498

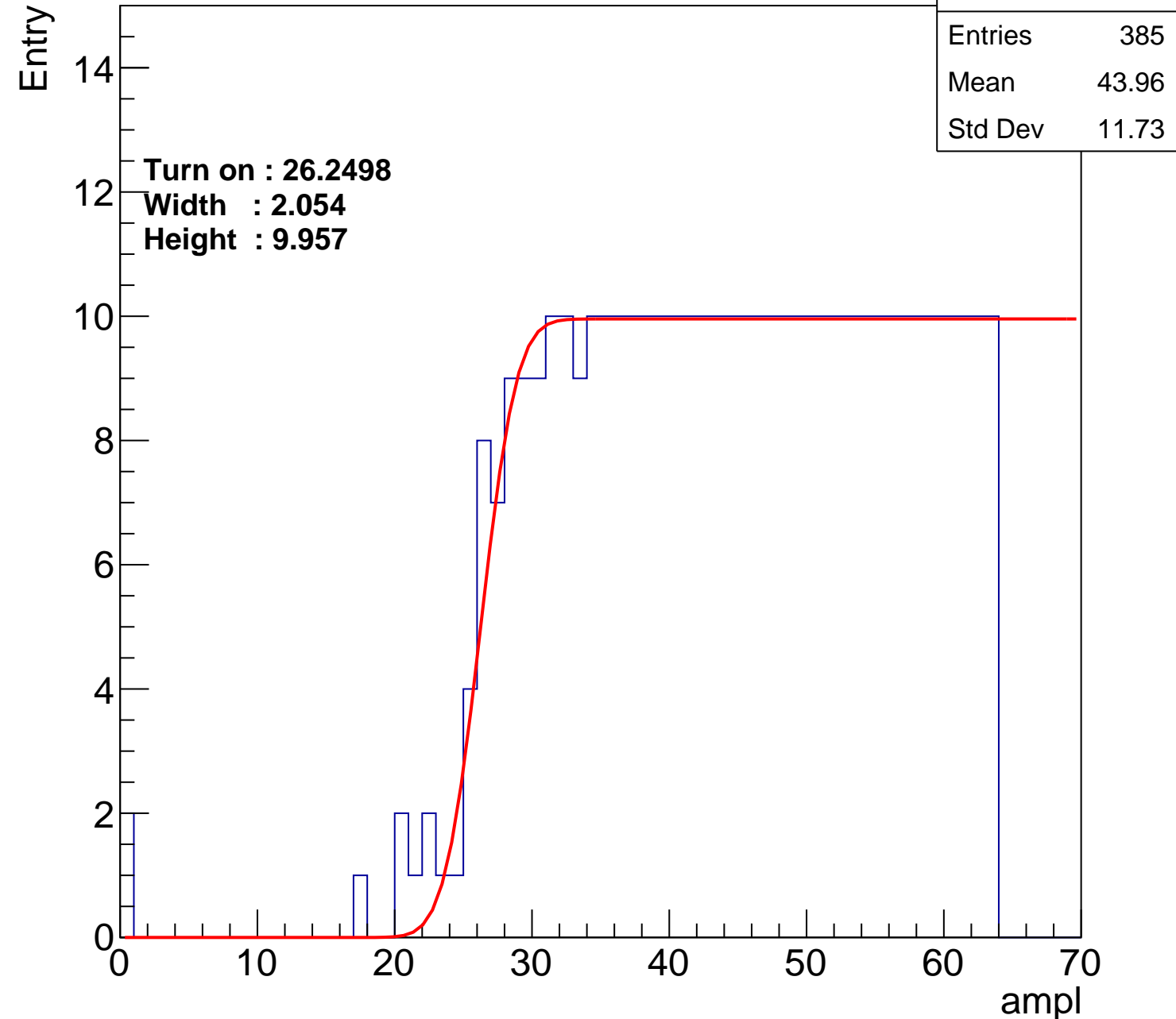
Width : 2.054

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch109

calib_packv5_042523_0143.root, FC#5, port B1

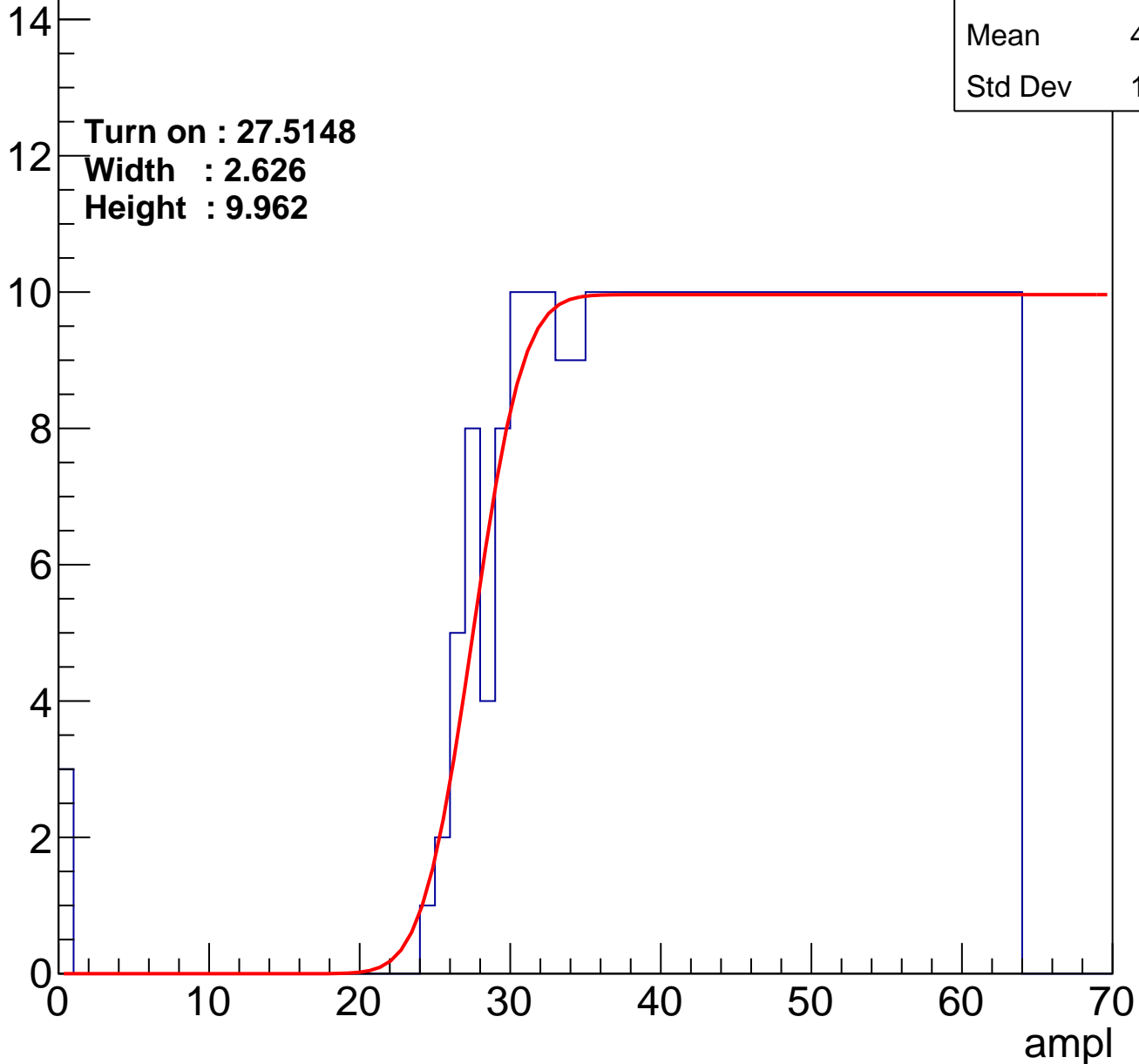
Entries	369
Mean	44.73
Std Dev	11.42

Turn on : 27.5148

Width : 2.626

Height : 9.962

Entry



B0L000S, U25-ch110

calib_packv5_042523_0143.root, FC#5, port B1

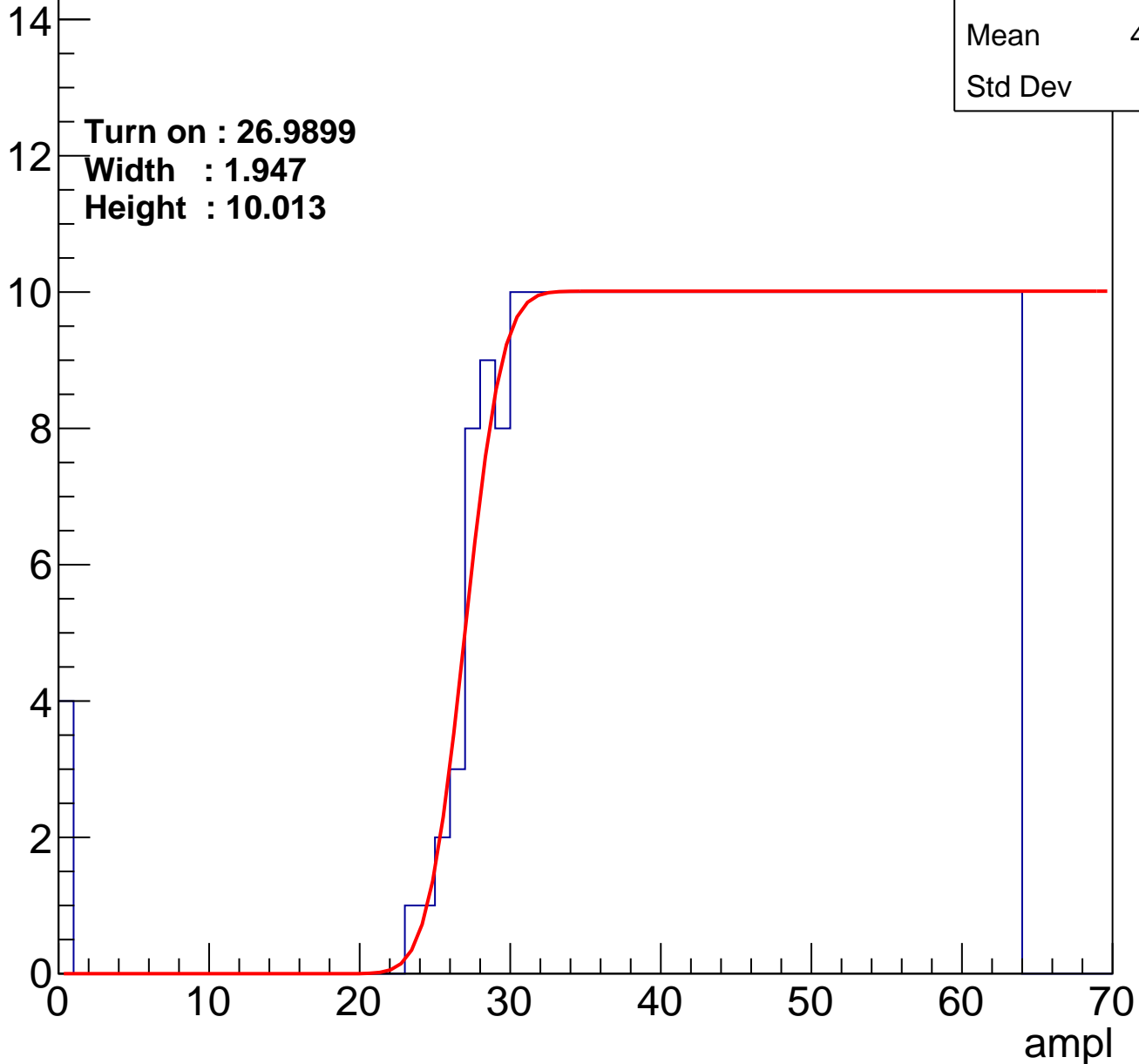
Entries	376
Mean	44.38
Std Dev	11.7

Turn on : 26.9899

Width : 1.947

Height : 10.013

Entry



B0L000S, U25-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.64
Std Dev	11.52

Turn on : 27.3648

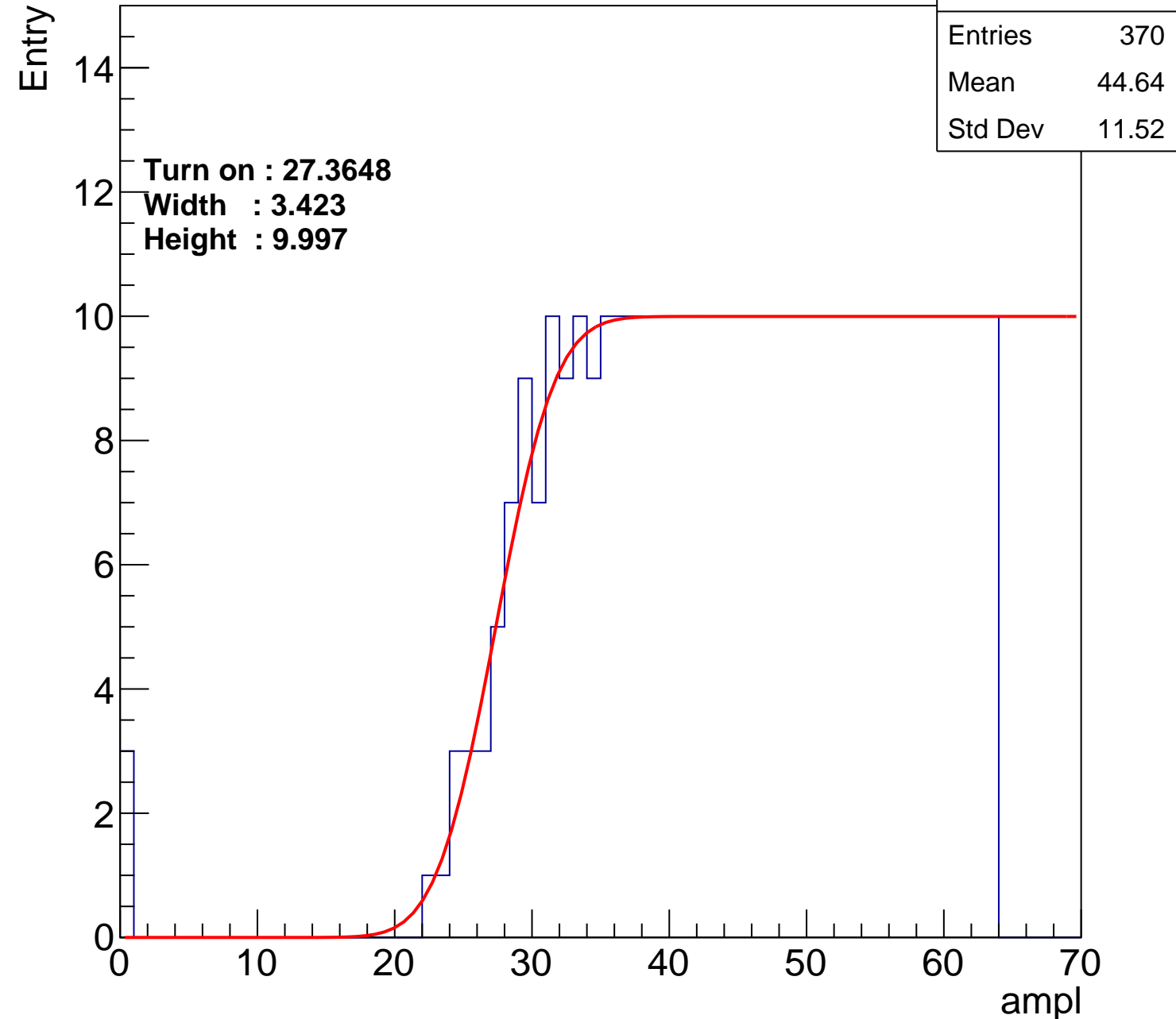
Width : 3.423

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch112

calib_packv5_042523_0143.root, FC#5, port B1

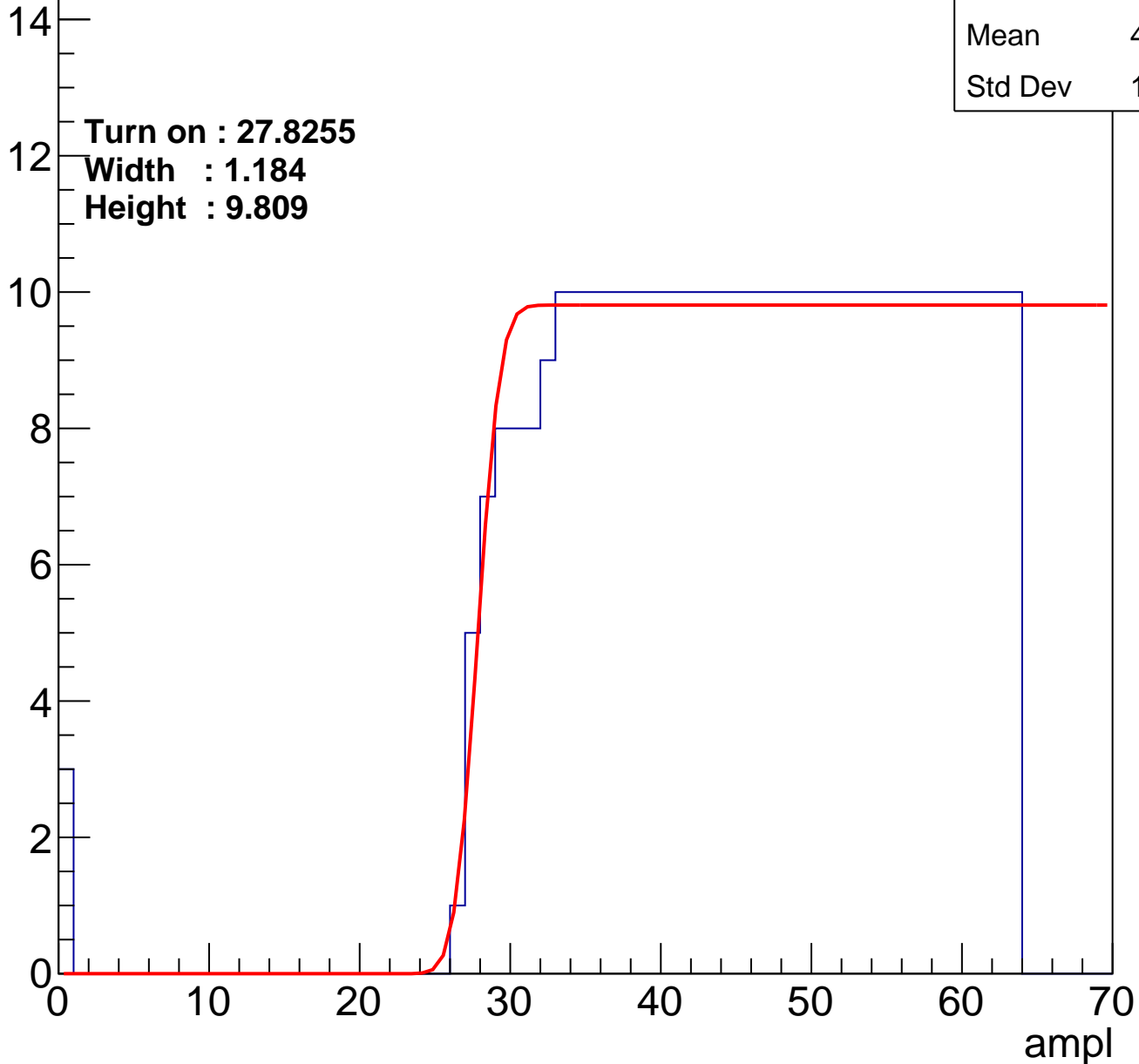
Entries	359
Mean	45.25
Std Dev	11.14

Turn on : 27.8255

Width : 1.184

Height : 9.809

Entry



B0L000S, U25-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.19
Std Dev	11.25

Turn on : 29.0725

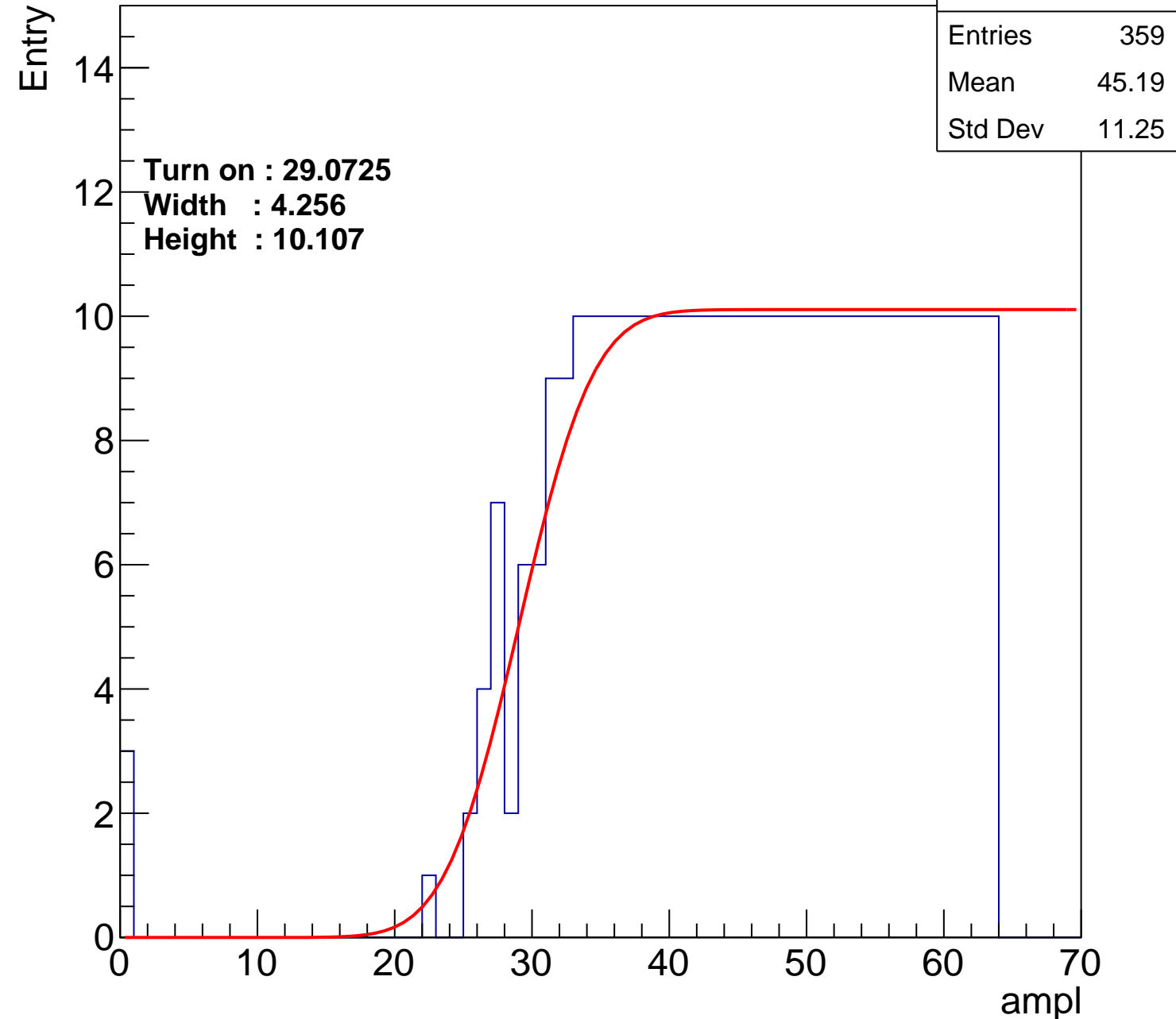
Width : 4.256

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 26.9343

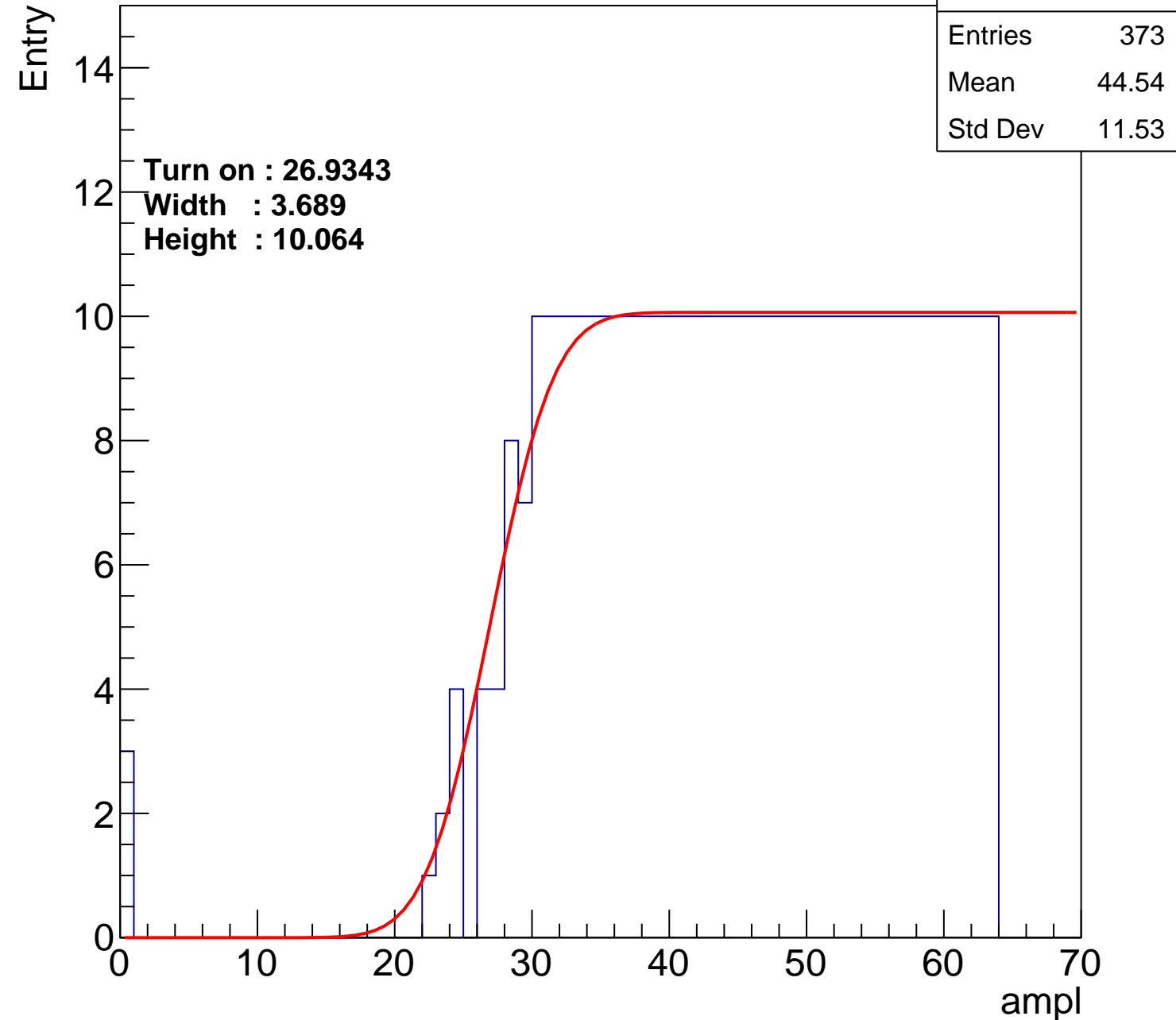
Width : 3.689

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch115

calib_packv5_042523_0143.root, FC#5, port B1

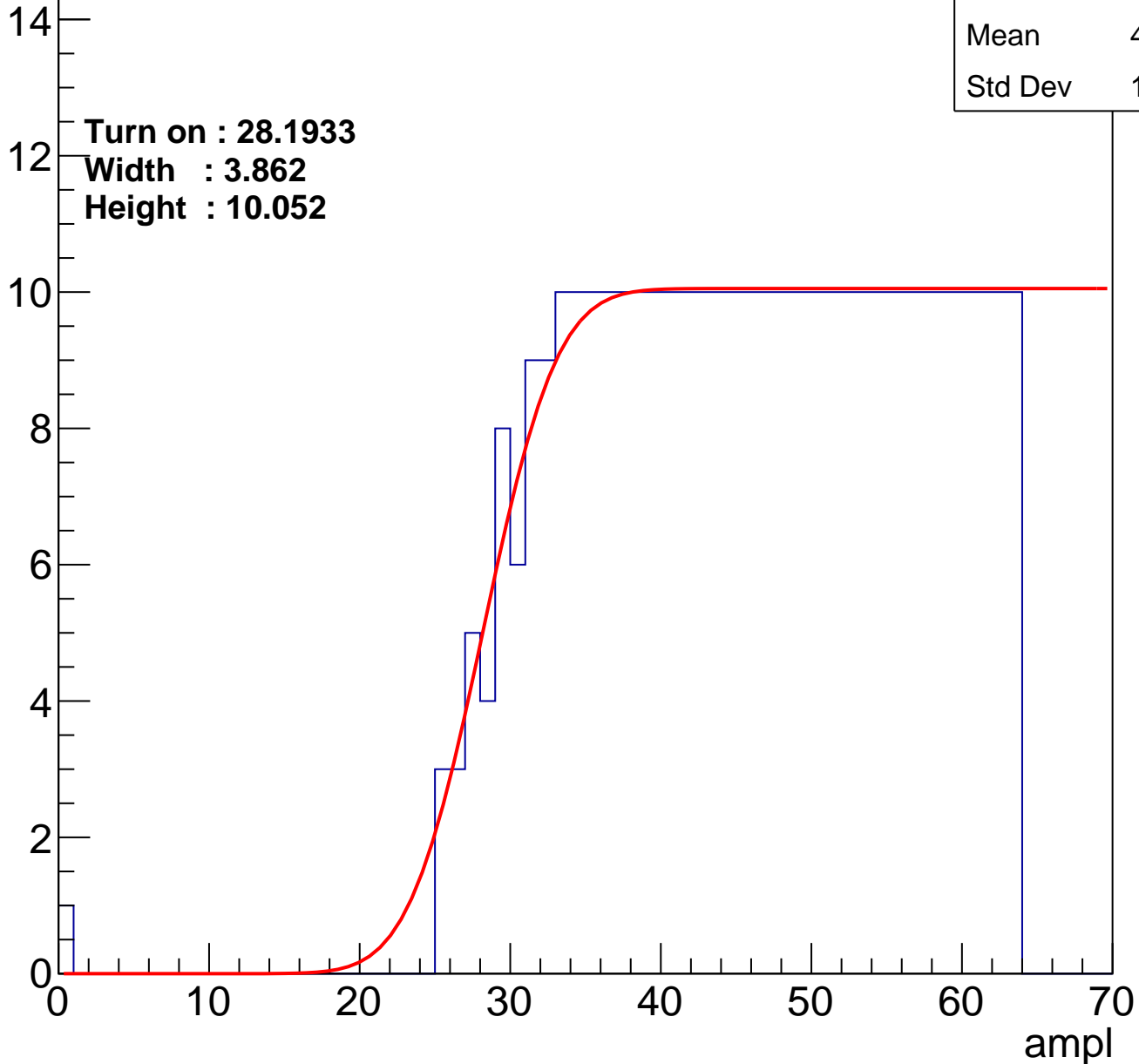
Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 28.1933

Width : 3.862

Height : 10.052

Entry



B0L000S, U25-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 26.4125

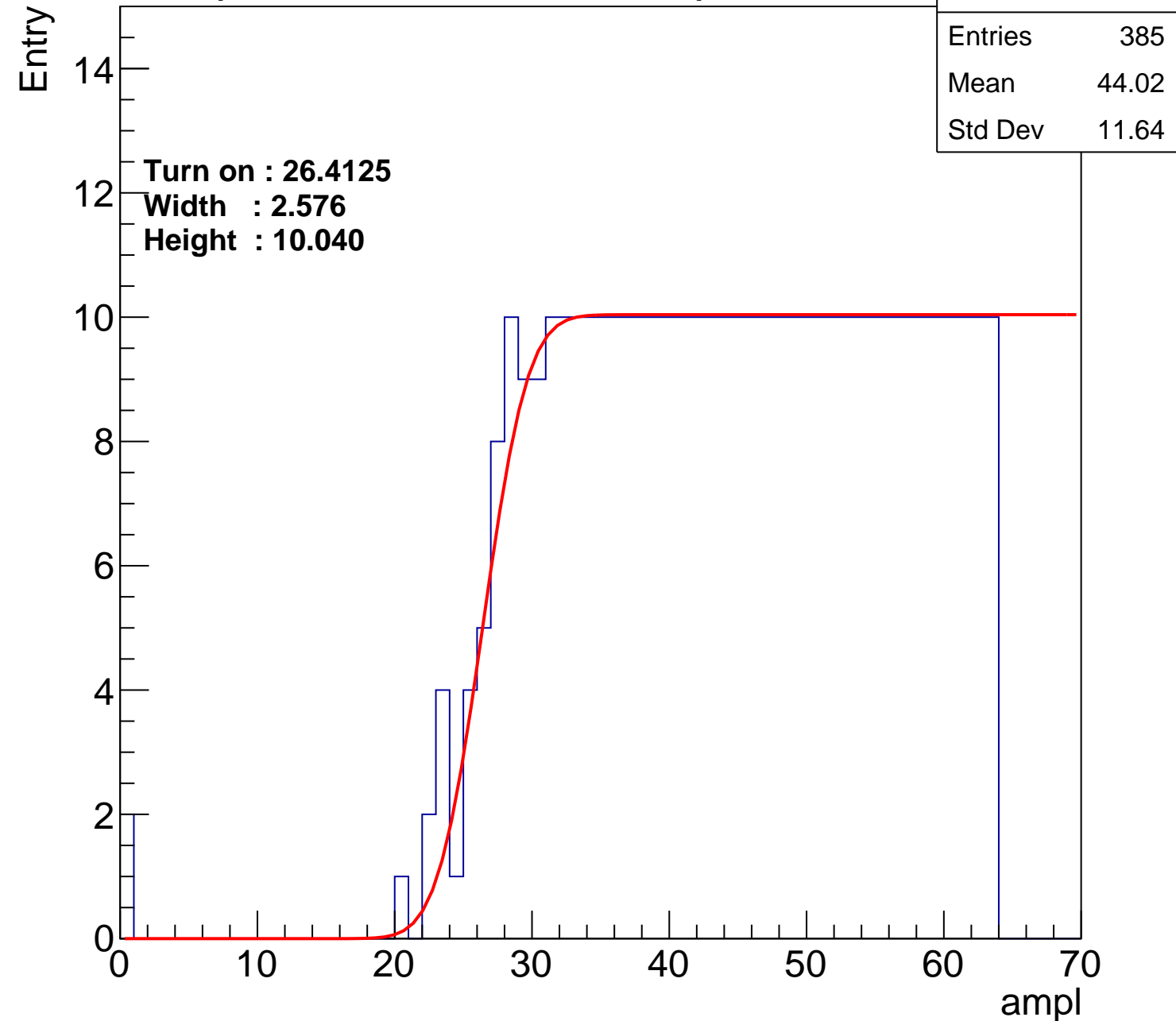
Width : 2.576

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch117

calib_packv5_042523_0143.root, FC#5, port B1

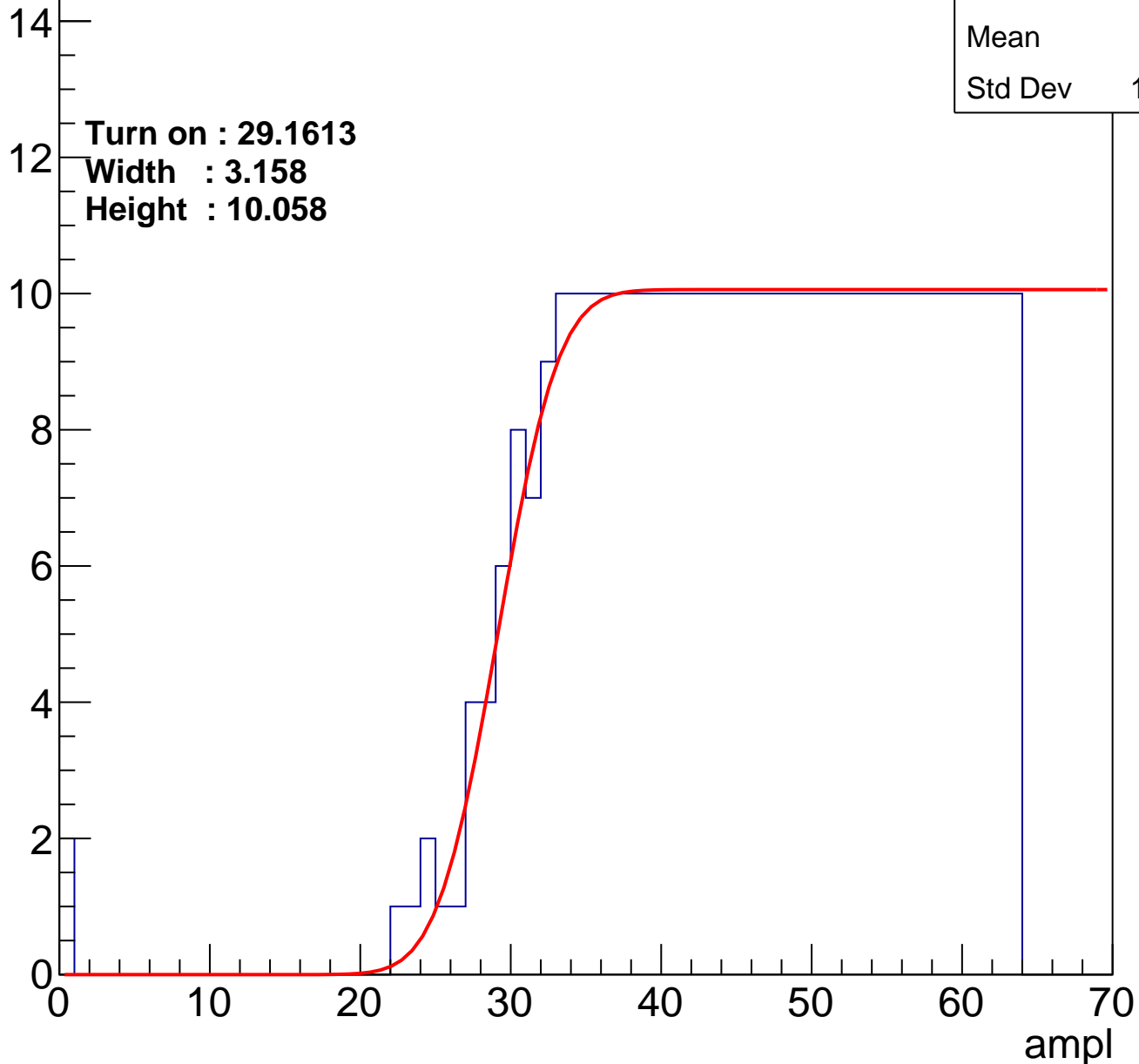
Entries	356
Mean	45.4
Std Dev	10.98

Turn on : 29.1613

Width : 3.158

Height : 10.058

Entry



B0L000S, U25-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.23
Std Dev	10.84

Turn on : 27.9898

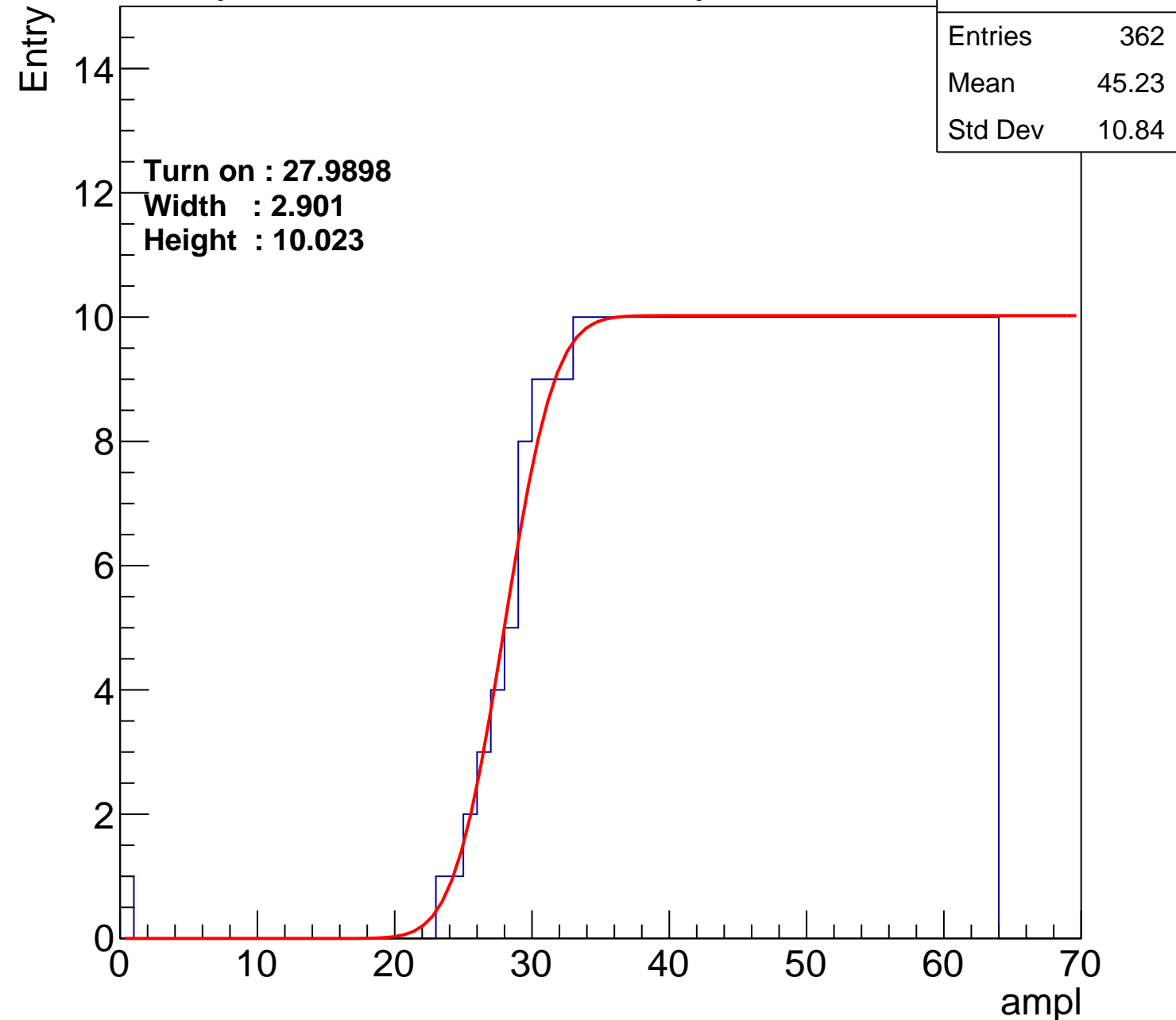
Width : 2.901

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.1
Std Dev	10.93

Turn on : 28.1263

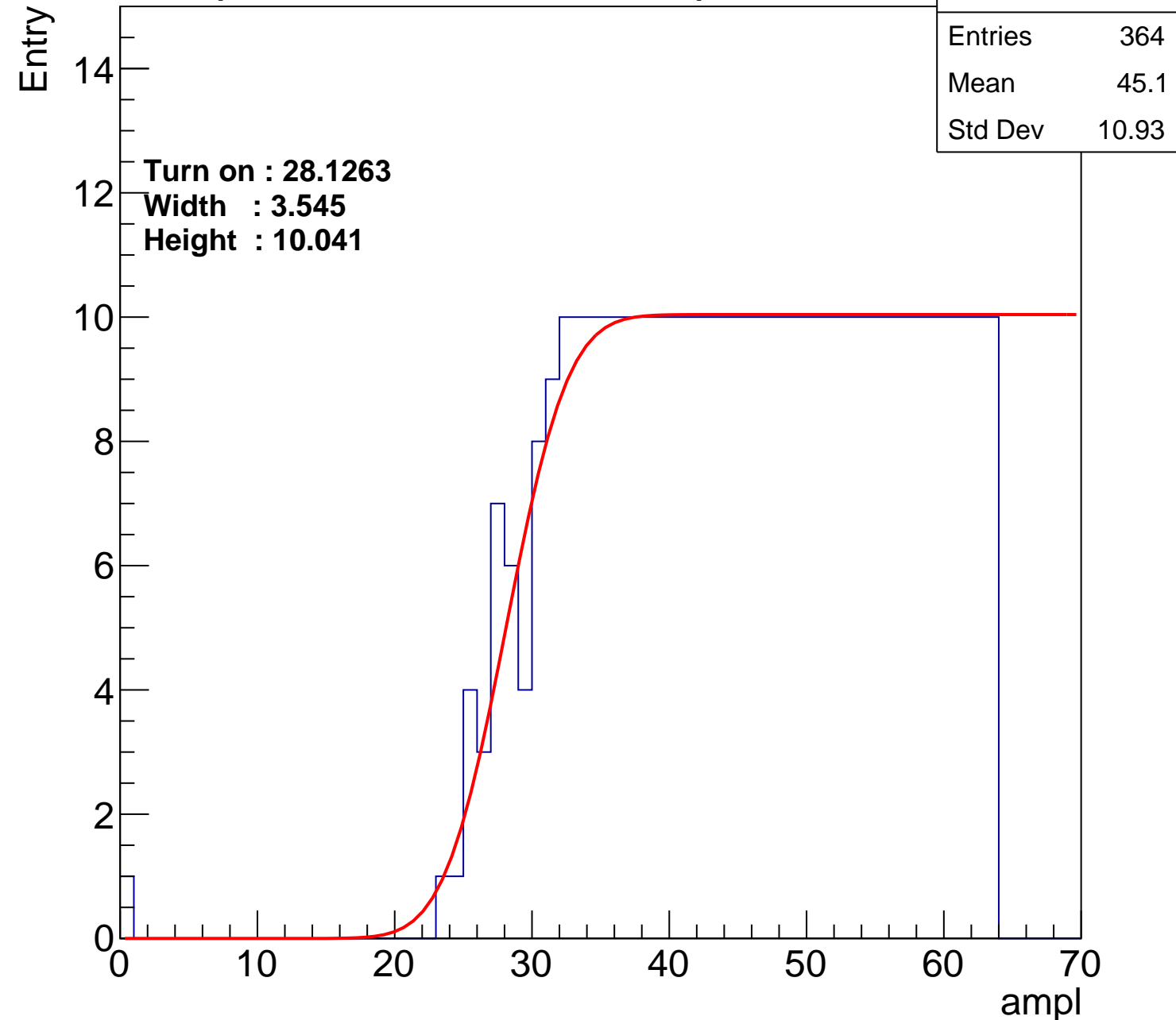
Width : 3.545

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.25
Std Dev	11.35

Turn on : 28.6010

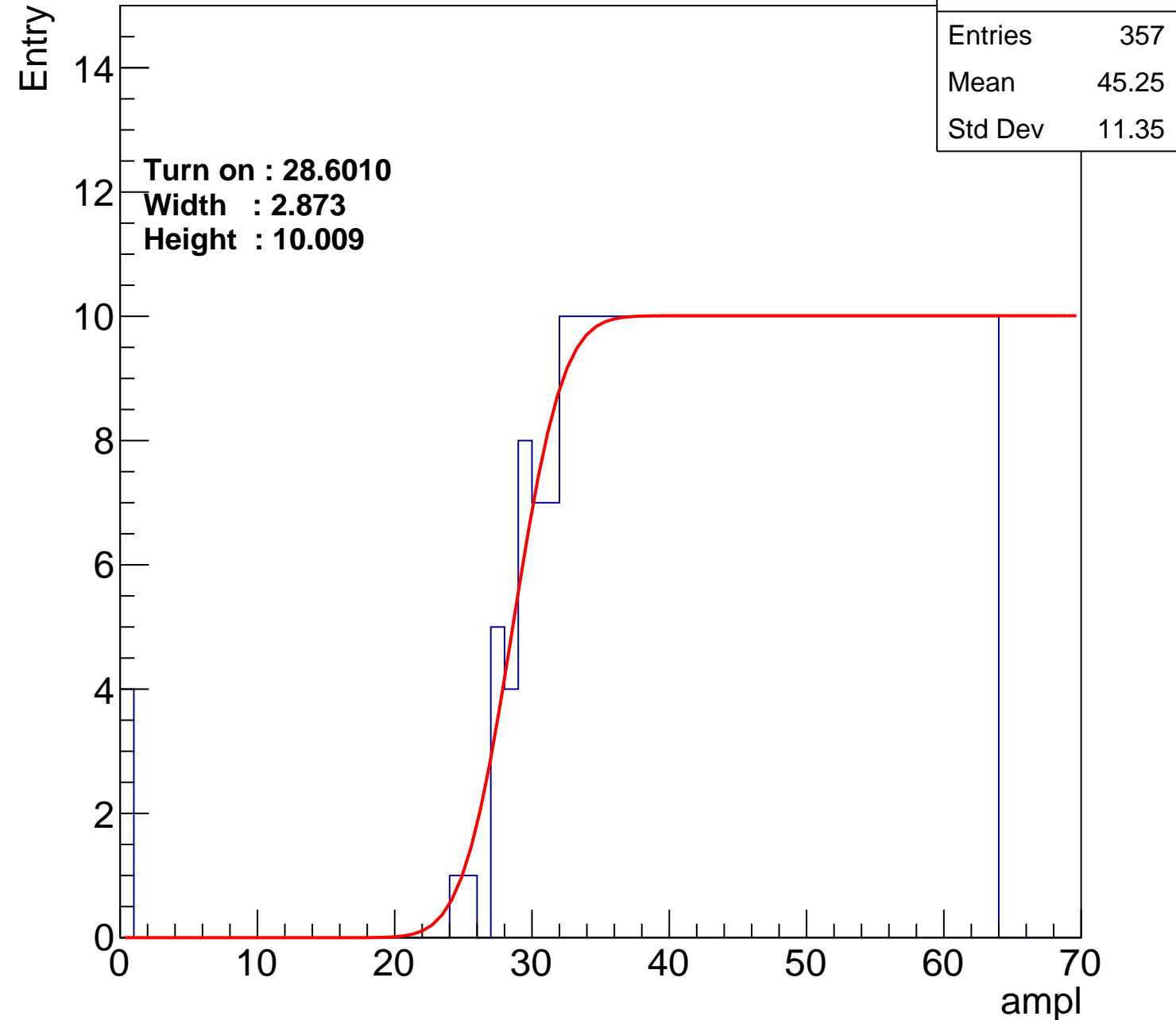
Width : 2.873

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch121

calib_packv5_042523_0143.root, FC#5, port B1

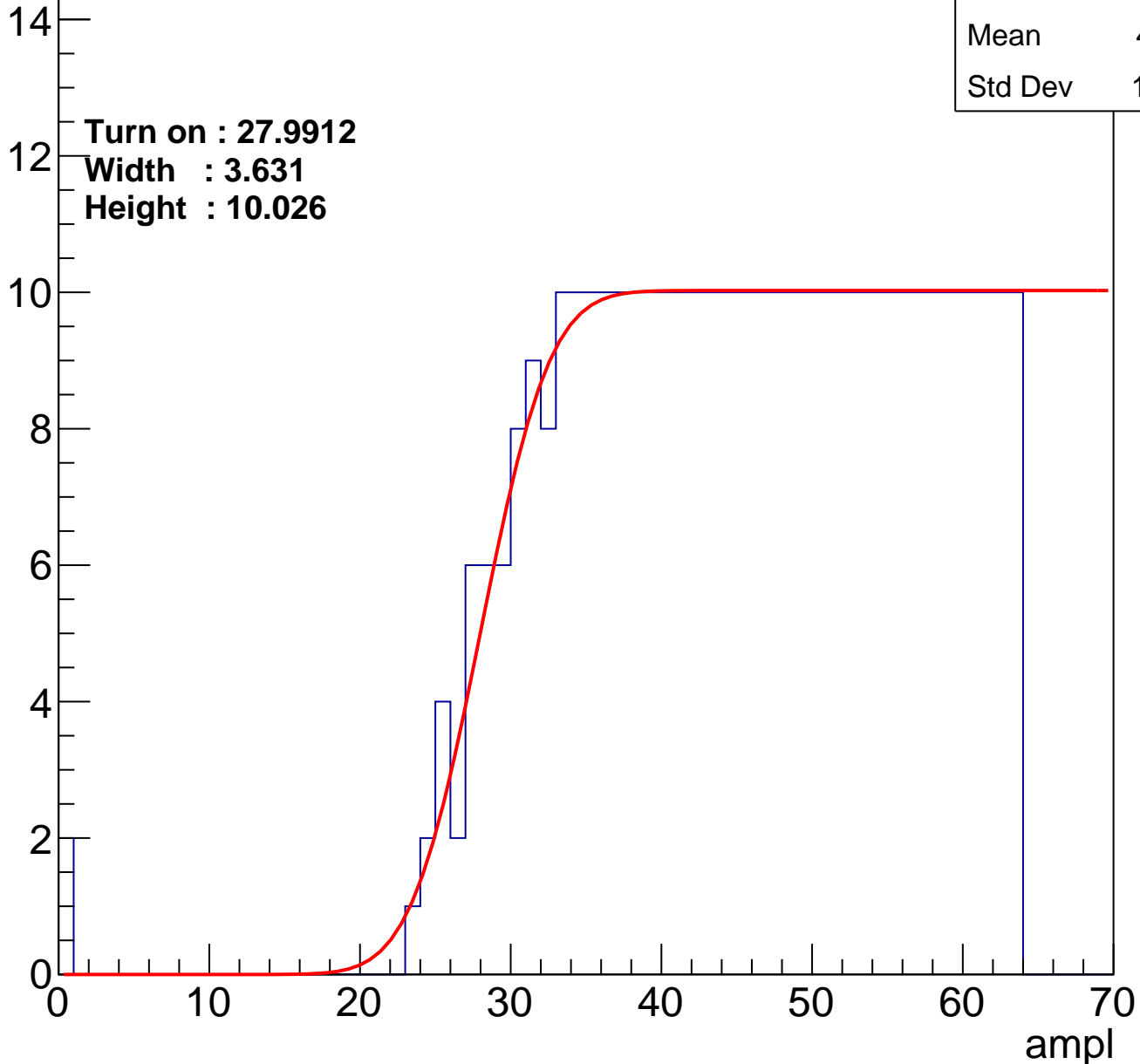
Entries	364
Mean	45.01
Std Dev	11.18

Turn on : 27.9912

Width : 3.631

Height : 10.026

Entry



B0L000S, U25-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.11
Std Dev	11.26

Turn on : 28.4837

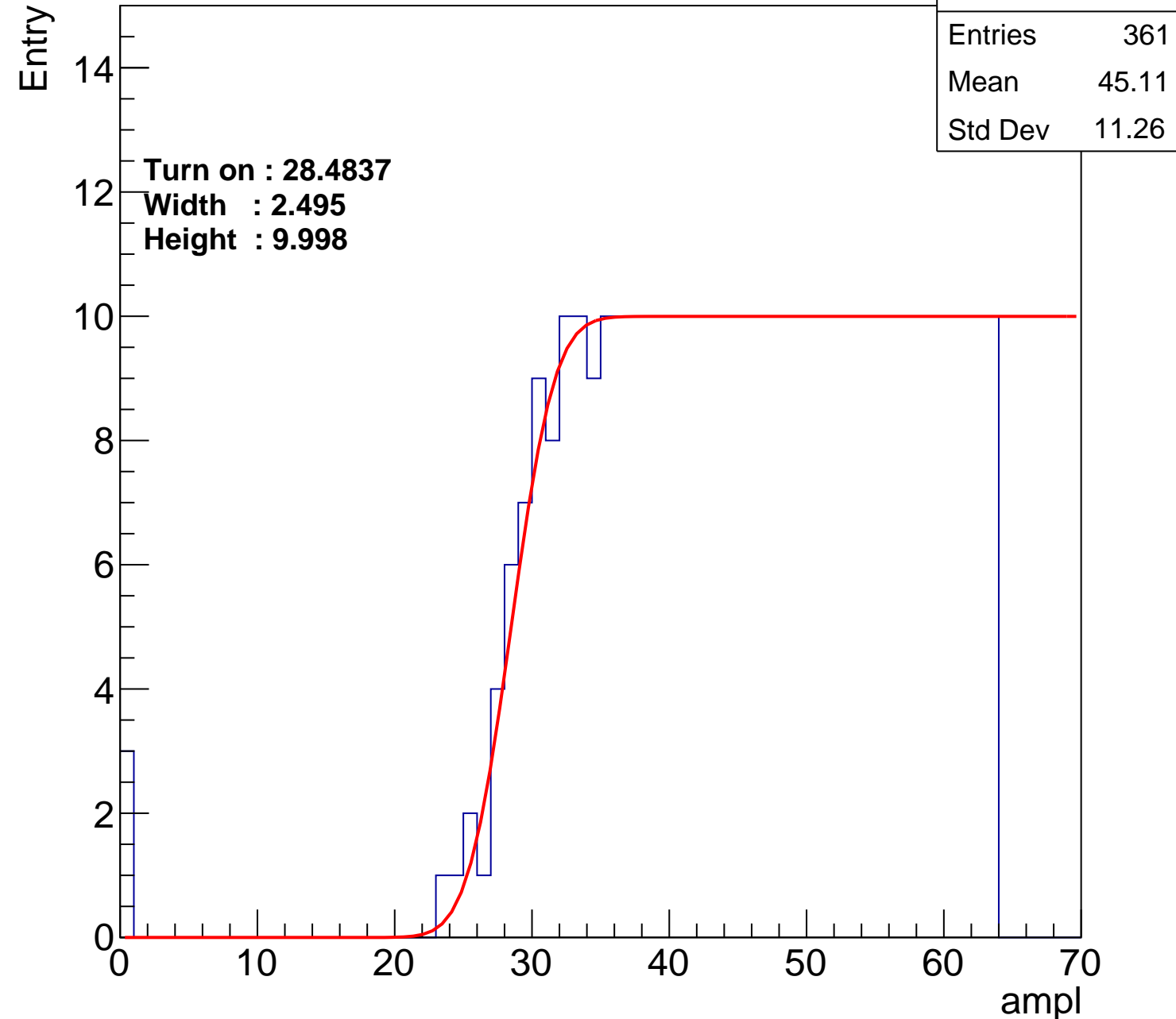
Width : 2.495

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.02
Std Dev	11.11

Turn on : 27.9307

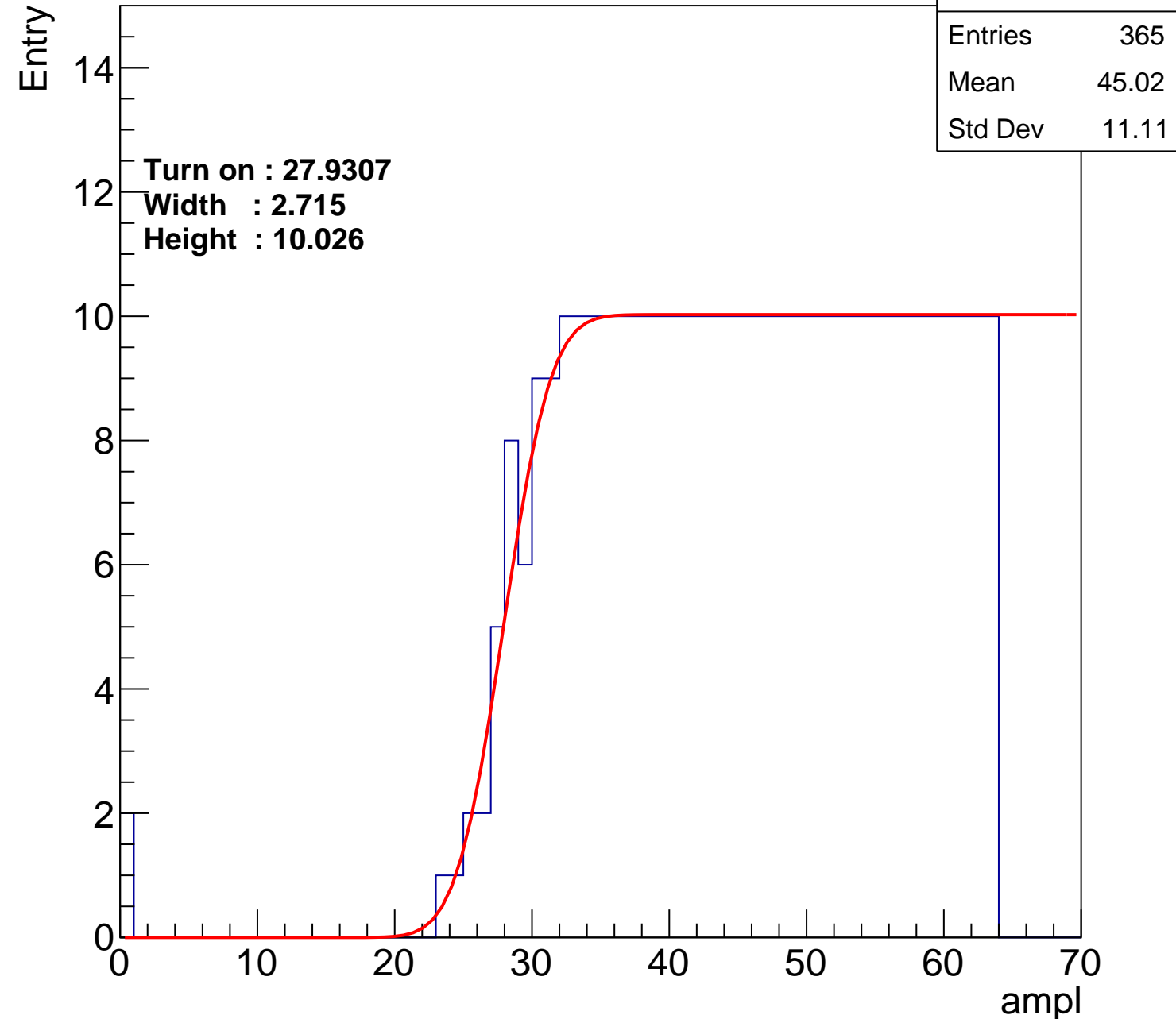
Width : 2.715

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.02
Std Dev	11.3

Turn on : 28.4914

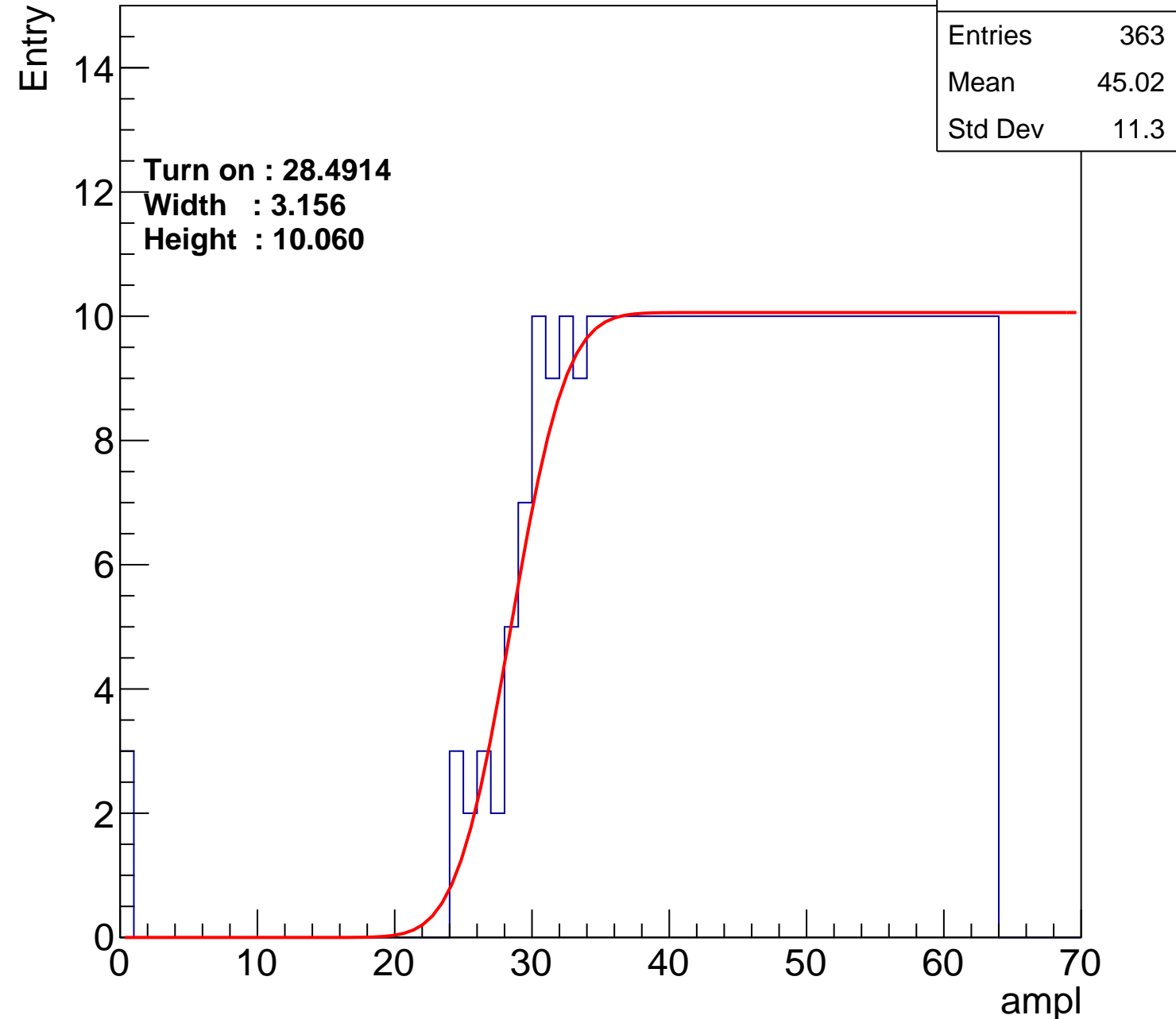
Width : 3.156

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 27.1518

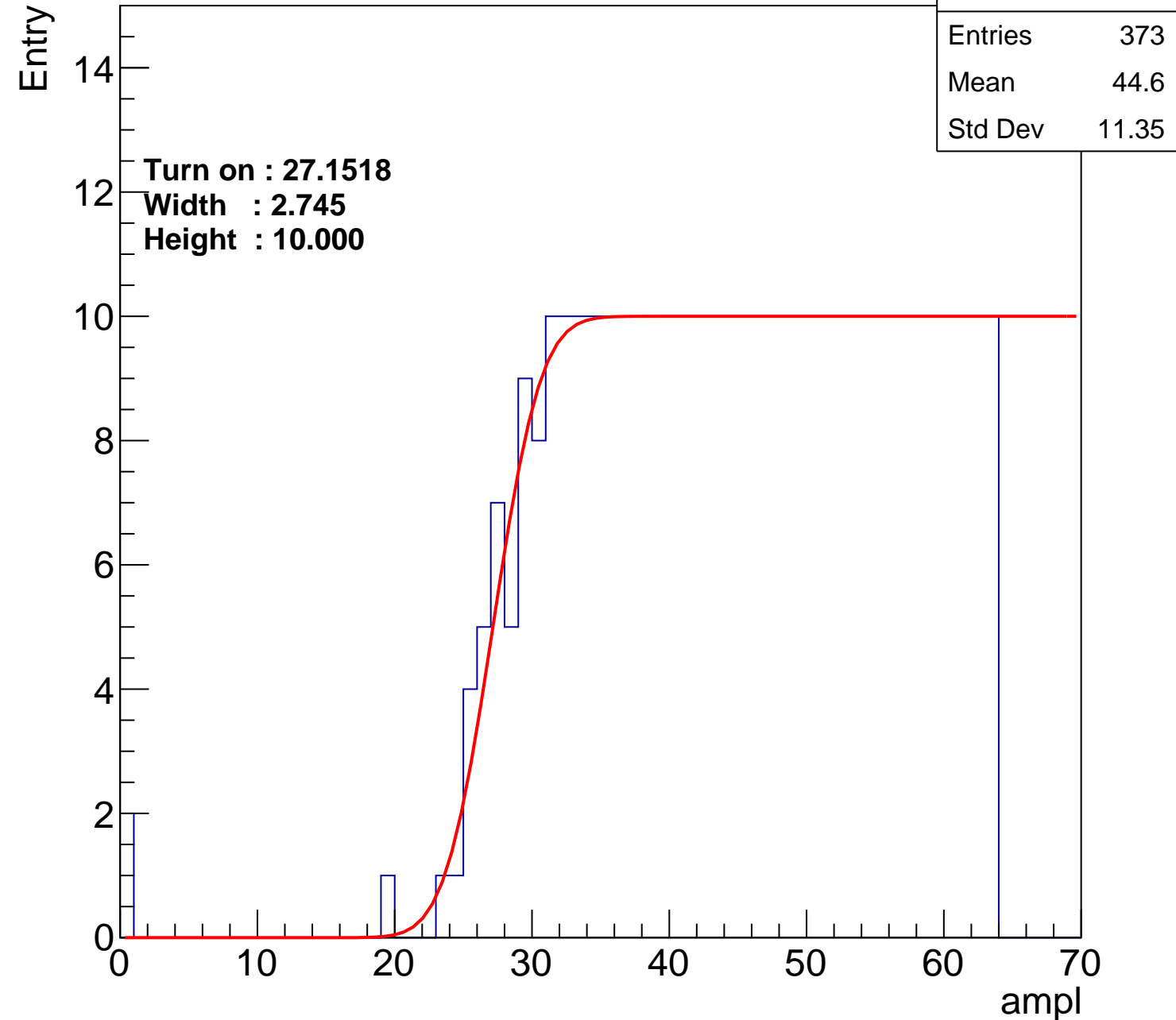
Width : 2.745

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.68
Std Dev	11.85

Turn on : 24.9013

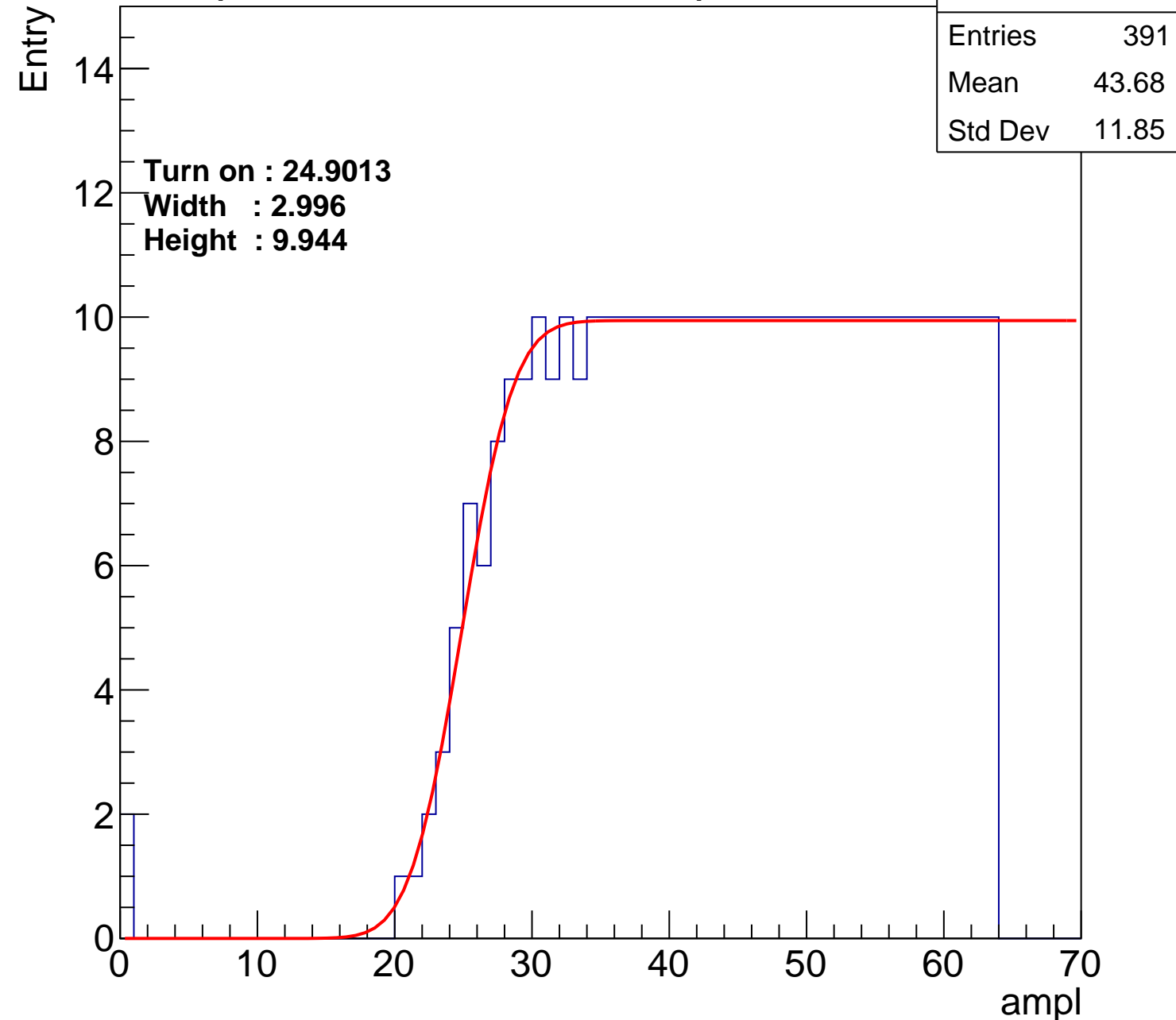
Width : 2.996

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U25-ch127

calib_packv5_042523_0143.root, FC#5, port B1

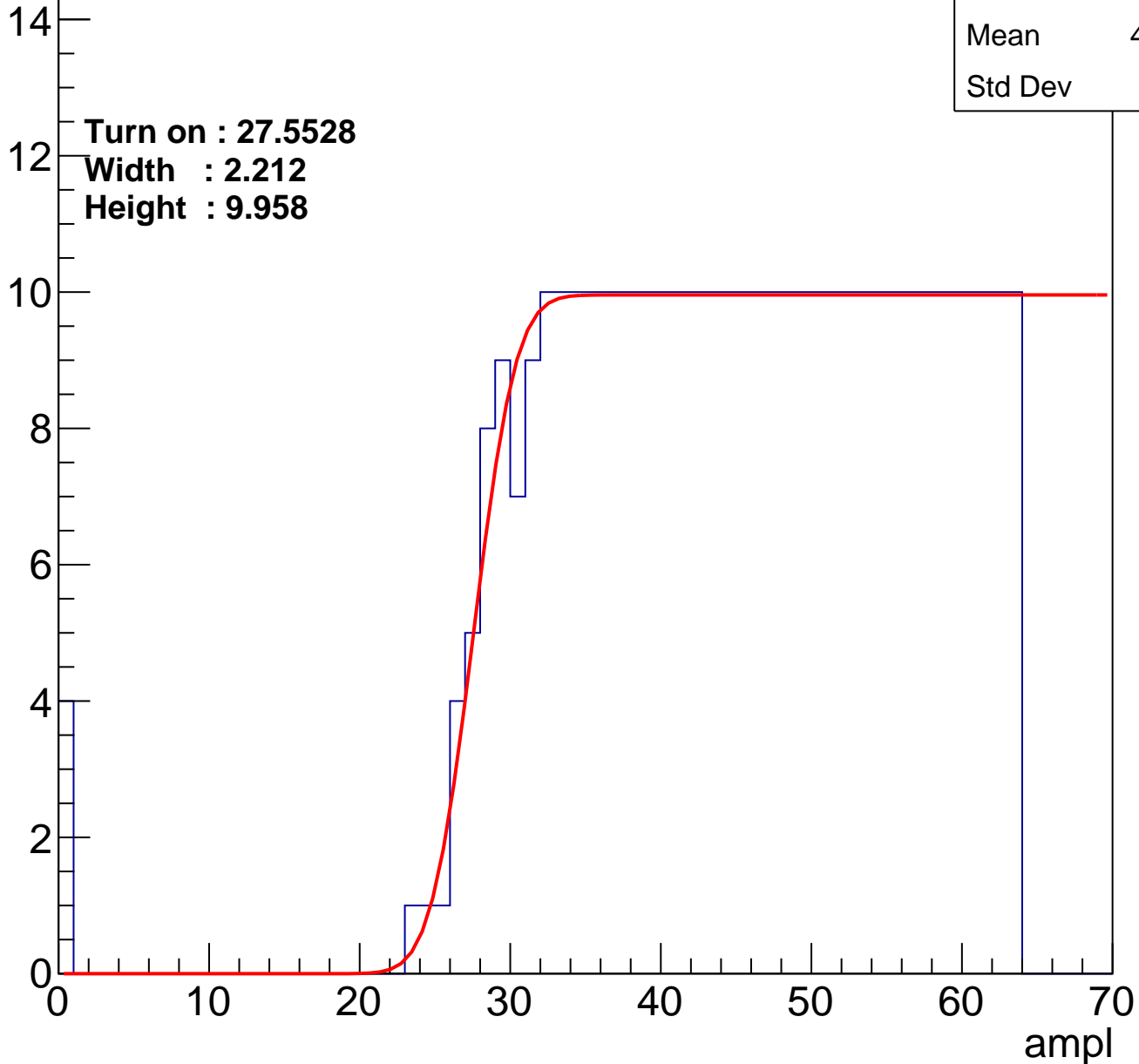
Entries	369
Mean	44.67
Std Dev	11.6

Turn on : 27.5528

Width : 2.212

Height : 9.958

Entry



B0L000S, U25-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.67
Std Dev	11.6

Turn on : 27.5528

Width : 2.212

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

