

B1L000S, U10-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.5926

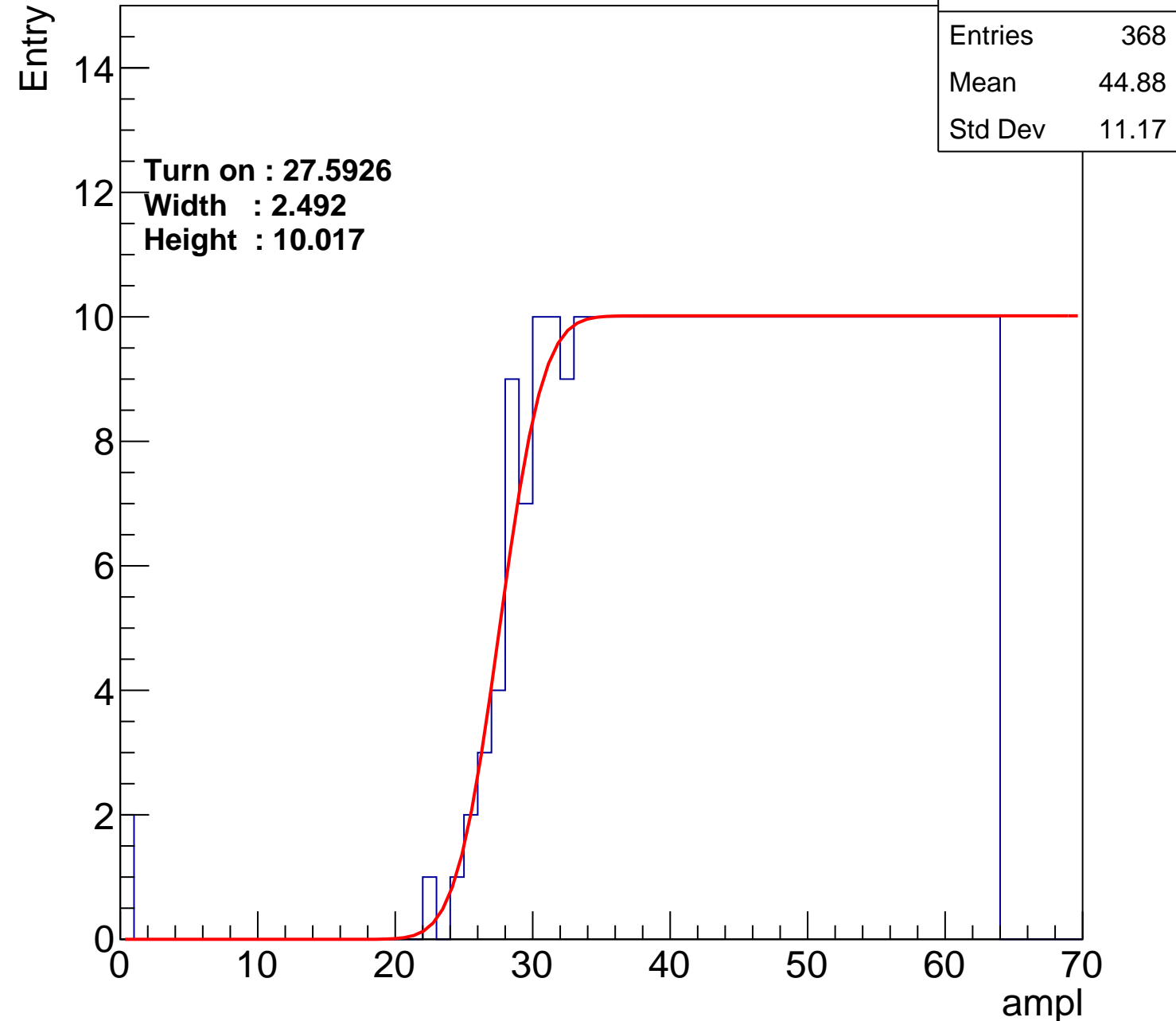
Width : 2.492

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.79
Std Dev	11.2

Turn on : 27.5975

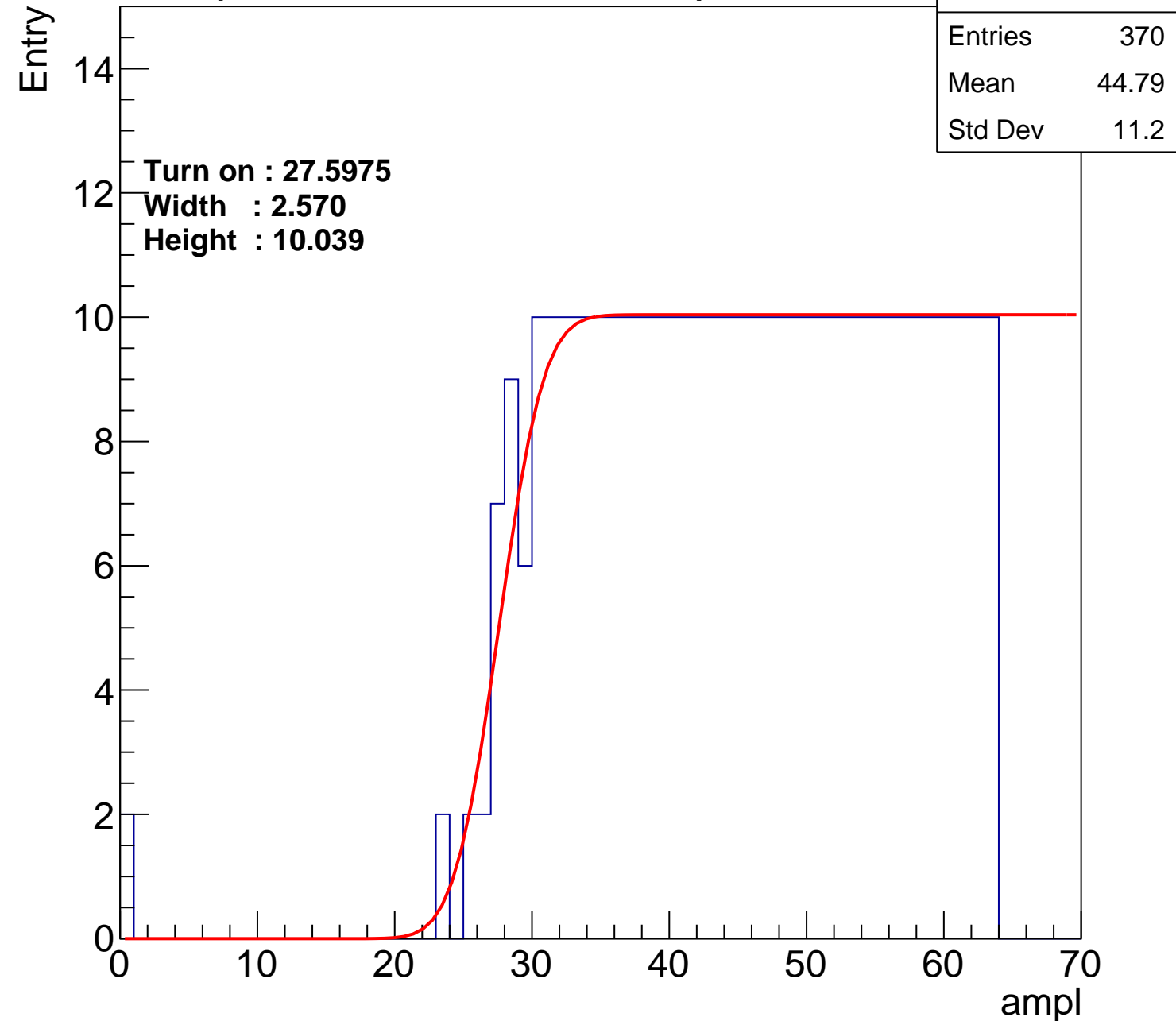
Width : 2.570

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 24.8368

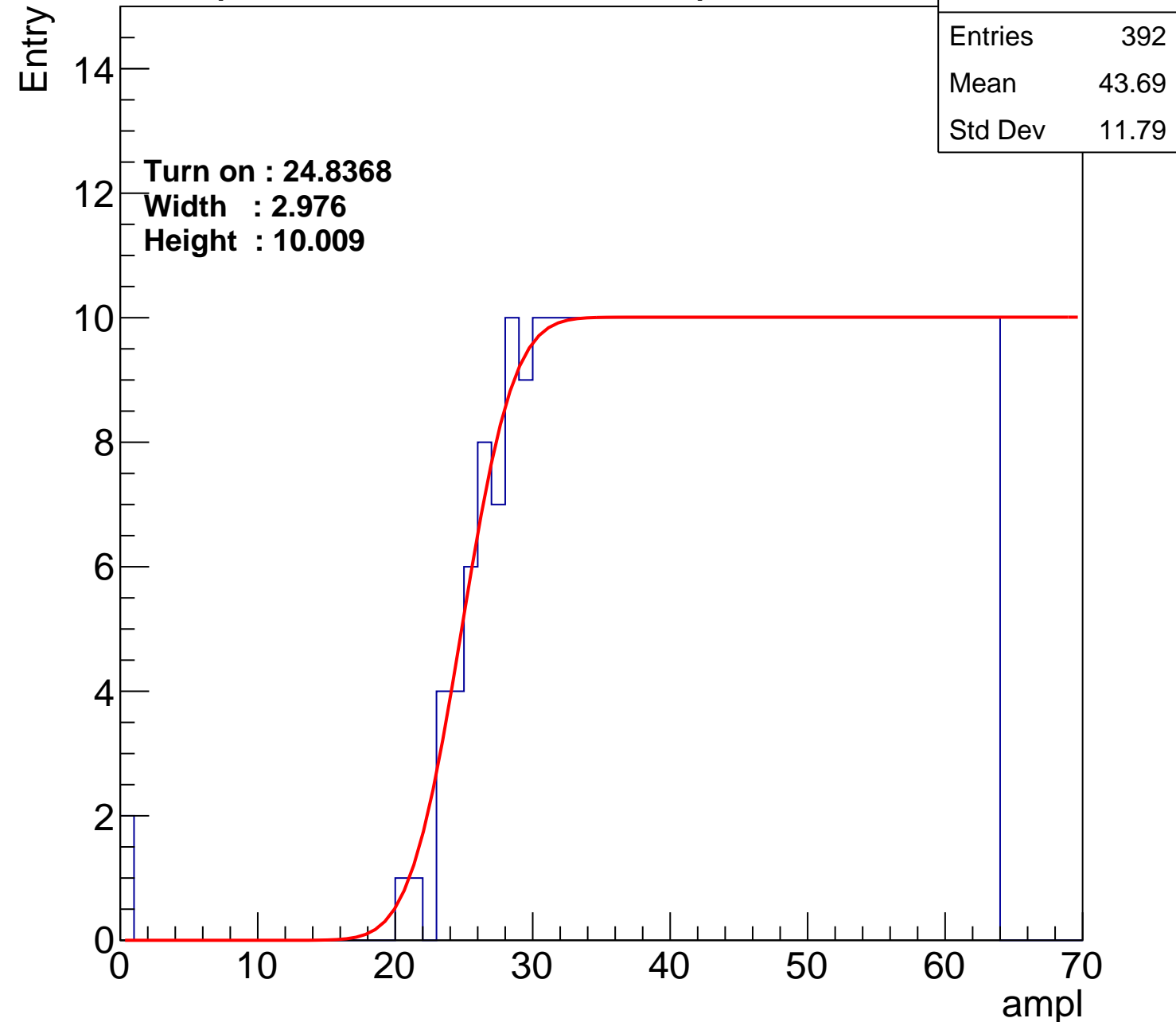
Width : 2.976

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch3

calib_packv5_042523_0143.root, FC#3, port B3

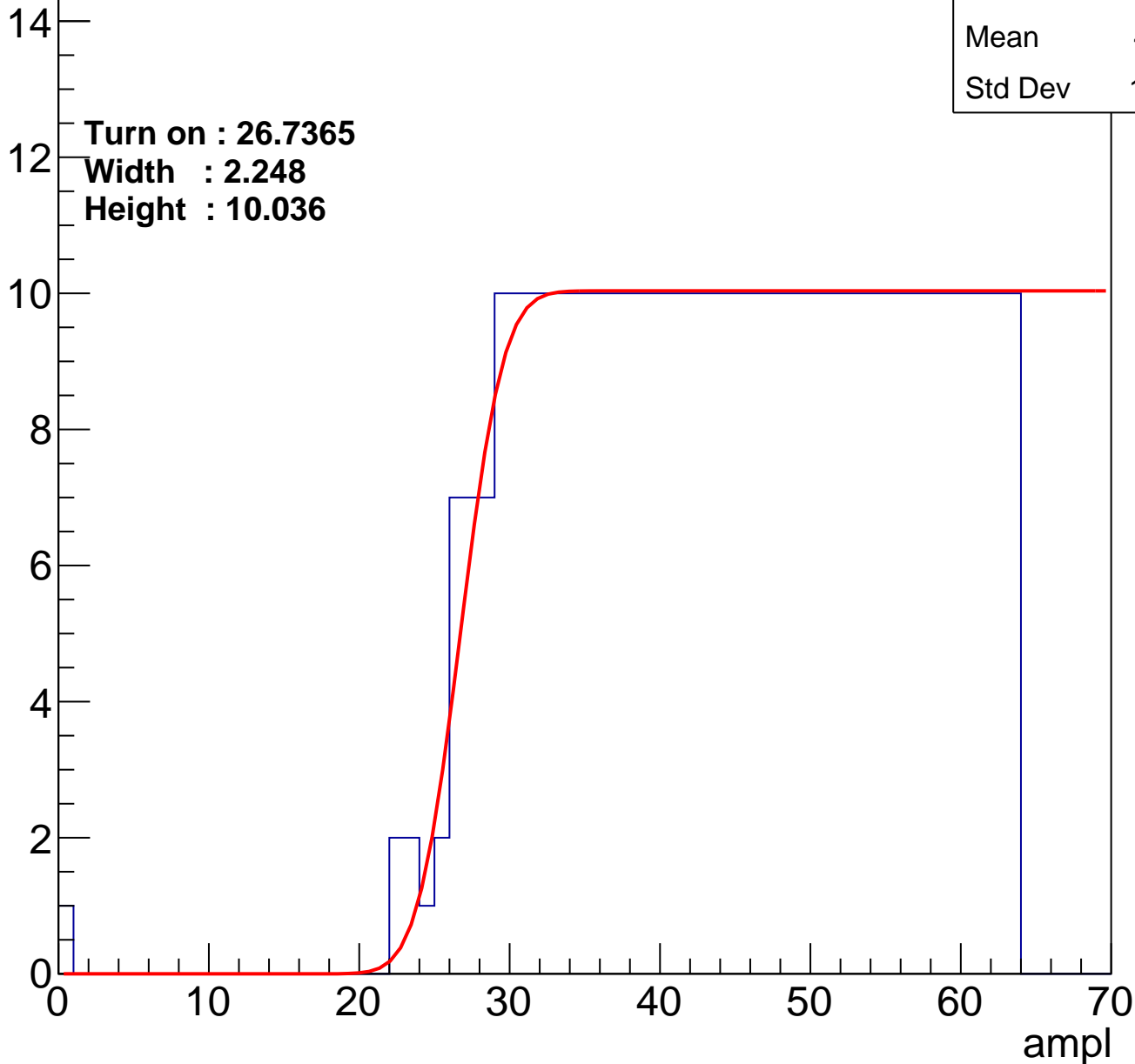
Entry

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.7365

Width : 2.248

Height : 10.036



B1L000S, U10-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.05
Std Dev	11.72

Turn on : 25.9478

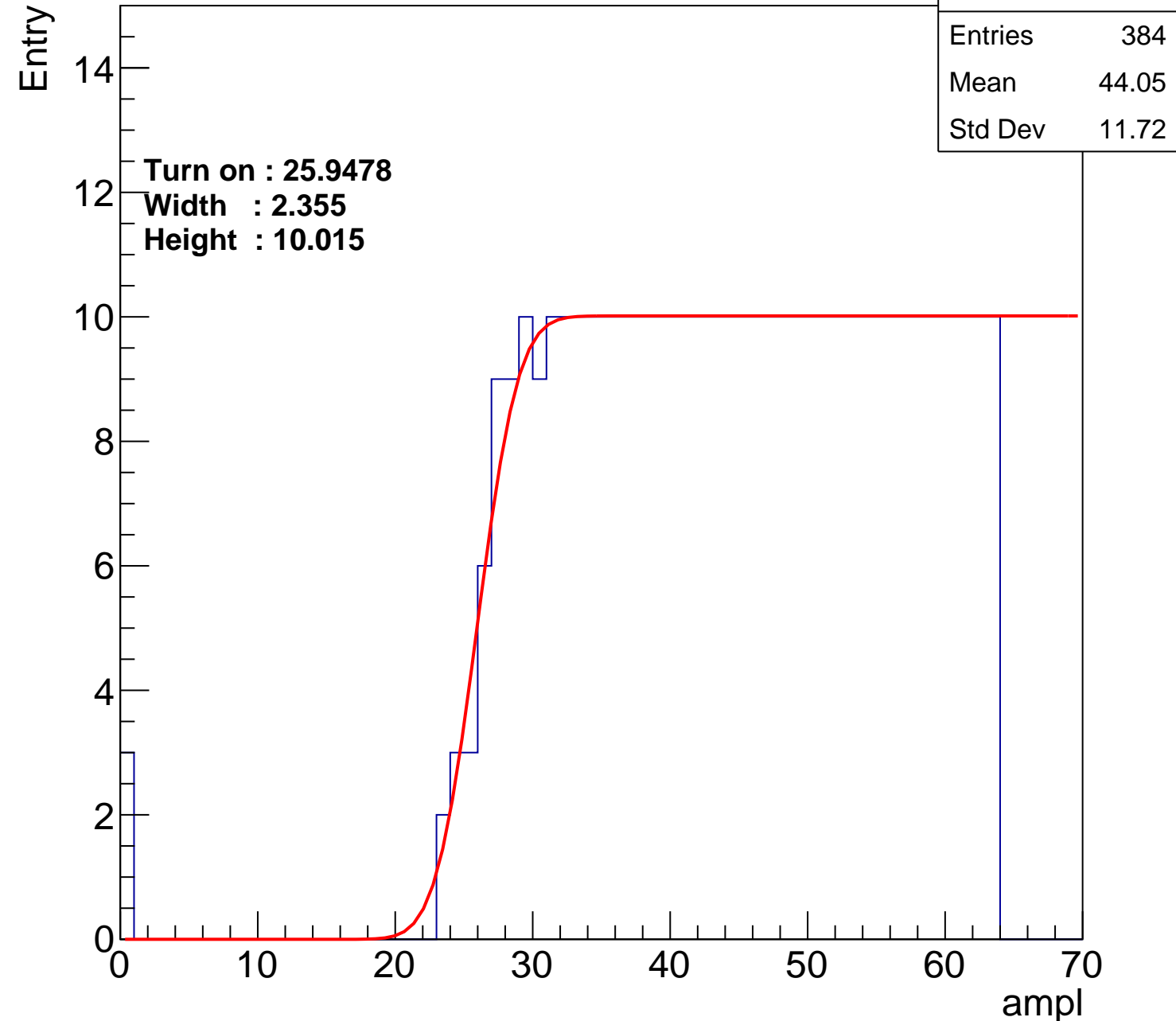
Width : 2.355

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch5

calib_packv5_042523_0143.root, FC#3, port B3

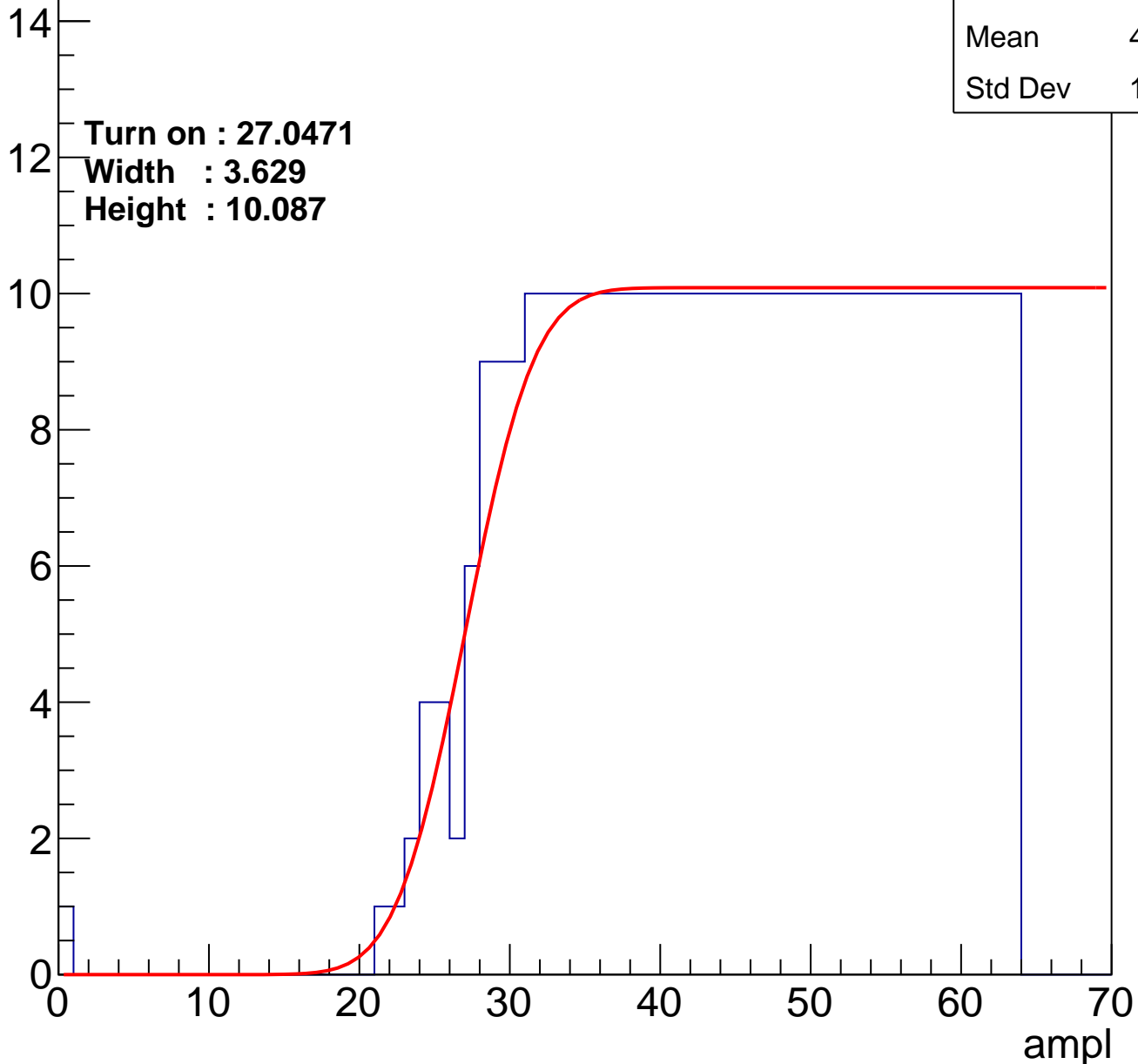
Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 27.0471

Width : 3.629

Height : 10.087

Entry



B1L000S, U10-ch6

calib_packv5_042523_0143.root, FC#3, port B3

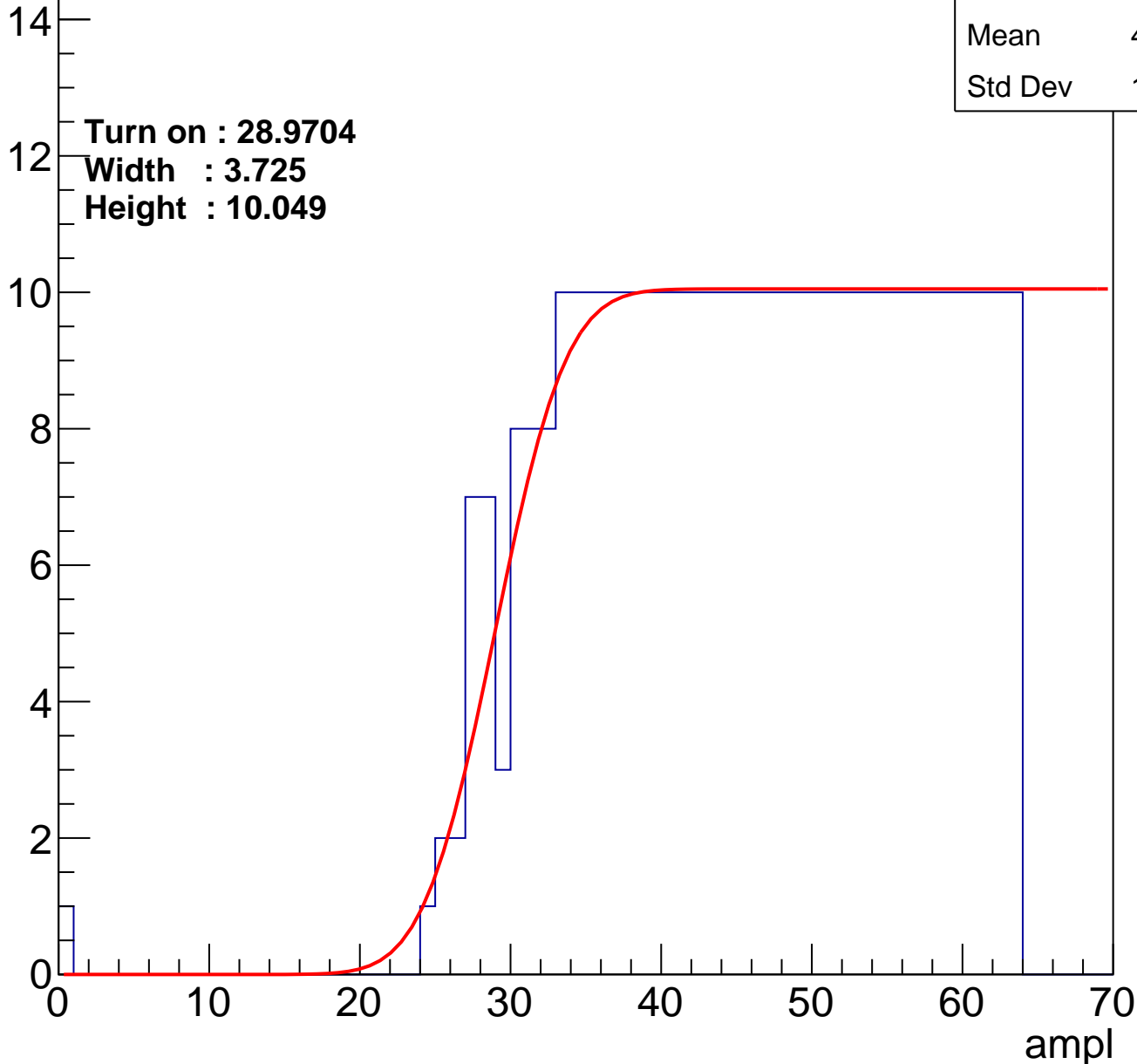
Entries	357
Mean	45.44
Std Dev	10.75

Turn on : 28.9704

Width : 3.725

Height : 10.049

Entry



B1L000S, U10-ch7

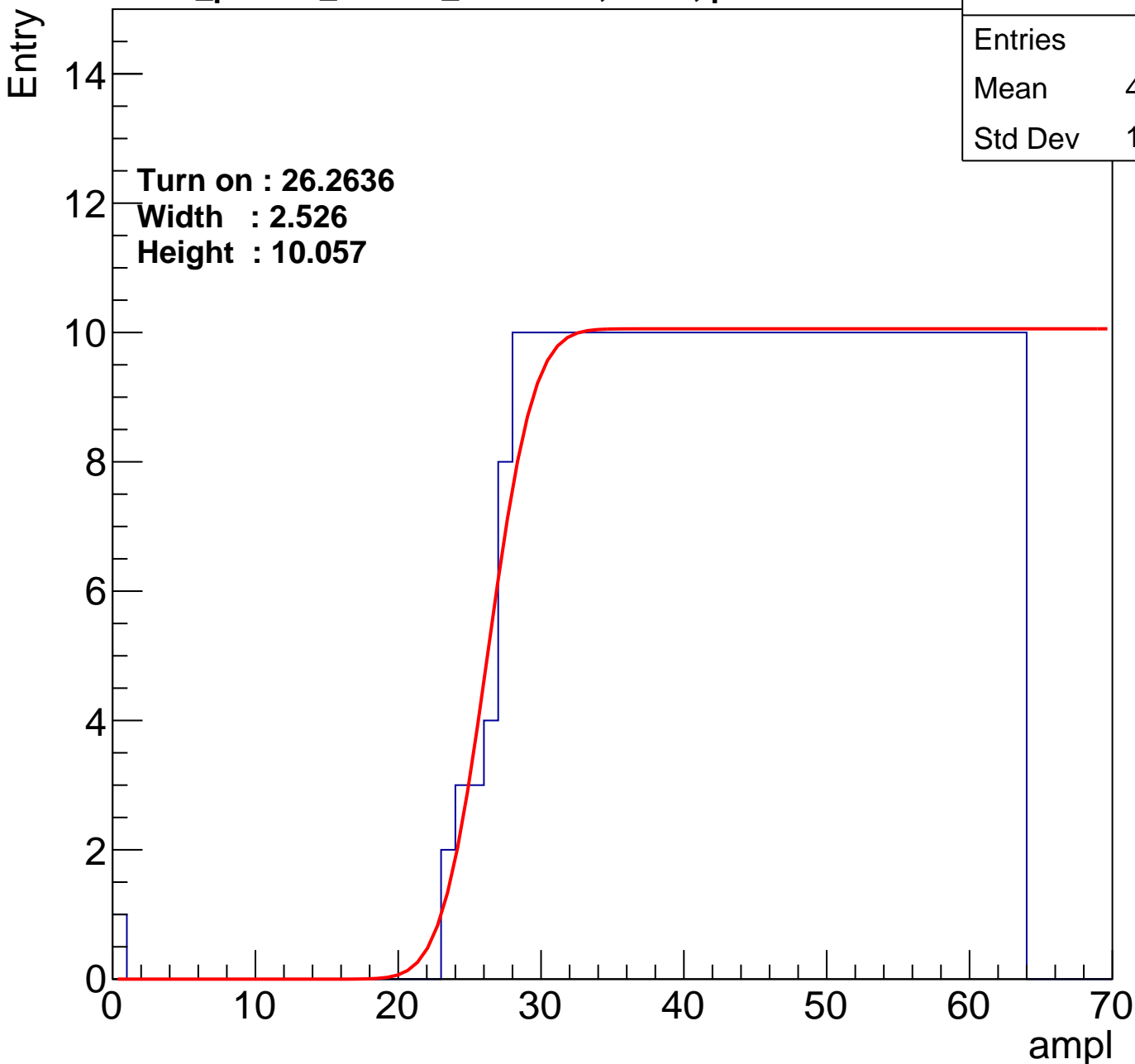
calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.34
Std Dev	11.26

Turn on : 26.2636

Width : 2.526

Height : 10.057



B1L000S, U10-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.2781

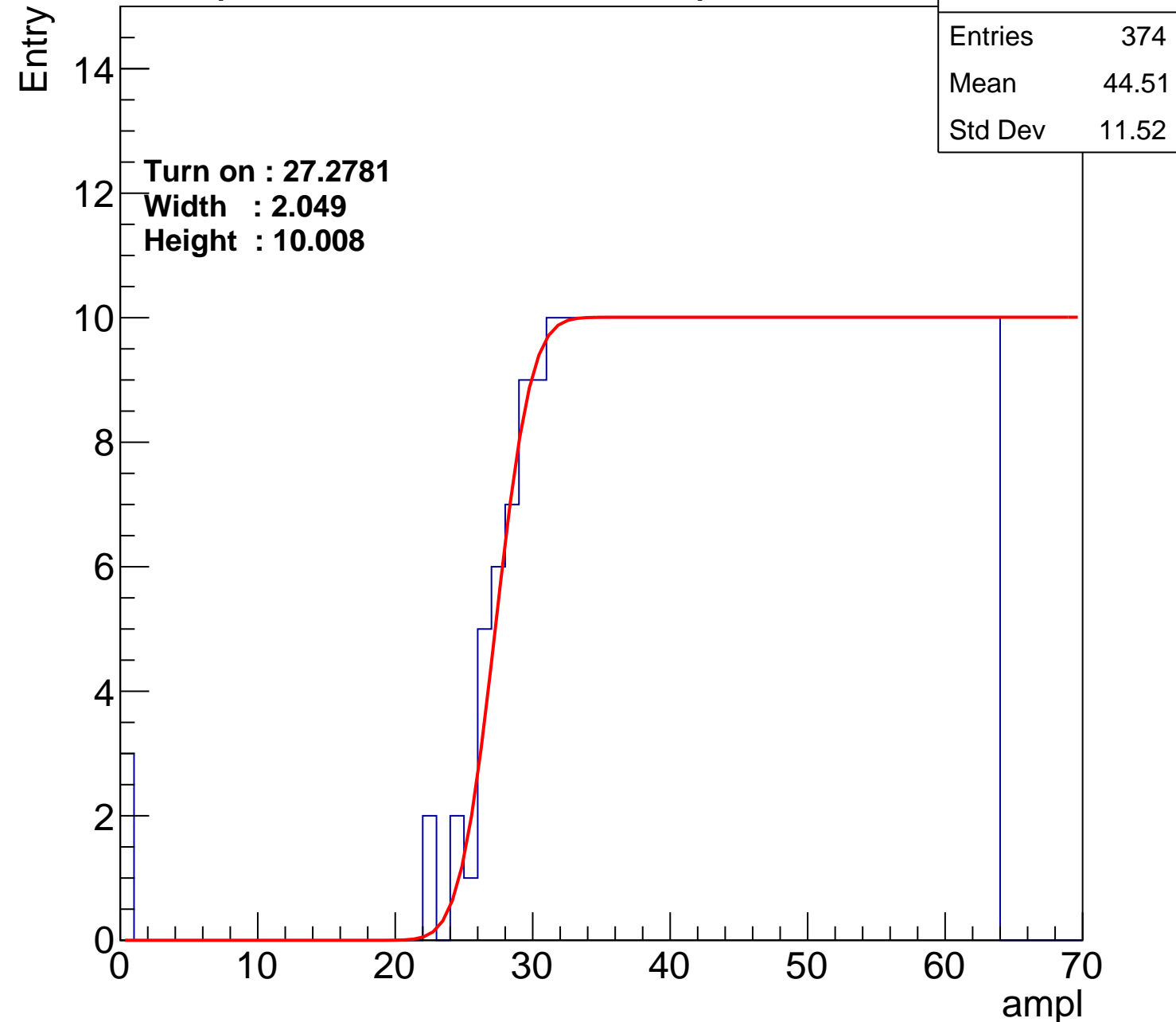
Width : 2.049

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.5148

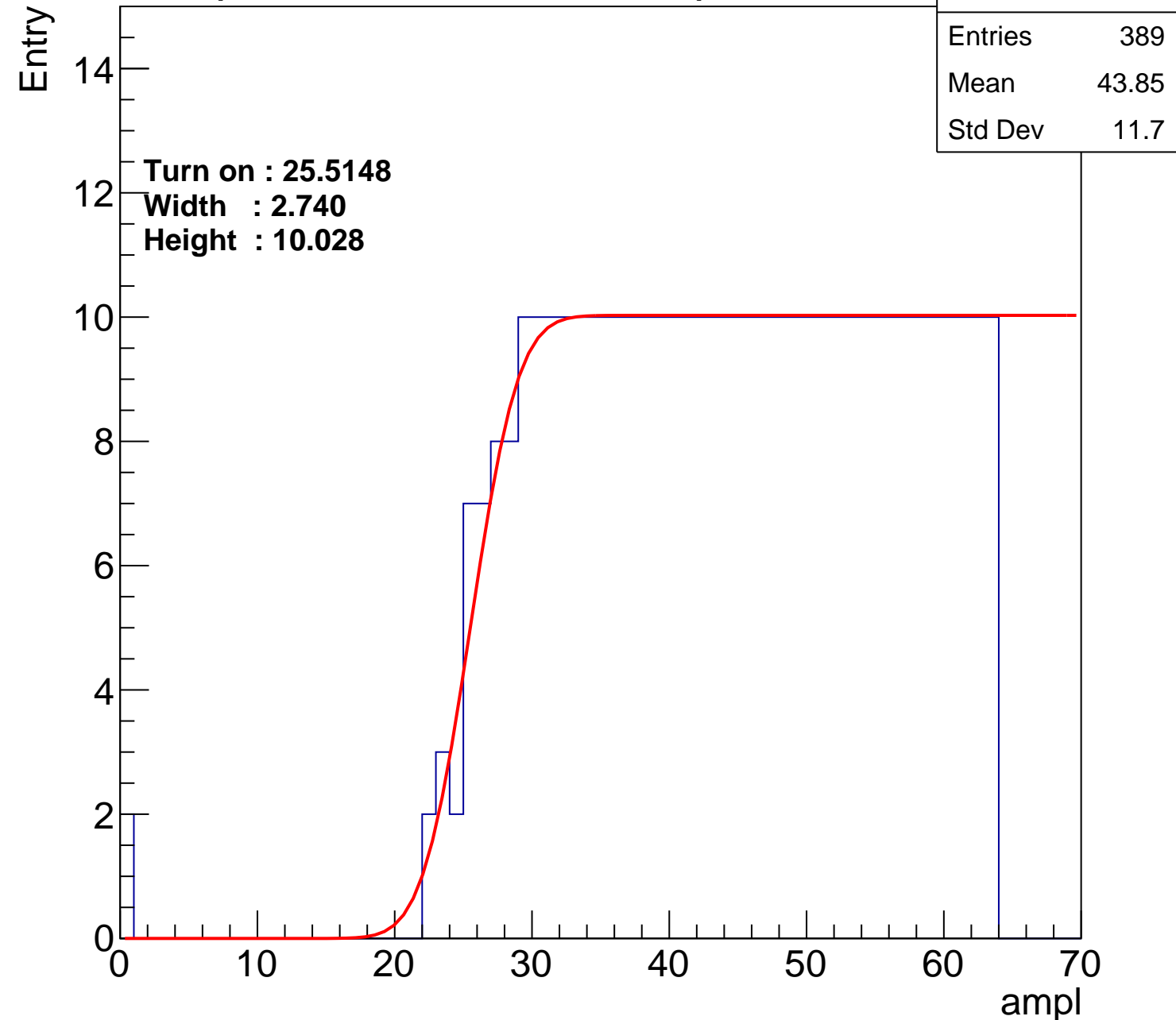
Width : 2.740

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.17
Std Dev	11.7

Turn on : 26.7679

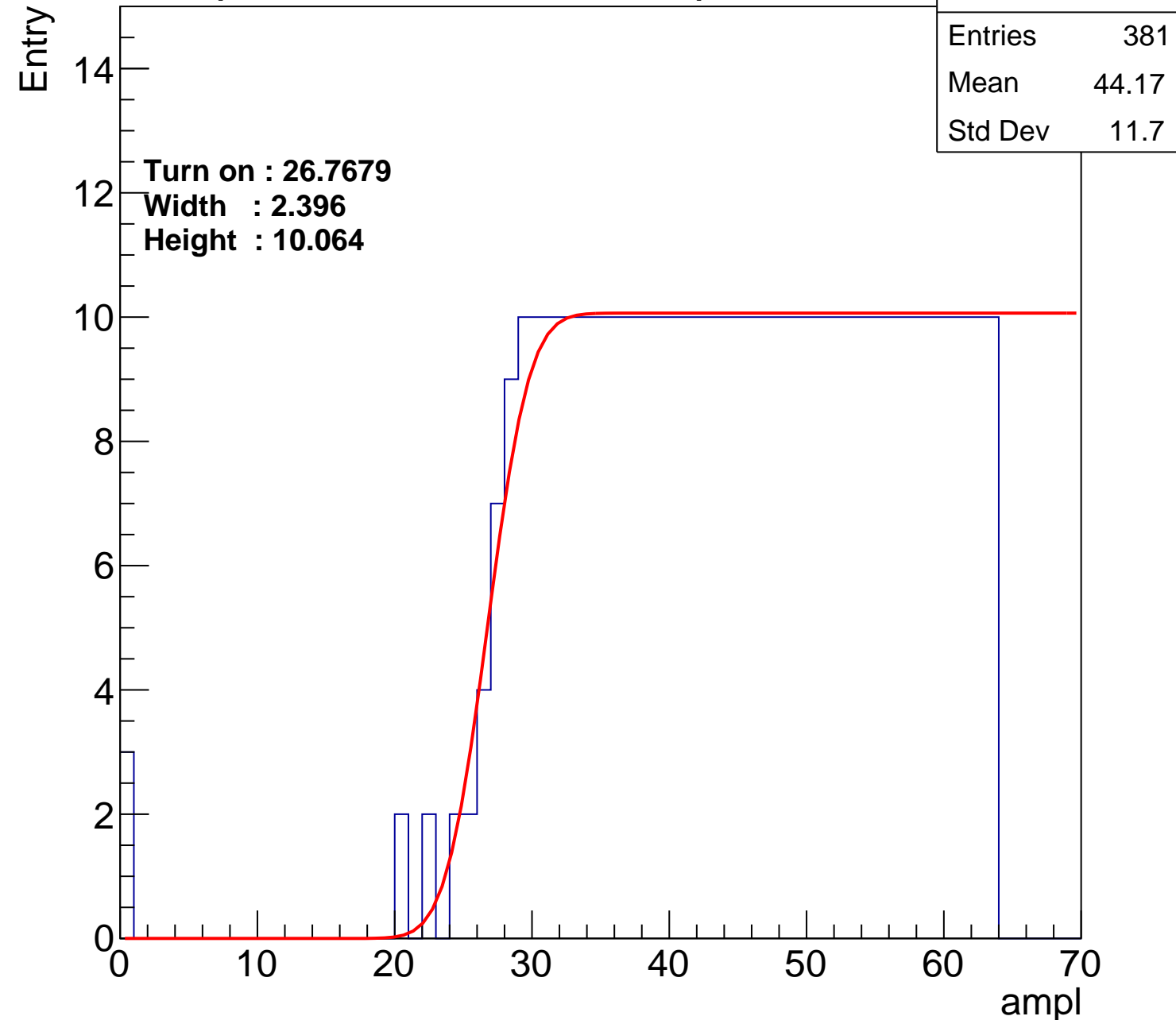
Width : 2.396

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.55
Std Dev	11.39

Turn on : 27.5484

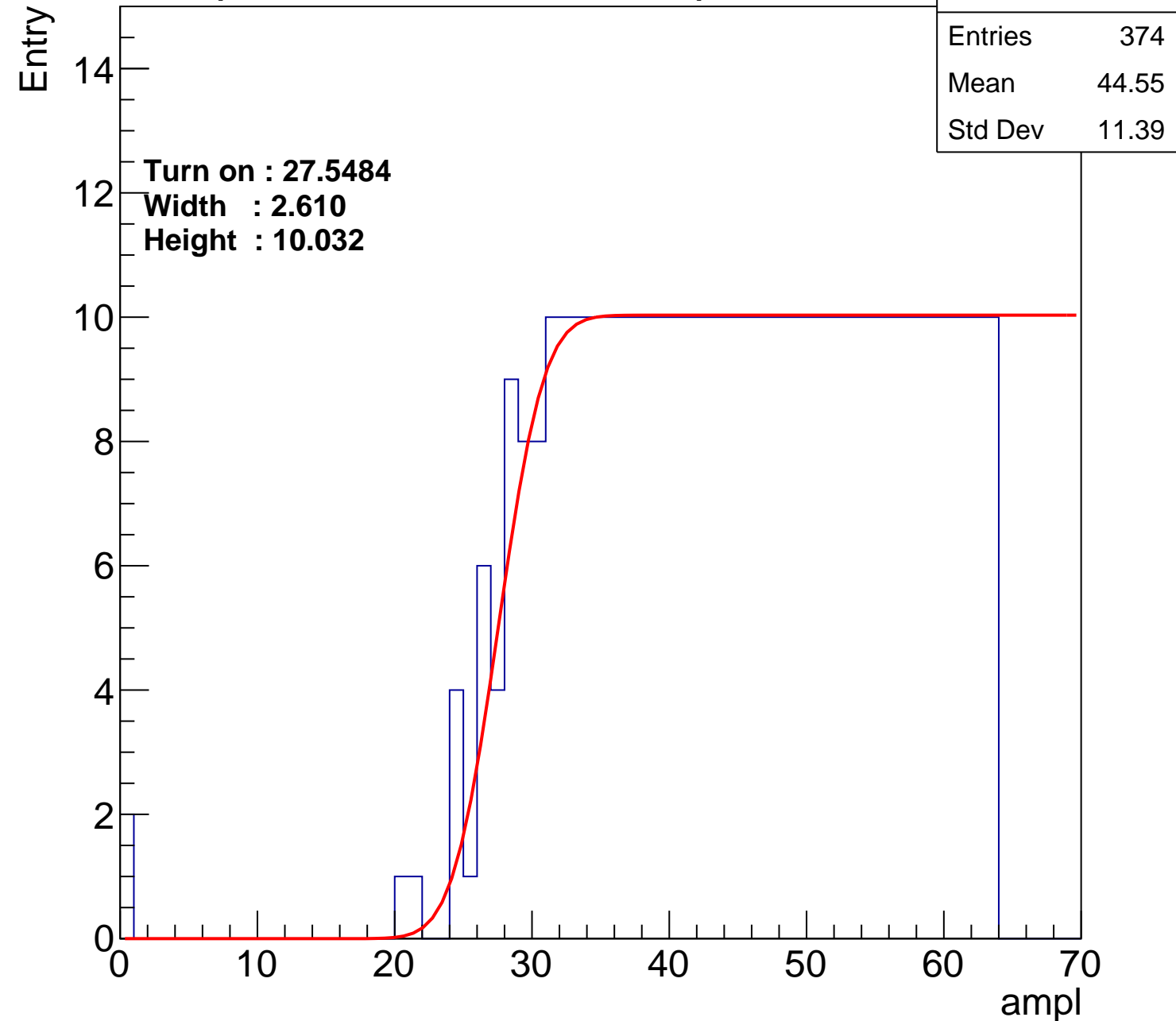
Width : 2.610

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch12

calib_packv5_042523_0143.root, FC#3, port B3

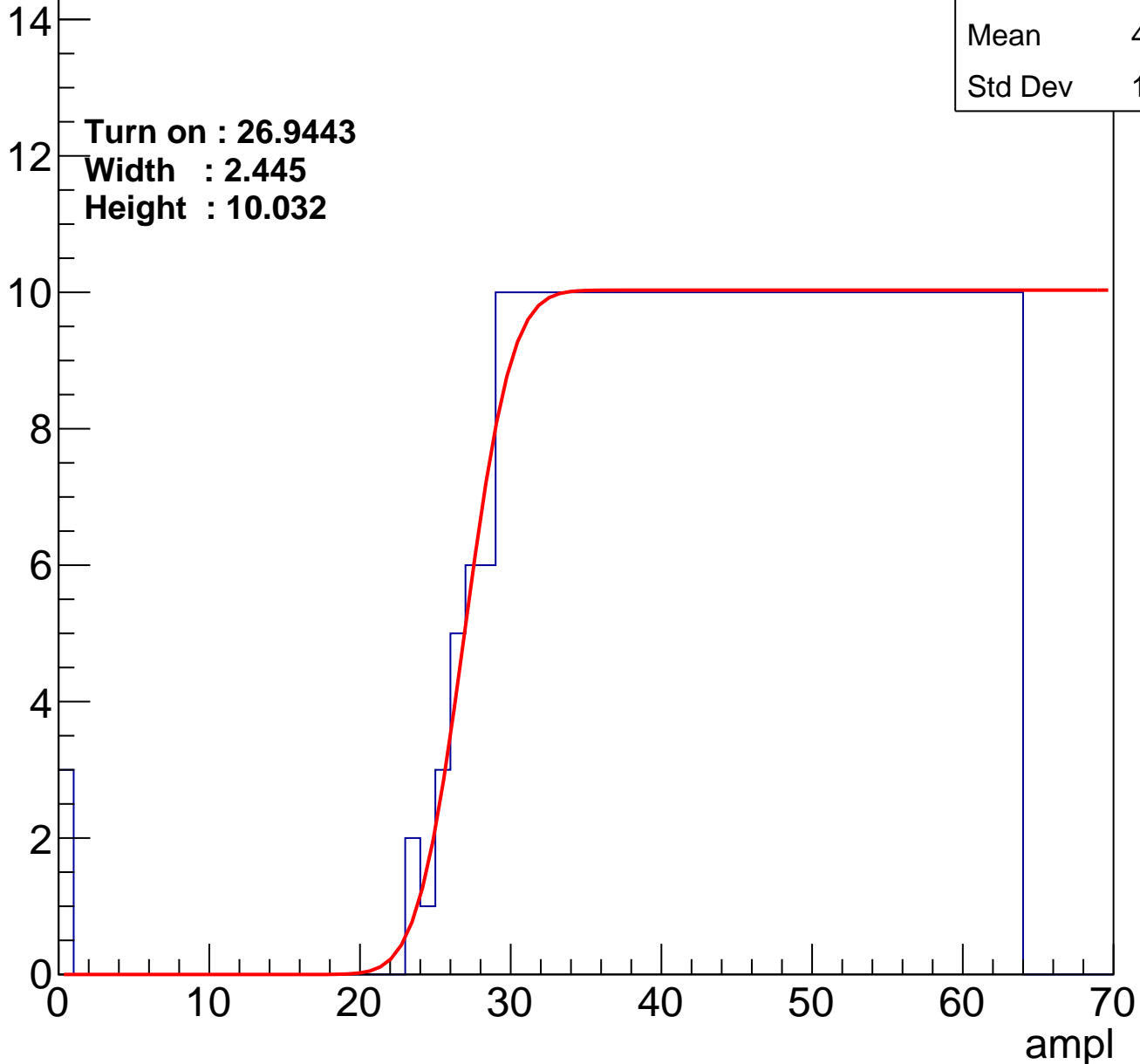
Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 26.9443

Width : 2.445

Height : 10.032

Entry



B1L000S, U10-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.28
Std Dev	12.14

Turn on : 24.5114

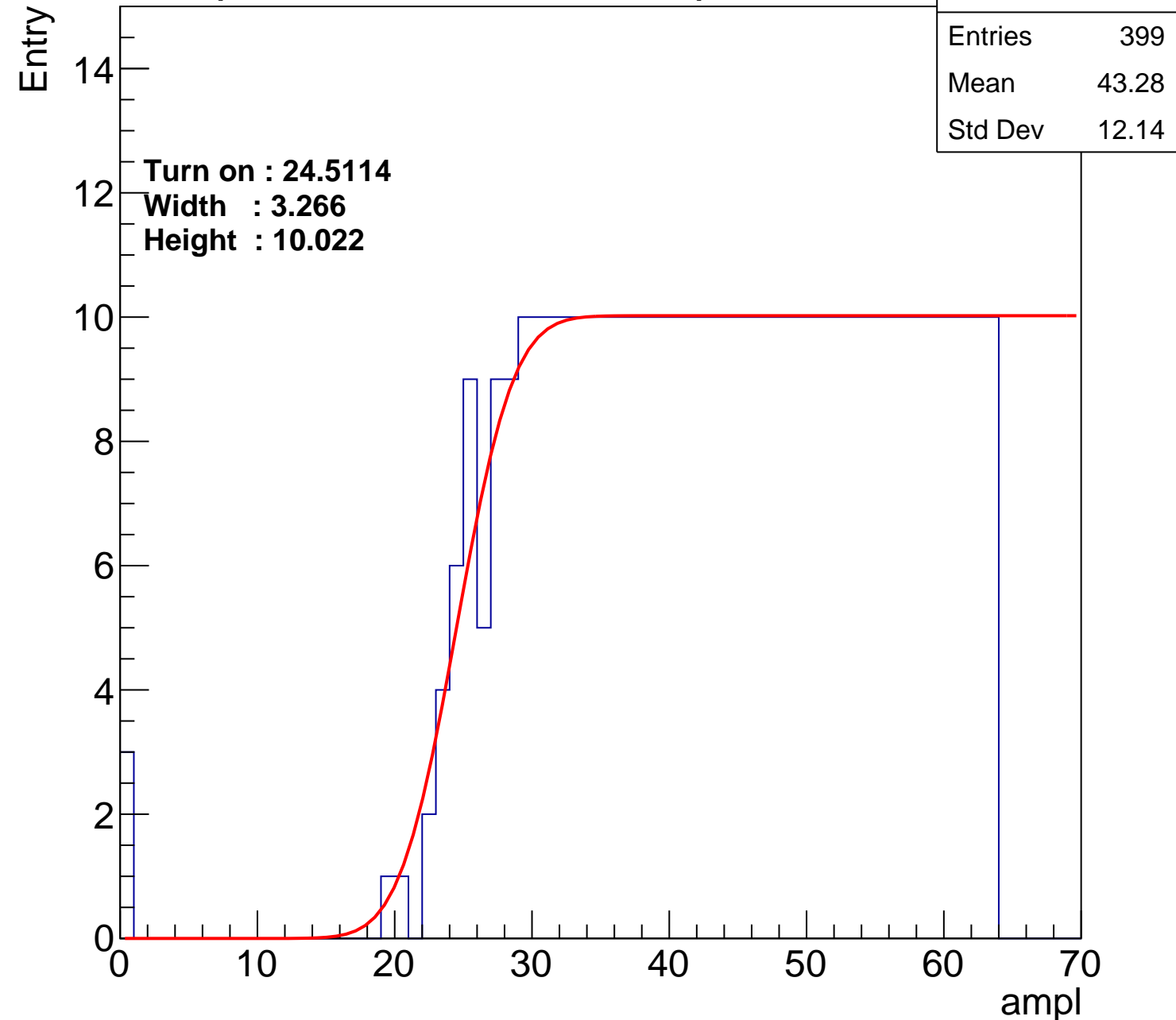
Width : 3.266

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 28.2724

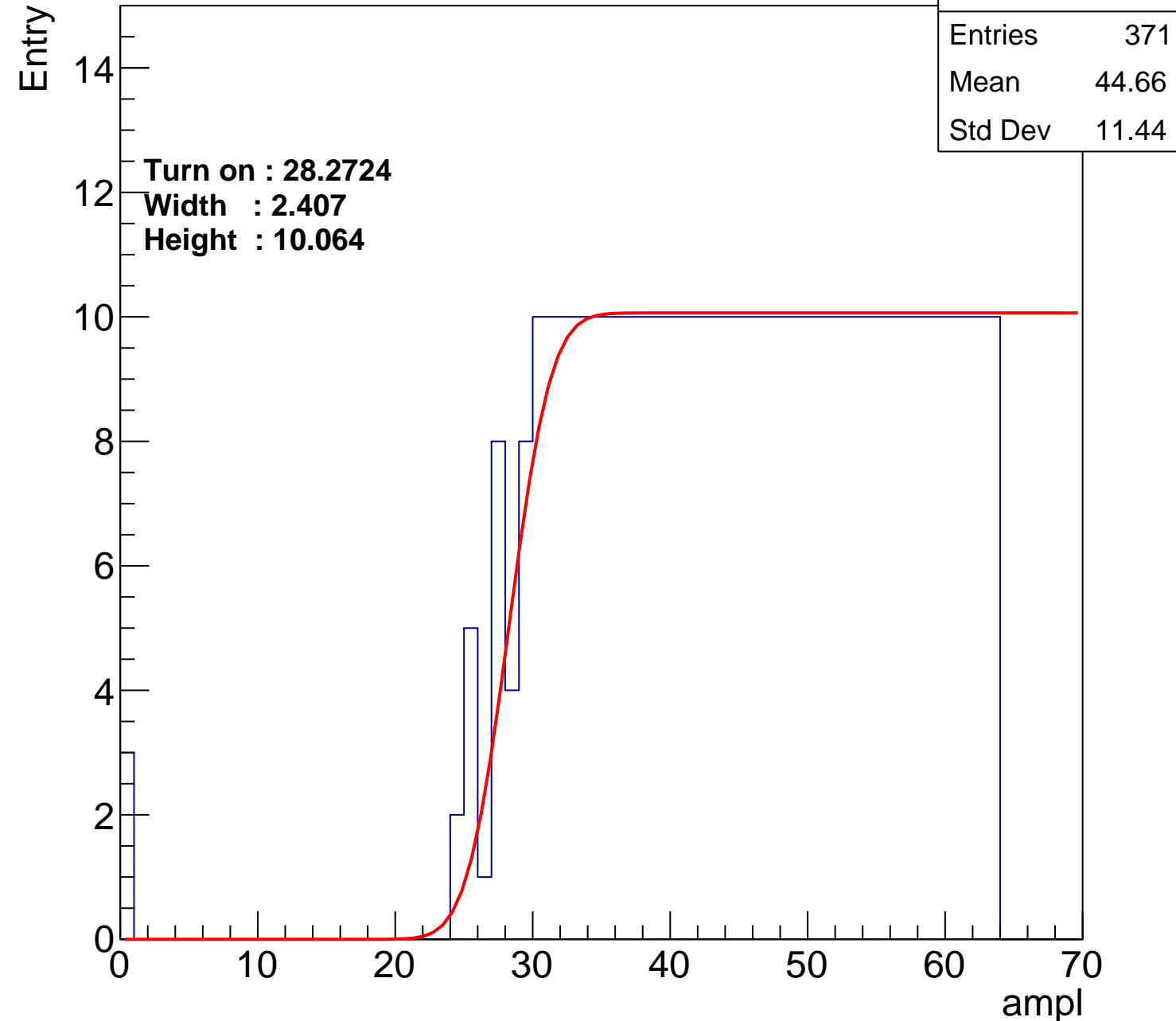
Width : 2.407

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.66
Std Dev	11.85

Turn on : 25.1440

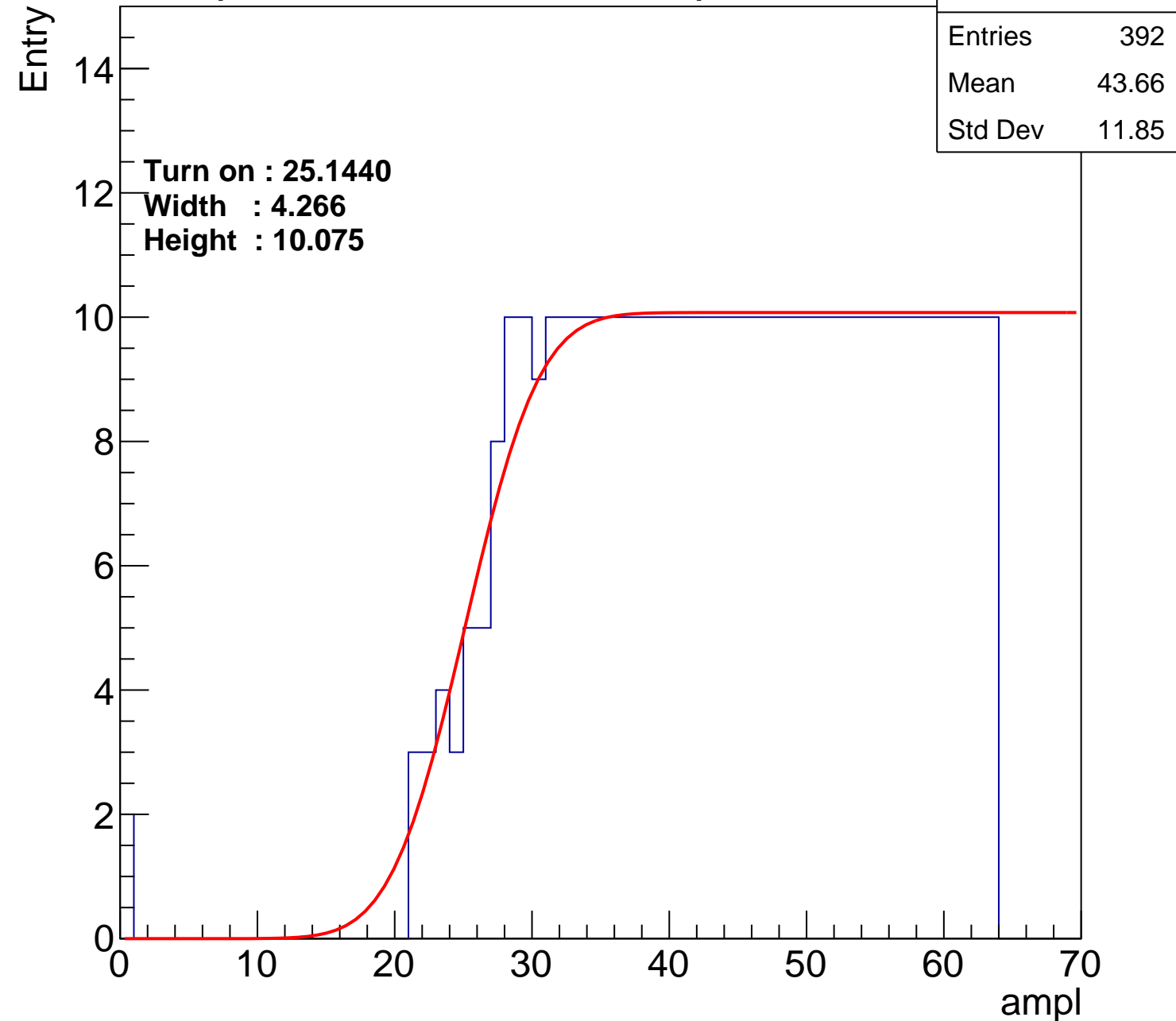
Width : 4.266

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch16

calib_packv5_042523_0143.root, FC#3, port B3

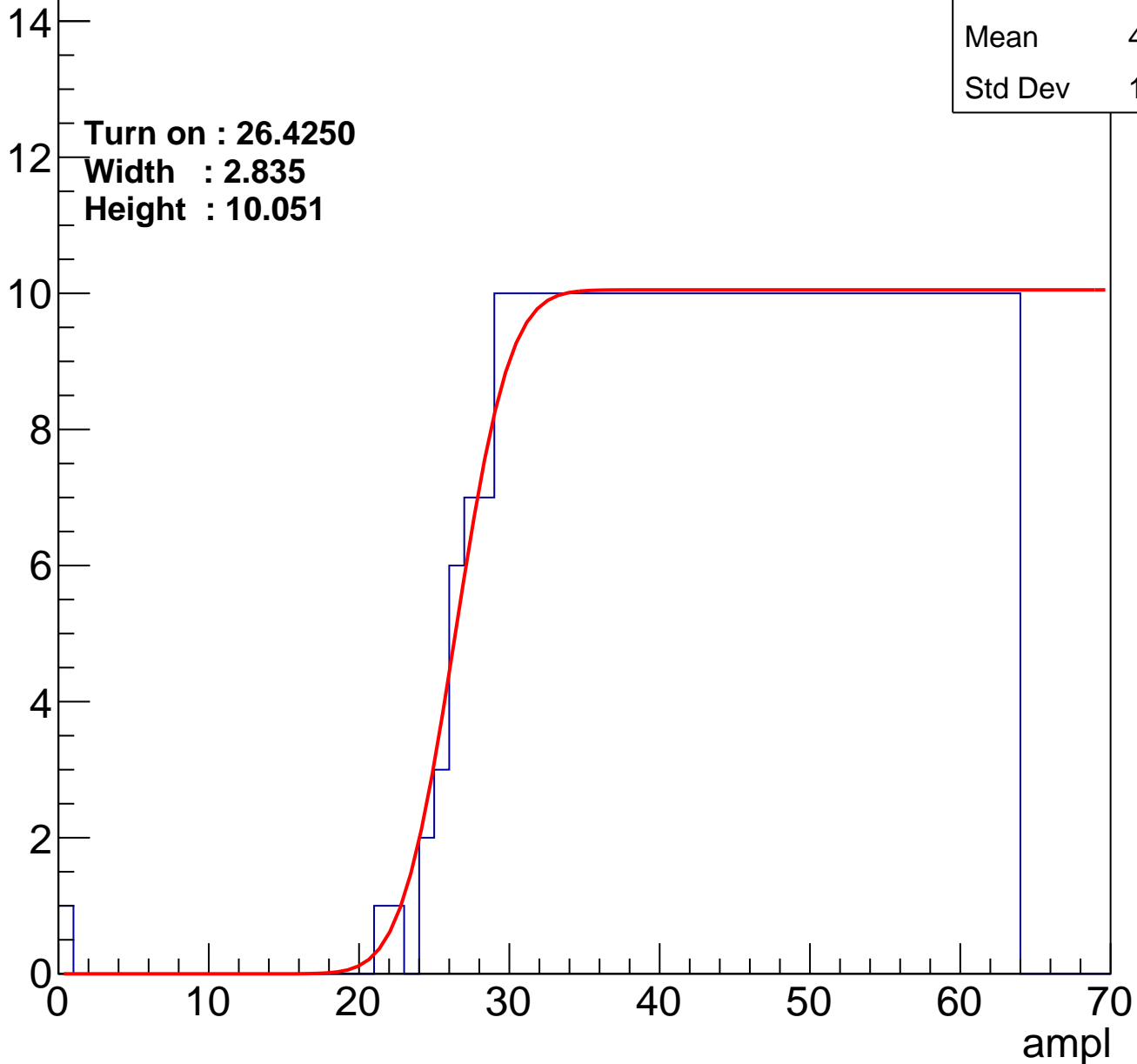
Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 26.4250

Width : 2.835

Height : 10.051

Entry



B1L000S, U10-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.6340

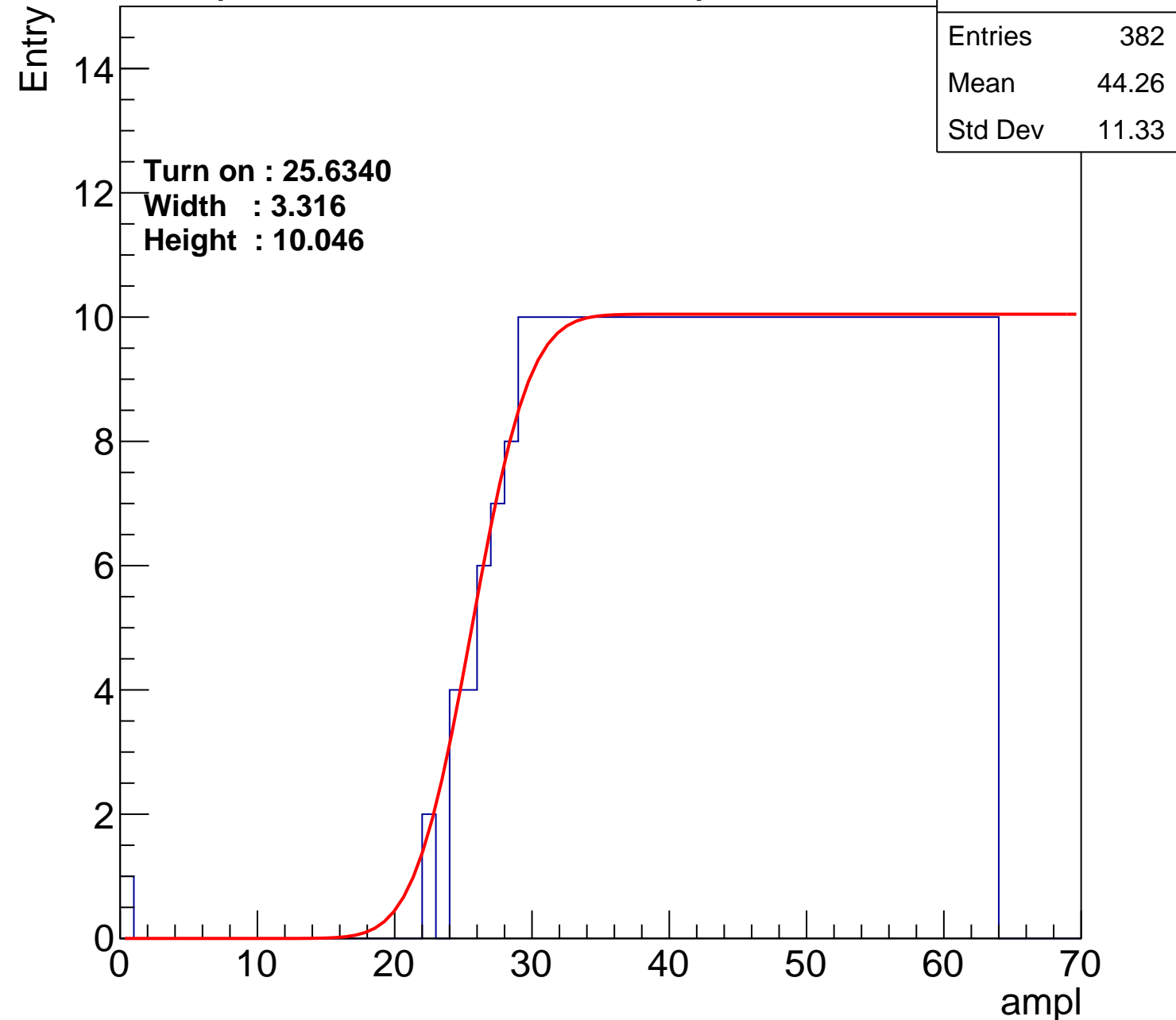
Width : 3.316

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	353
Mean	45.52
Std Dev	11.04

Turn on : 29.2027

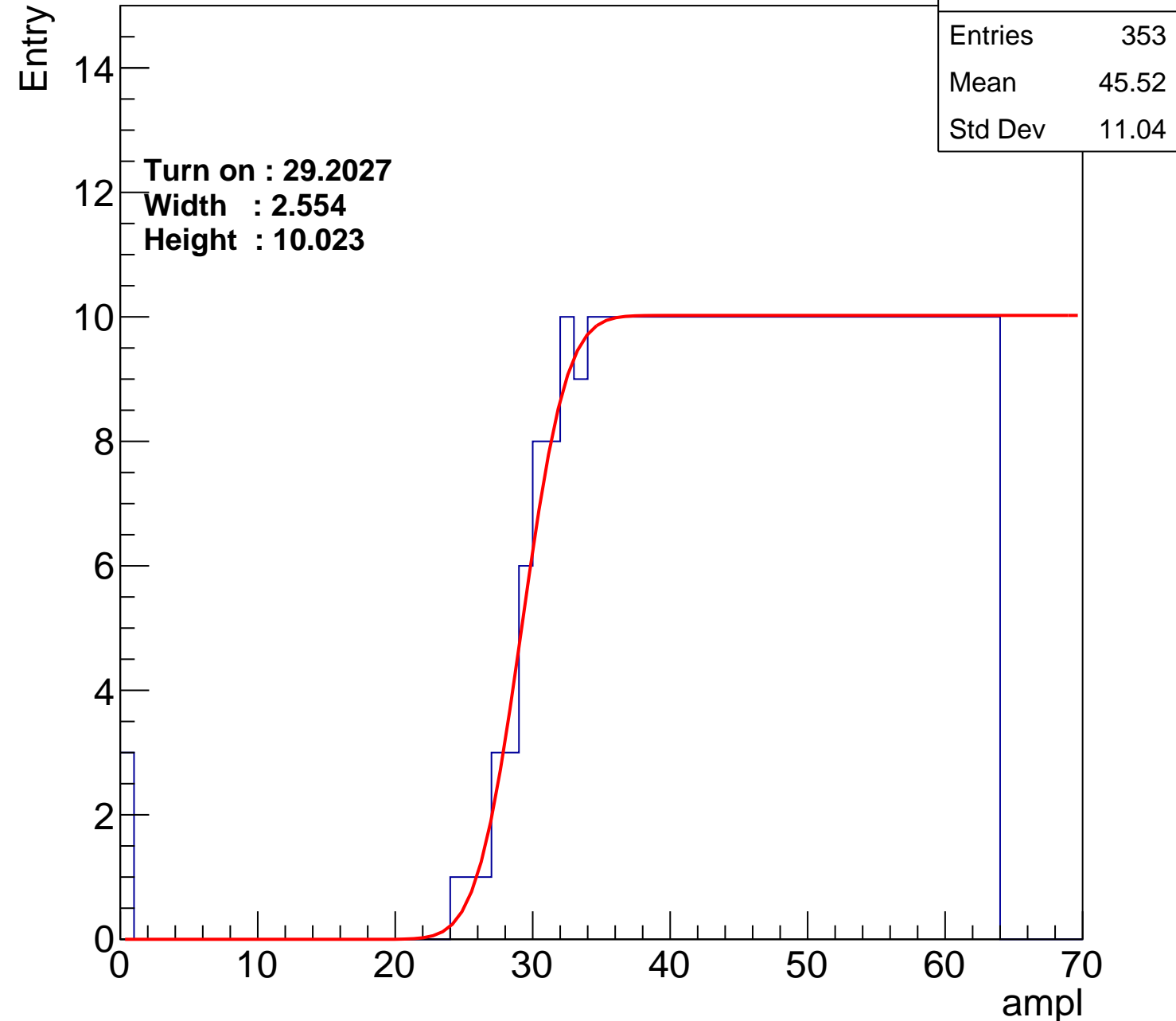
Width : 2.554

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch19

calib_packv5_042523_0143.root, FC#3, port B3

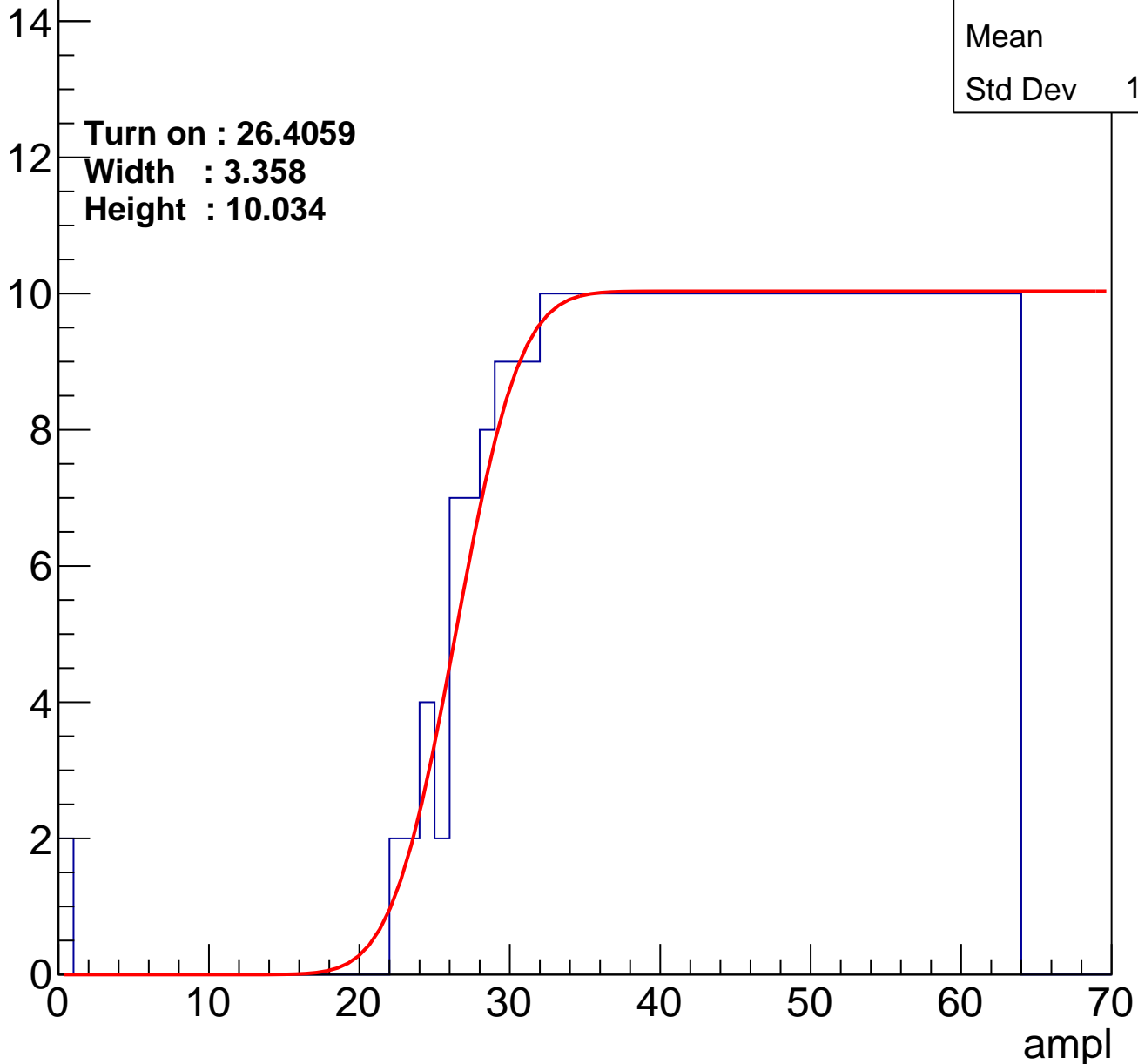
Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.4059

Width : 3.358

Height : 10.034

Entry



B1L000S, U10-ch20

calib_packv5_042523_0143.root, FC#3, port B3

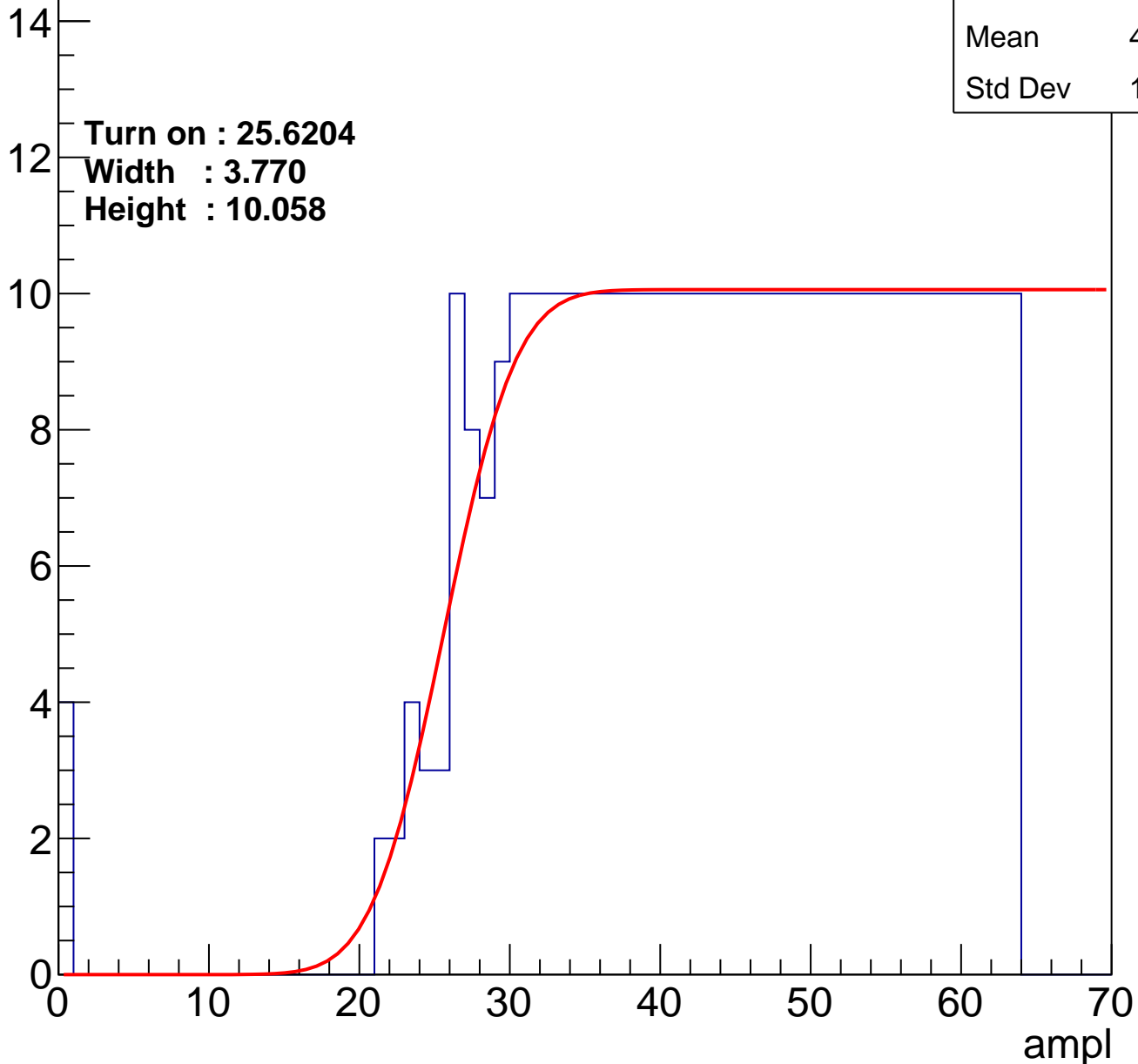
Entries	392
Mean	43.54
Std Dev	12.16

Turn on : 25.6204

Width : 3.770

Height : 10.058

Entry



B1L000S, U10-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.44
Std Dev	11.93

Turn on : 24.6278

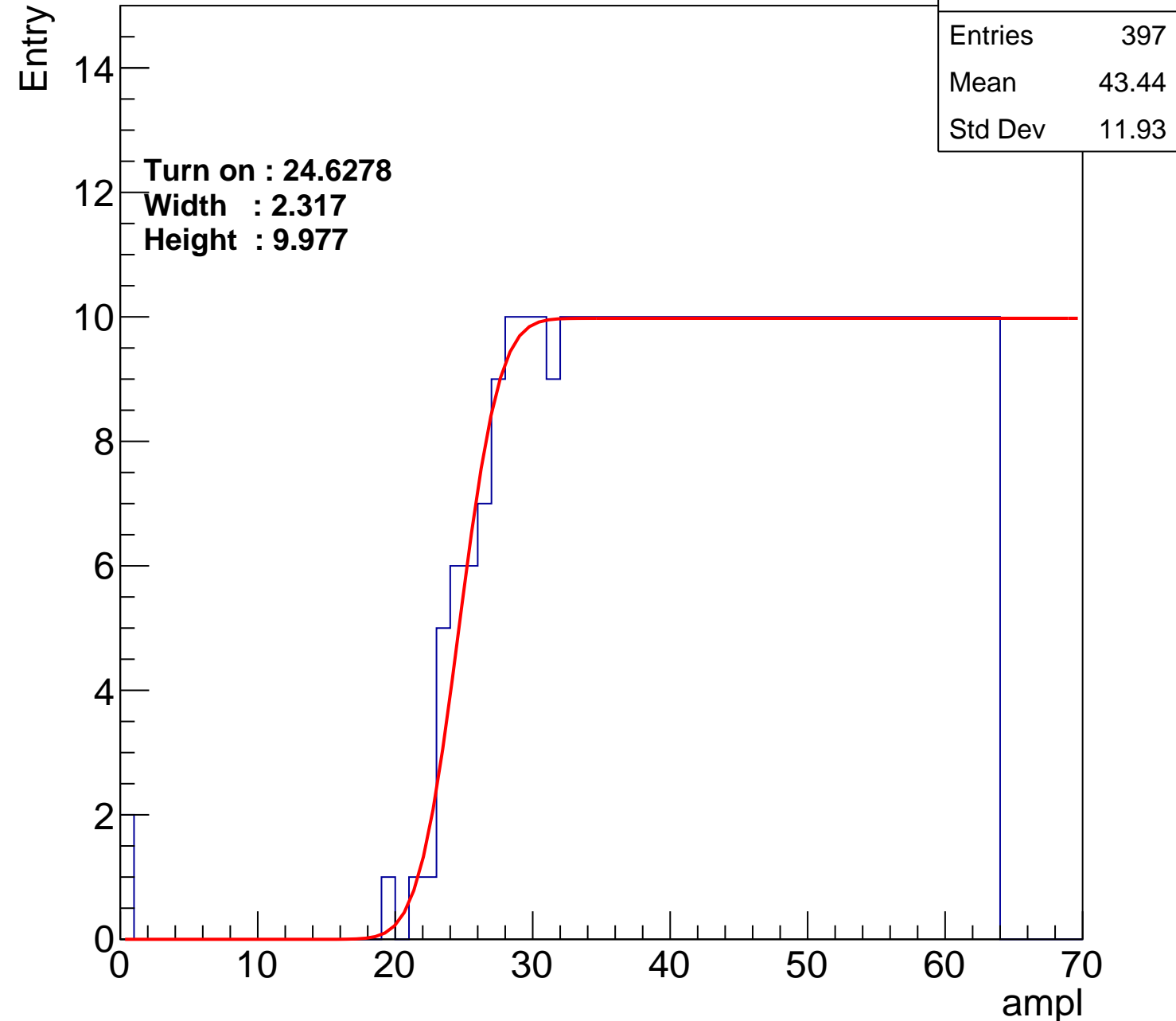
Width : 2.317

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.71
Std Dev	11.7

Turn on : 25.3933

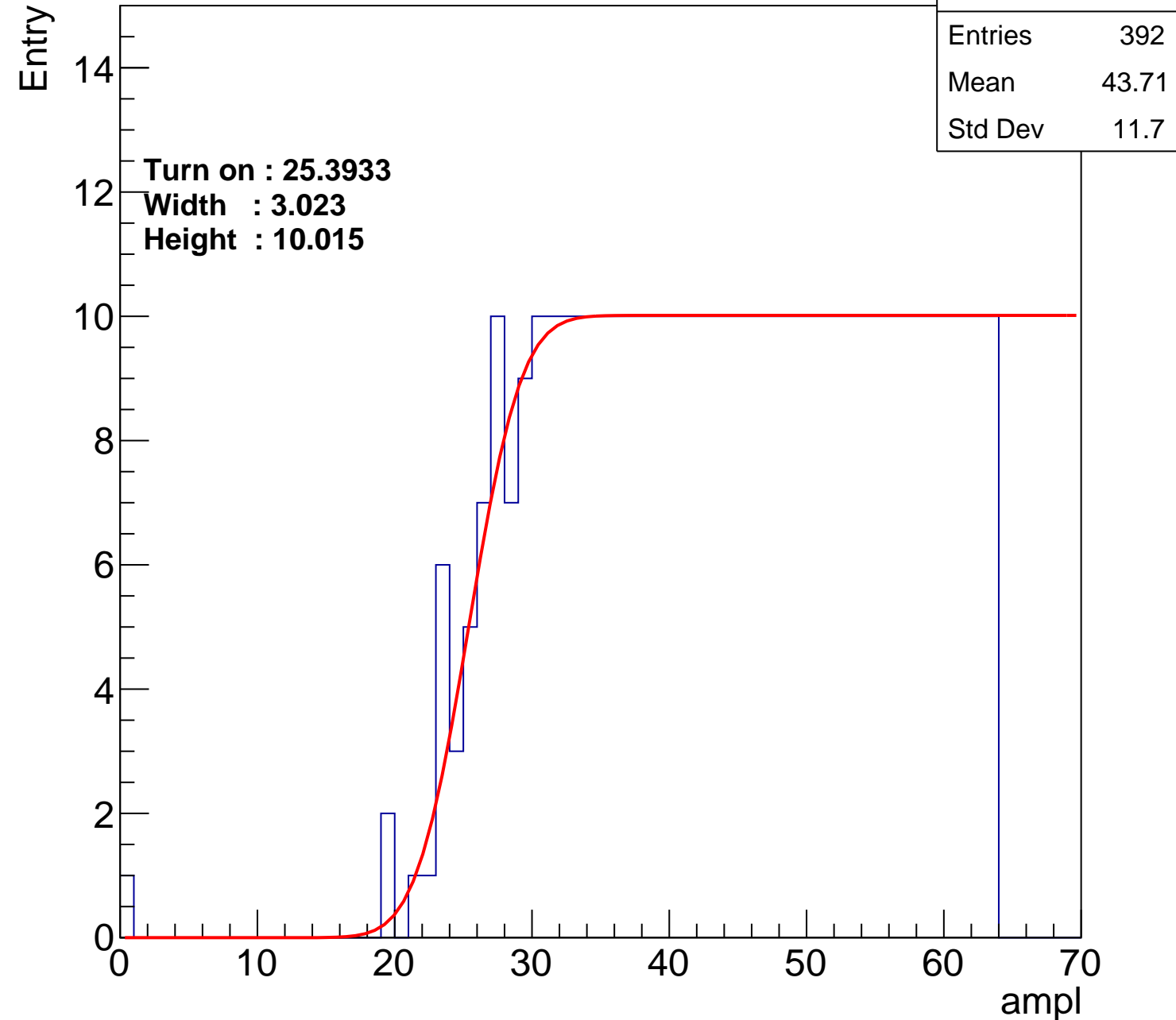
Width : 3.023

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.51
Std Dev	11.57

Turn on : 27.7889

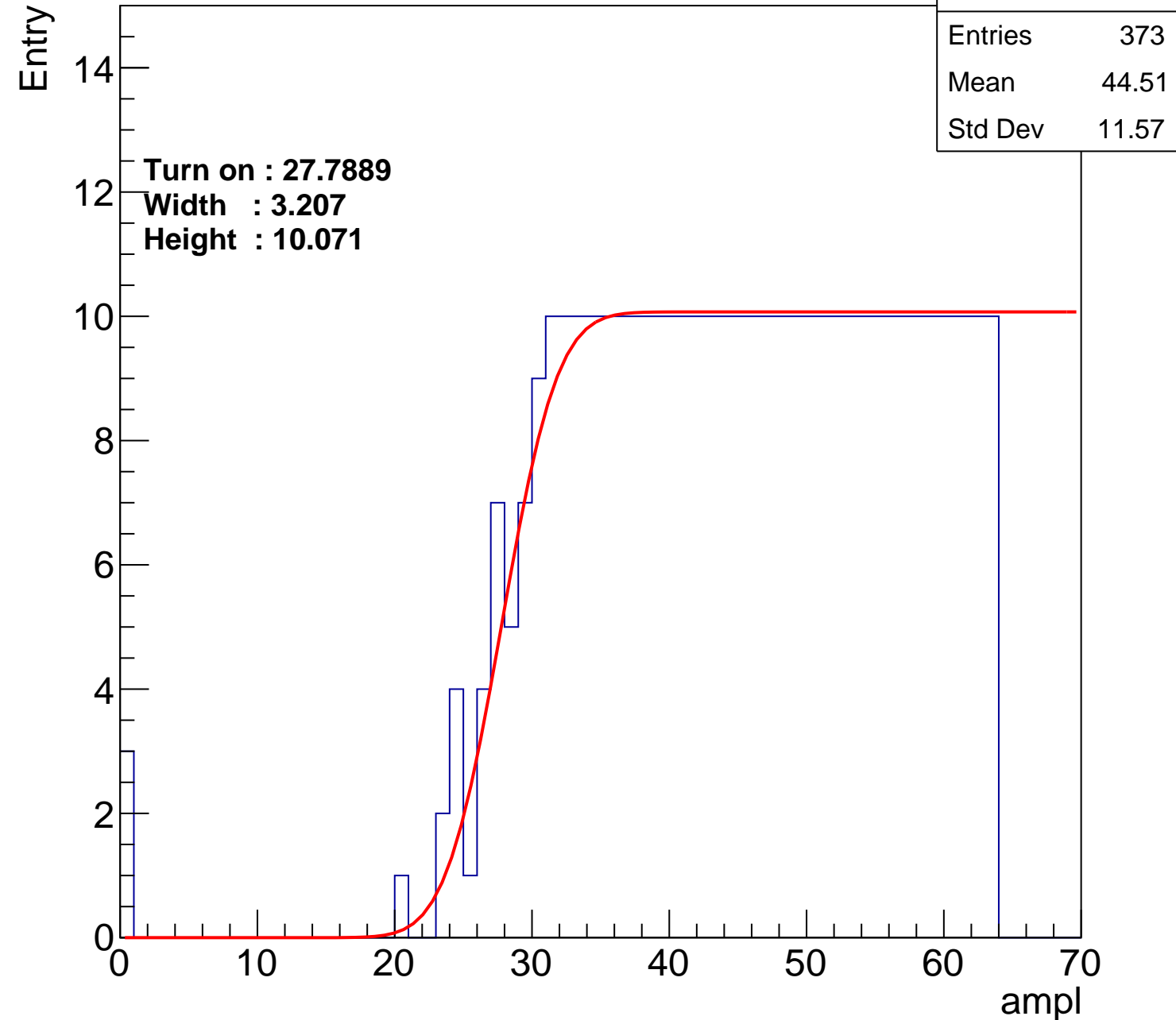
Width : 3.207

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 27.1106

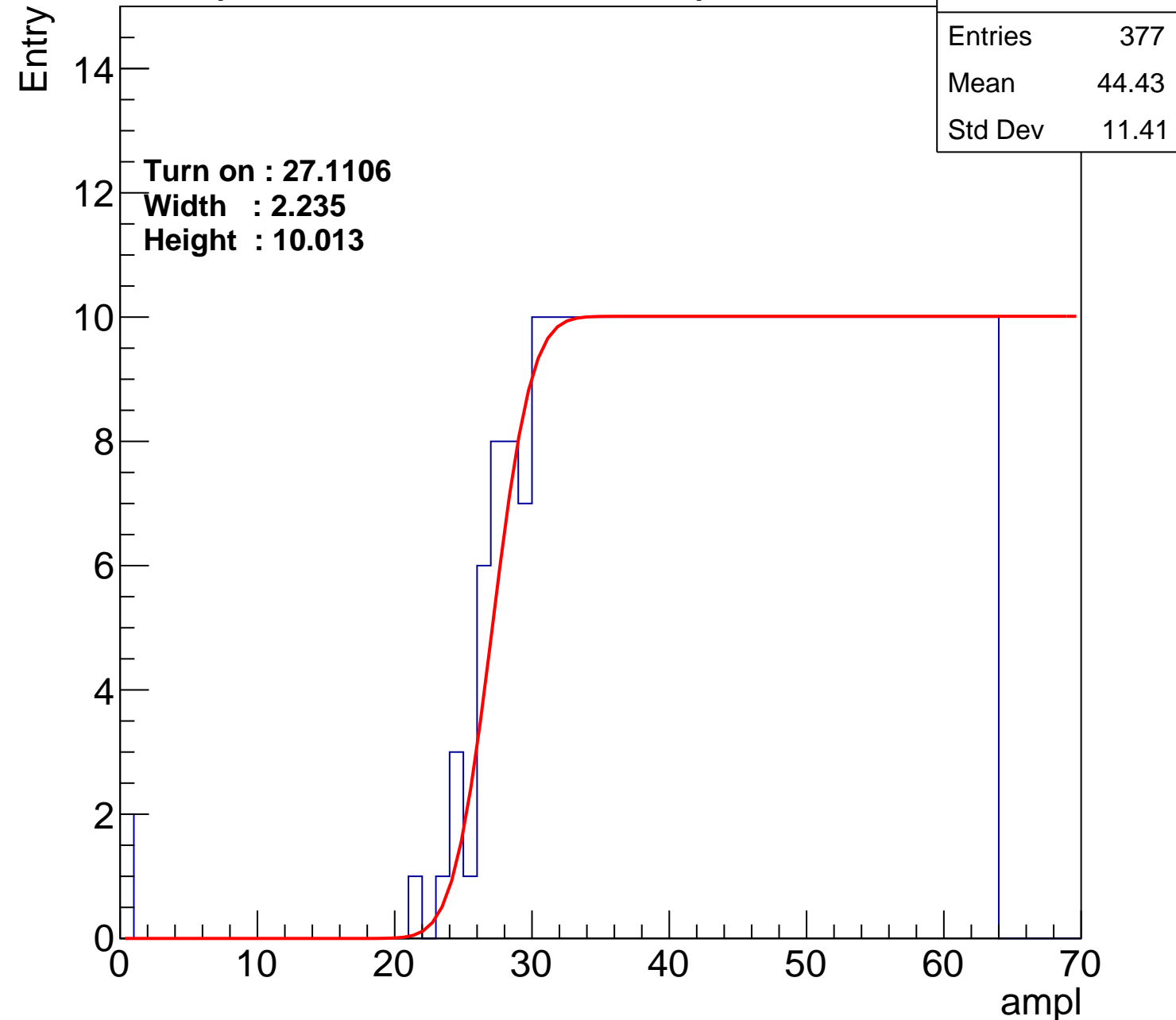
Width : 2.235

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	43.08
Std Dev	12.08

Turn on : 23.6686

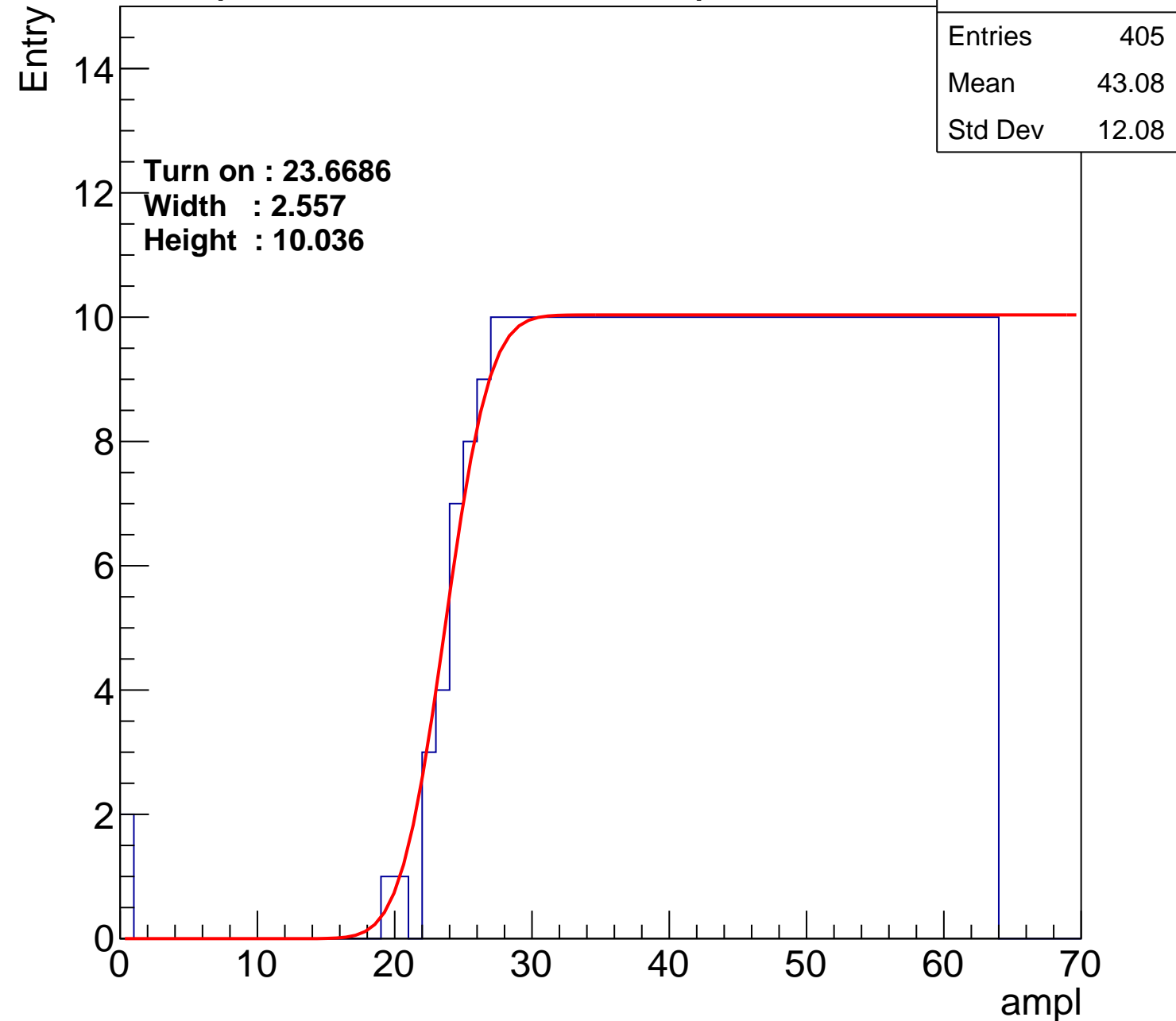
Width : 2.557

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.7143

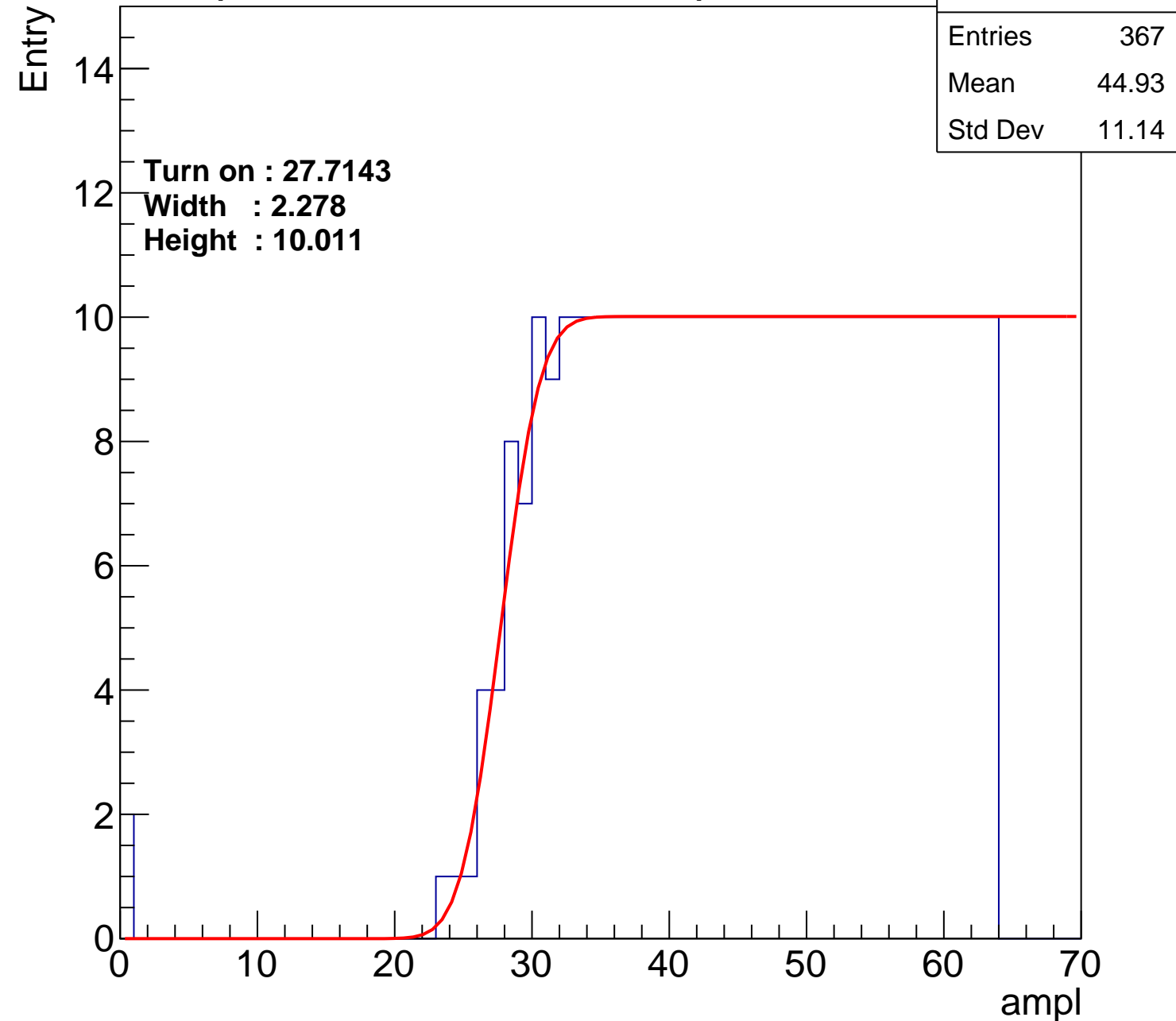
Width : 2.278

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	44.82
Std Dev	11.73

Turn on : 28.1117

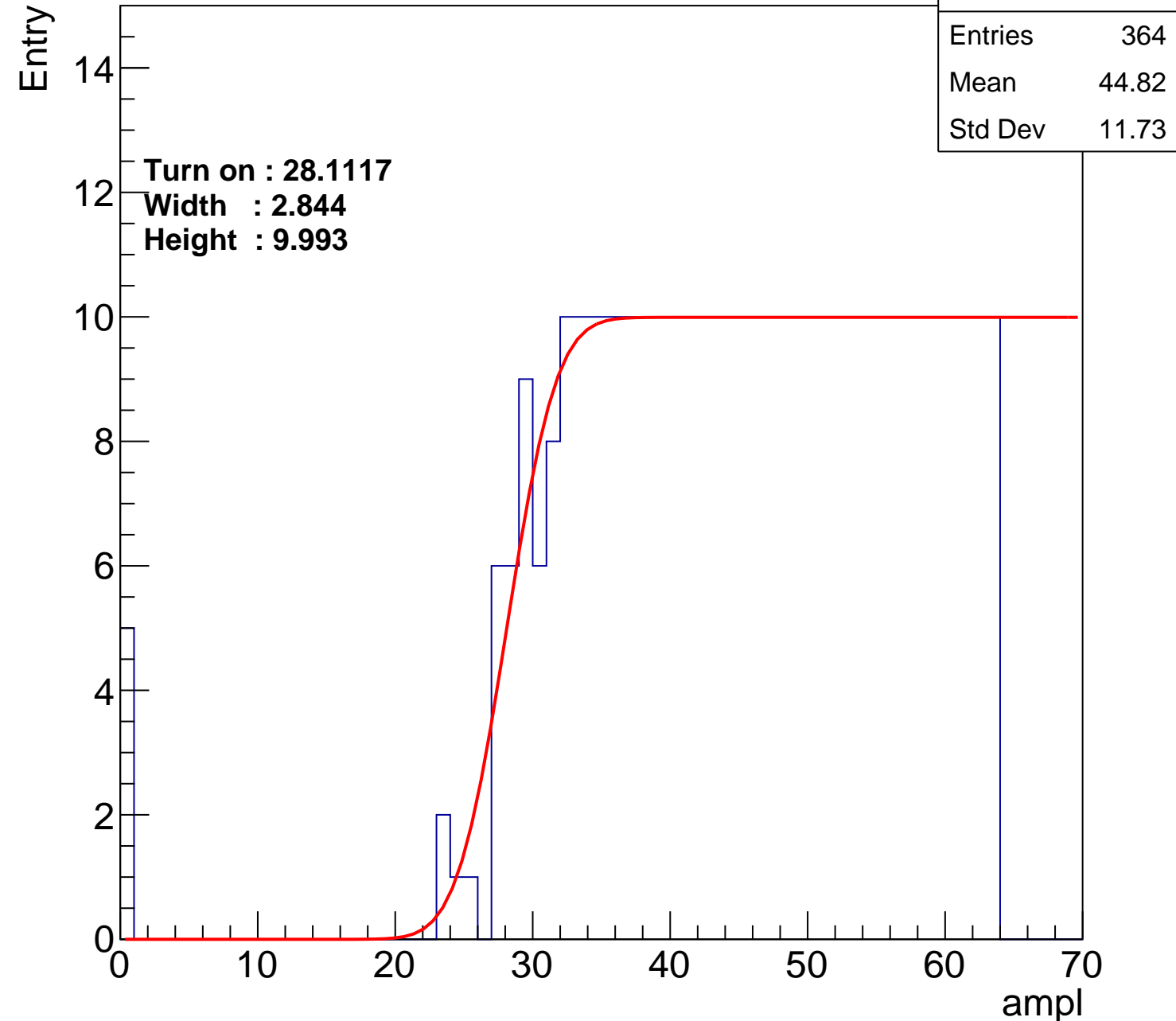
Width : 2.844

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.0164

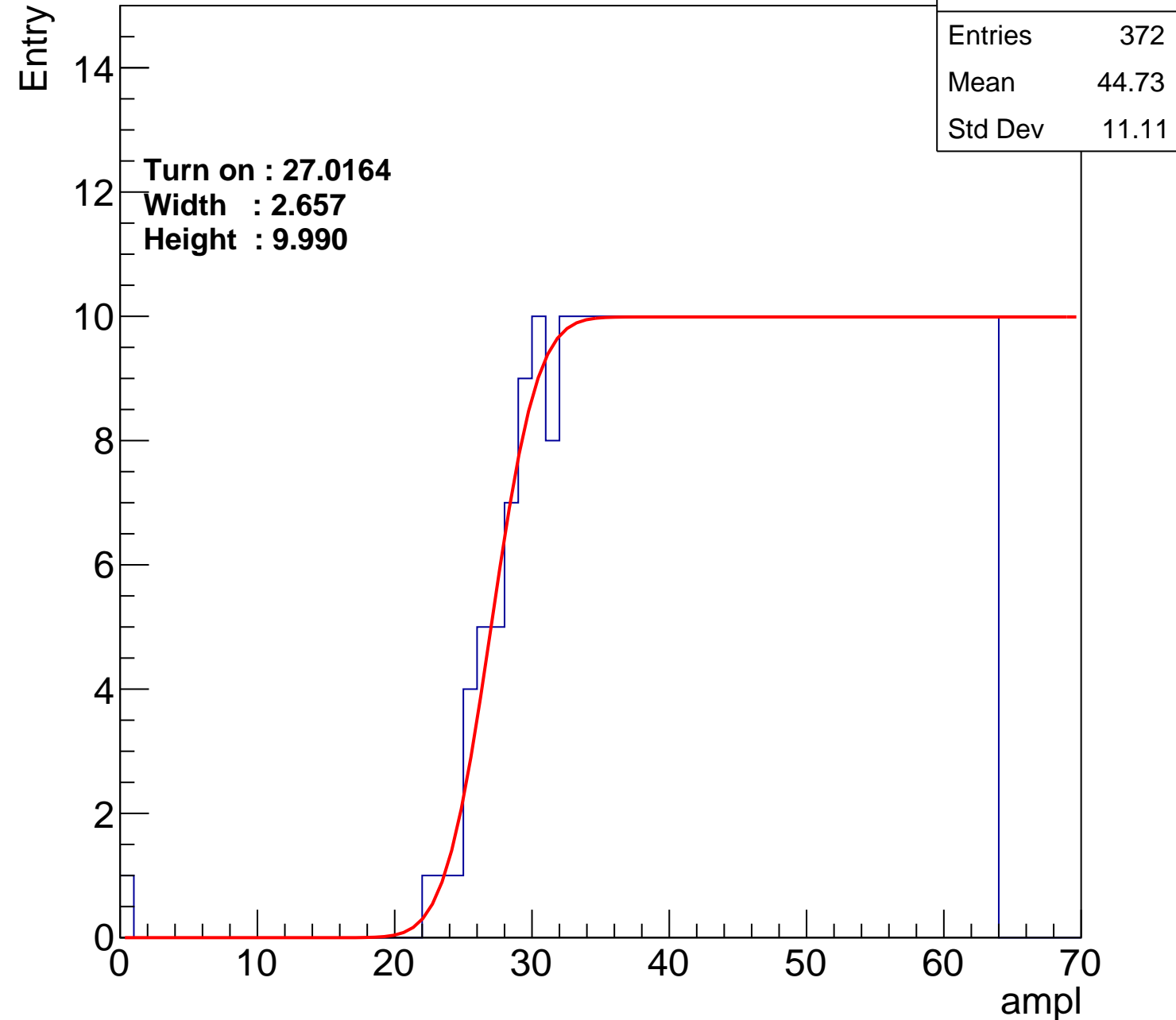
Width : 2.657

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch29

calib_packv5_042523_0143.root, FC#3, port B3

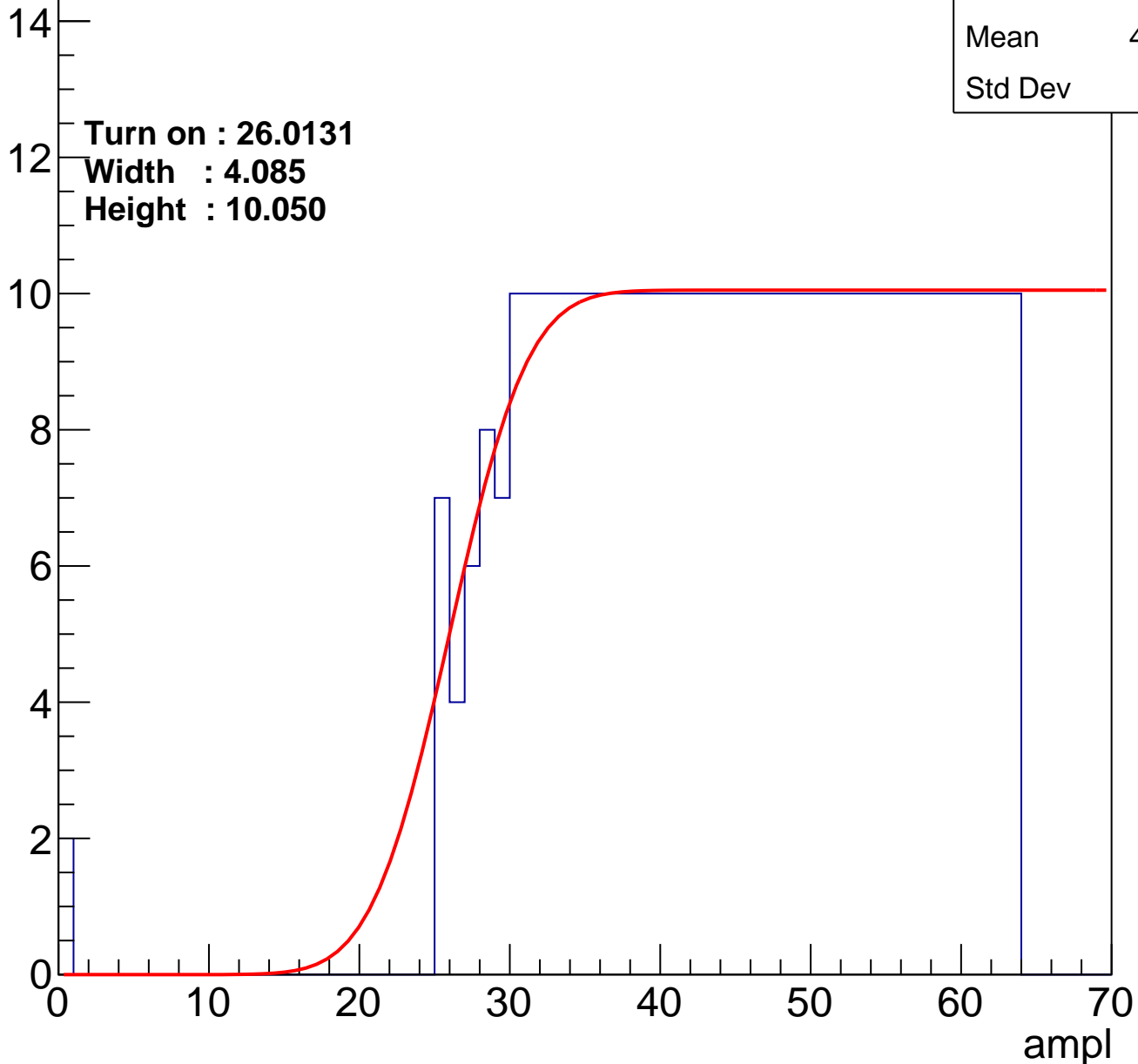
Entries	374
Mean	44.59
Std Dev	11.3

Turn on : 26.0131

Width : 4.085

Height : 10.050

Entry



B1L000S, U10-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.6516

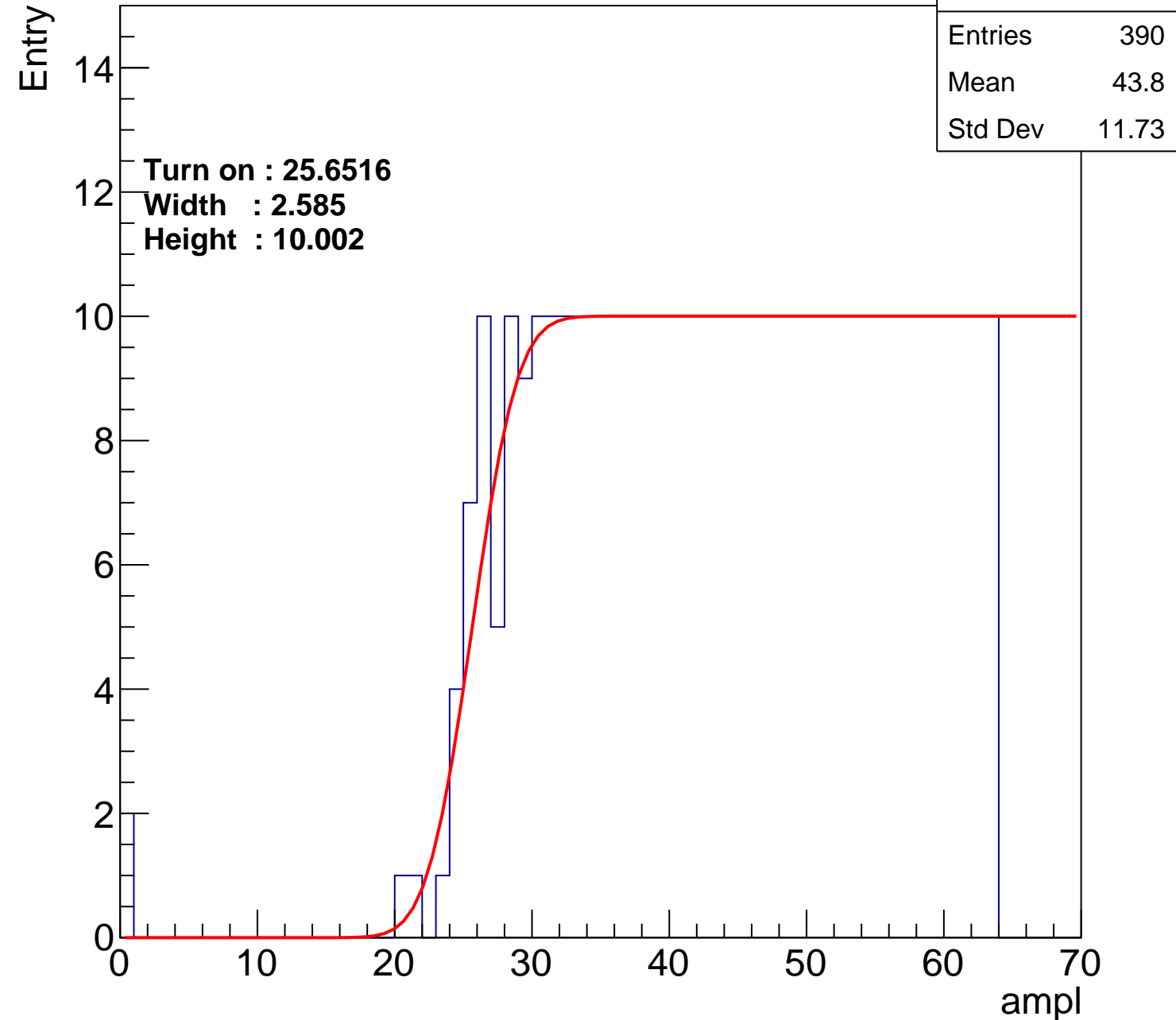
Width : 2.585

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.02
Std Dev	10.96

Turn on : 27.7636

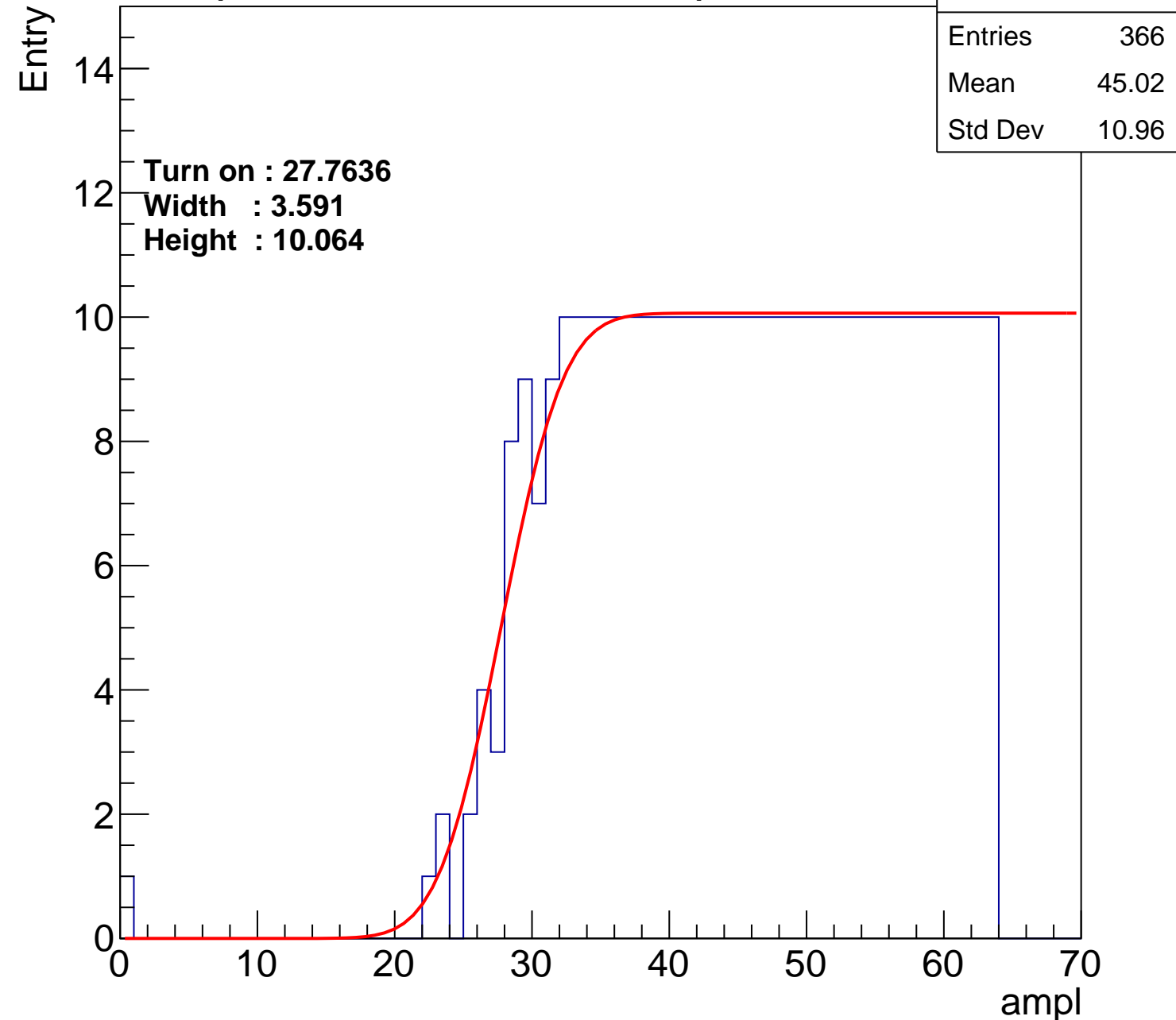
Width : 3.591

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch32

calib_packv5_042523_0143.root, FC#3, port B3

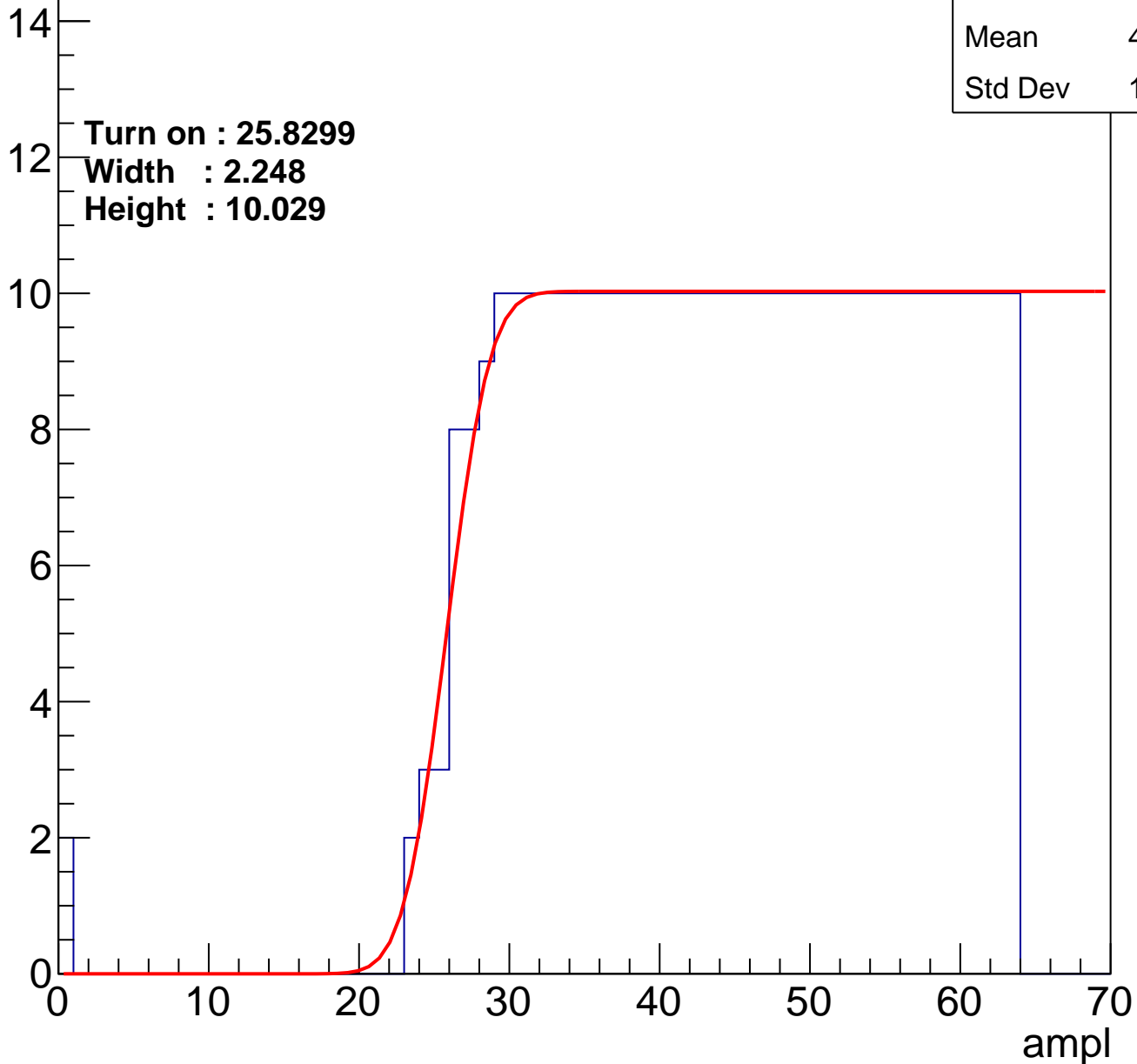
Entries	385
Mean	44.08
Std Dev	11.55

Turn on : 25.8299

Width : 2.248

Height : 10.029

Entry



B1L000S, U10-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.1388

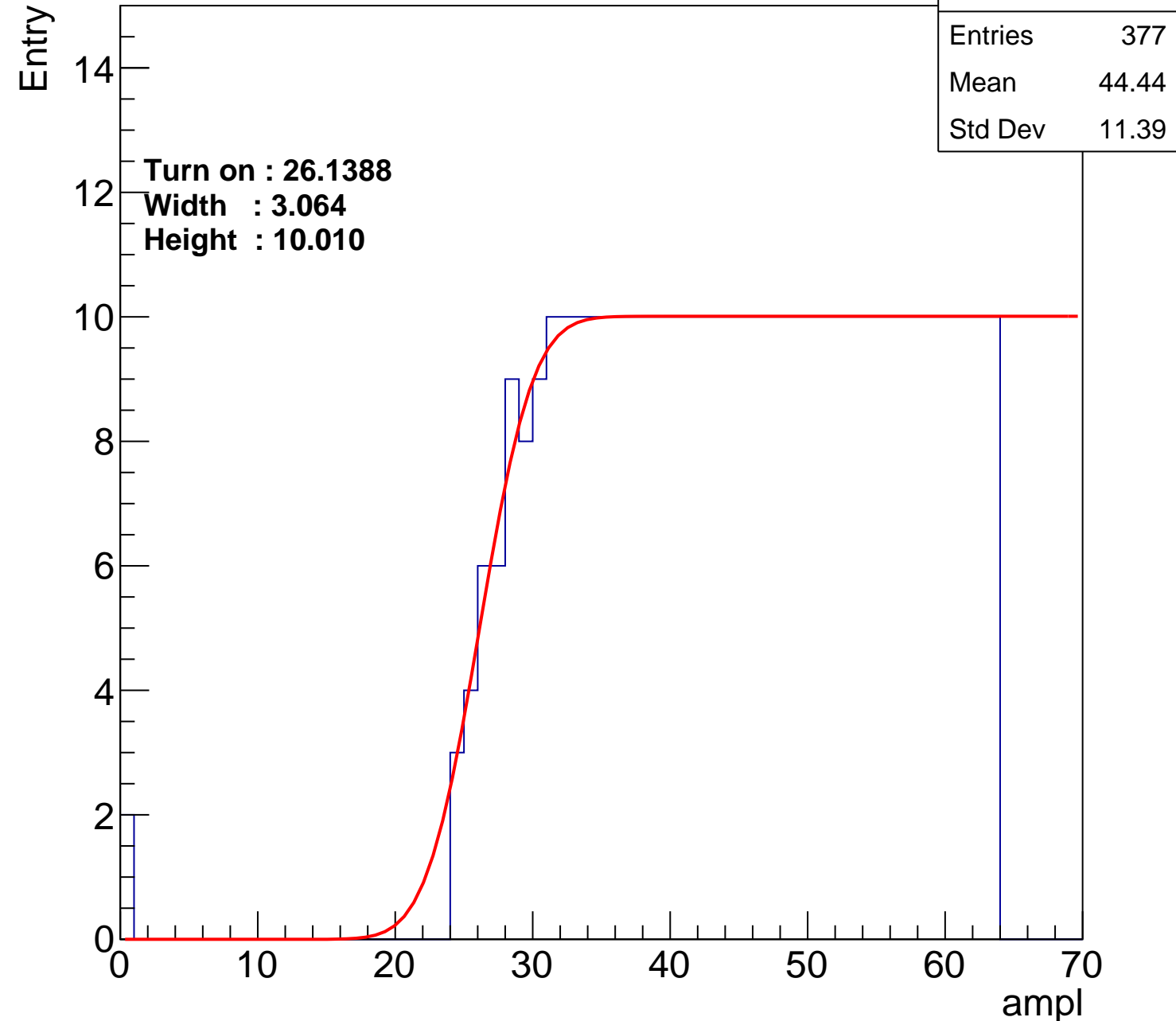
Width : 3.064

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.1974

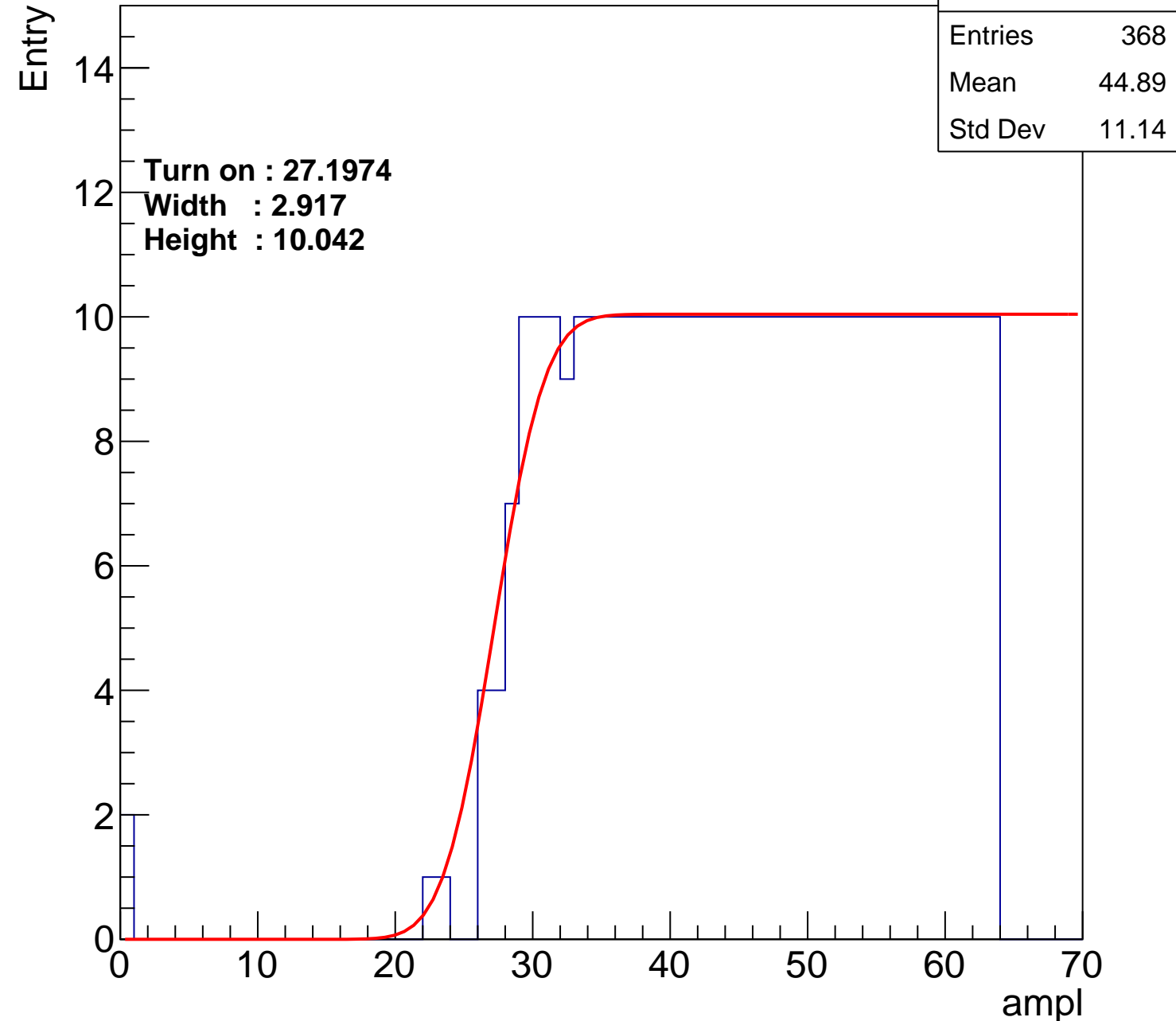
Width : 2.917

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch35

calib_packv5_042523_0143.root, FC#3, port B3

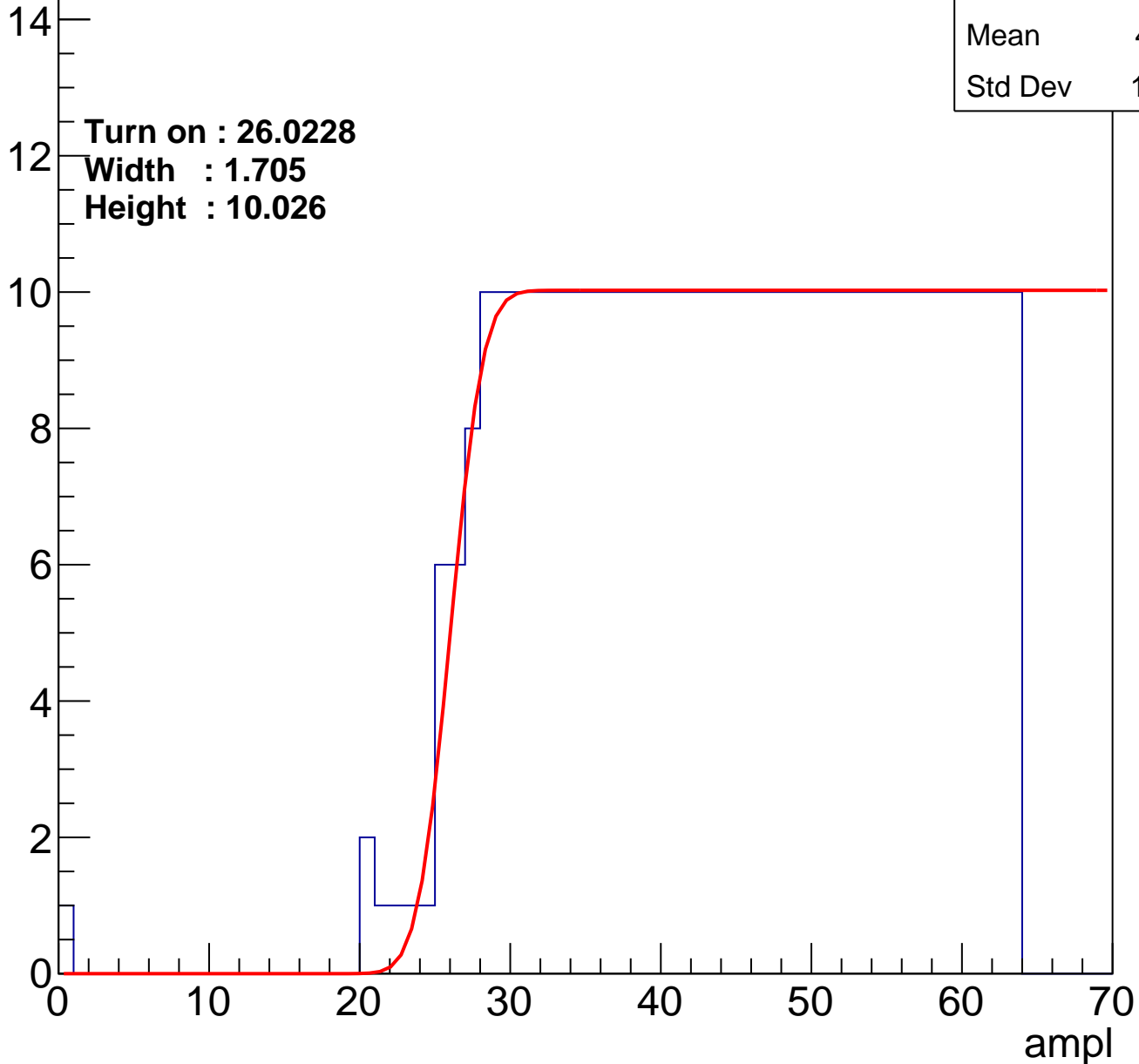
Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 26.0228

Width : 1.705

Height : 10.026

Entry



B1L000S, U10-ch36

calib_packv5_042523_0143.root, FC#3, port B3

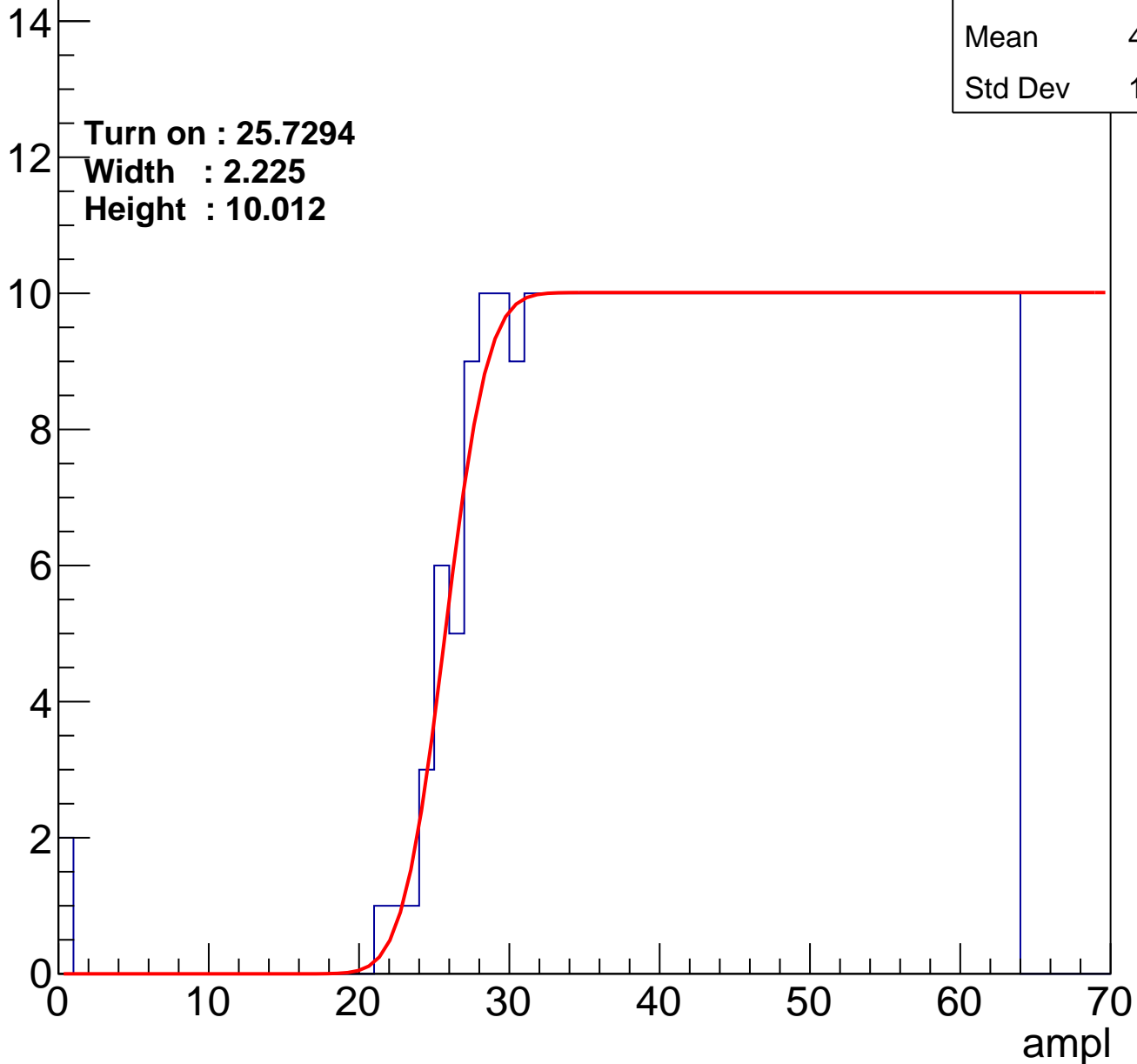
Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.7294

Width : 2.225

Height : 10.012

Entry



B1L000S, U10-ch37

calib_packv5_042523_0143.root, FC#3, port B3

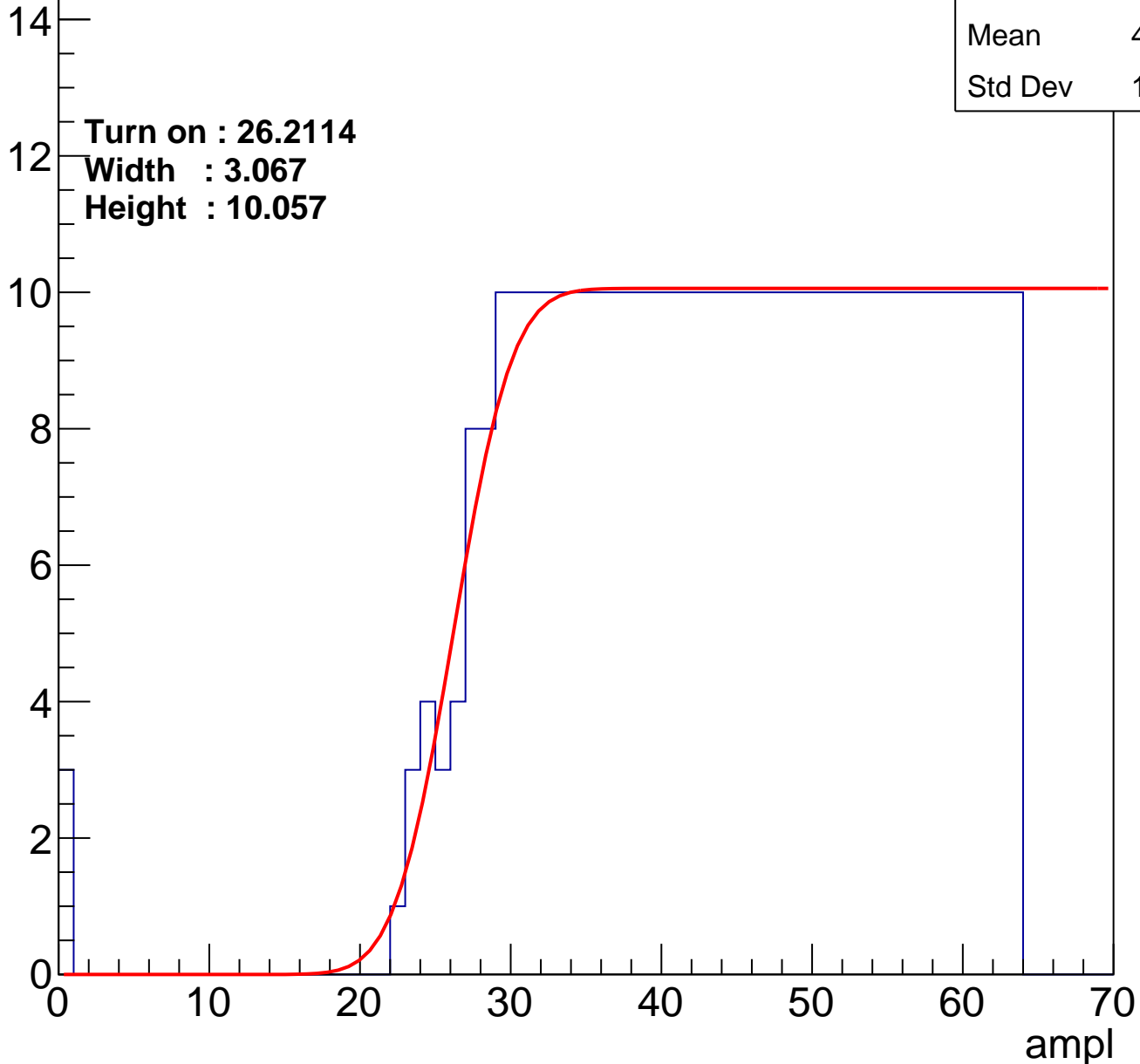
Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 26.2114

Width : 3.067

Height : 10.057

Entry



B1L000S, U10-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 26.9026

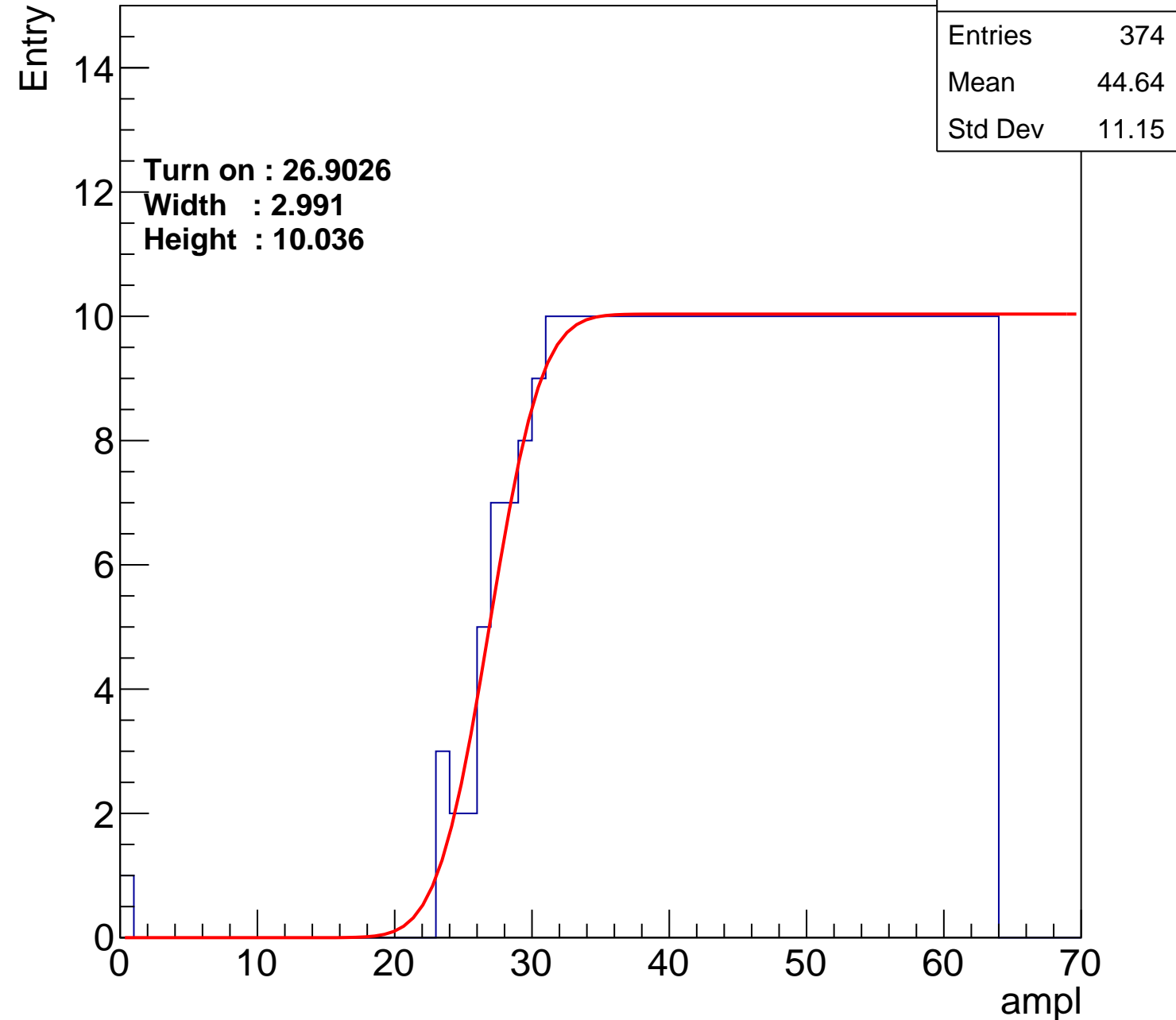
Width : 2.991

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.73
Std Dev	11.52

Turn on : 27.5613

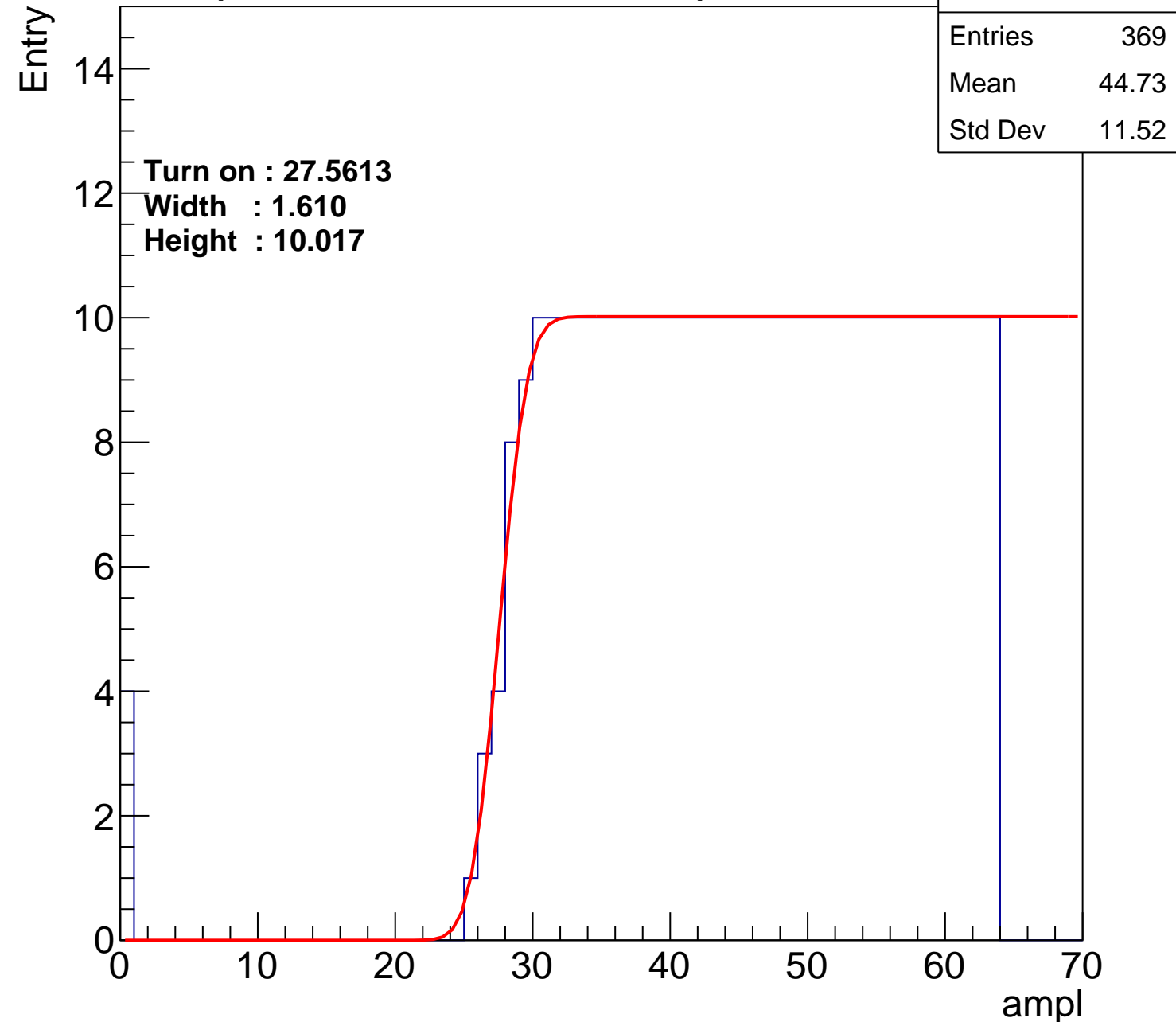
Width : 1.610

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 25.5506

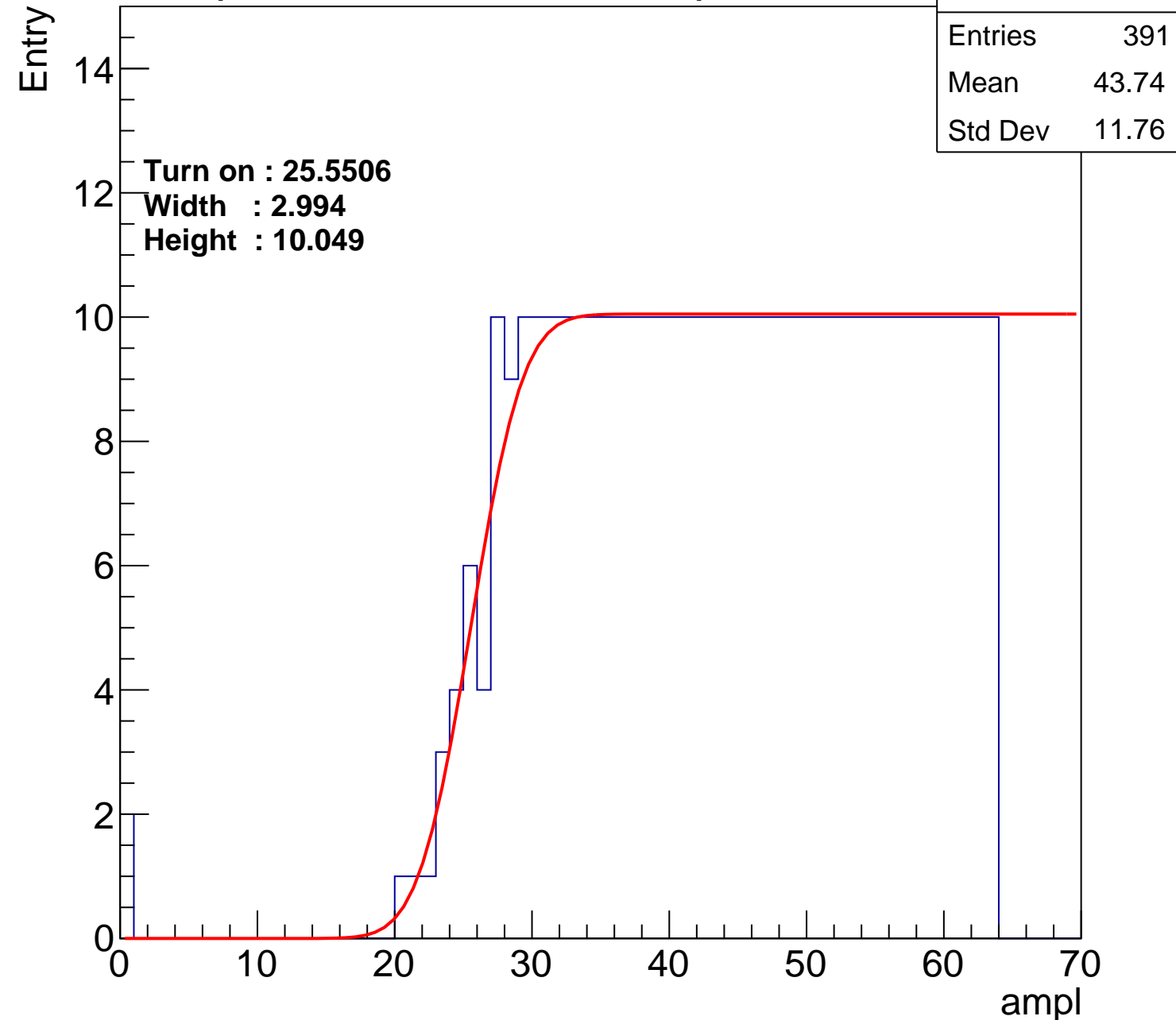
Width : 2.994

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
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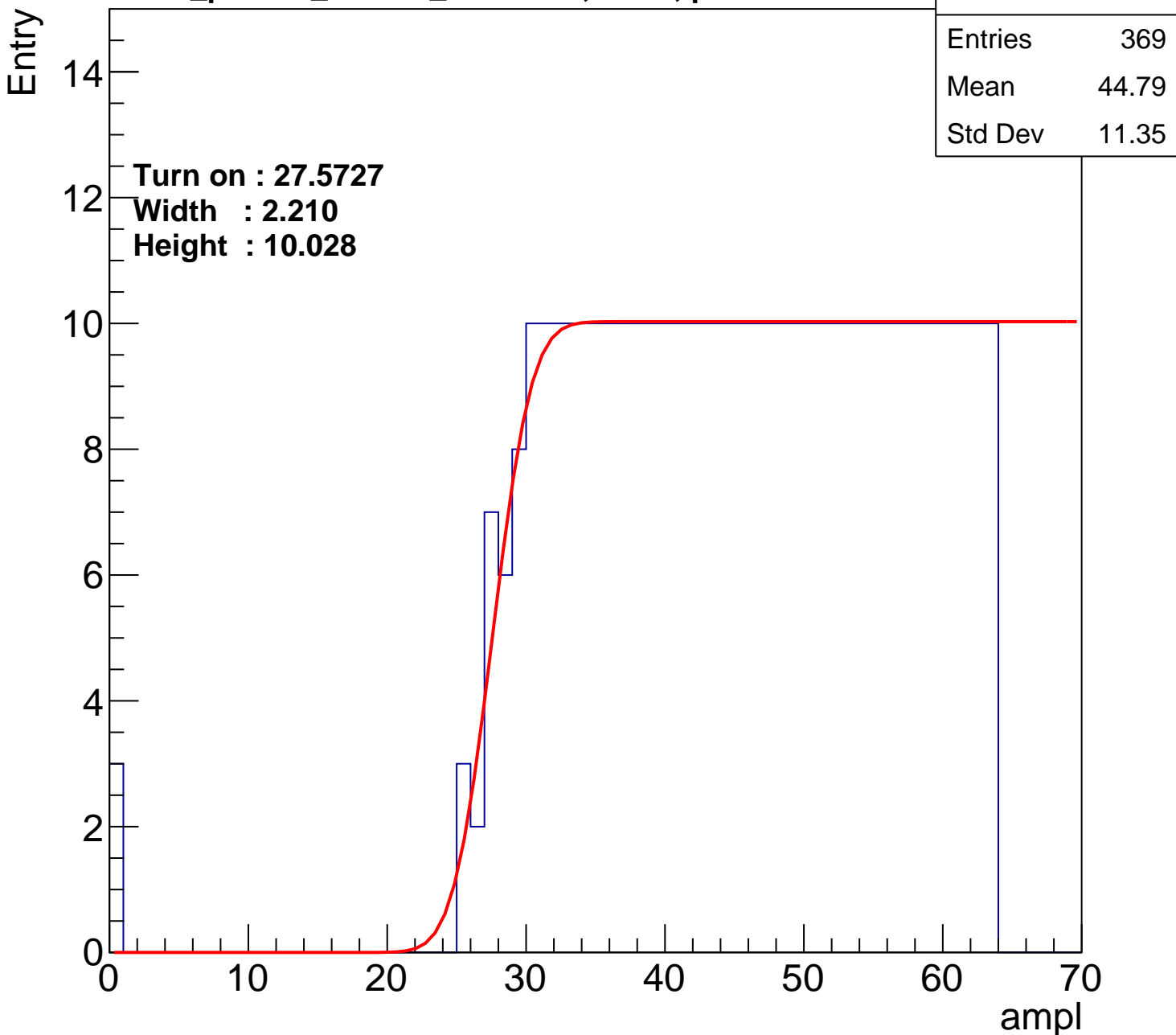
Mean	44.79
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Std Dev	11.35
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Turn on : 27.5727

Width : 2.210

Height : 10.028



B1L000S, U10-ch42

calib_packv5_042523_0143.root, FC#3, port B3

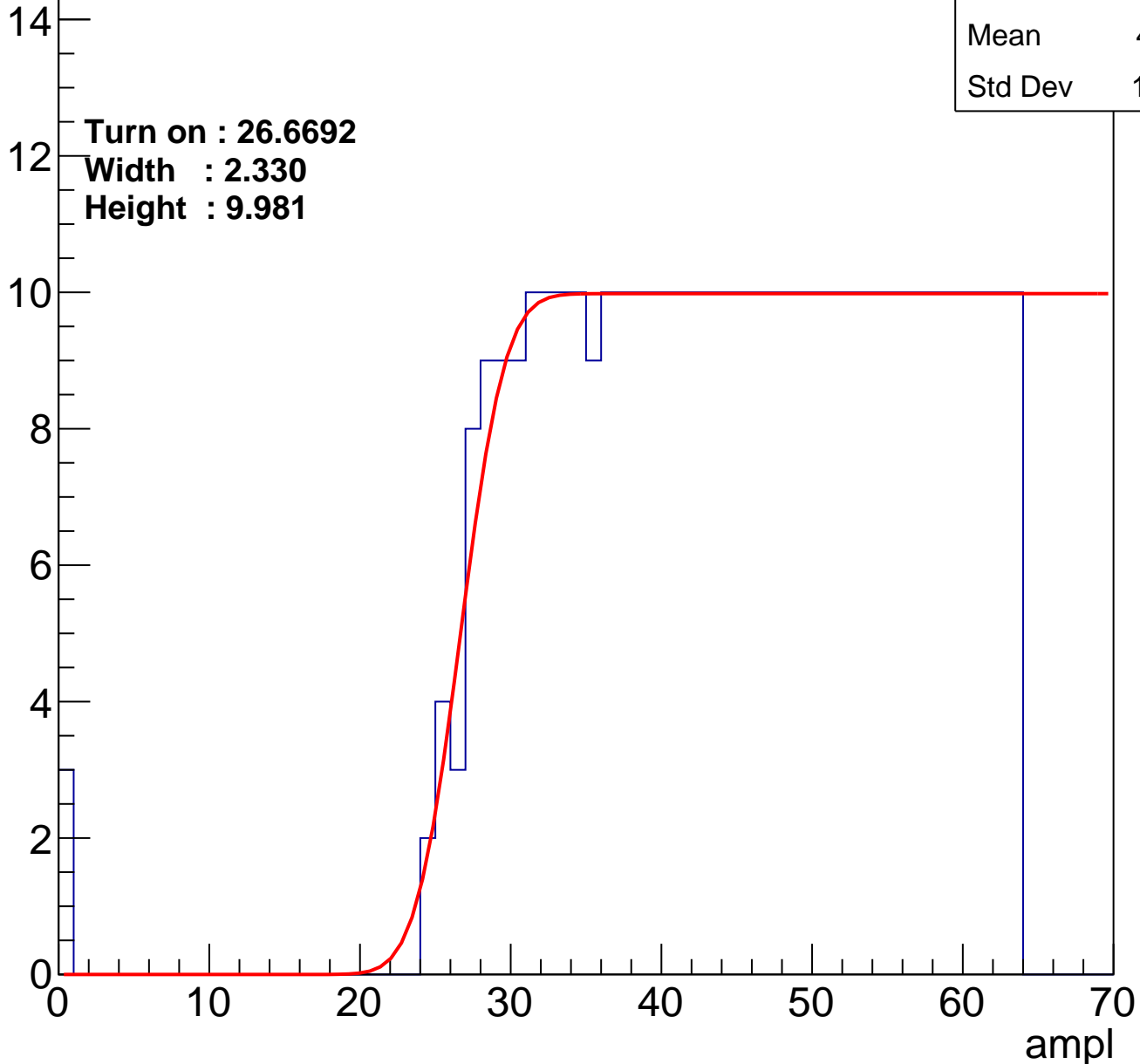
Entries	376
Mean	44.41
Std Dev	11.55

Turn on : 26.6692

Width : 2.330

Height : 9.981

Entry



B1L000S, U10-ch43

calib_packv5_042523_0143.root, FC#3, port B3

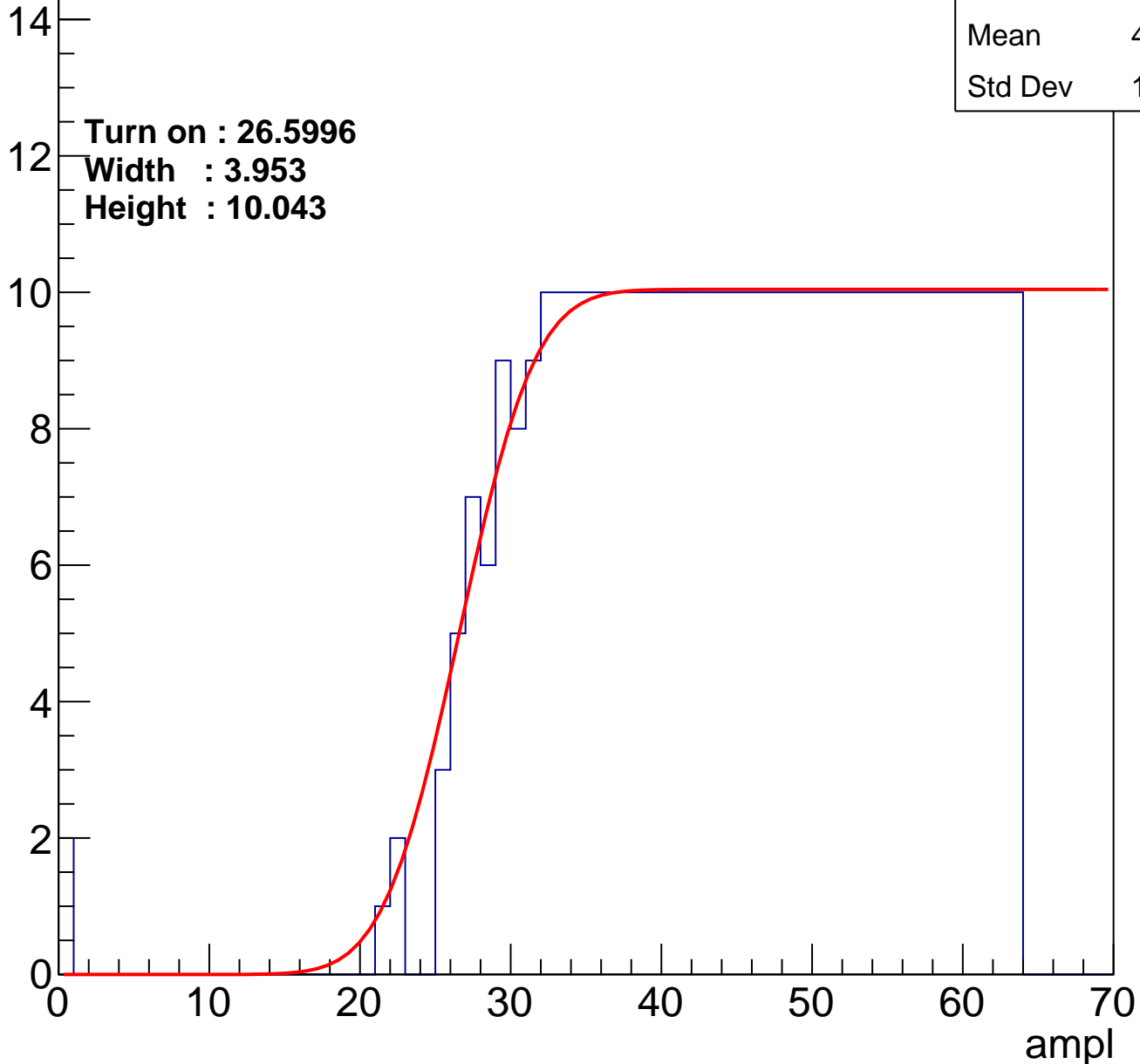
Entries	372
Mean	44.64
Std Dev	11.33

Turn on : 26.5996

Width : 3.953

Height : 10.043

Entry



B1L000S, U10-ch44

calib_packv5_042523_0143.root, FC#3, port B3

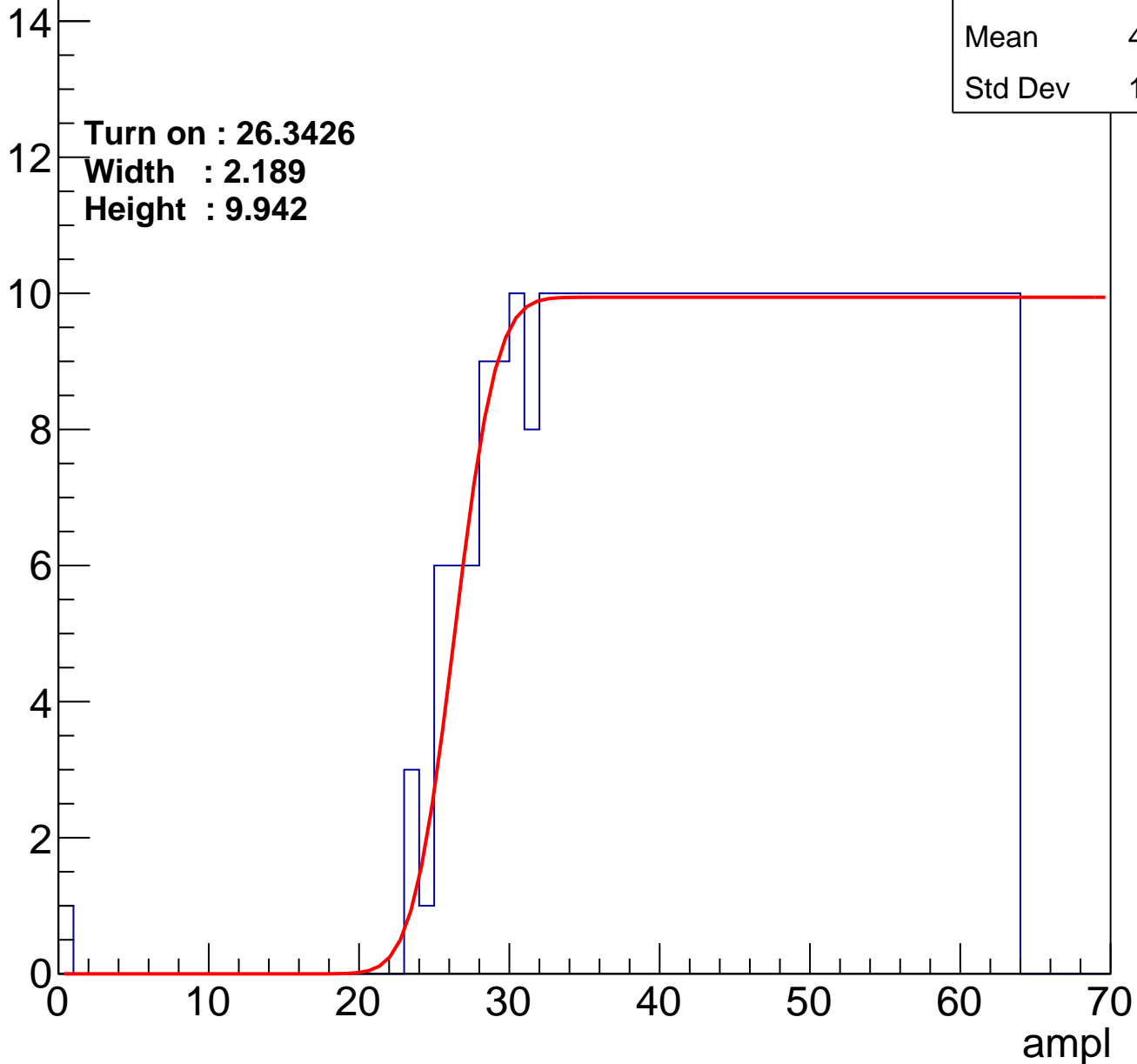
Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.3426

Width : 2.189

Height : 9.942

Entry



B1L000S, U10-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.77
Std Dev	11.1

Turn on : 27.4597

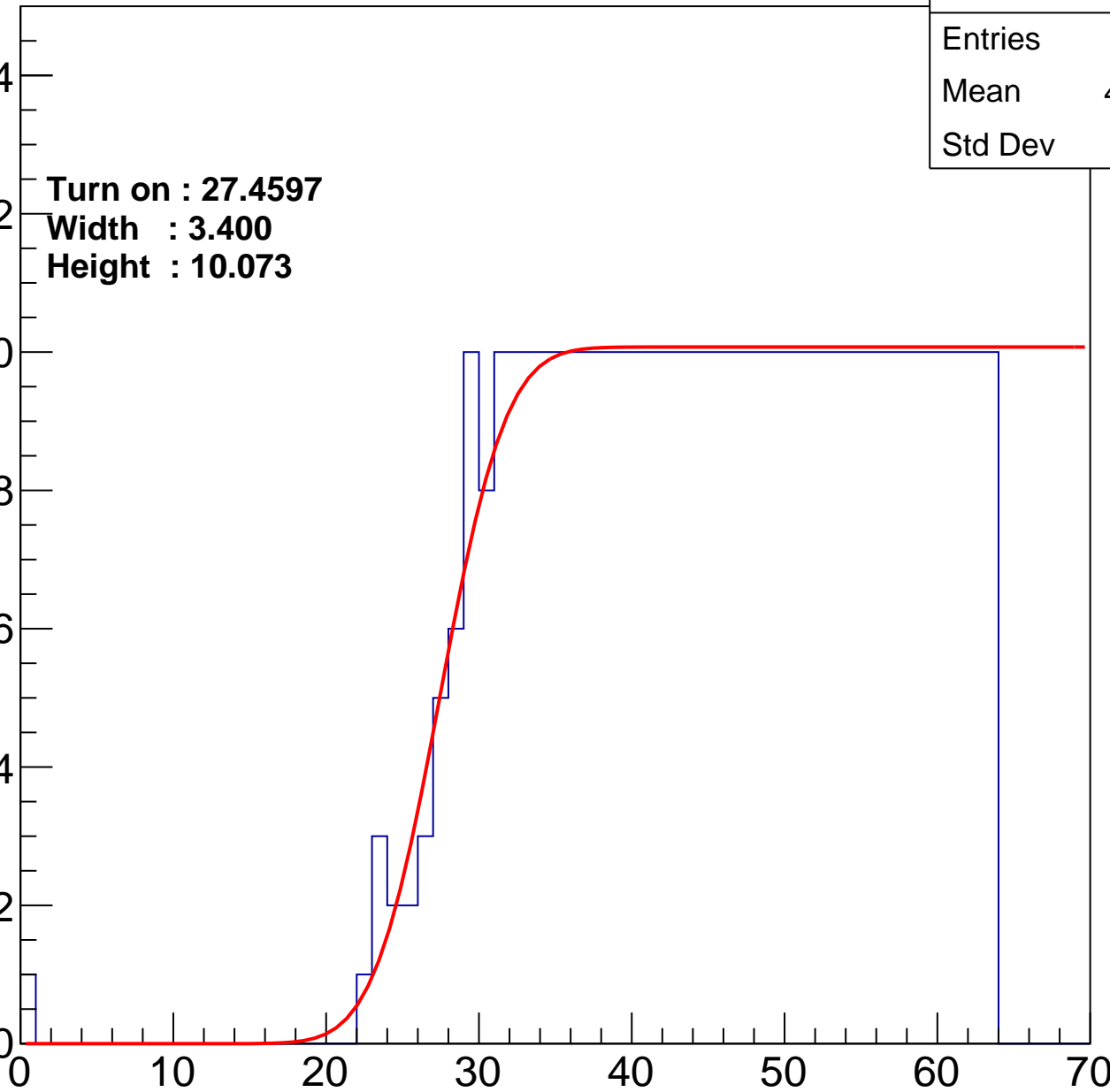
Width : 3.400

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.11
Std Dev	11.06

Turn on : 28.0160

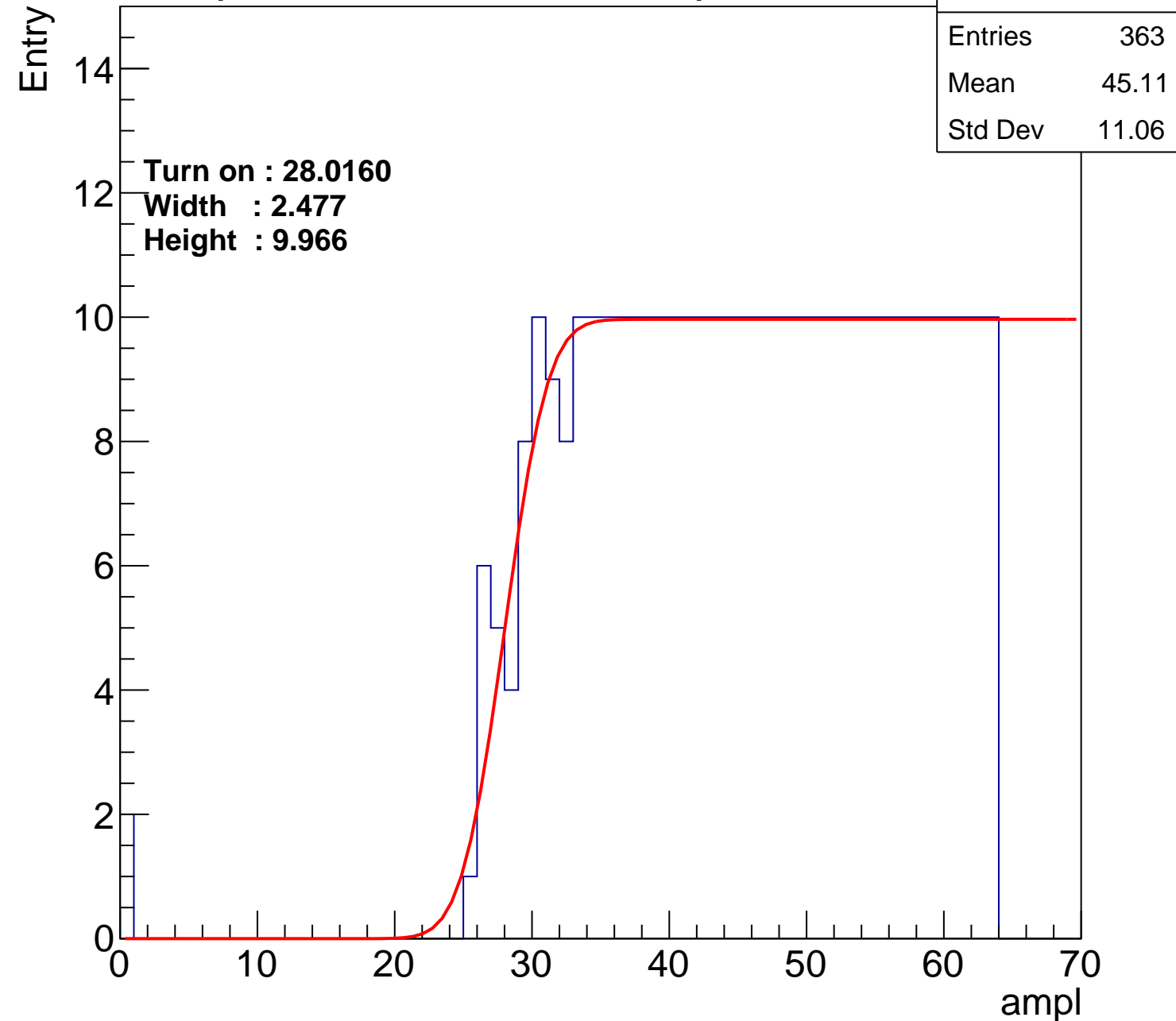
Width : 2.477

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 26.2642

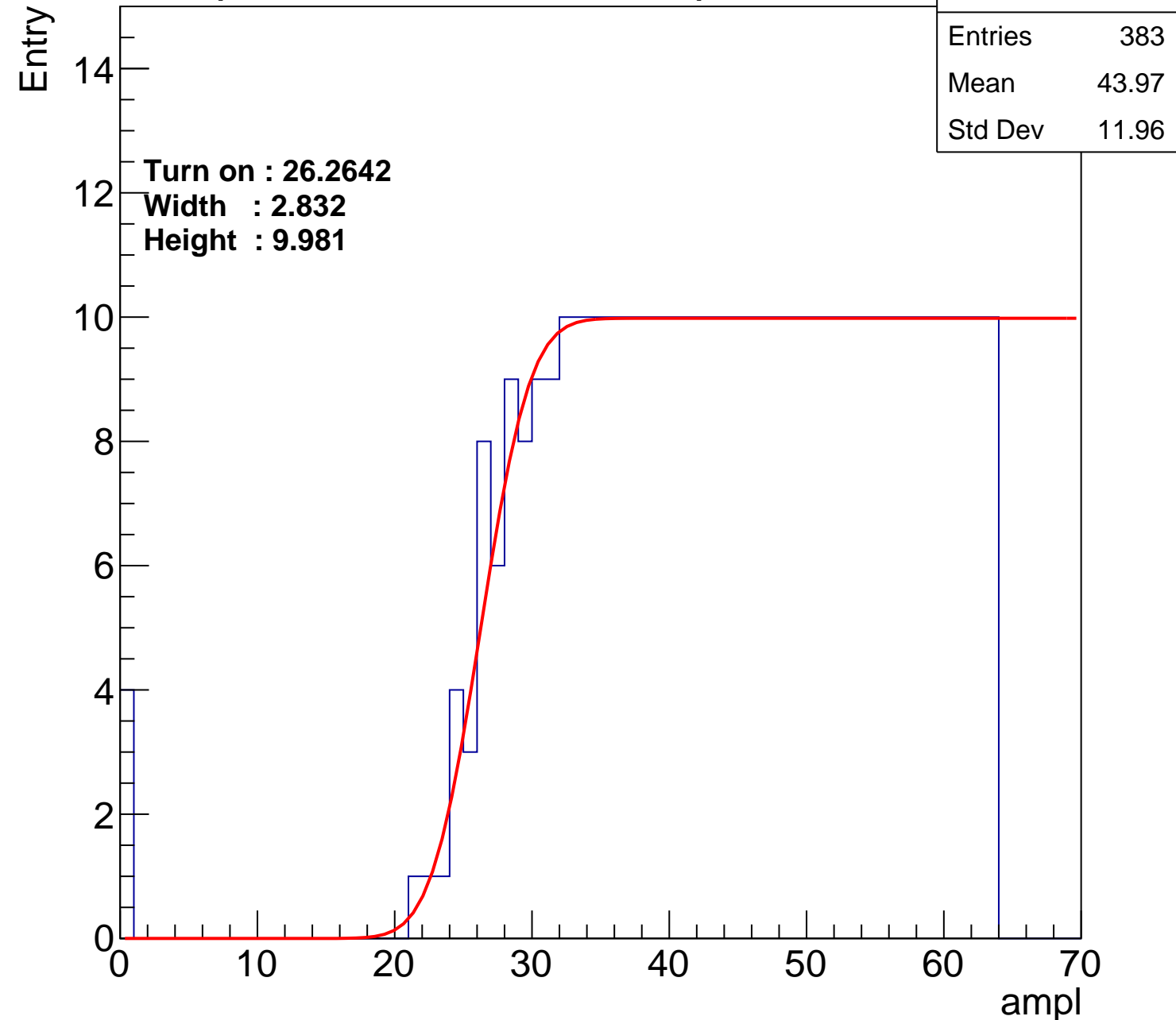
Width : 2.832

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch48

calib_packv5_042523_0143.root, FC#3, port B3

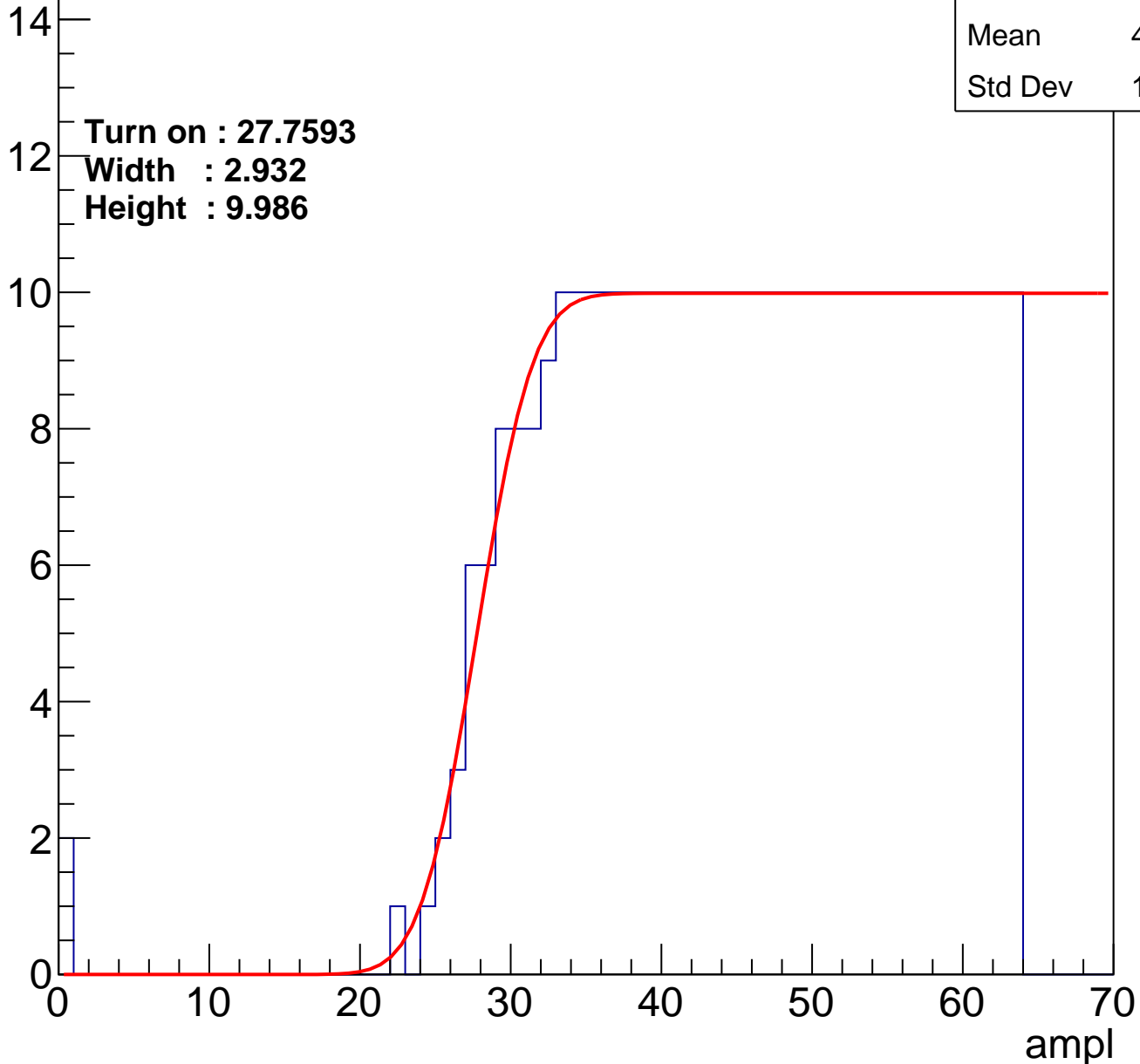
Entries	364
Mean	45.03
Std Dev	11.13

Turn on : 27.7593

Width : 2.932

Height : 9.986

Entry



B1L000S, U10-ch49

calib_packv5_042523_0143.root, FC#3, port B3

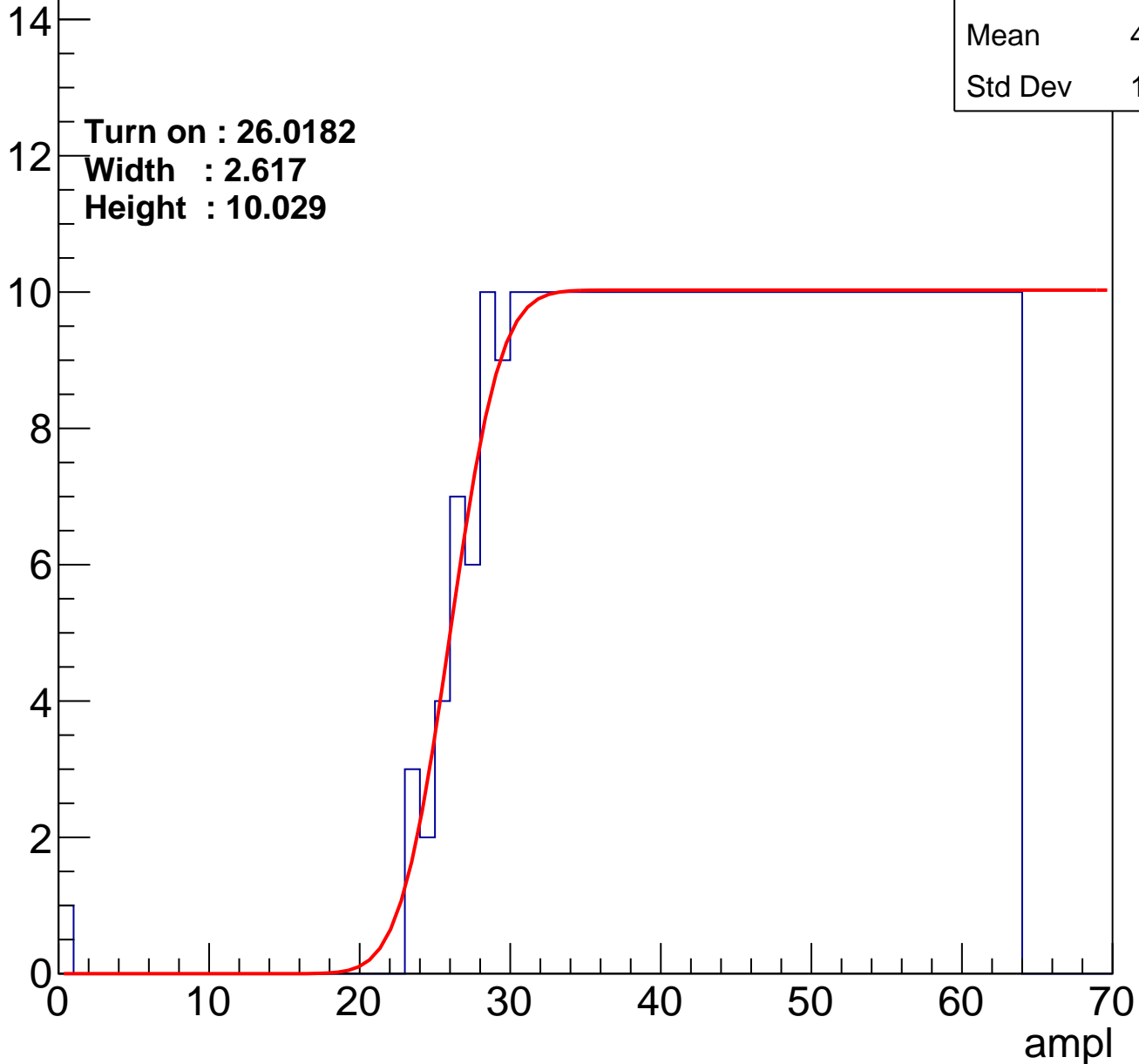
Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 26.0182

Width : 2.617

Height : 10.029

Entry



B1L000S, U10-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.5915

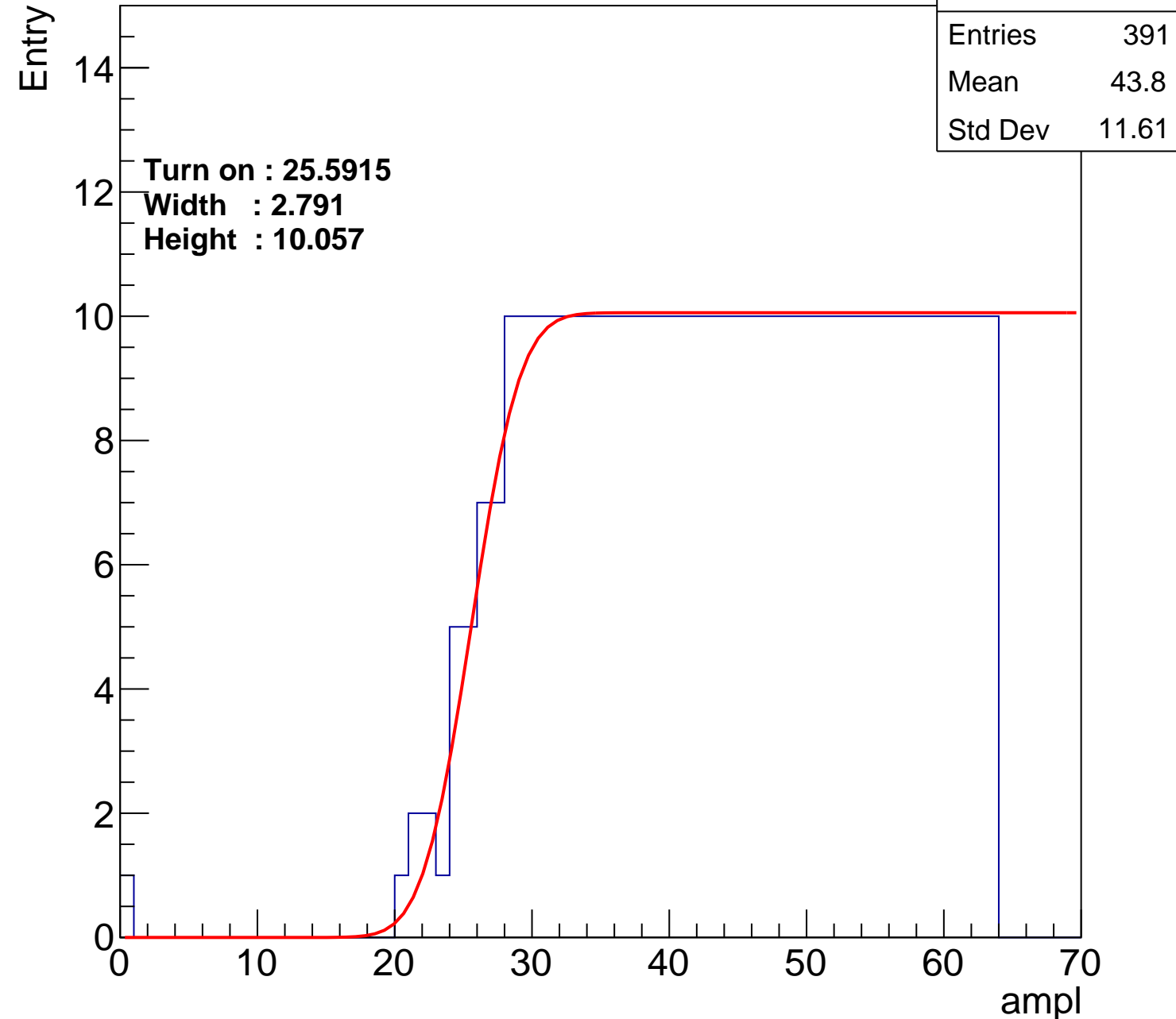
Width : 2.791

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.7740

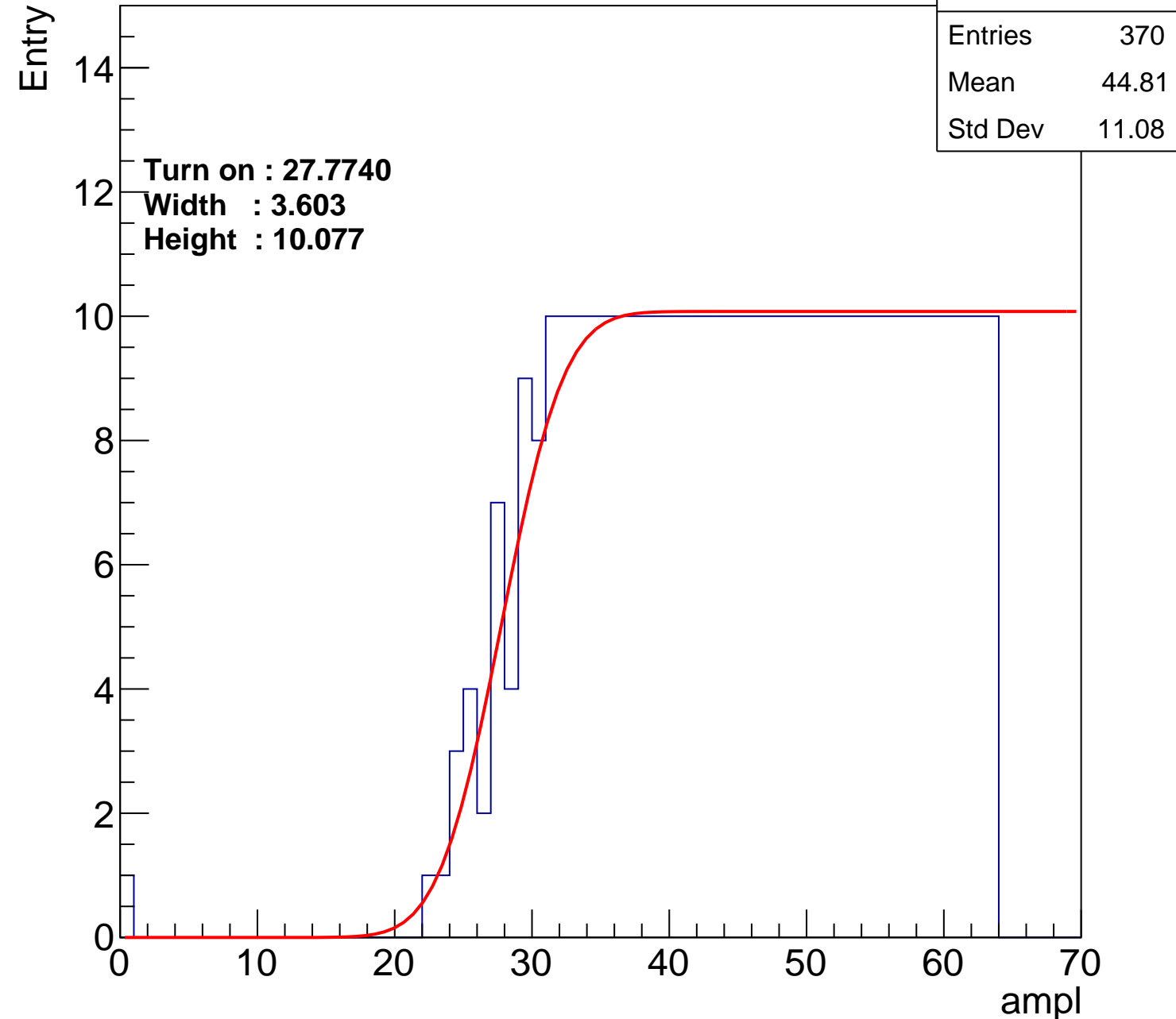
Width : 3.603

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch52

calib_packv5_042523_0143.root, FC#3, port B3

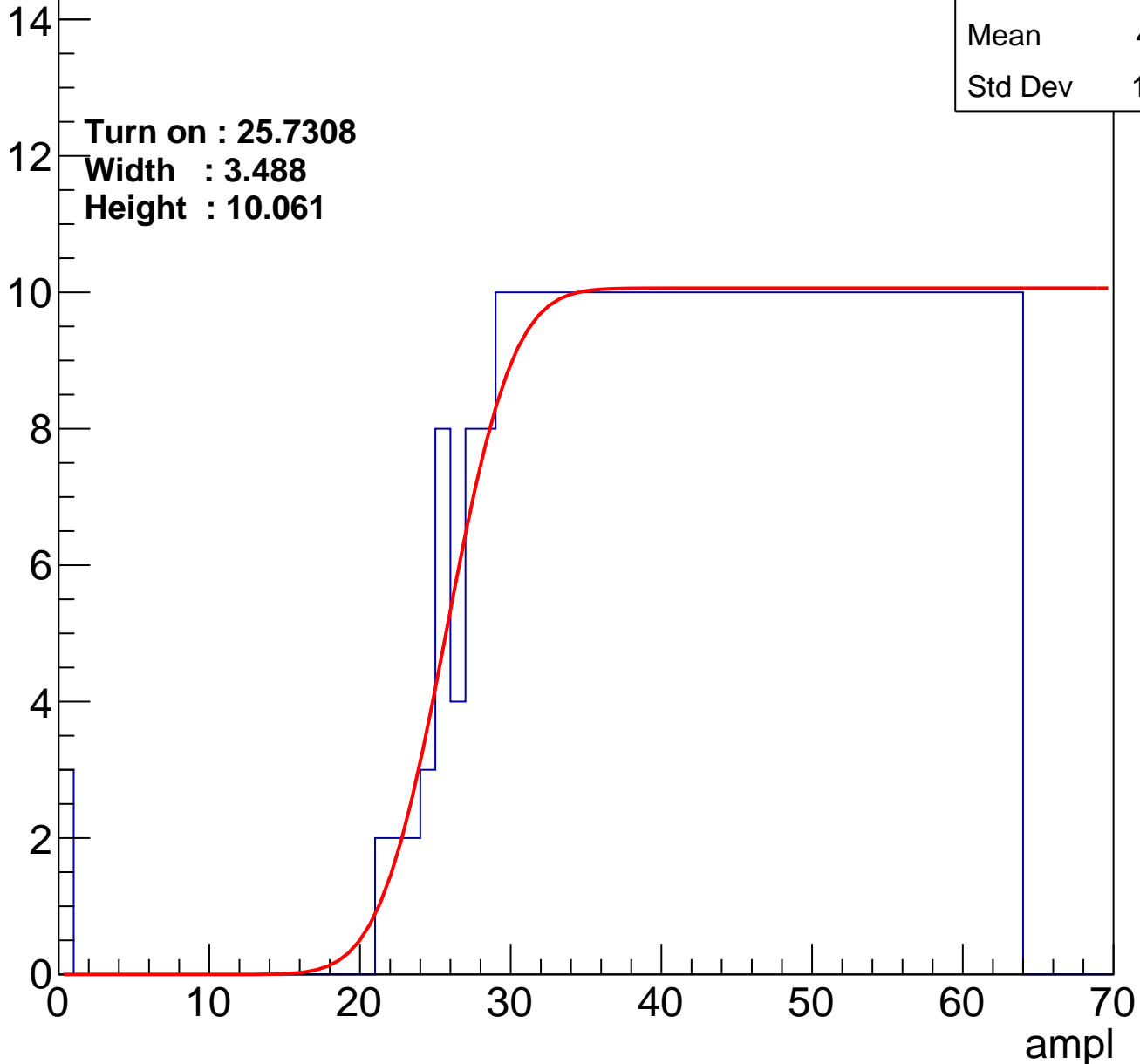
Entries	390
Mean	43.71
Std Dev	11.93

Turn on : 25.7308

Width : 3.488

Height : 10.061

Entry



B1L000S, U10-ch53

calib_packv5_042523_0143.root, FC#3, port B3

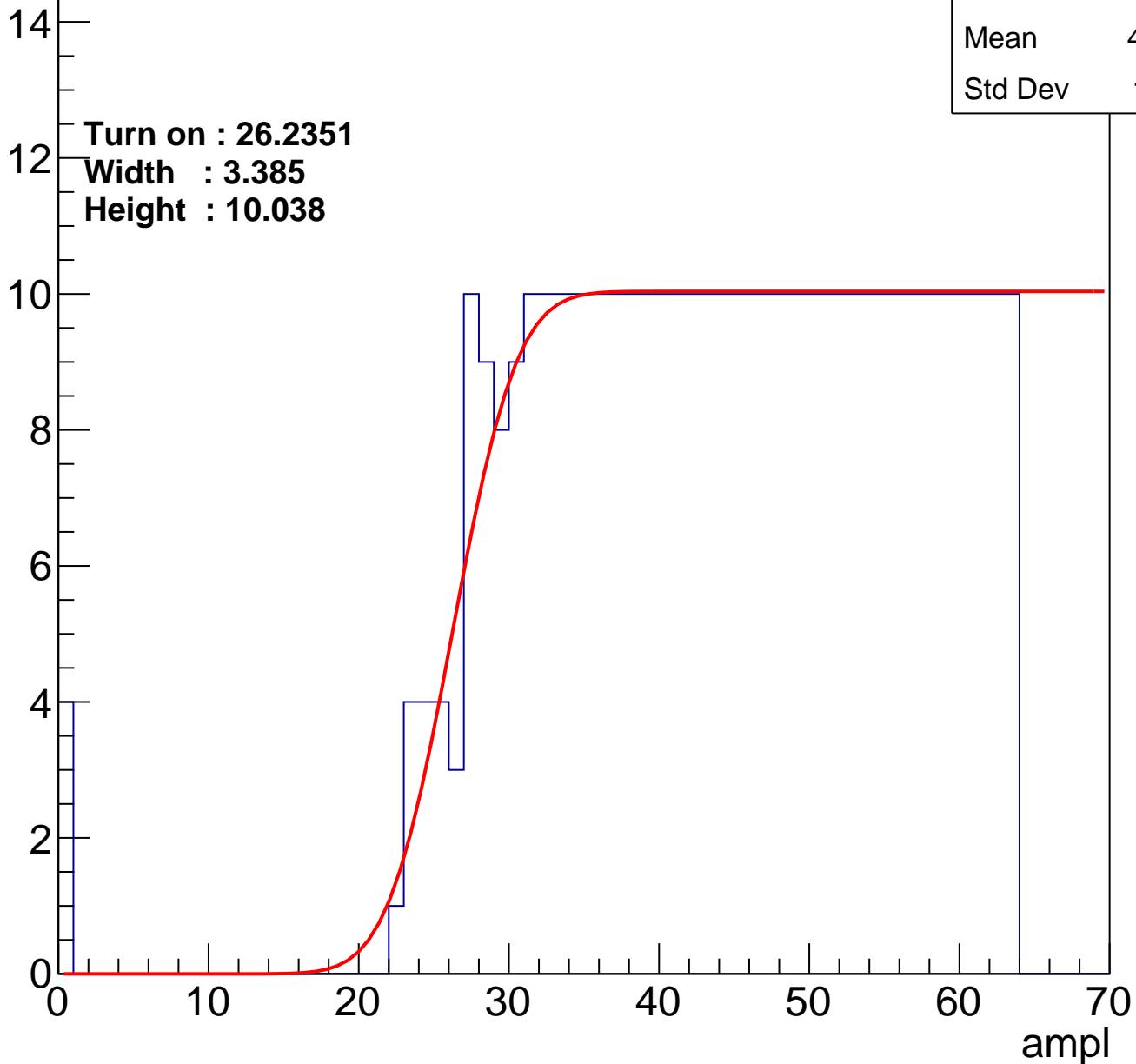
Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.2351

Width : 3.385

Height : 10.038

Entry



B1L000S, U10-ch54

calib_packv5_042523_0143.root, FC#3, port B3

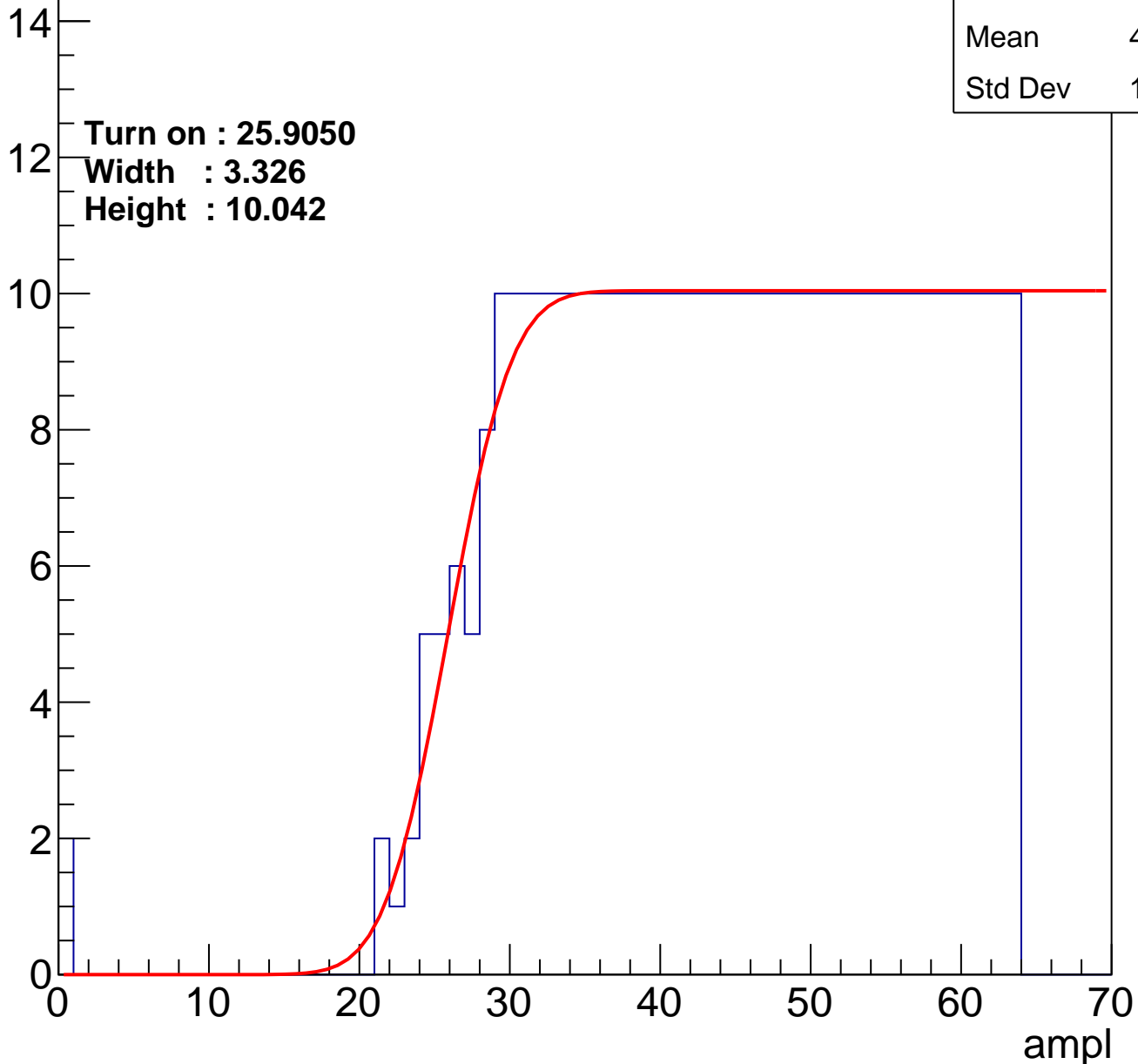
Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.9050

Width : 3.326

Height : 10.042

Entry



B1L000S, U10-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.53
Std Dev	12.41

Turn on : 25.8163

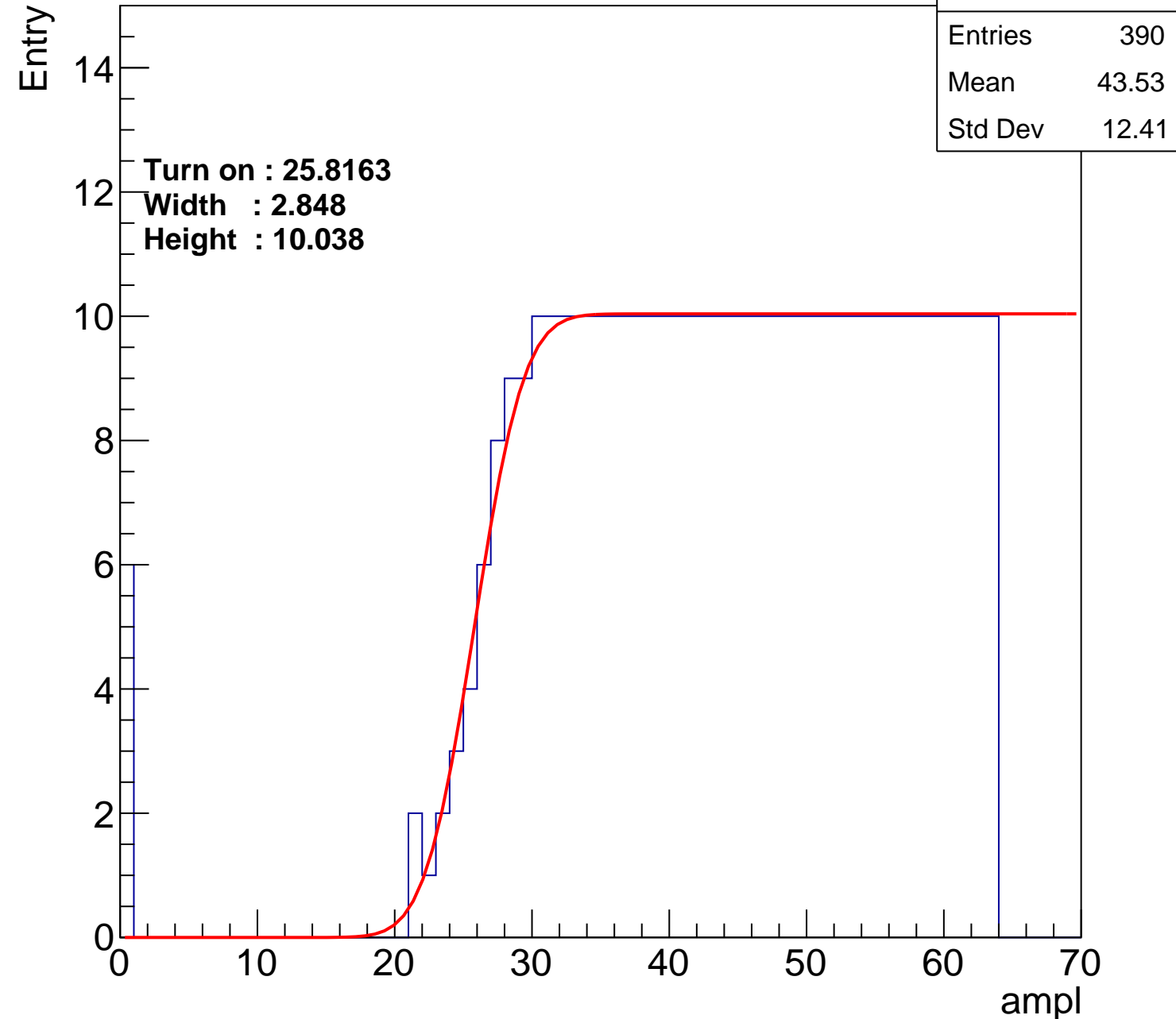
Width : 2.848

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch56

calib_packv5_042523_0143.root, FC#3, port B3

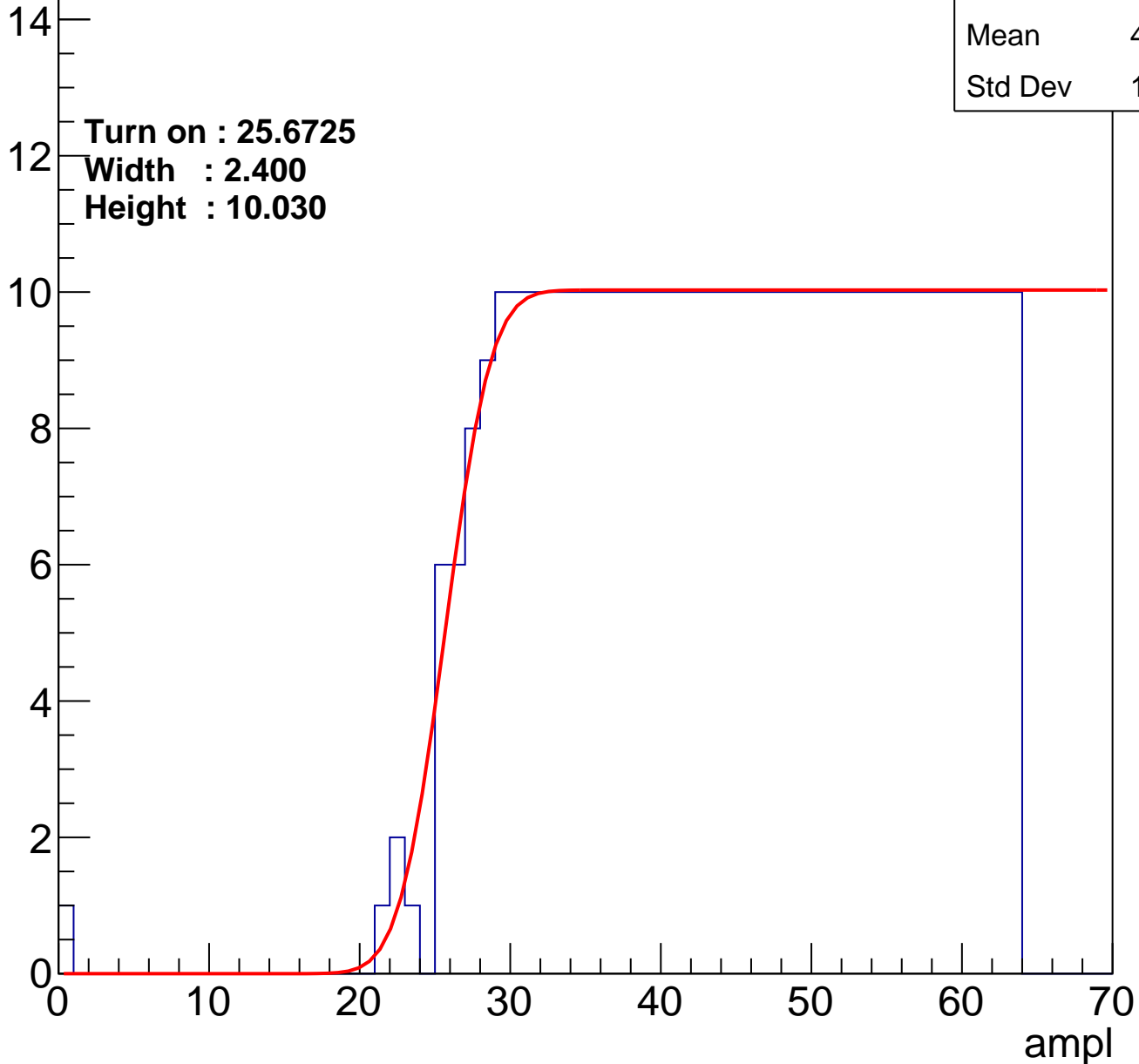
Entries	384
Mean	44.17
Std Dev	11.38

Turn on : 25.6725

Width : 2.400

Height : 10.030

Entry



B1L000S, U10-ch57

calib_packv5_042523_0143.root, FC#3, port B3

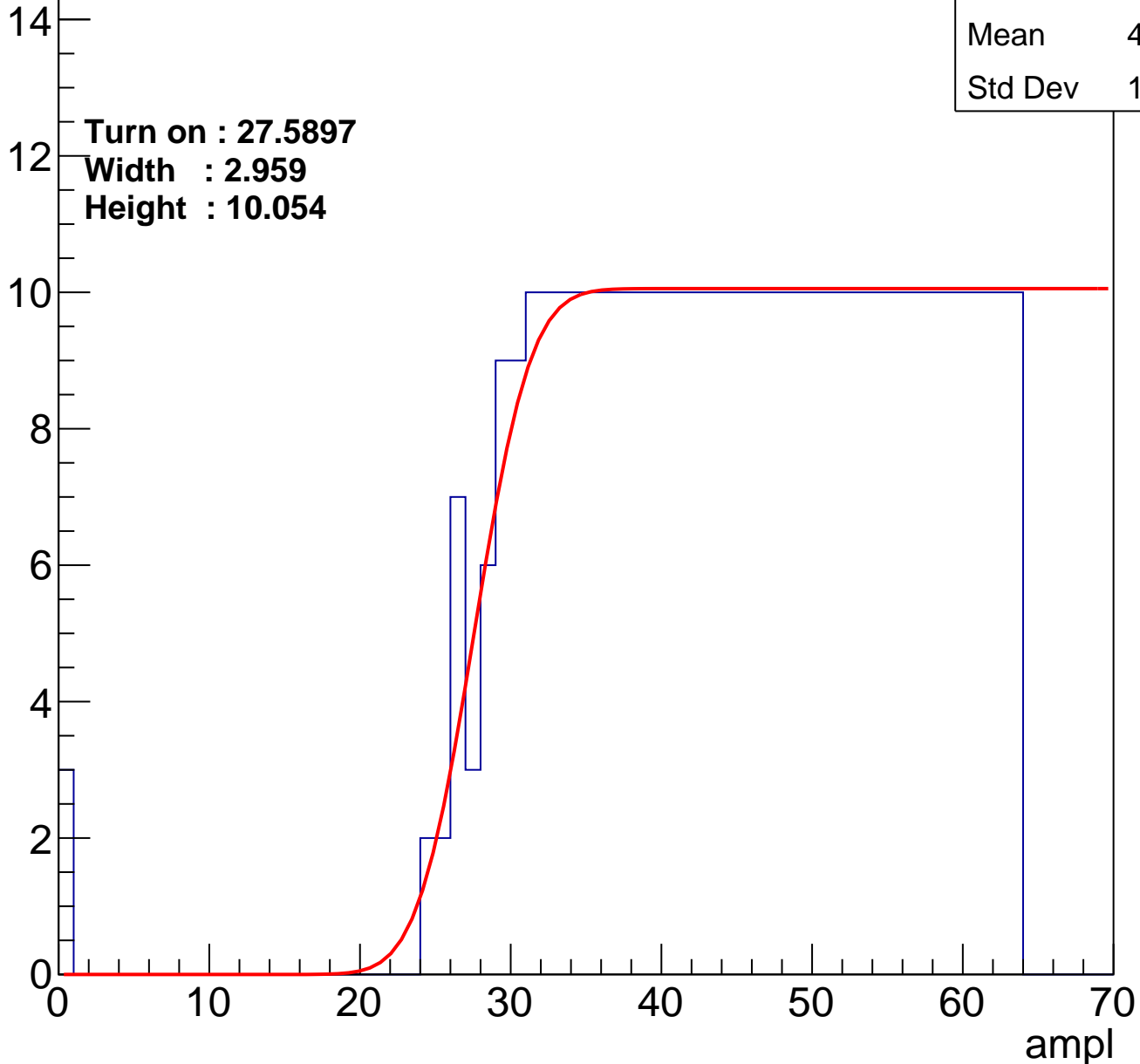
Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 27.5897

Width : 2.959

Height : 10.054

Entry



B1L000S, U10-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 27.0172

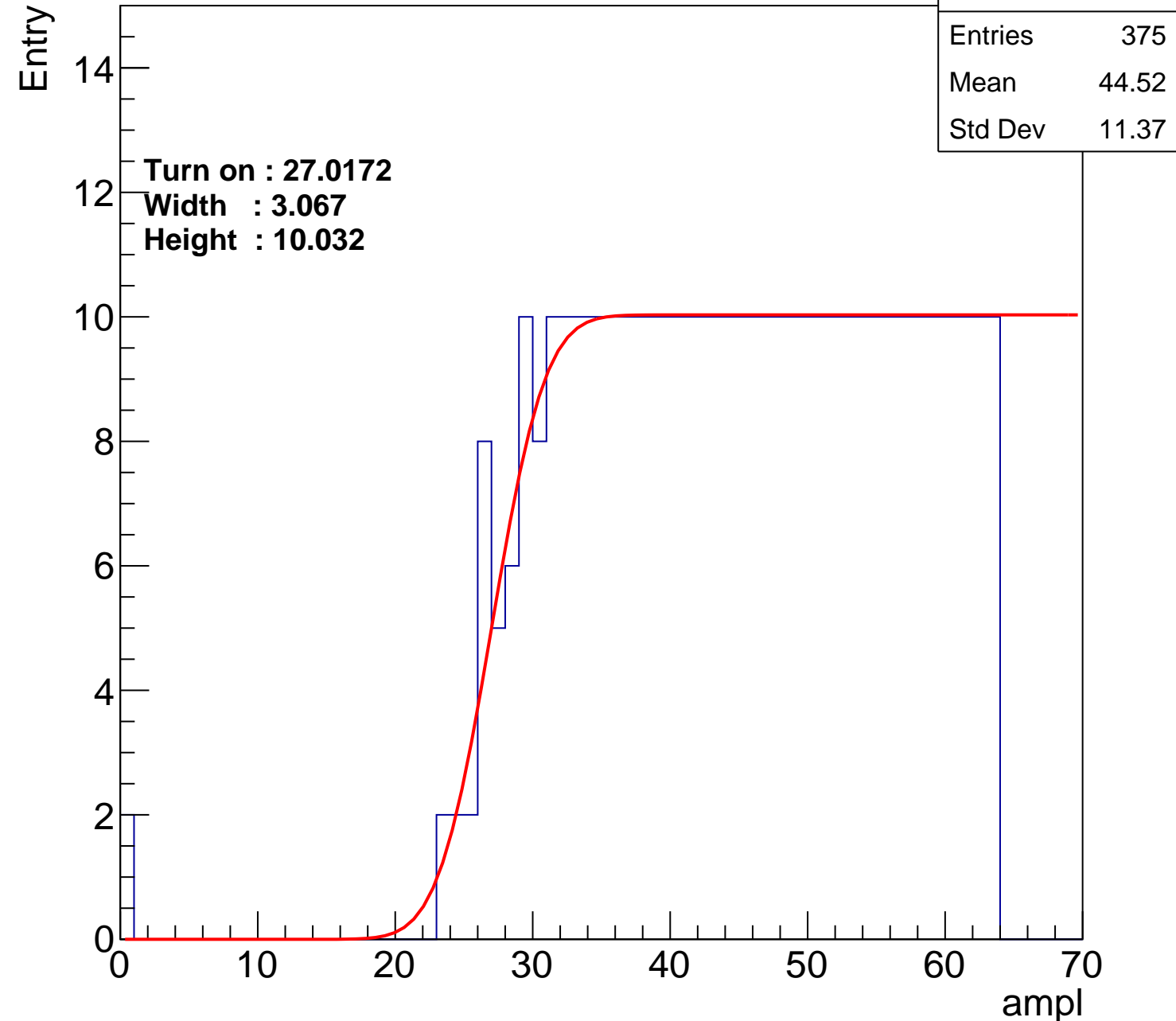
Width : 3.067

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch59

calib_packv5_042523_0143.root, FC#3, port B3

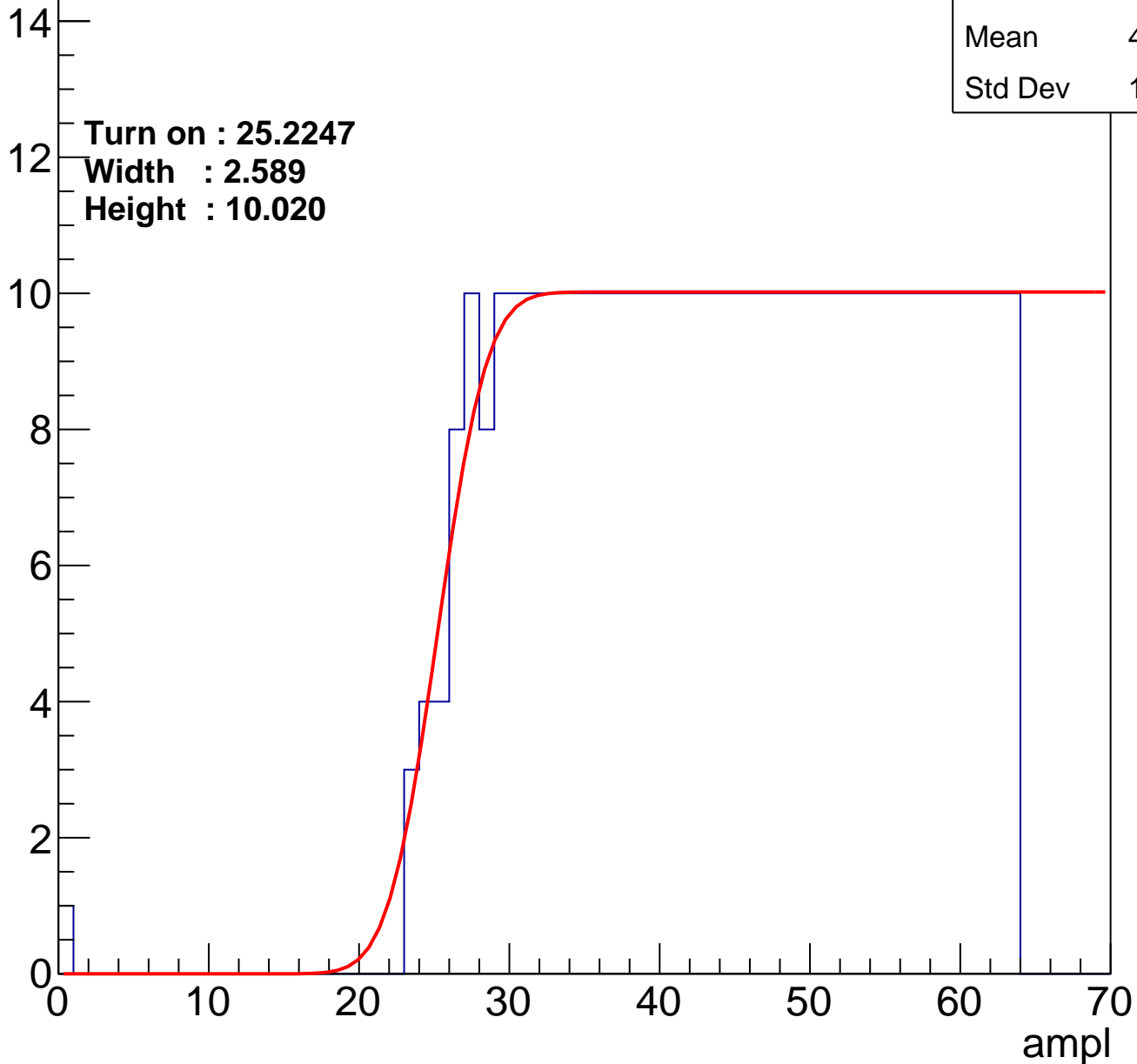
Entries	388
Mean	43.99
Std Dev	11.46

Turn on : 25.2247

Width : 2.589

Height : 10.020

Entry



B1L000S, U10-ch60

calib_packv5_042523_0143.root, FC#3, port B3

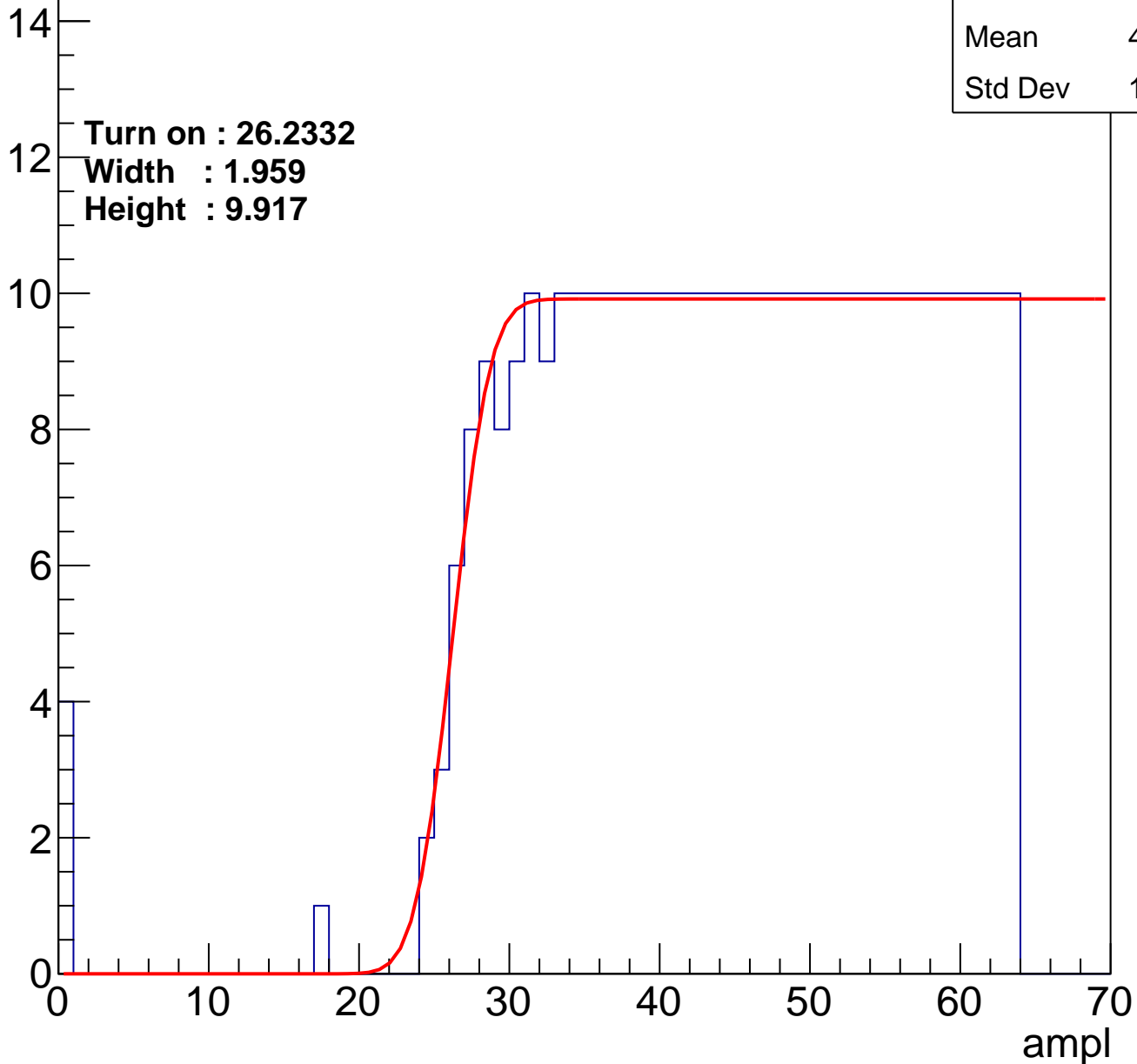
Entries	379
Mean	44.18
Std Dev	11.85

Turn on : 26.2332

Width : 1.959

Height : 9.917

Entry



B1L000S, U10-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.06
Std Dev	11.09

Turn on : 28.2992

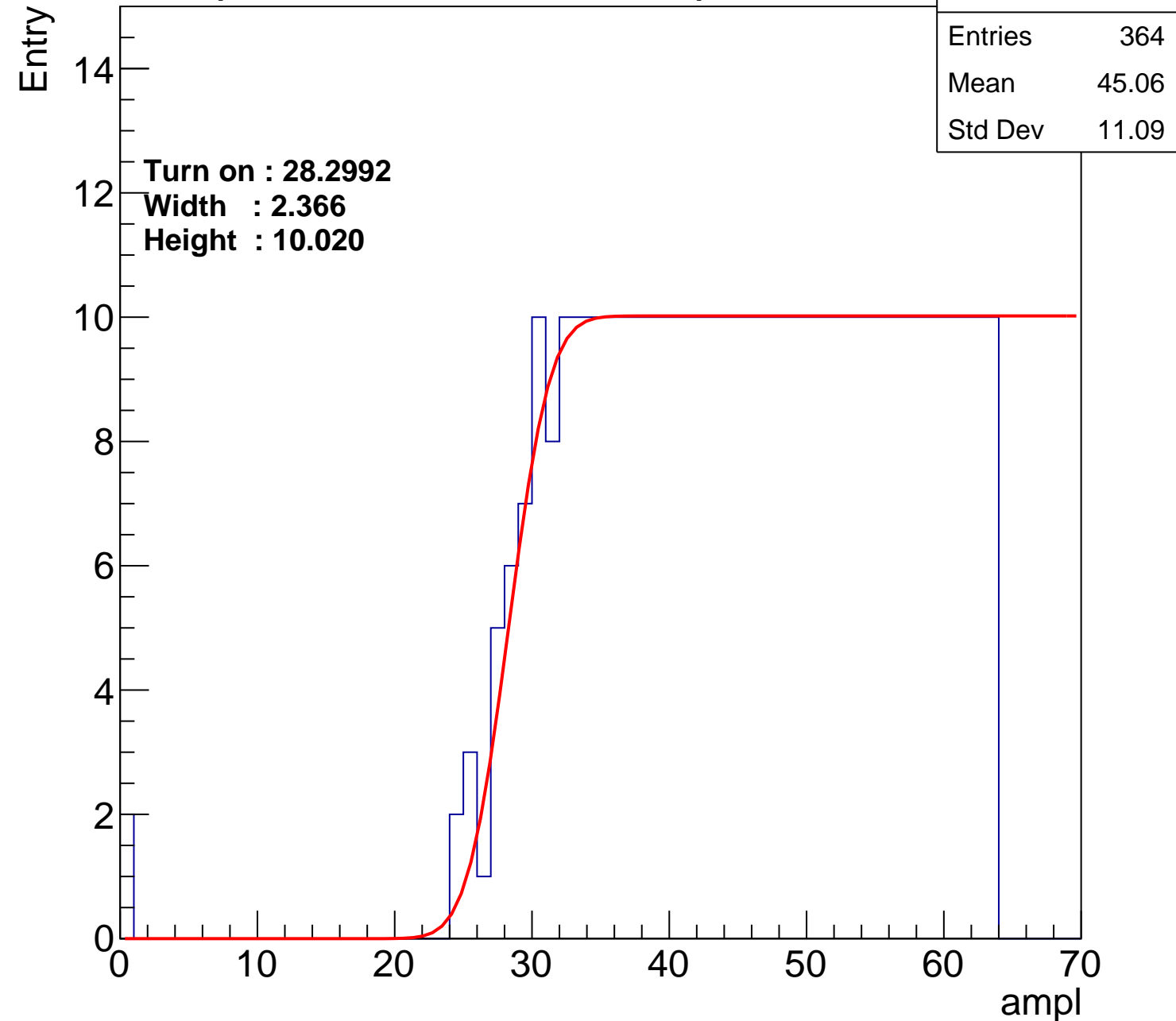
Width : 2.366

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.8276

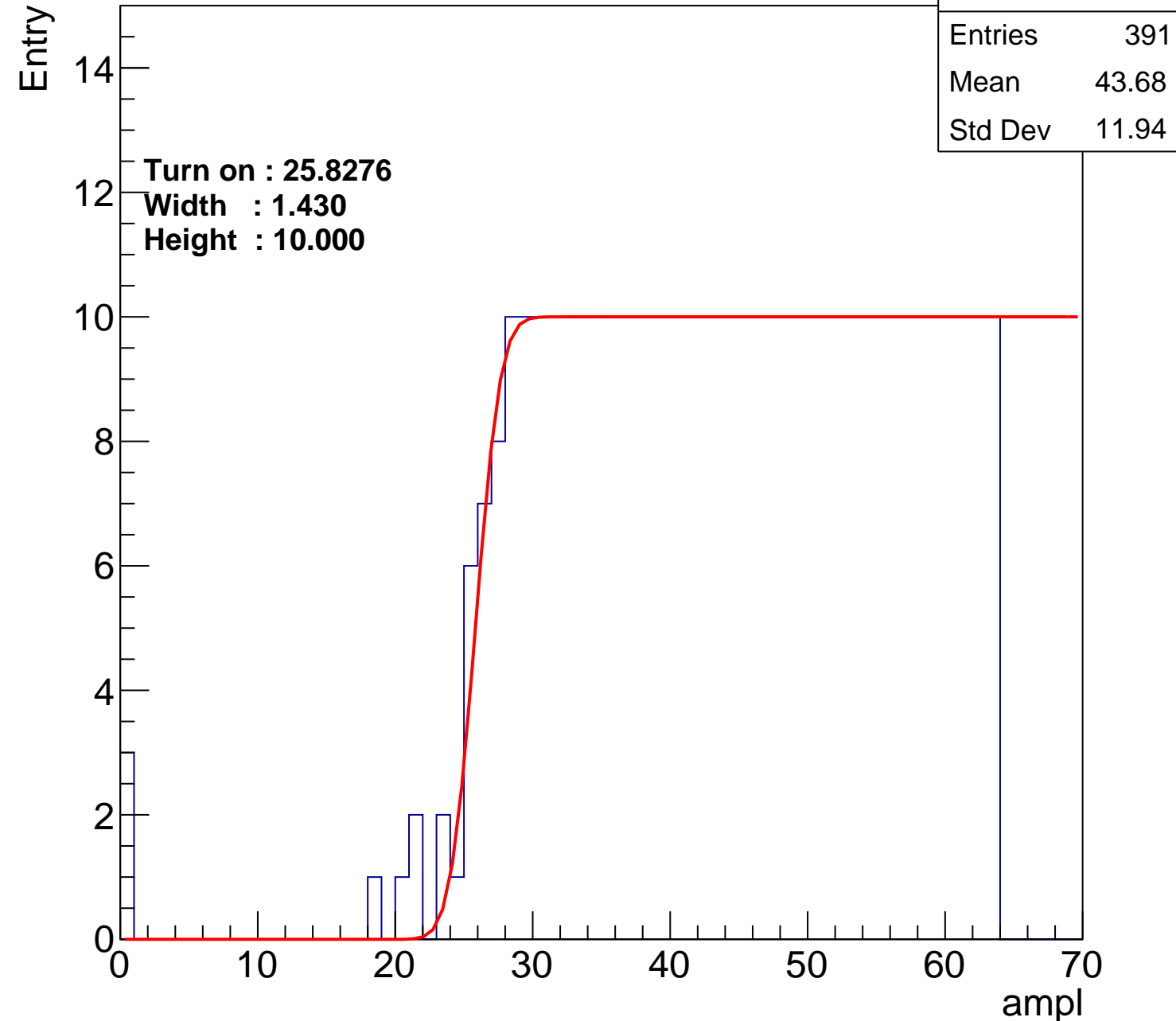
Width : 1.430

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.48
Std Dev	11.26

Turn on : 26.6174

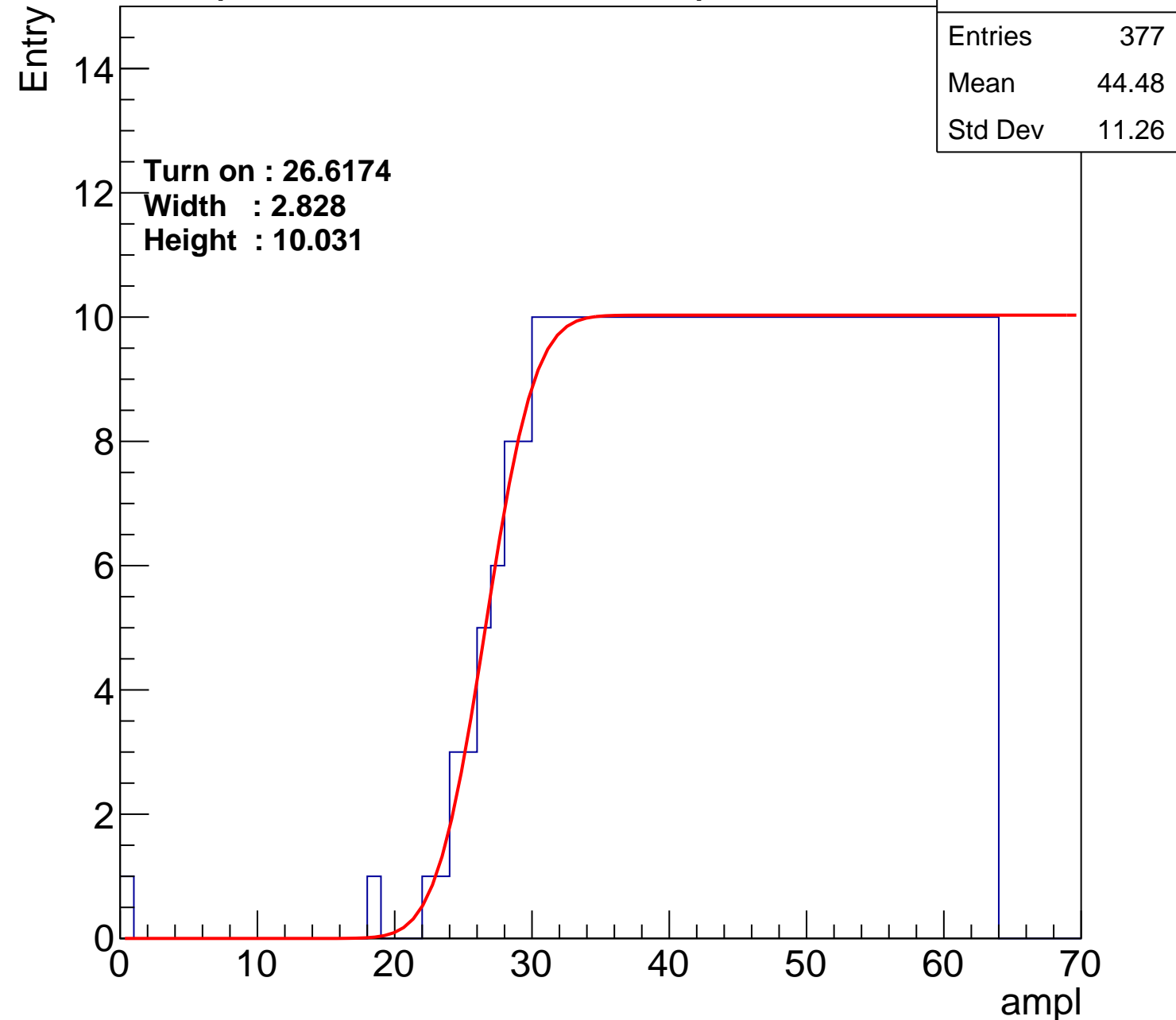
Width : 2.828

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 24.9827

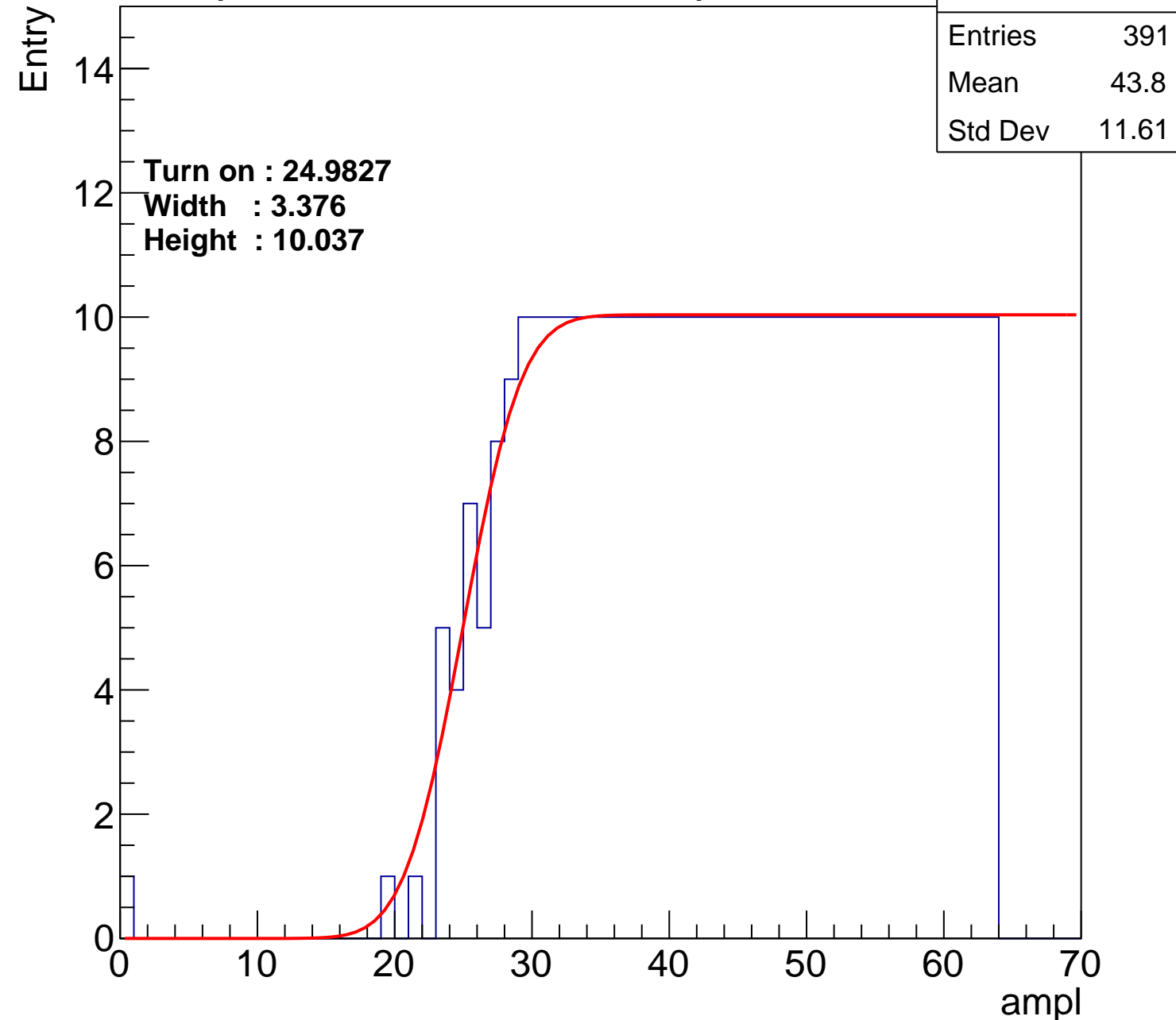
Width : 3.376

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.11
Std Dev	10.86

Turn on : 27.6642

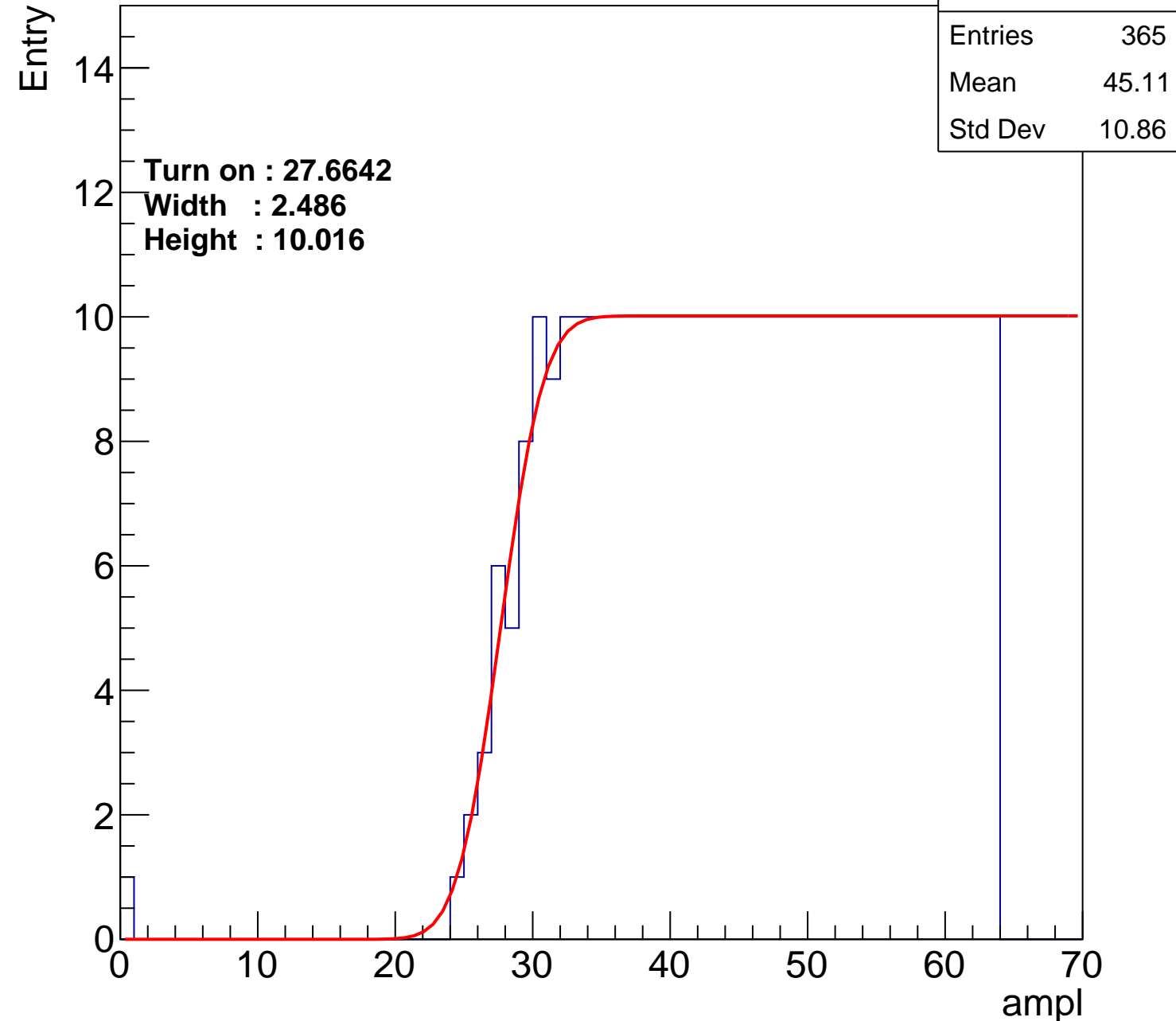
Width : 2.486

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch66

calib_packv5_042523_0143.root, FC#3, port B3

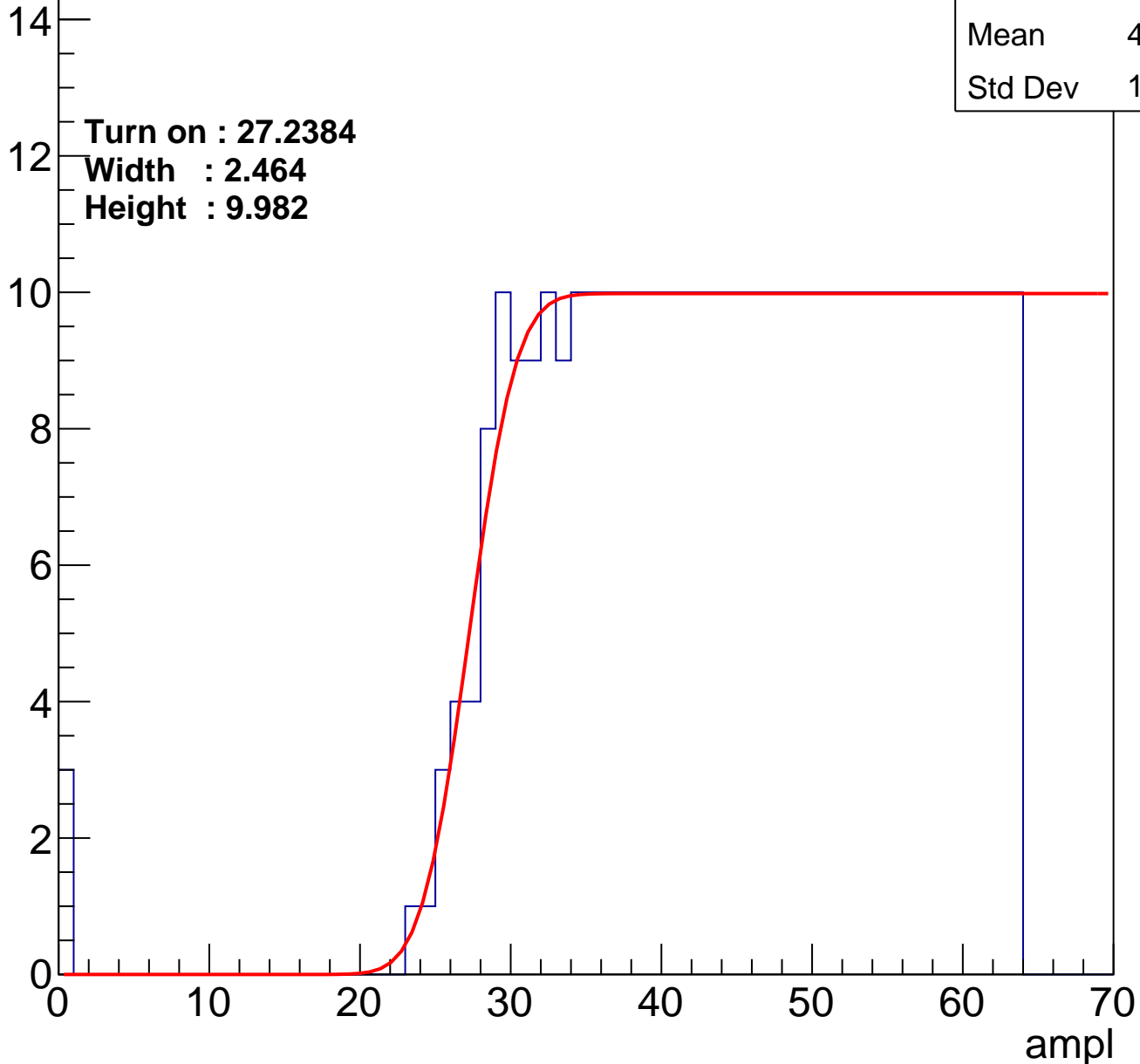
Entries	371
Mean	44.65
Std Dev	11.46

Turn on : 27.2384

Width : 2.464

Height : 9.982

Entry



B1L000S, U10-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.68
Std Dev	11.76

Turn on : 24.5150

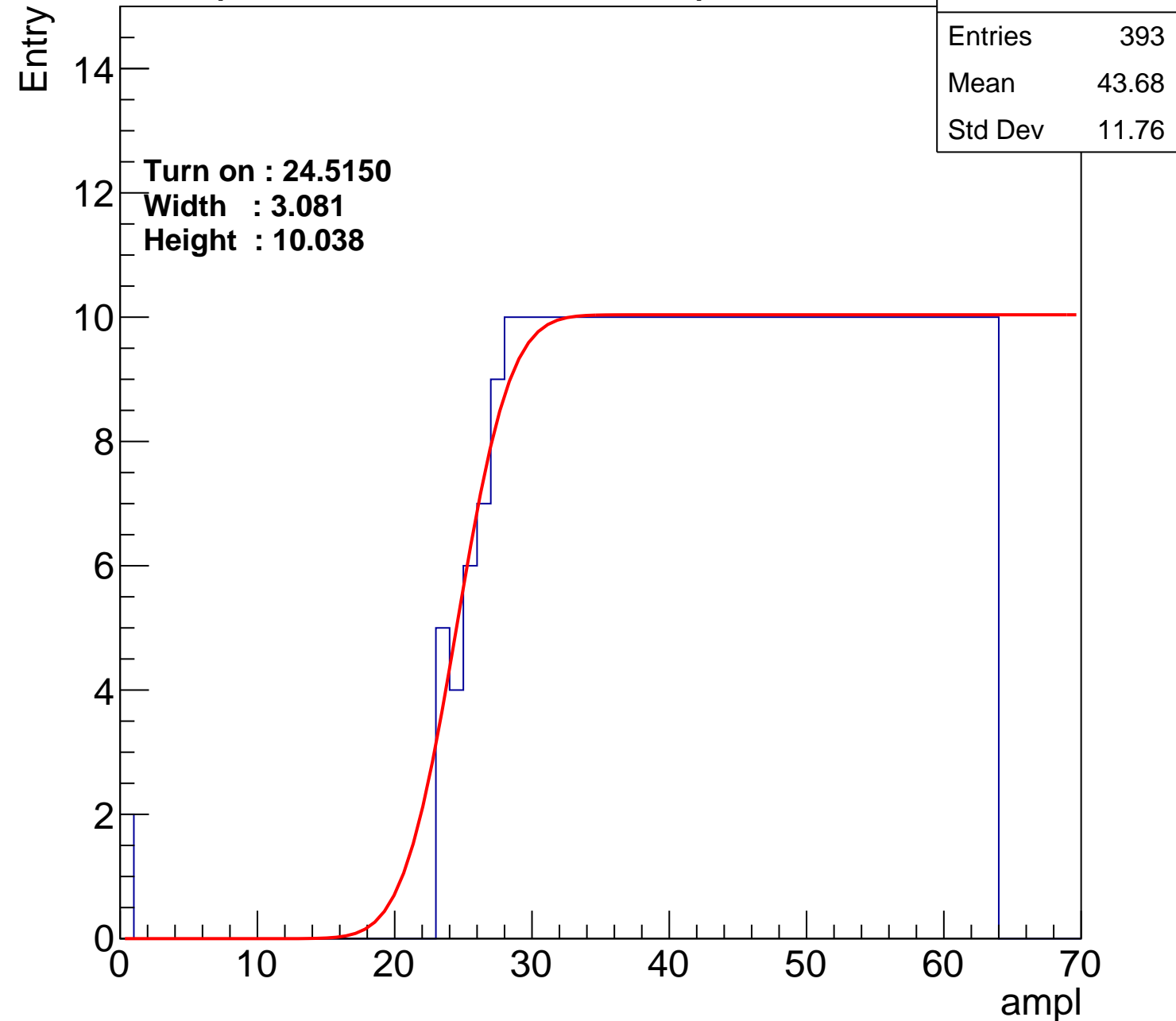
Width : 3.081

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.2384
Width : 3.697
Height : 10.078



B1L000S, U10-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.4236

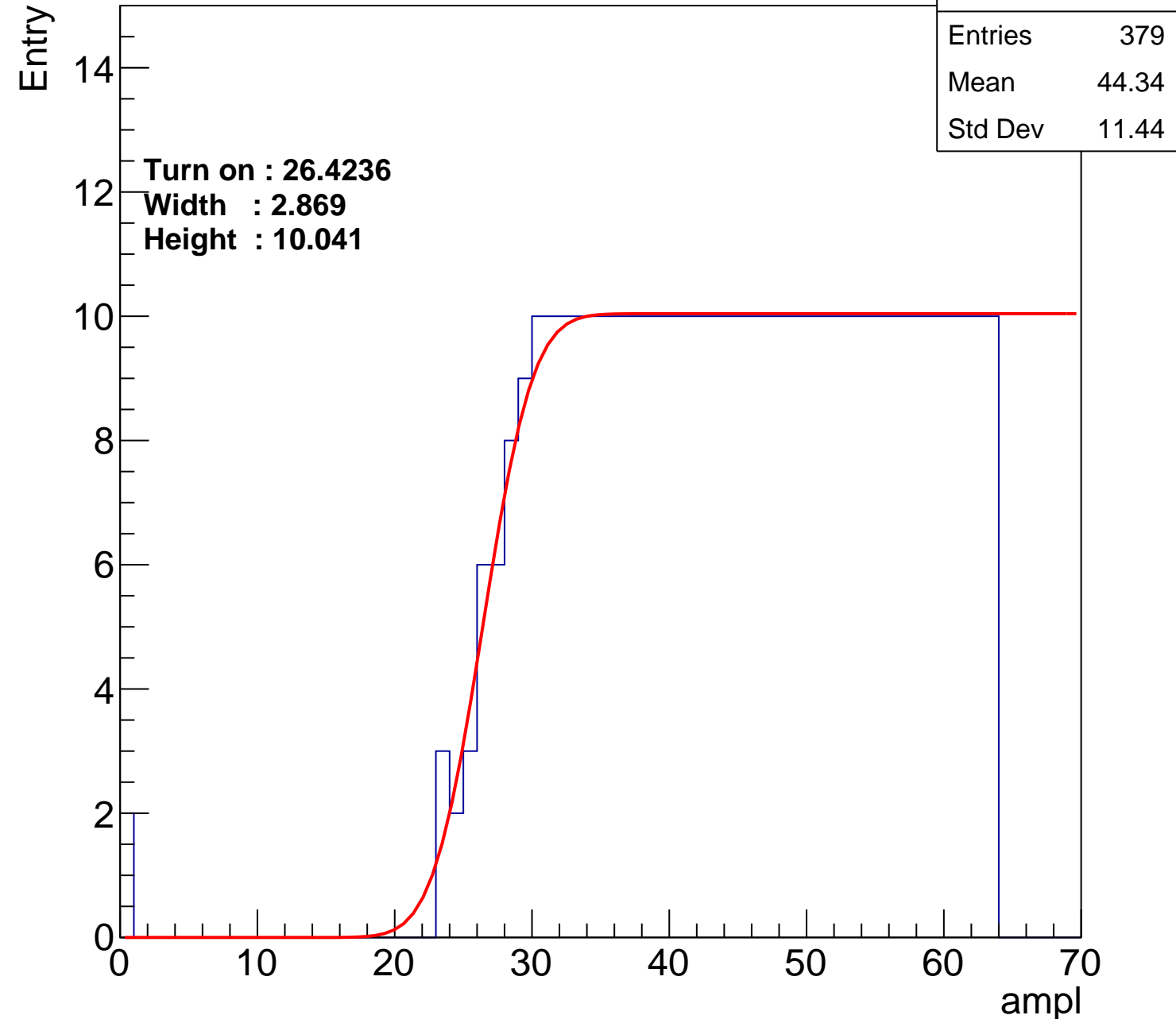
Width : 2.869

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch70

calib_packv5_042523_0143.root, FC#3, port B3

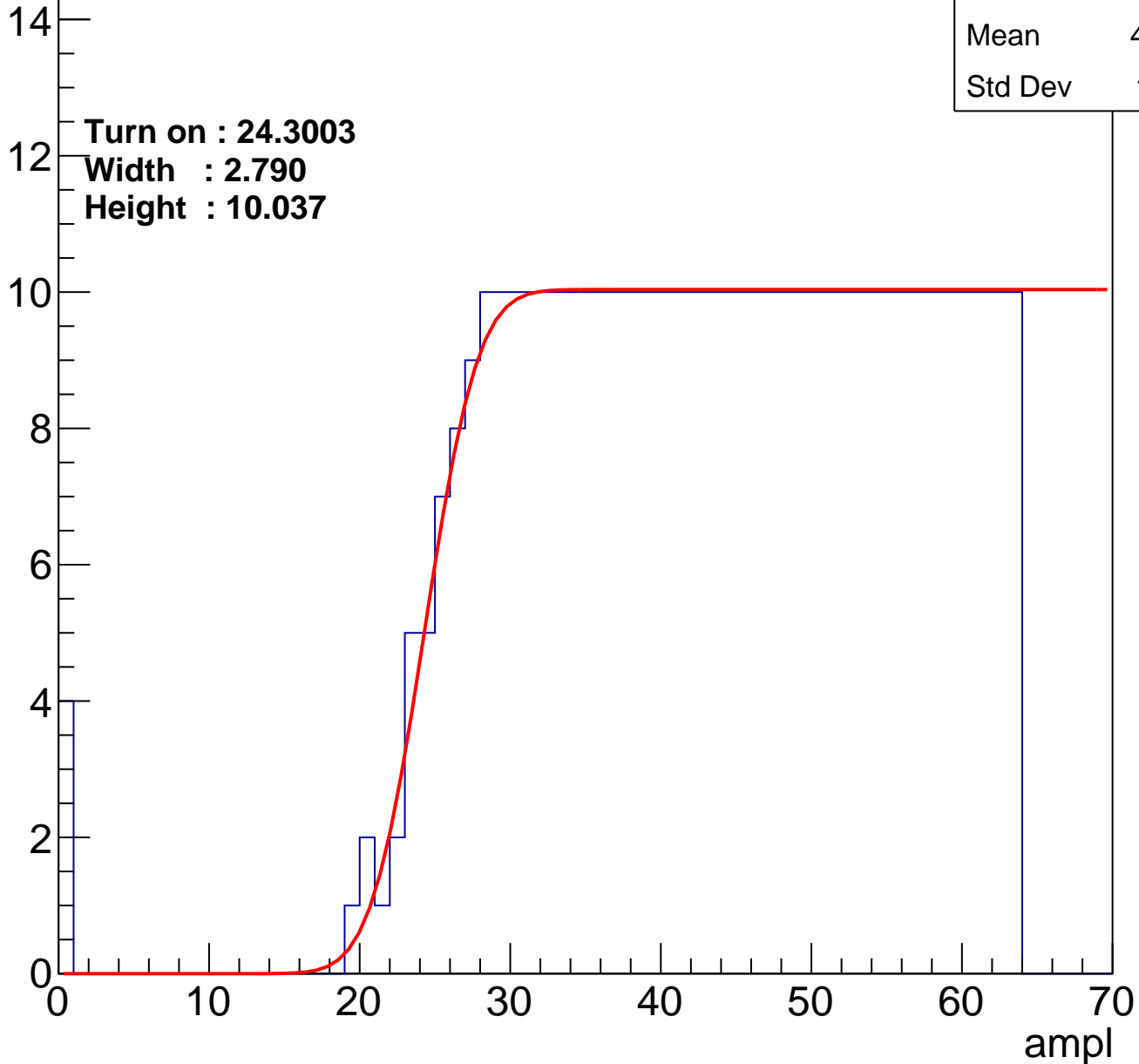
Entries	404
Mean	42.98
Std Dev	12.41

Turn on : 24.3003

Width : 2.790

Height : 10.037

Entry



B1L000S, U10-ch71

calib_packv5_042523_0143.root, FC#3, port B3

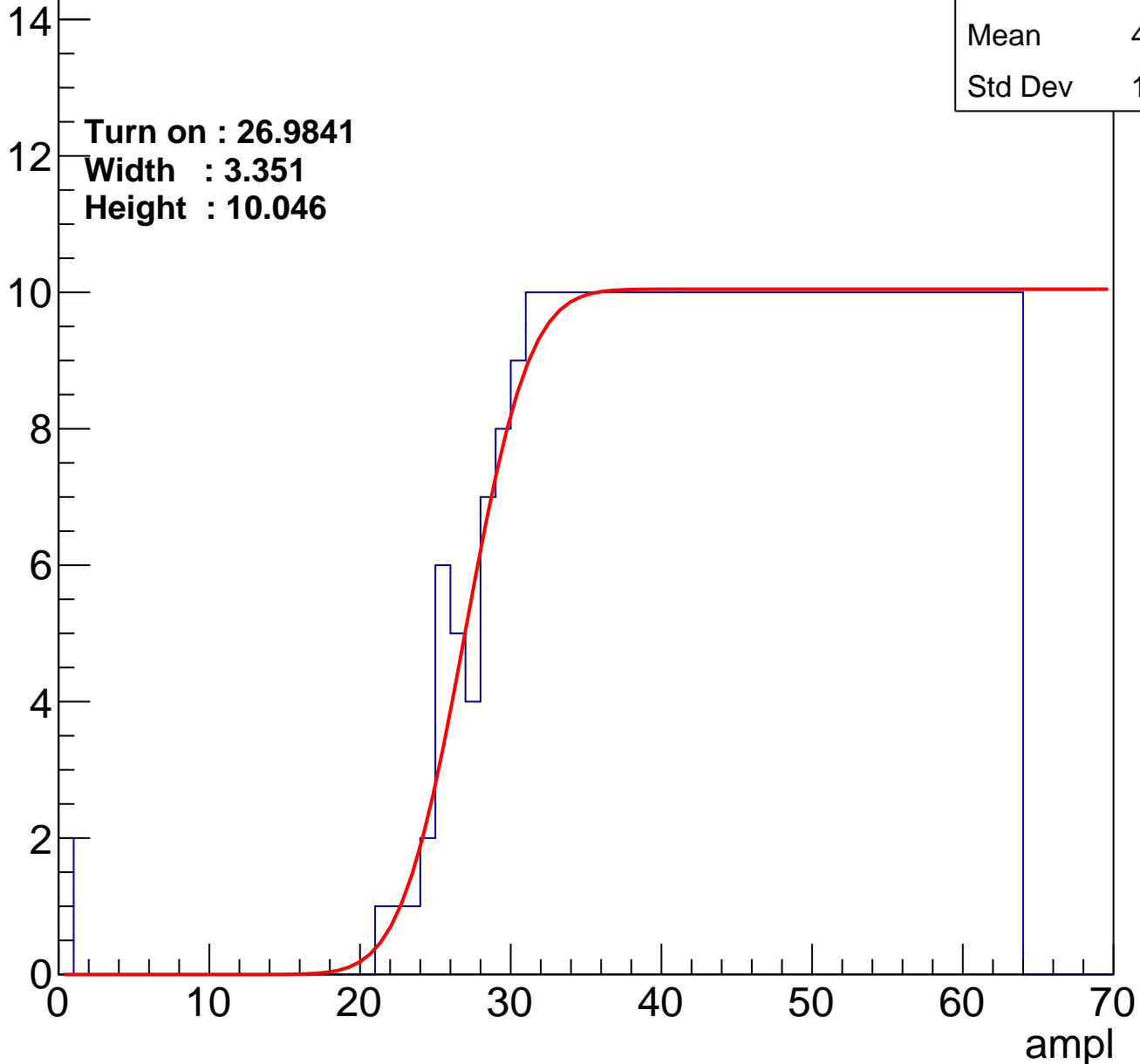
Entries	376
Mean	44.44
Std Dev	11.44

Turn on : 26.9841

Width : 3.351

Height : 10.046

Entry



B1L000S, U10-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.7379

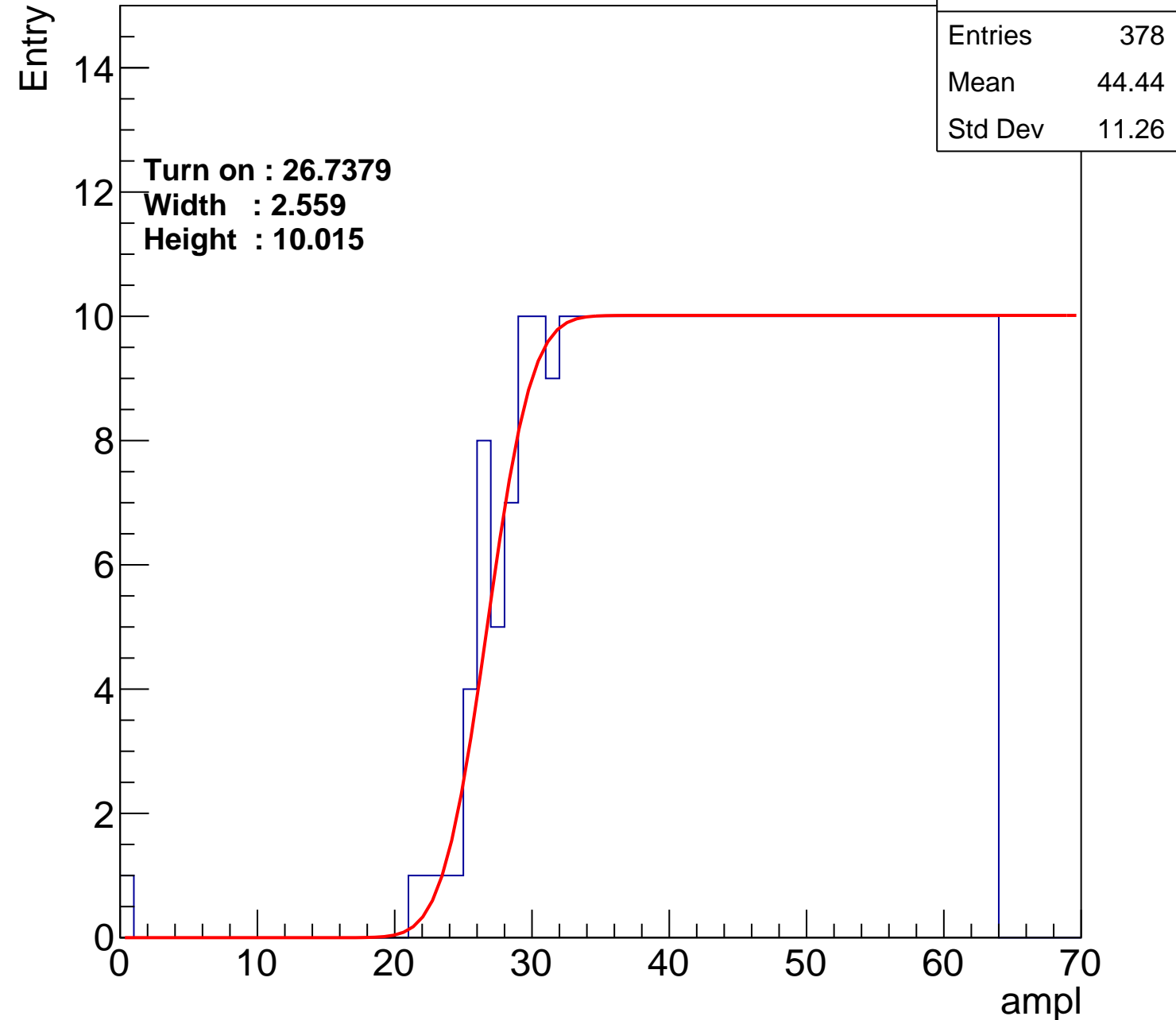
Width : 2.559

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch73

calib_packv5_042523_0143.root, FC#3, port B3

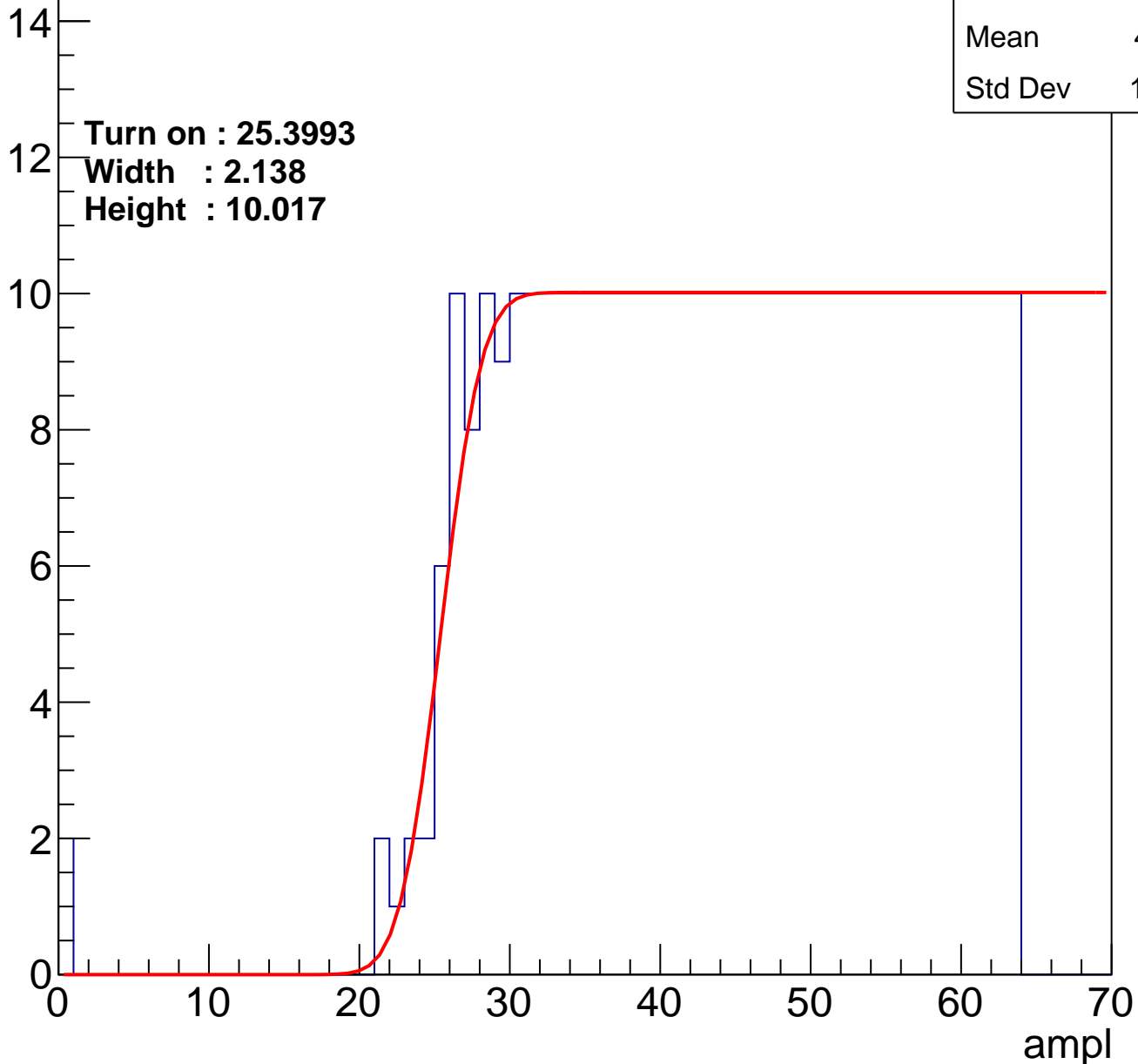
Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 25.3993

Width : 2.138

Height : 10.017

Entry



B1L000S, U10-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.96
Std Dev	11.19

Turn on : 28.0624

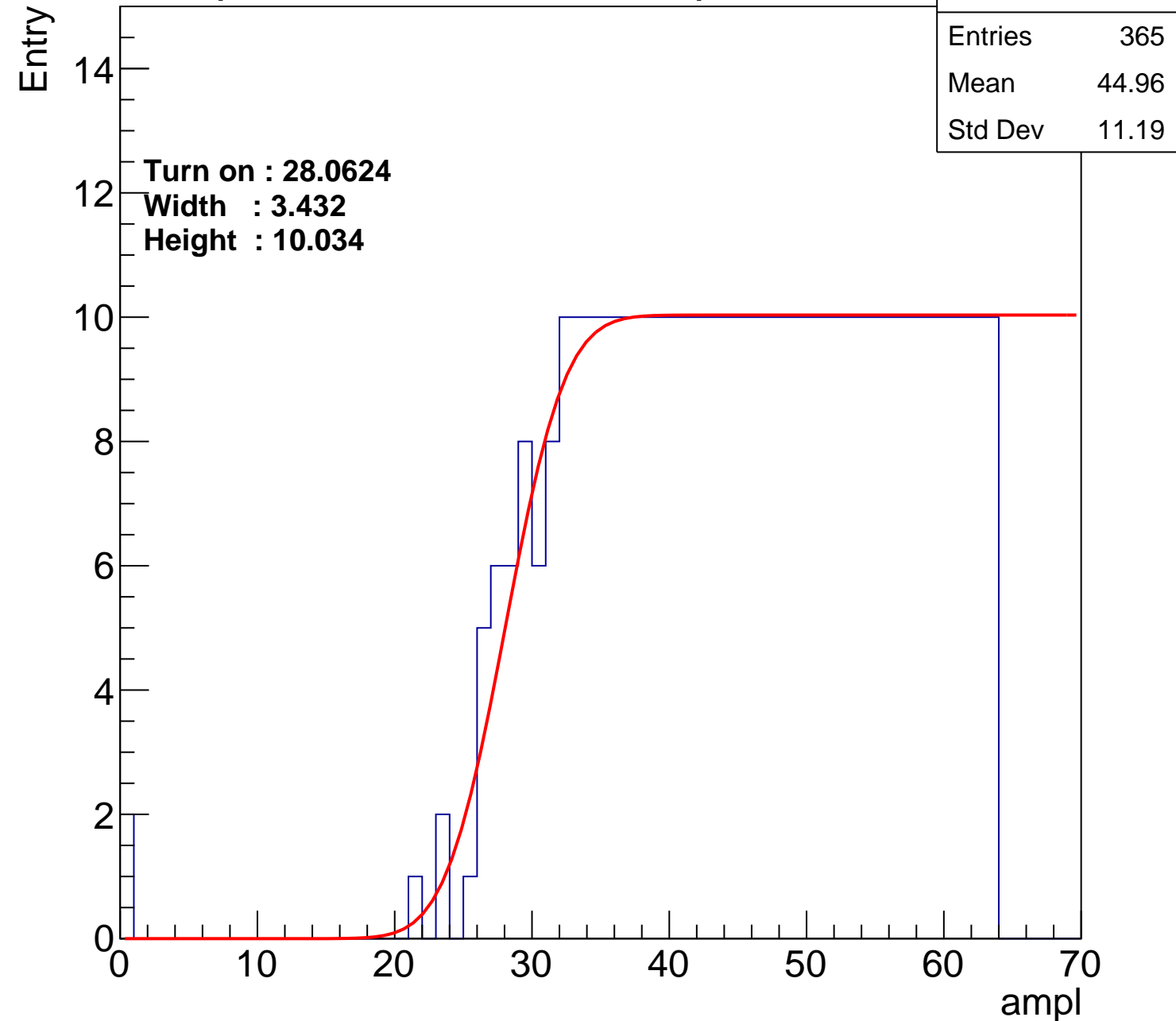
Width : 3.432

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.44
Std Dev	11.71

Turn on : 26.9497

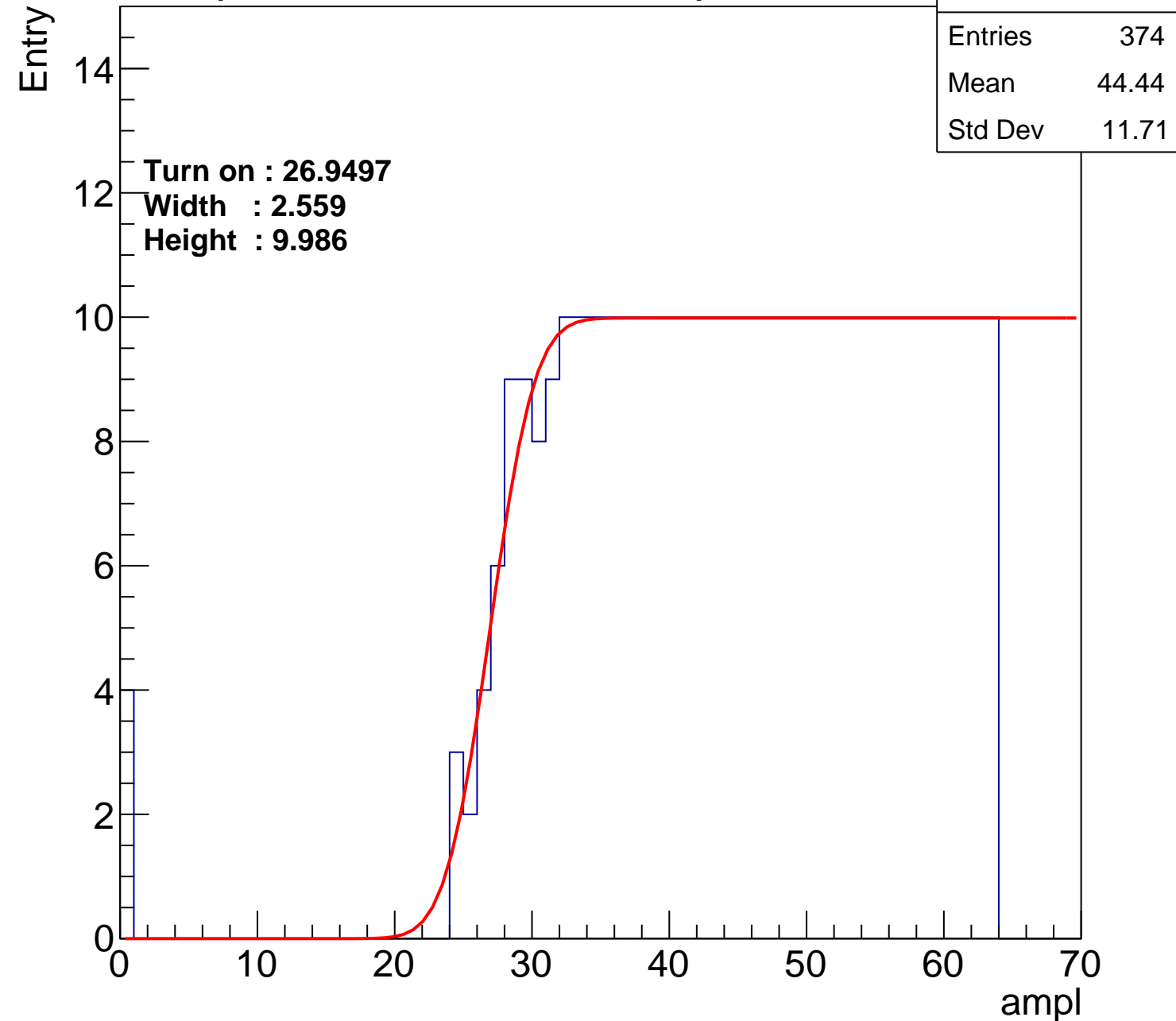
Width : 2.559

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch76

calib_packv5_042523_0143.root, FC#3, port B3

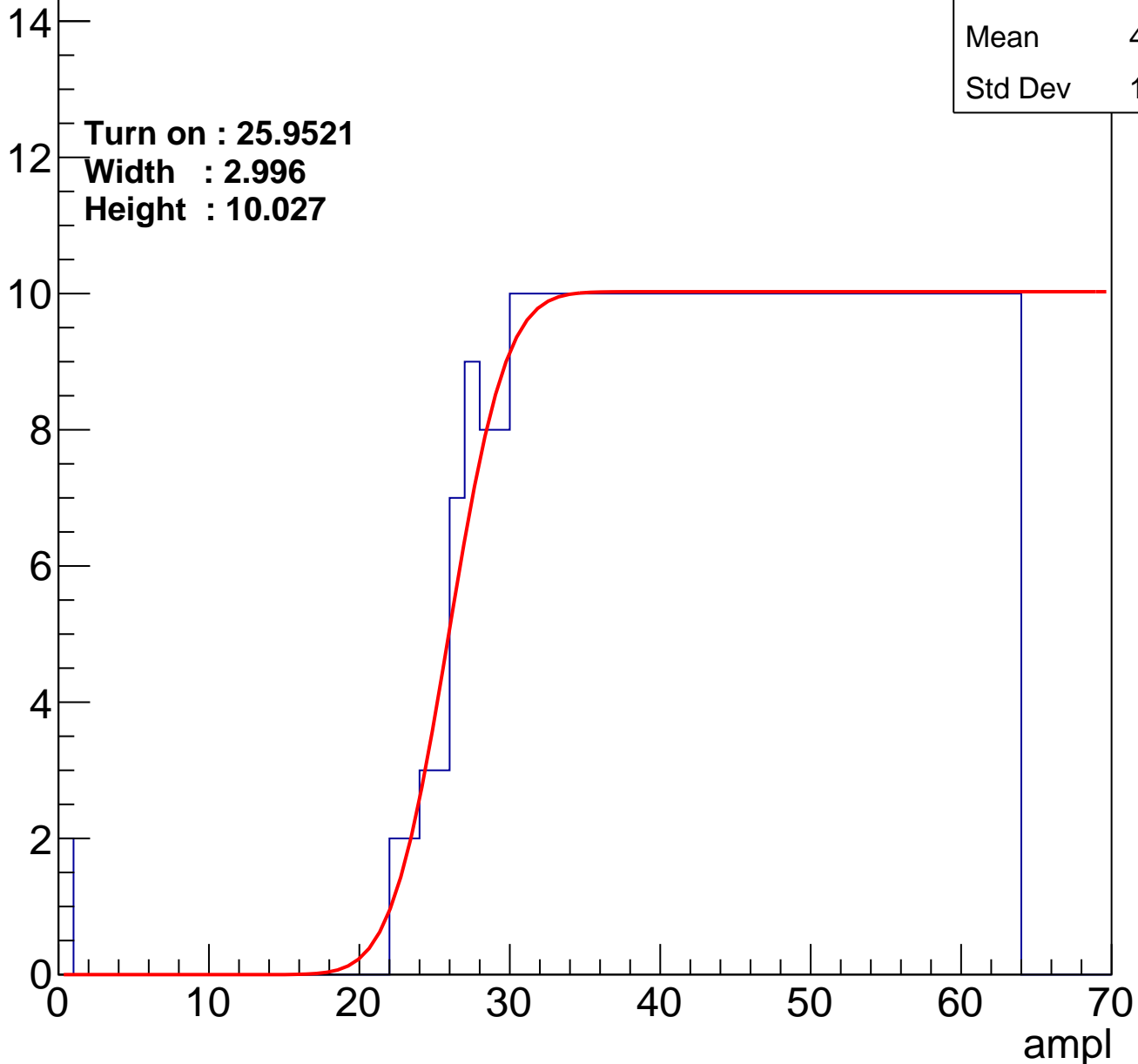
Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.9521

Width : 2.996

Height : 10.027

Entry



B1L000S, U10-ch77

calib_packv5_042523_0143.root, FC#3, port B3

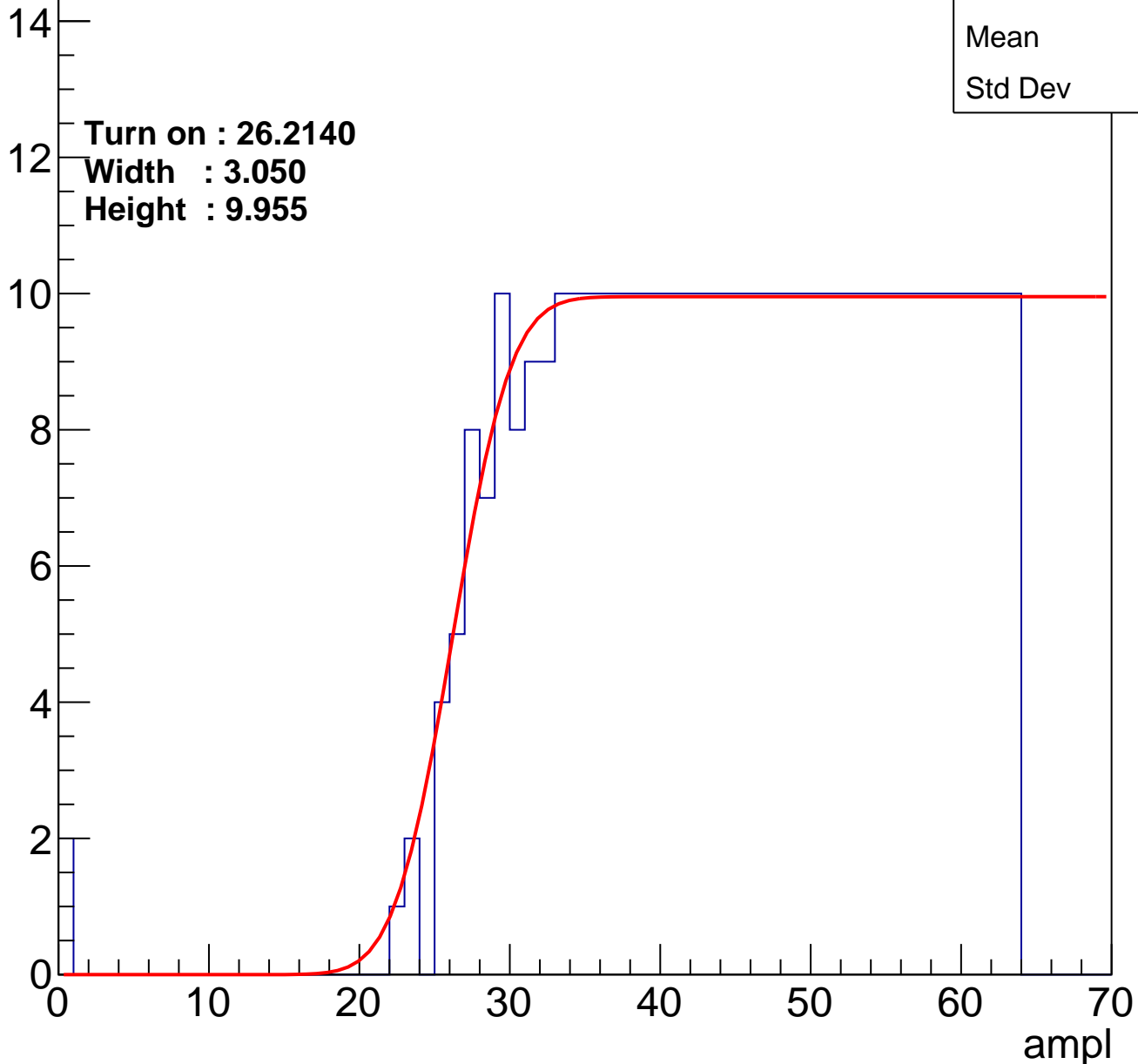
Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.2140

Width : 3.050

Height : 9.955

Entry



B1L000S, U10-ch78

calib_packv5_042523_0143.root, FC#3, port B3

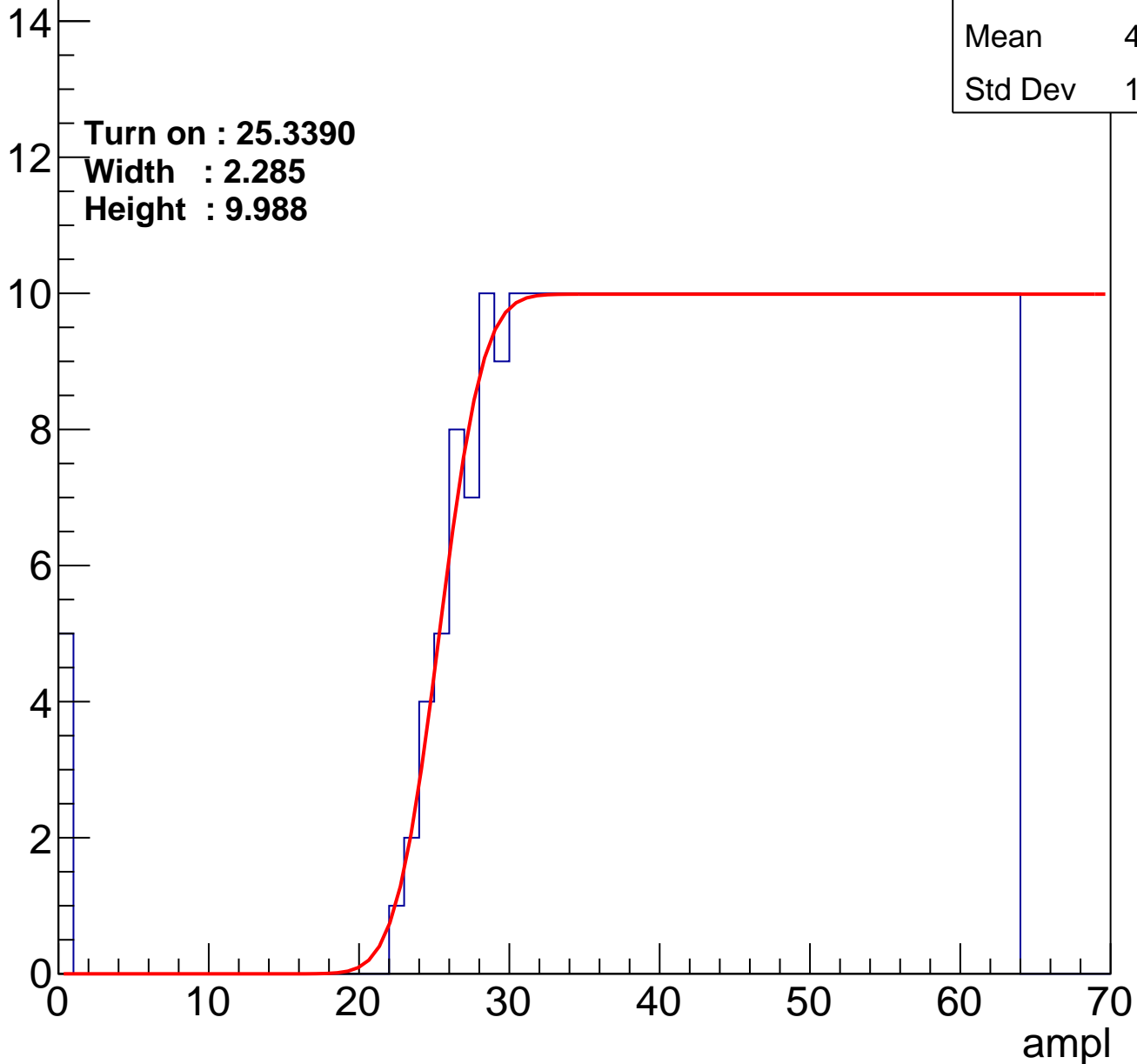
Entries	391
Mean	43.57
Std Dev	12.22

Turn on : 25.3390

Width : 2.285

Height : 9.988

Entry



B1L000S, U10-ch79

calib_packv5_042523_0143.root, FC#3, port B3

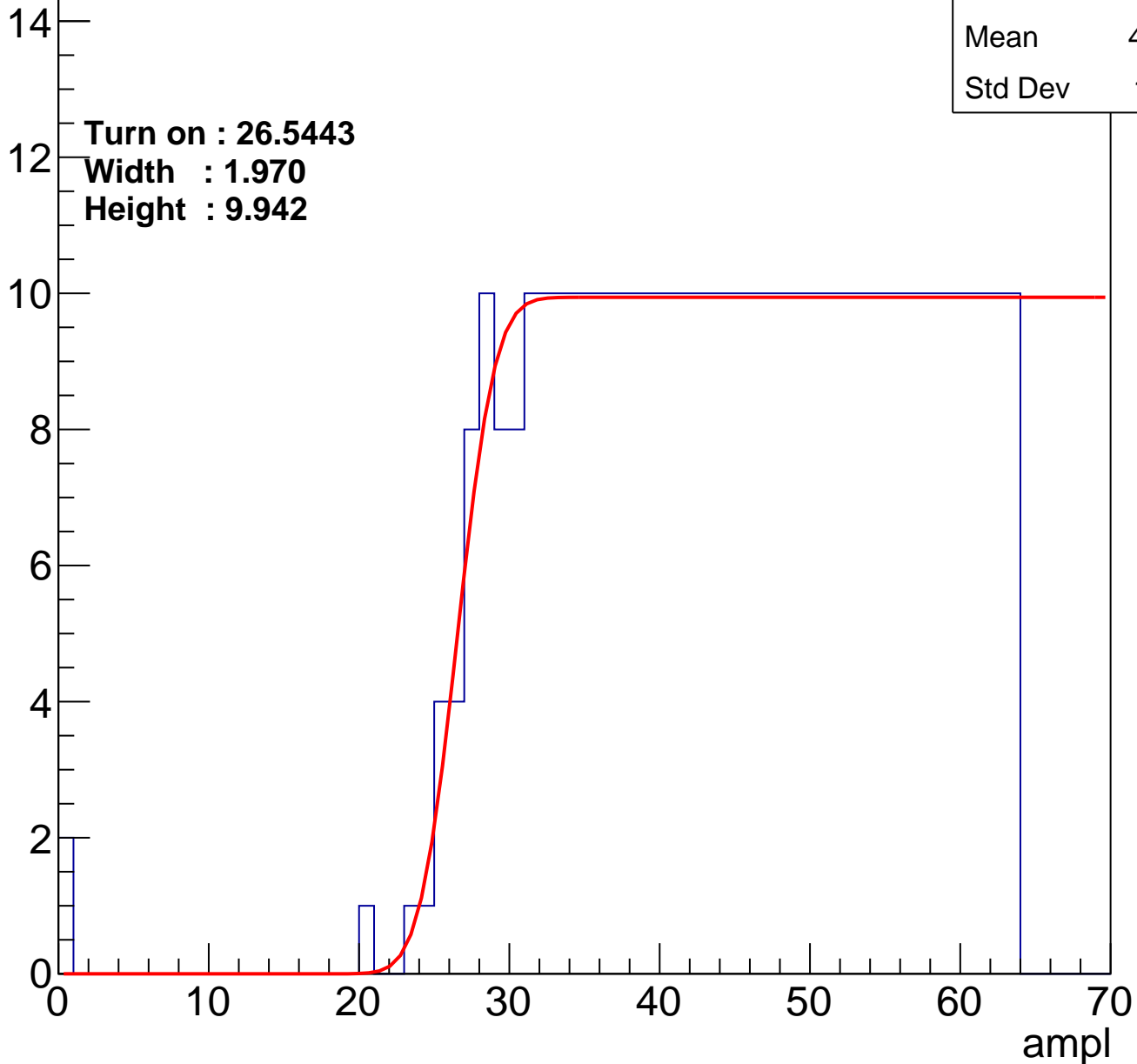
Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 26.5443

Width : 1.970

Height : 9.942

Entry



B1L000S, U10-ch80

calib_packv5_042523_0143.root, FC#3, port B3

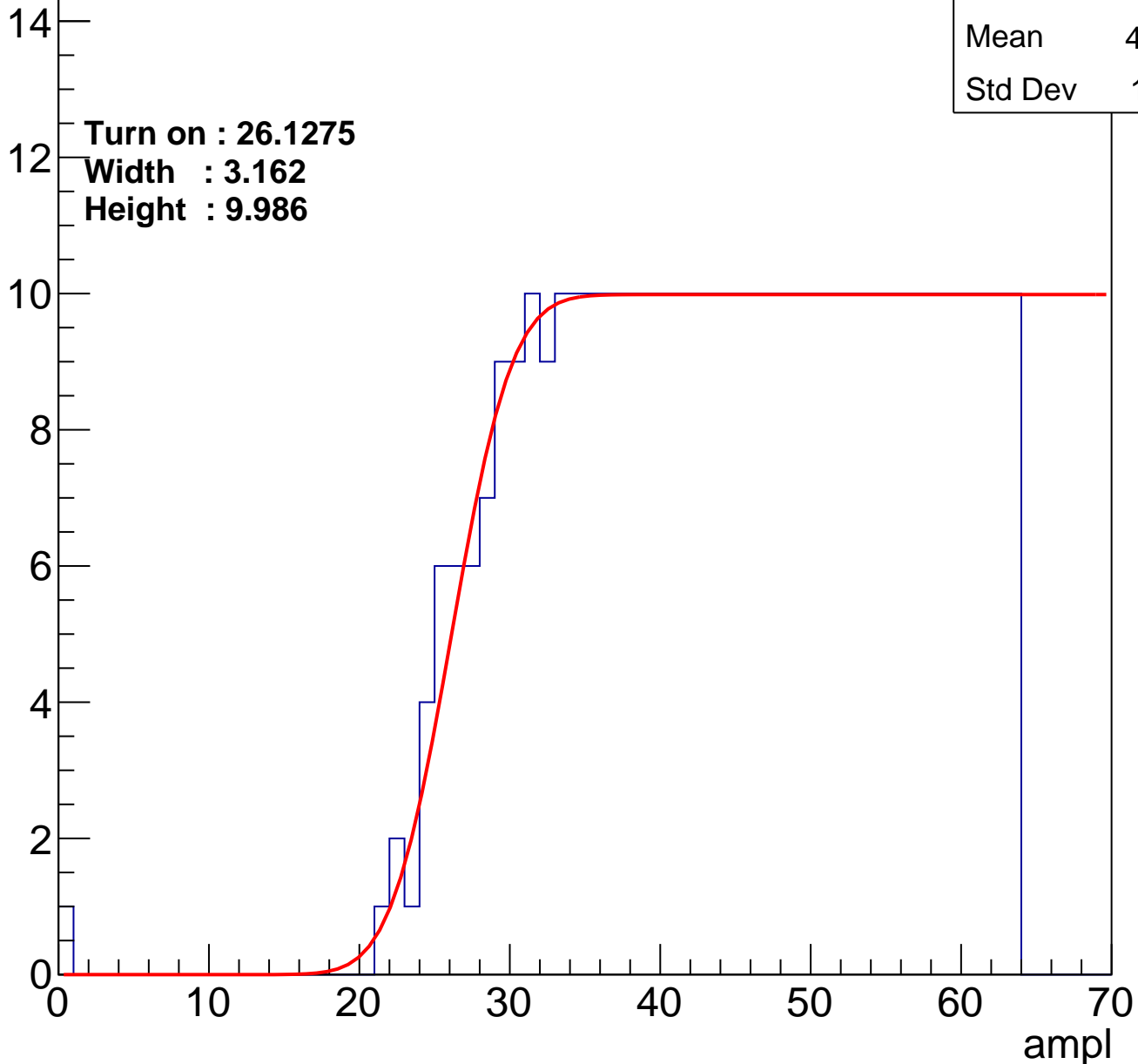
Entries	381
Mean	44.24
Std Dev	11.41

Turn on : 26.1275

Width : 3.162

Height : 9.986

Entry



B1L000S, U10-ch81

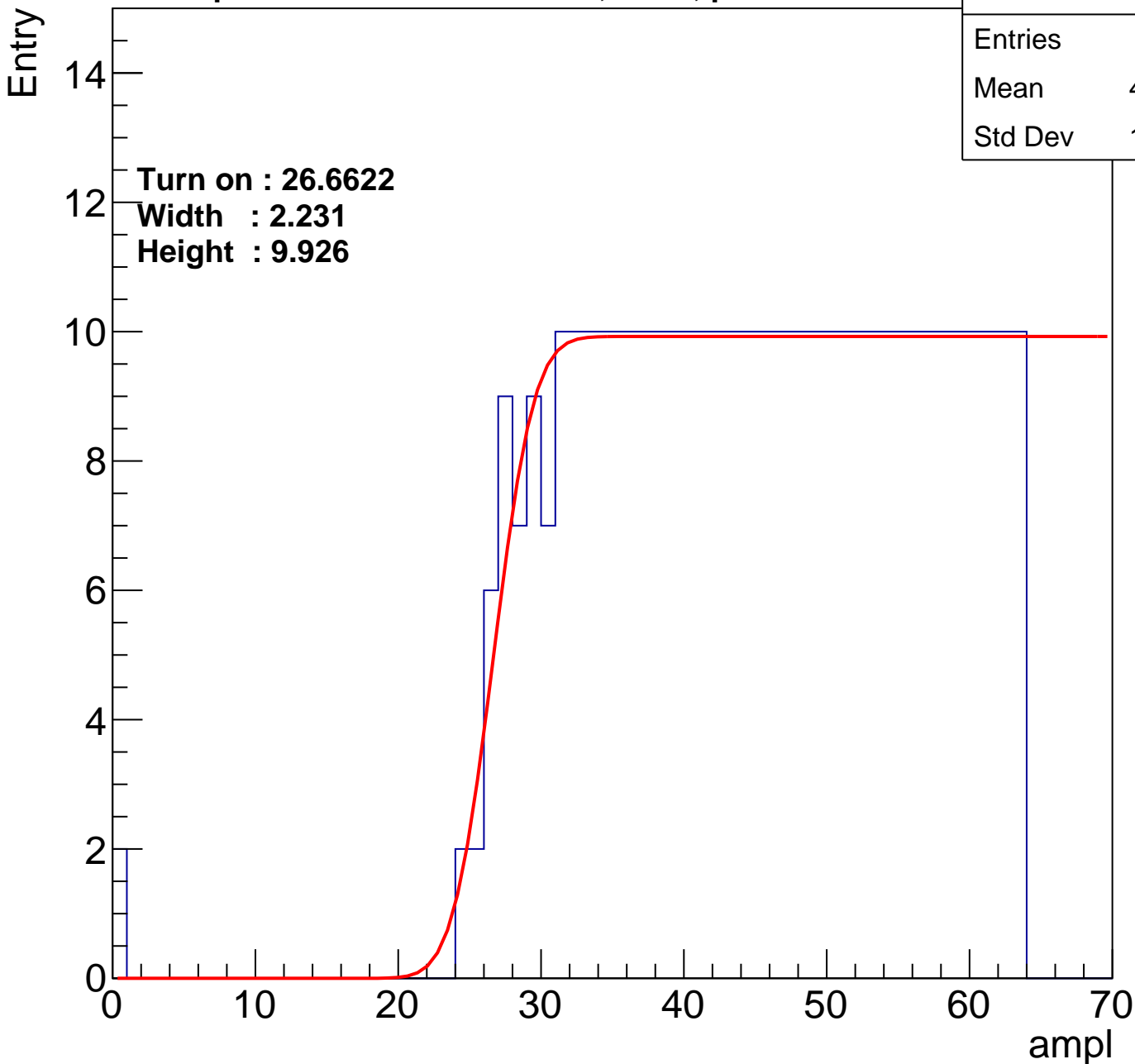
calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 26.6622

Width : 2.231

Height : 9.926



B1L000S, U10-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.93
Std Dev	10.95

Turn on : 27.3076

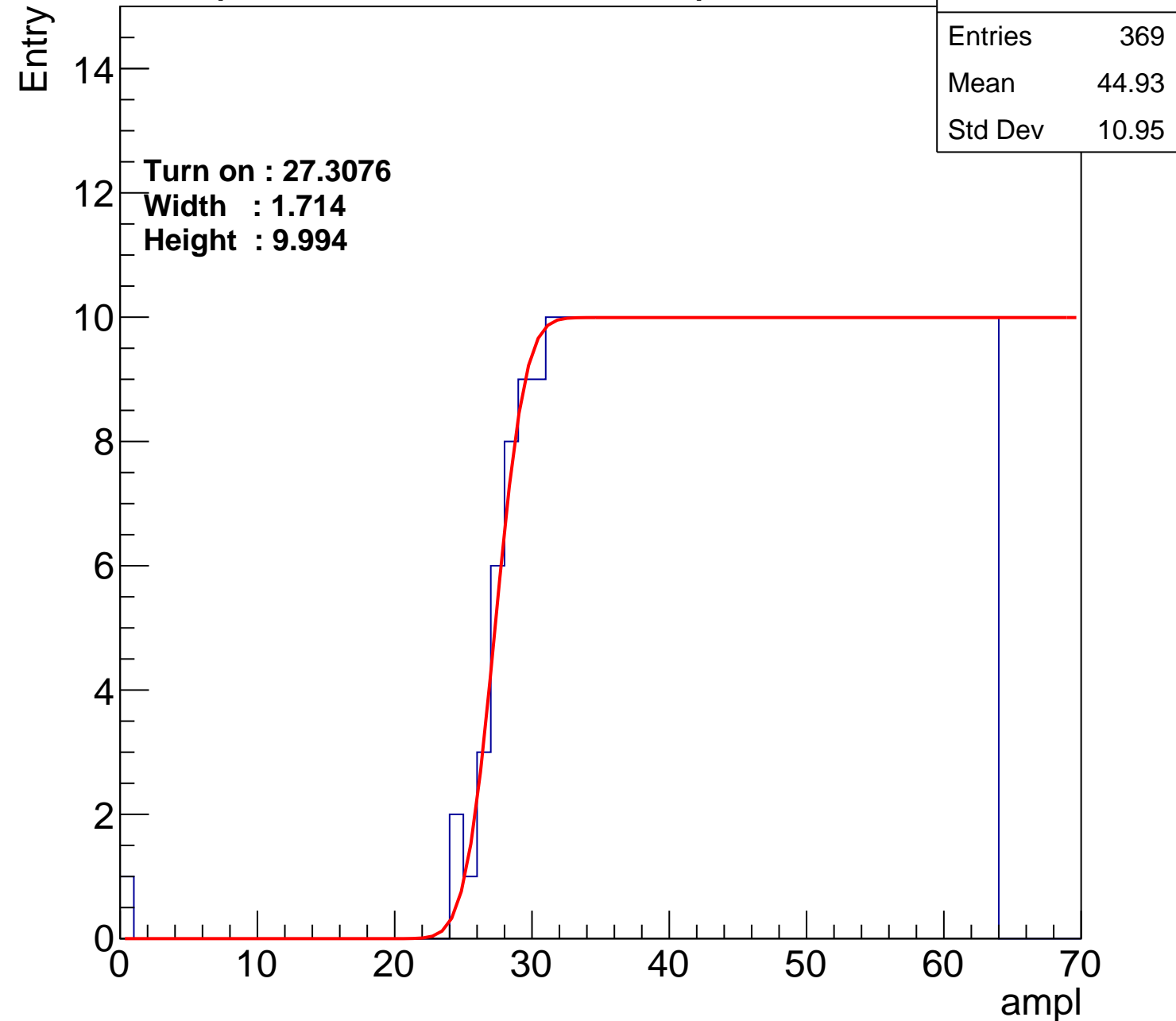
Width : 1.714

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.24
Std Dev	12.27

Turn on : 24.3979

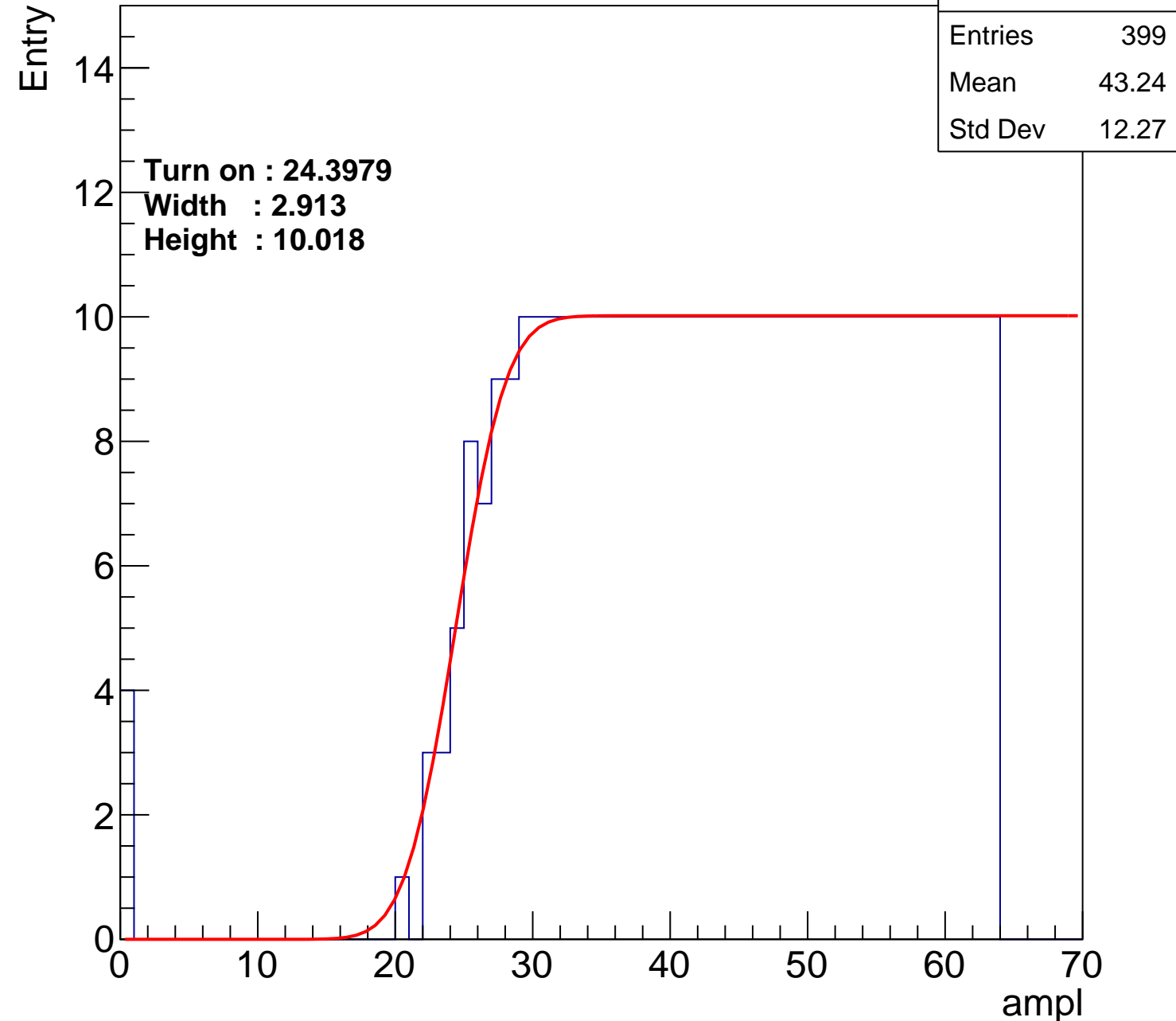
Width : 2.913

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch84

calib_packv5_042523_0143.root, FC#3, port B3

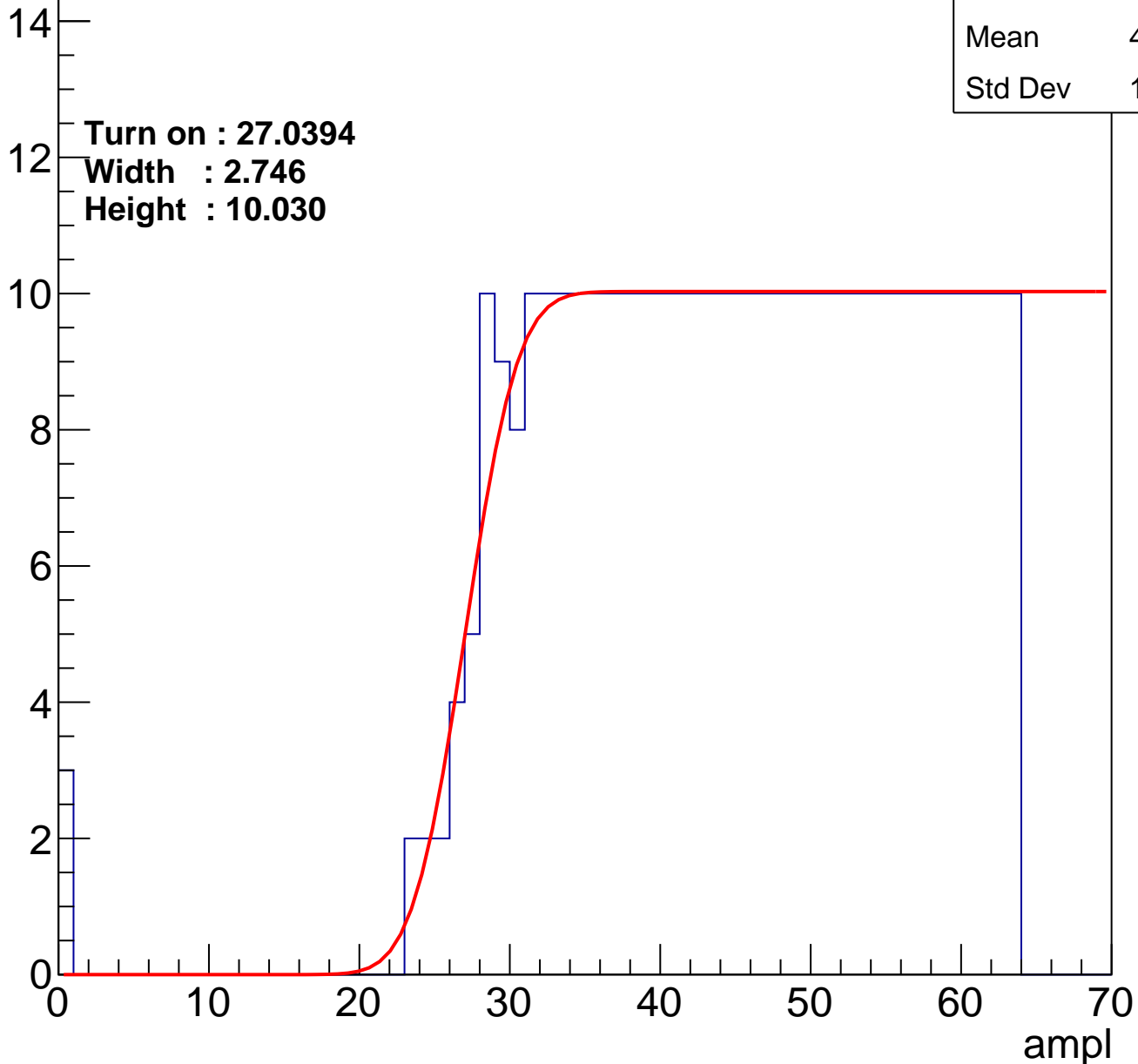
Entries	375
Mean	44.46
Std Dev	11.54

Turn on : 27.0394

Width : 2.746

Height : 10.030

Entry



B1L000S, U10-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.62
Std Dev	11.63

Turn on : 27.5111

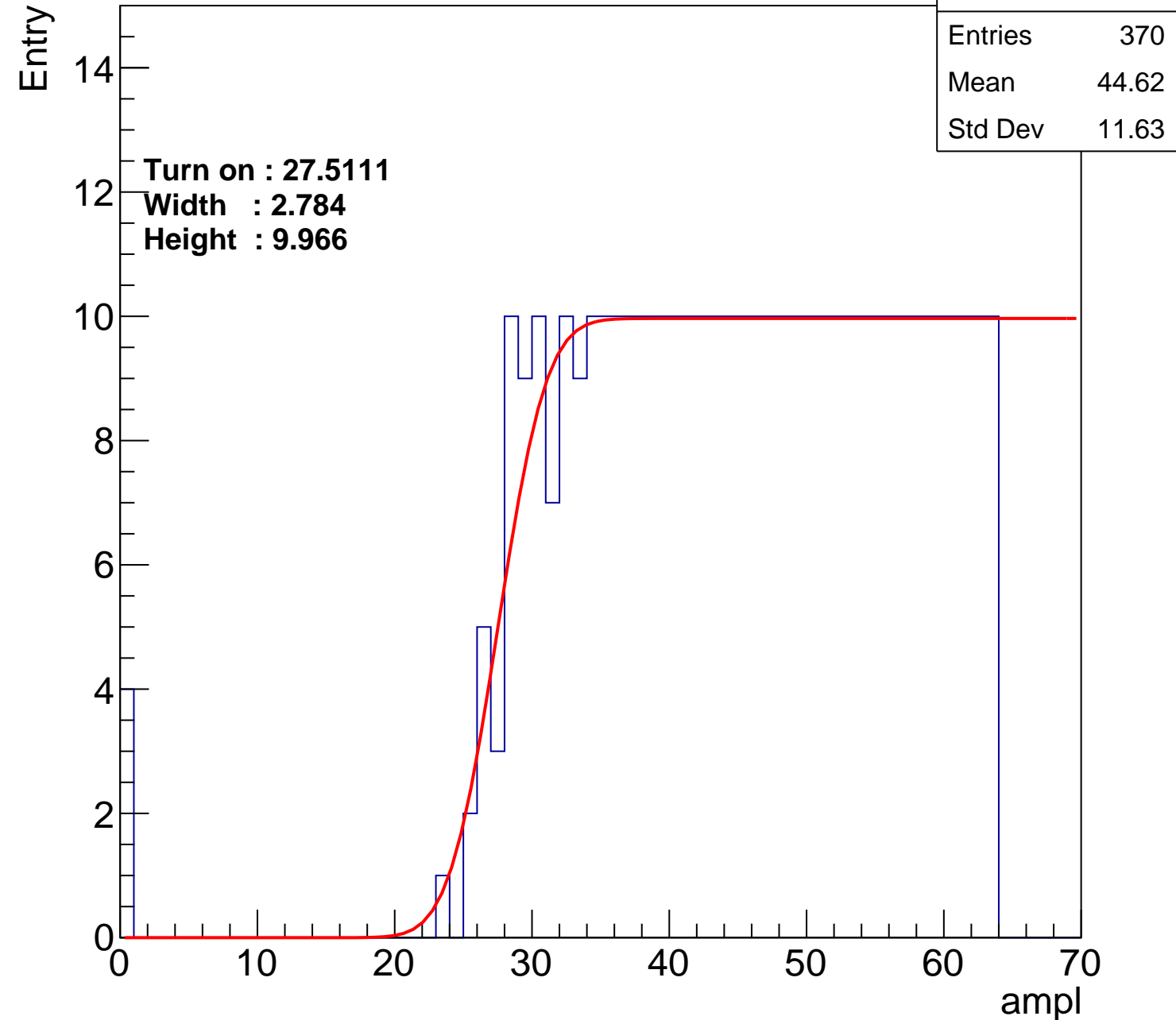
Width : 2.784

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch86

calib_packv5_042523_0143.root, FC#3, port B3

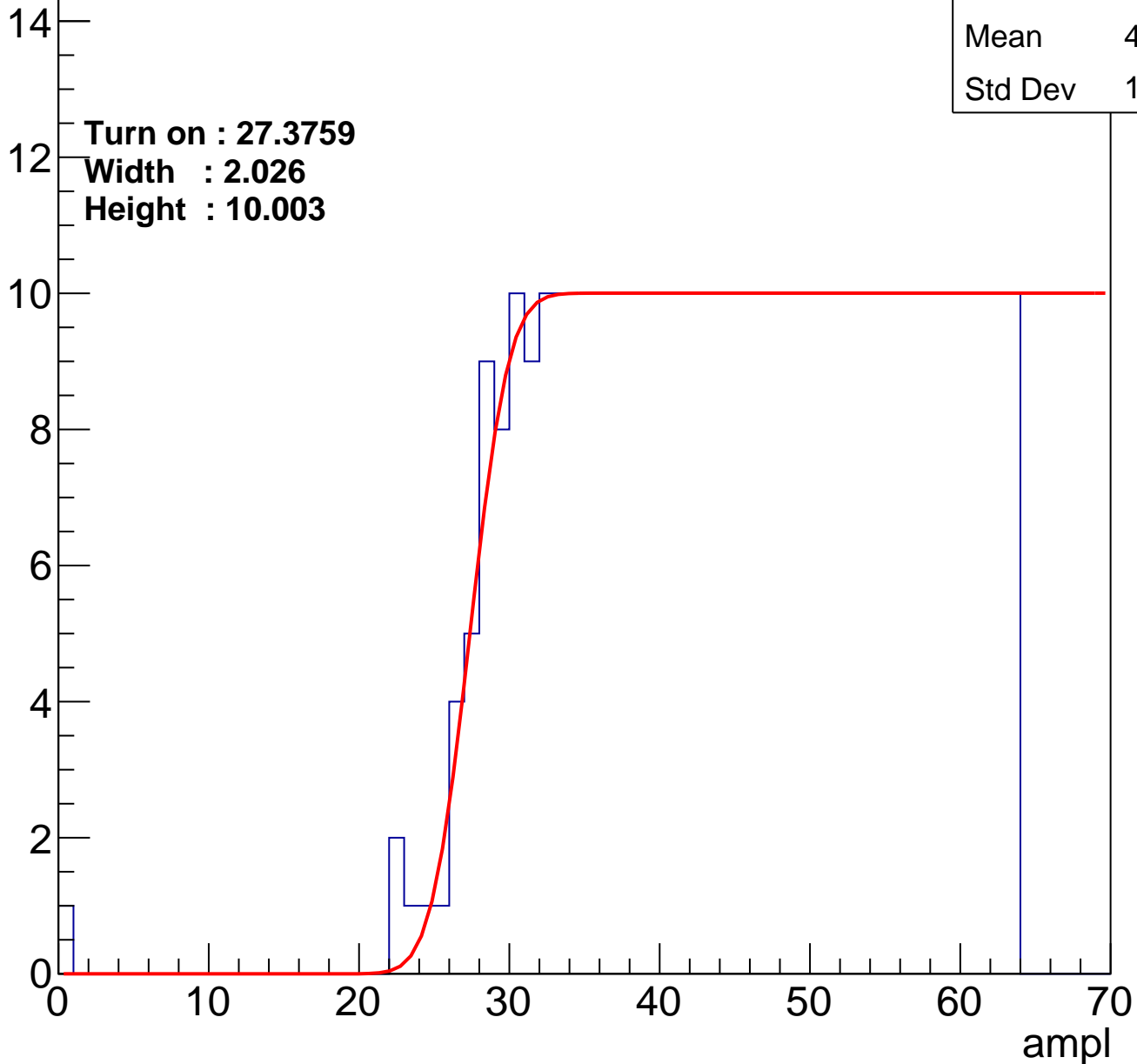
Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.3759

Width : 2.026

Height : 10.003

Entry



B1L000S, U10-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.05
Std Dev	11.6

Turn on : 26.0273

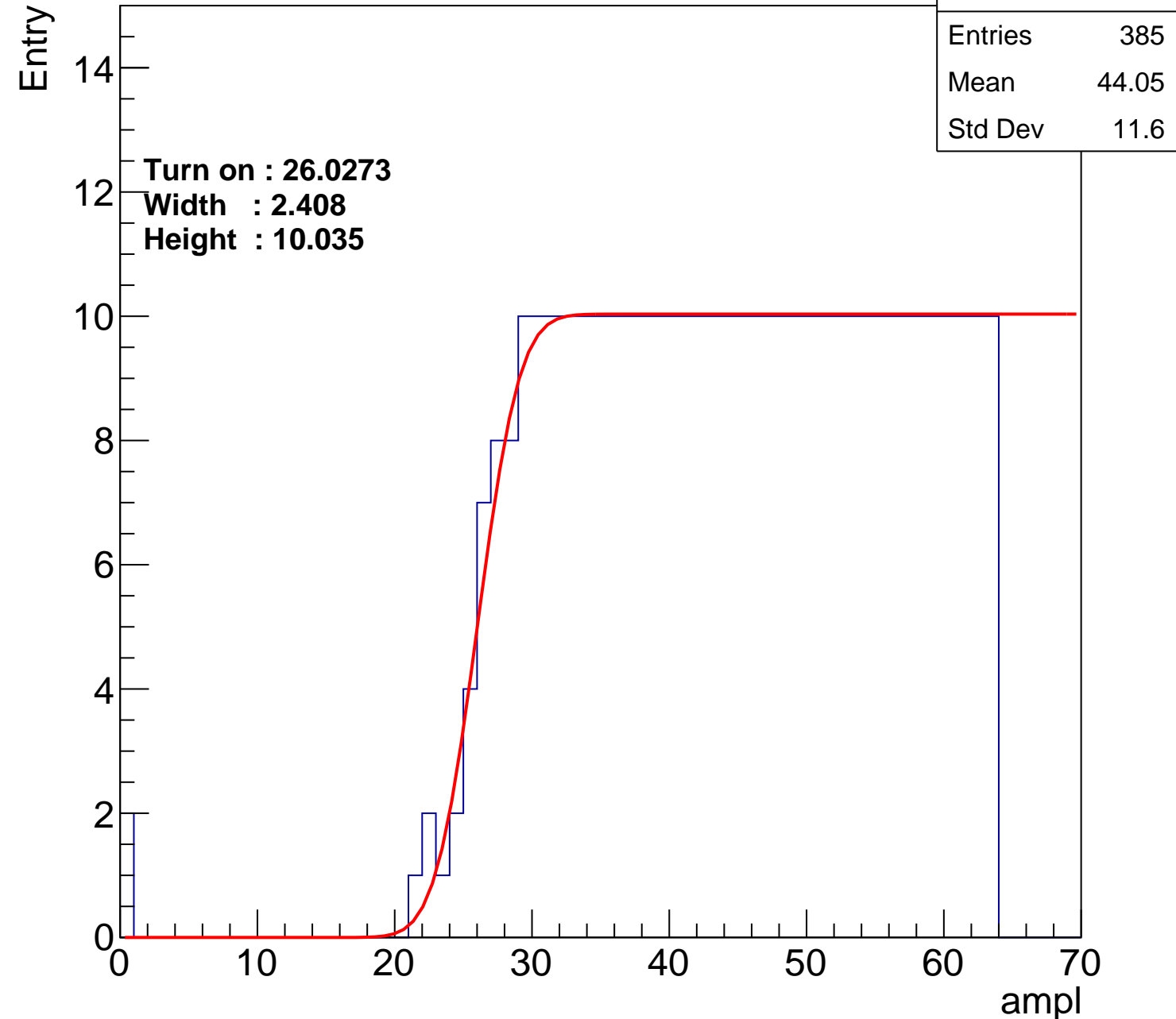
Width : 2.408

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch88

calib_packv5_042523_0143.root, FC#3, port B3

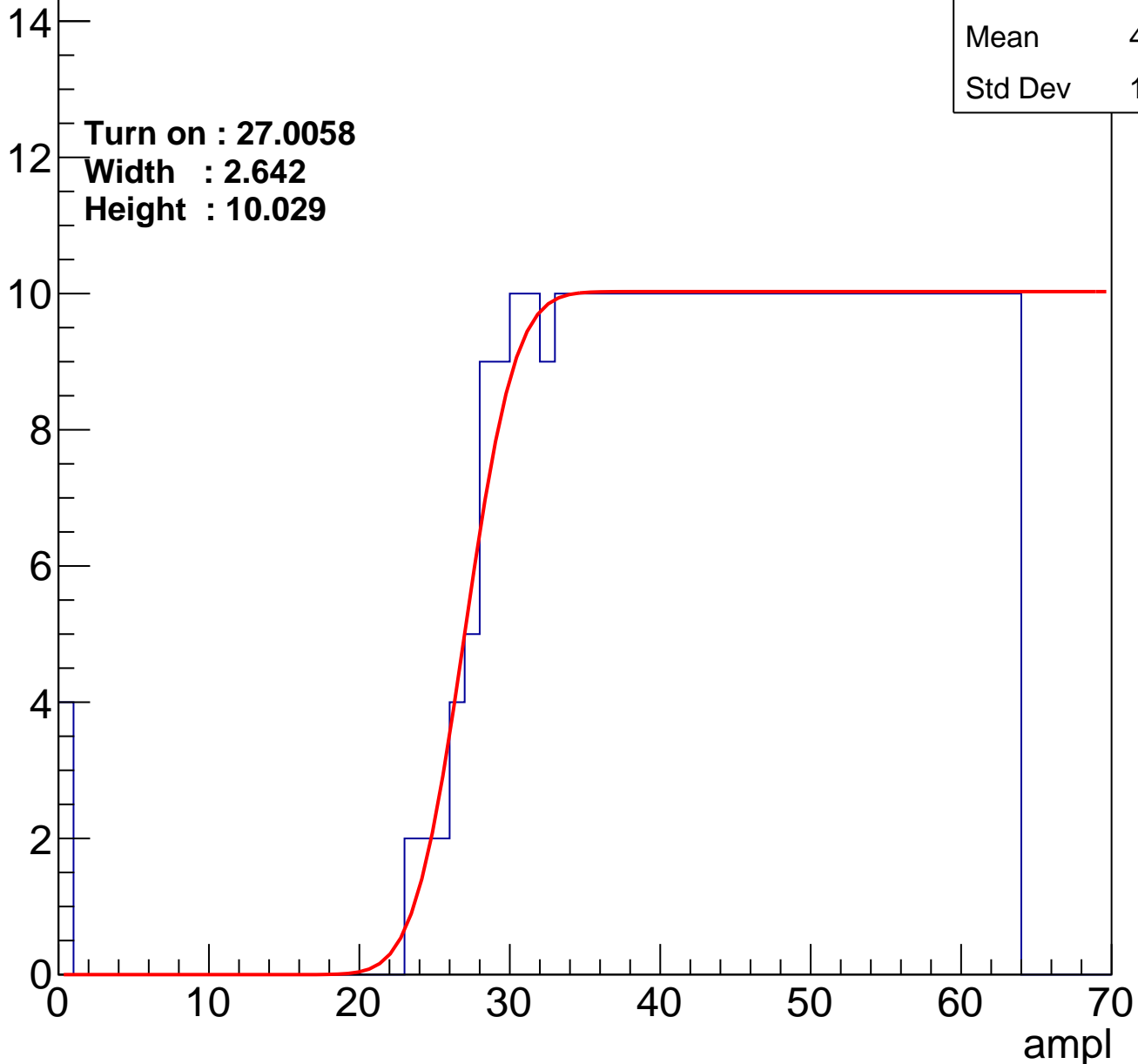
Entries	376
Mean	44.35
Std Dev	11.75

Turn on : 27.0058

Width : 2.642

Height : 10.029

Entry



B1L000S, U10-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.97
Std Dev	11.13

Turn on : 27.8289

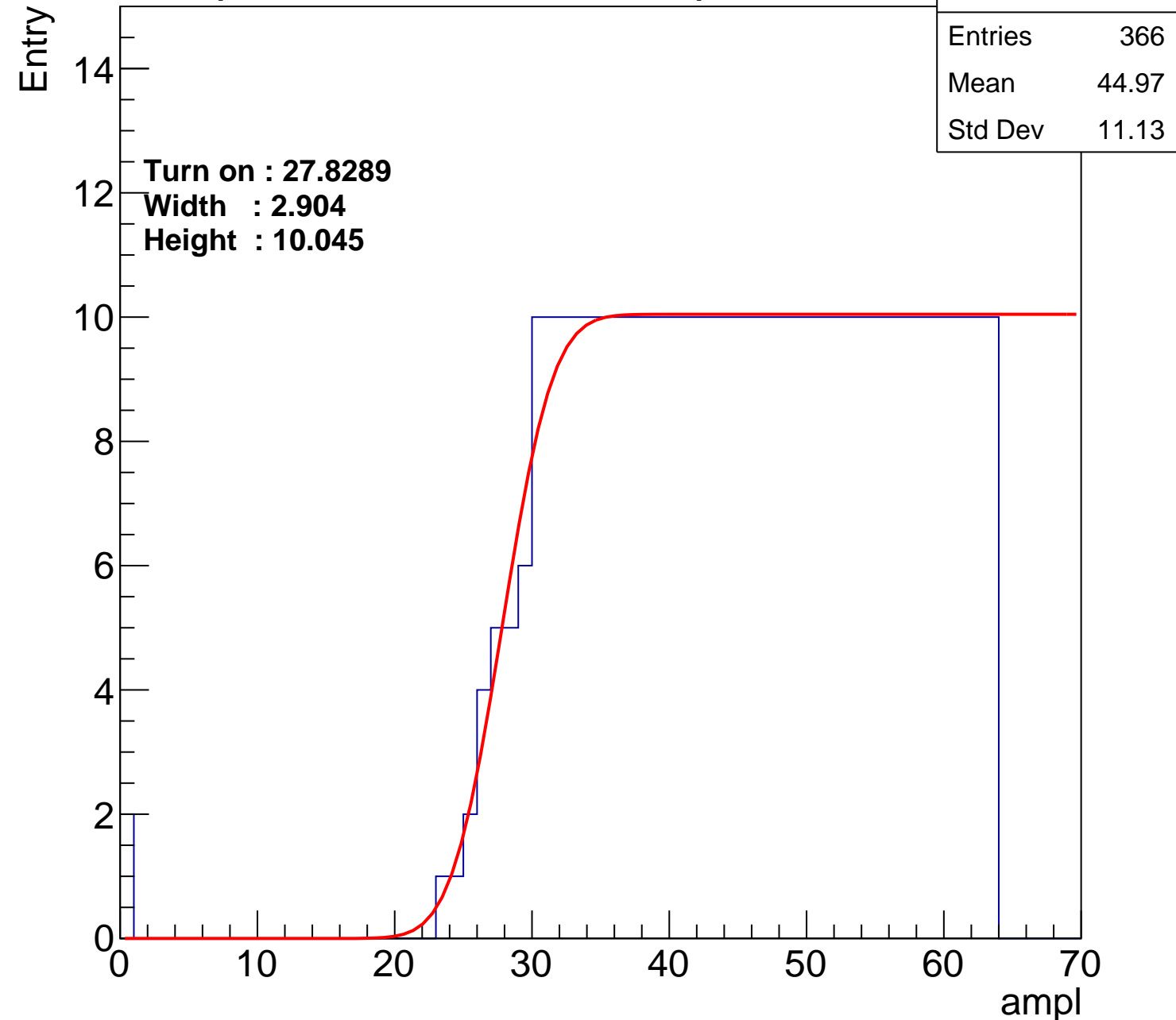
Width : 2.904

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch90

calib_packv5_042523_0143.root, FC#3, port B3

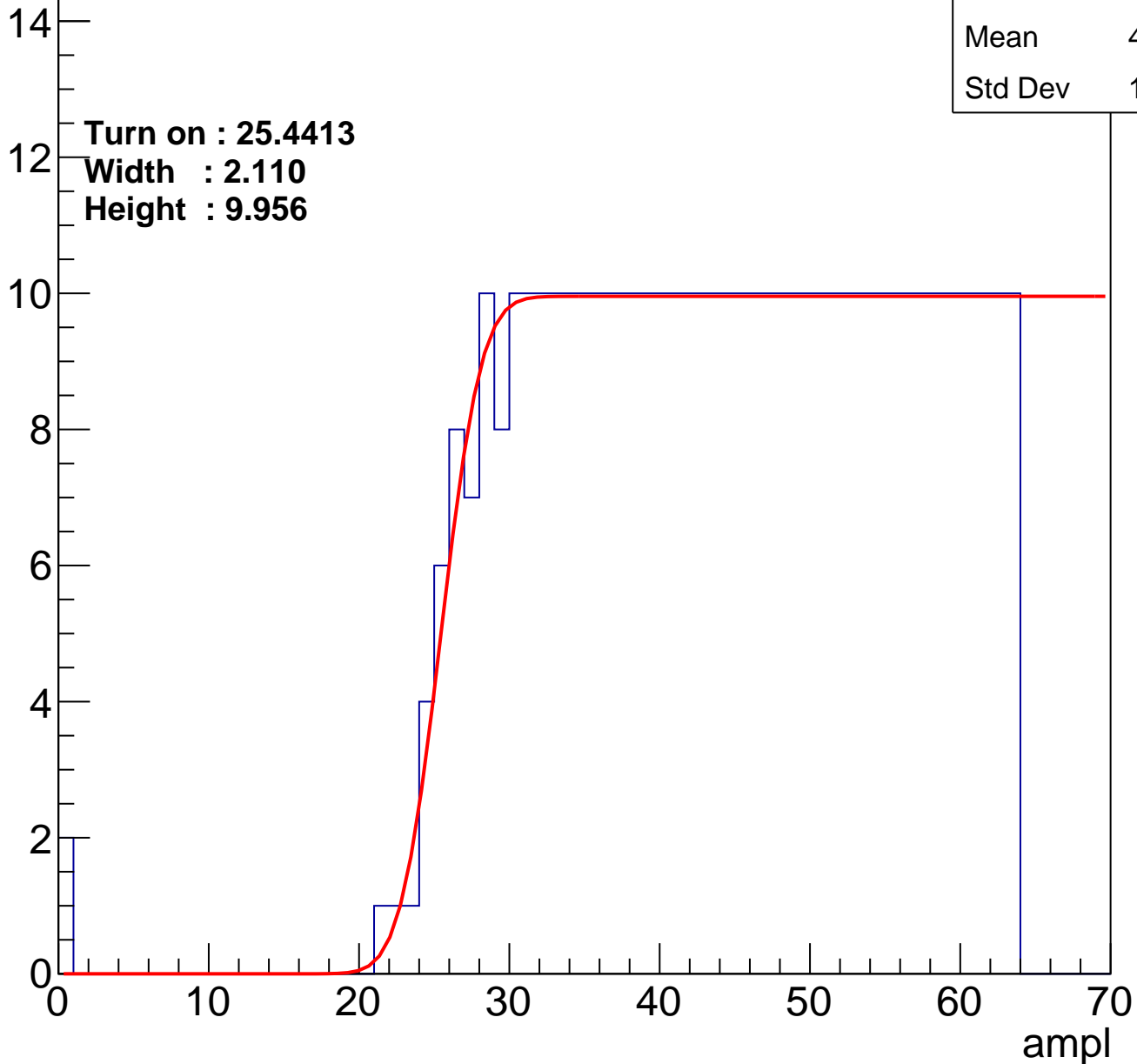
Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.4413

Width : 2.110

Height : 9.956

Entry



B1L000S, U10-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 28.2041

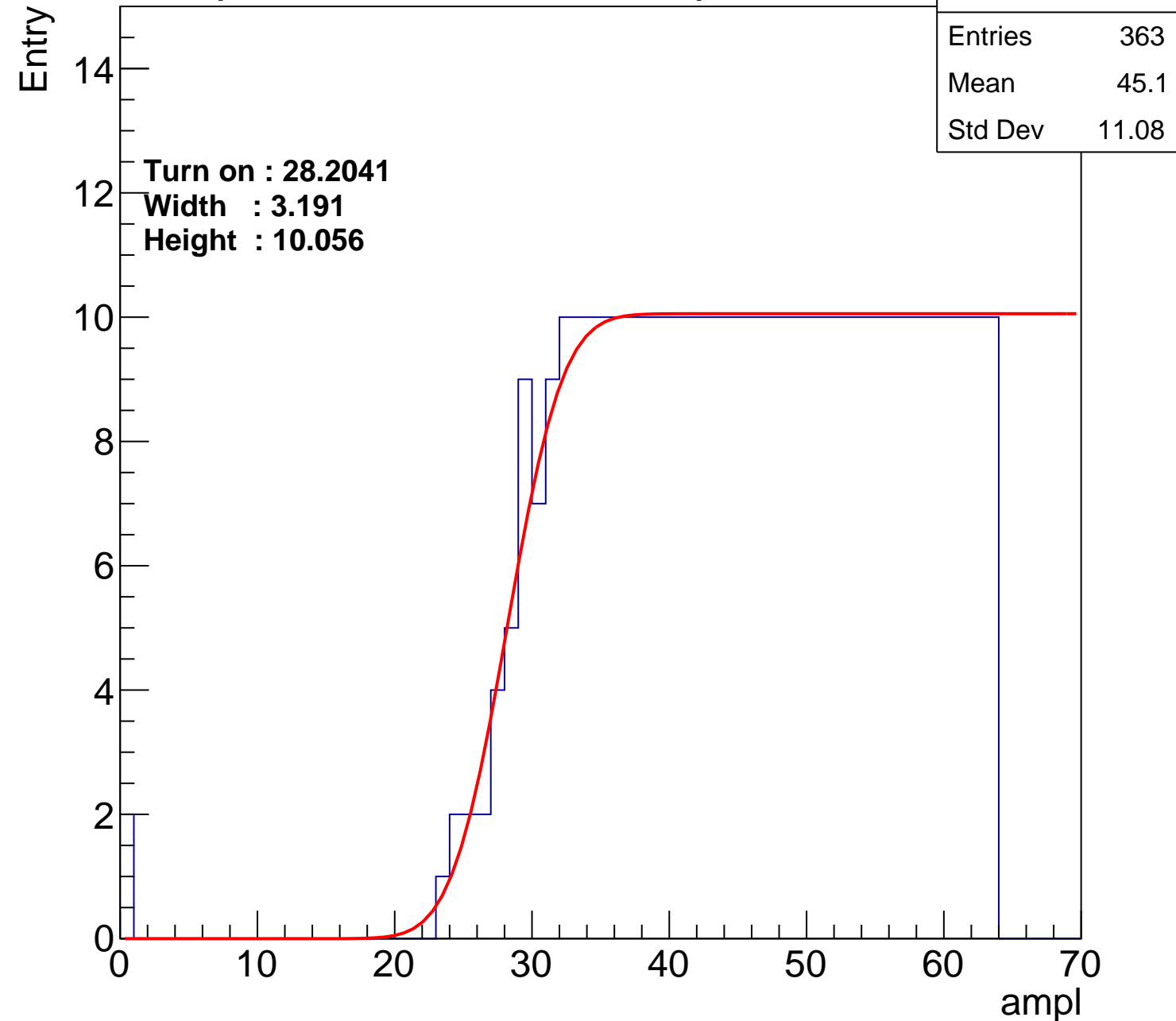
Width : 3.191

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.24
Std Dev	11.38

Turn on : 25.8598

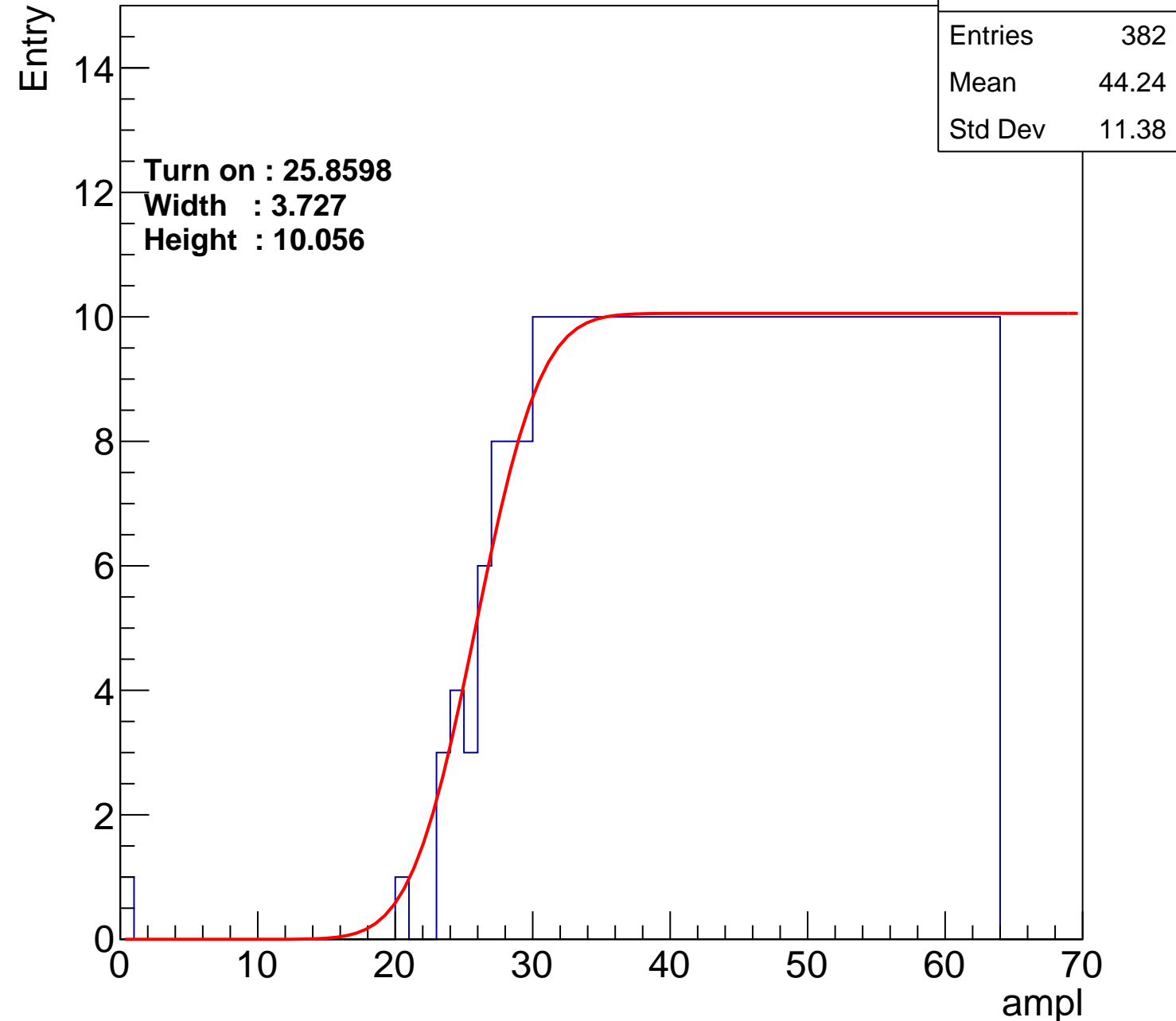
Width : 3.727

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.98
Std Dev	11.95

Turn on : 26.5741

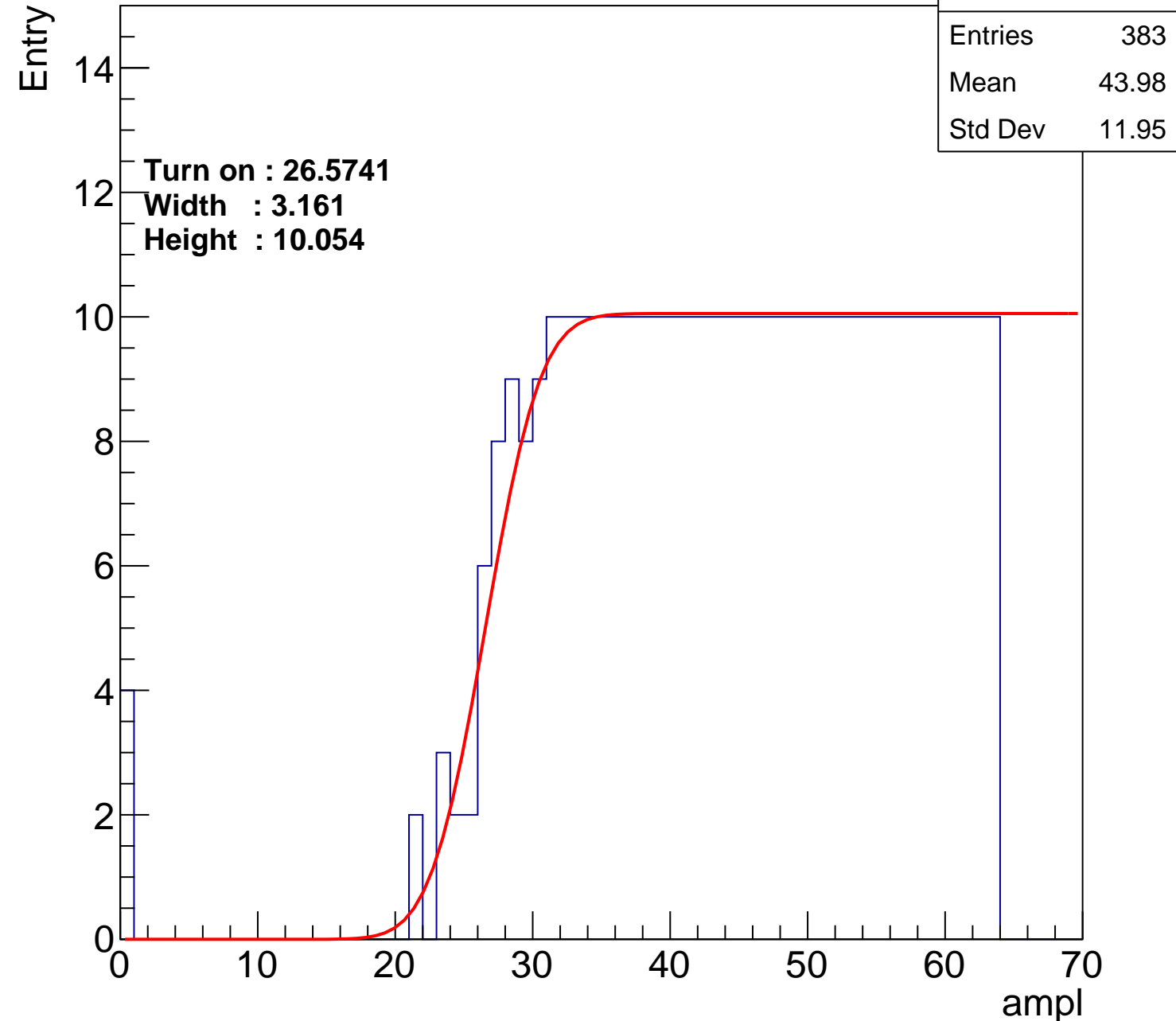
Width : 3.161

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.85
Std Dev	11.99

Turn on : 25.5109

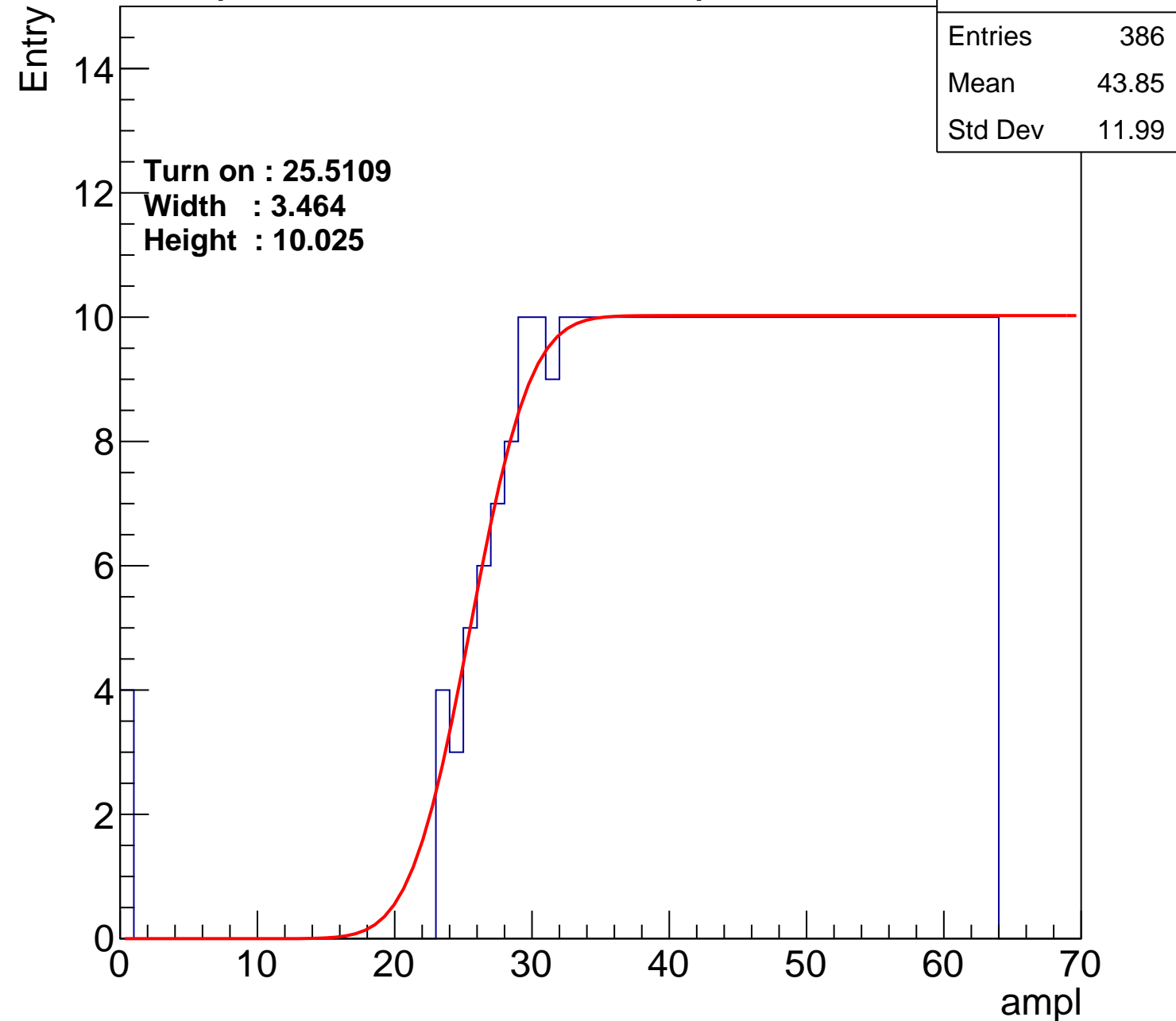
Width : 3.464

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.9029

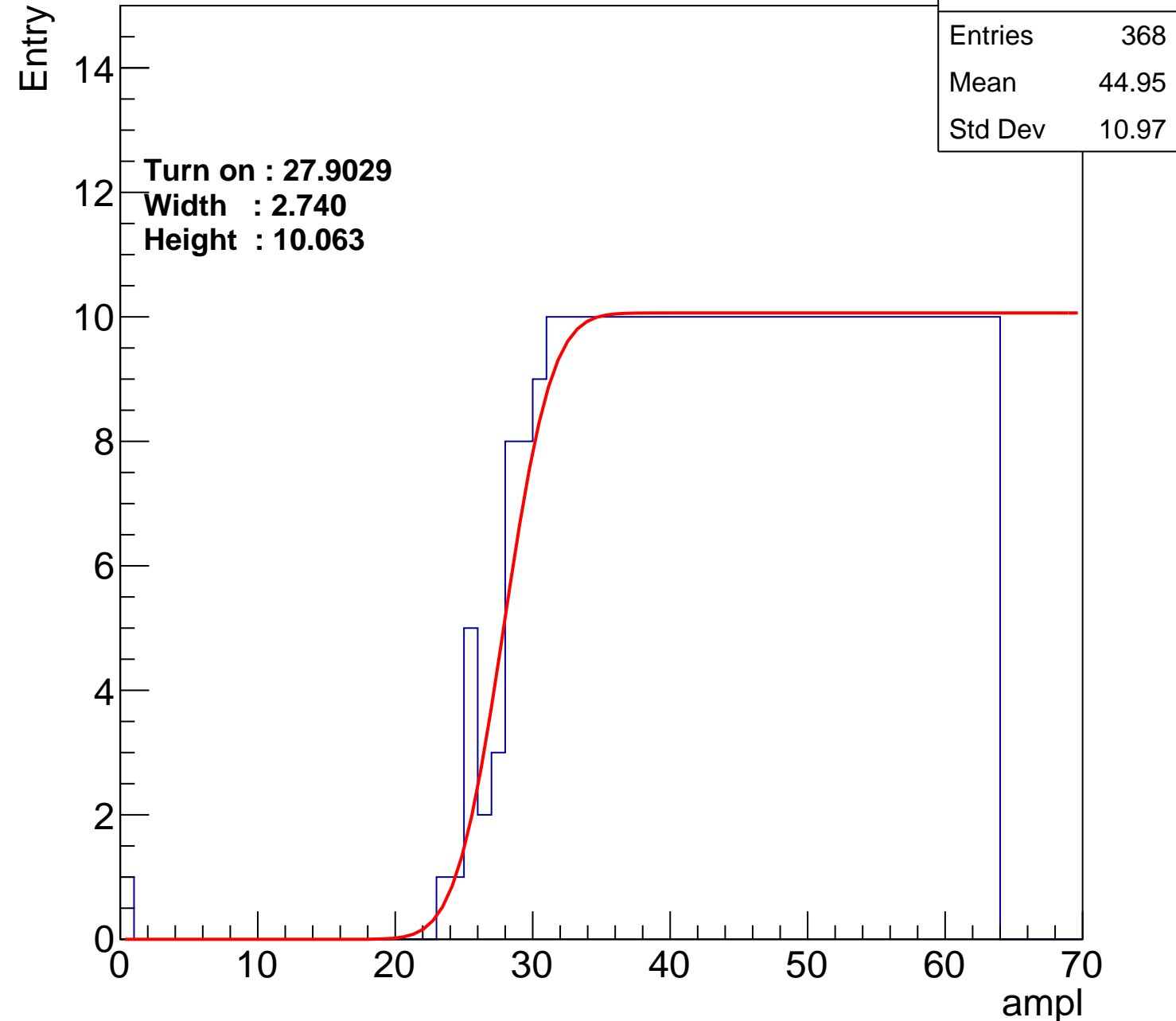
Width : 2.740

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.16
Std Dev	11.4

Turn on : 26.3307

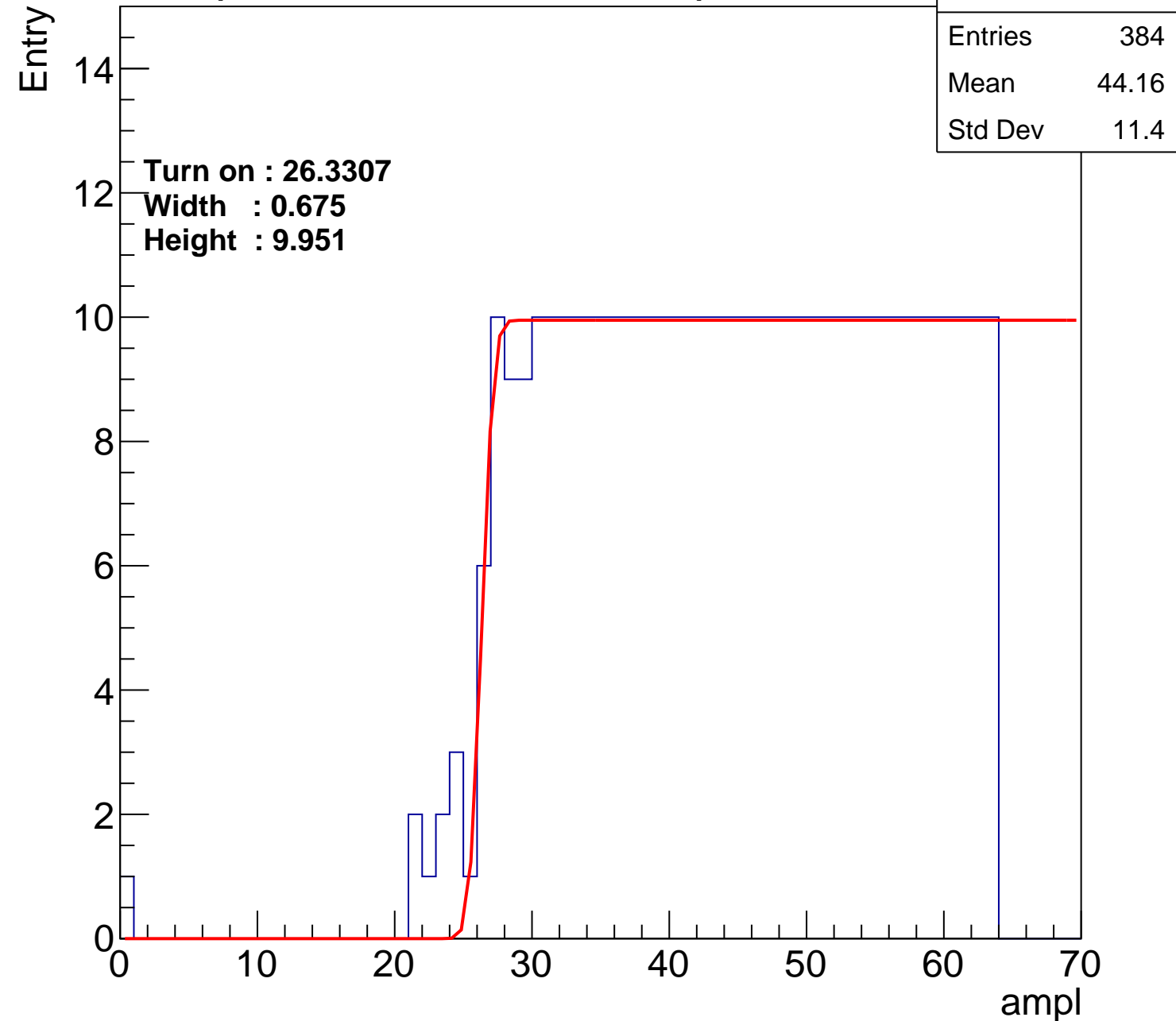
Width : 0.675

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch97

calib_packv5_042523_0143.root, FC#3, port B3

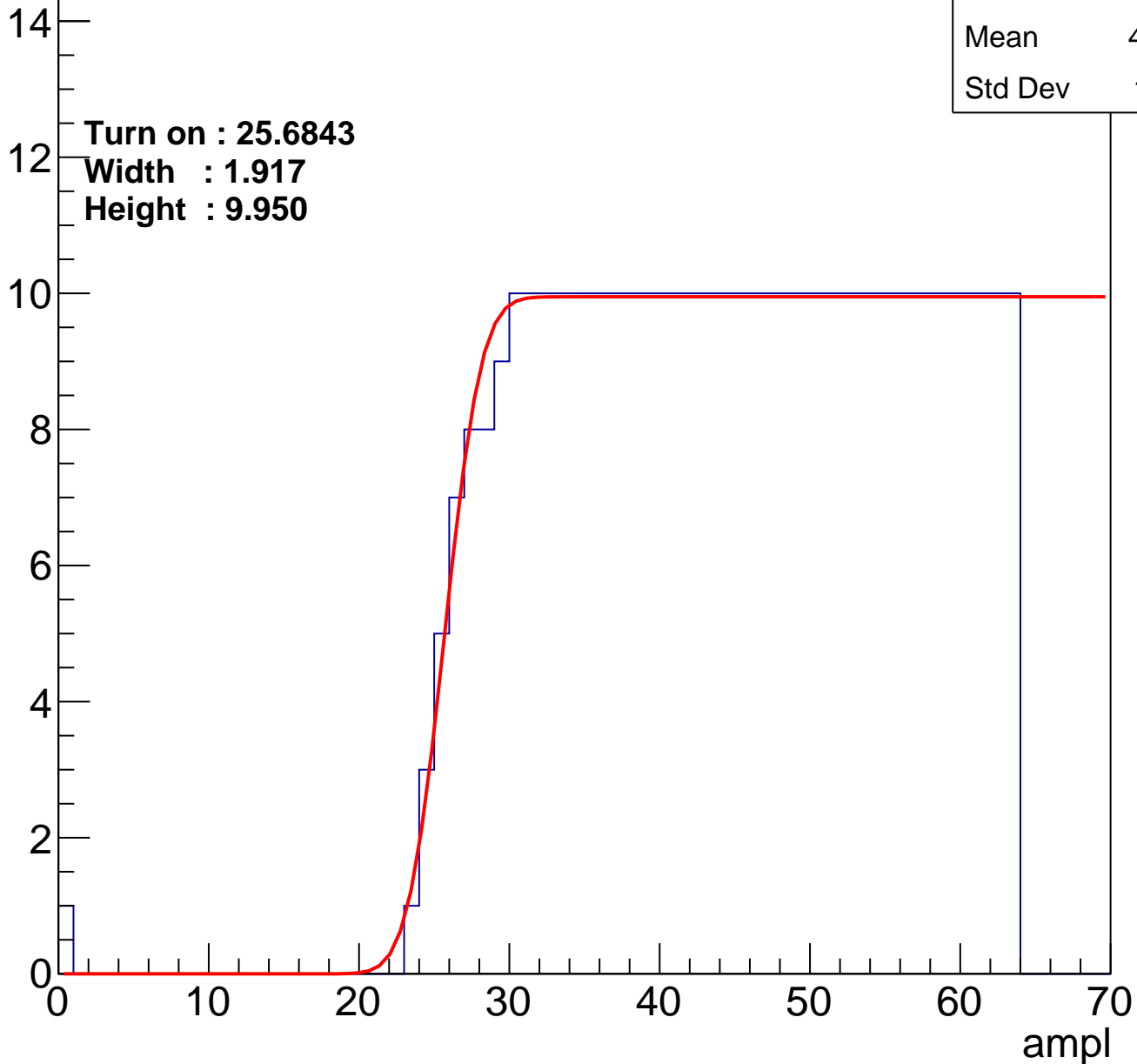
Entries	382
Mean	44.27
Std Dev	11.31

Turn on : 25.6843

Width : 1.917

Height : 9.950

Entry



B1L000S, U10-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 26.8401

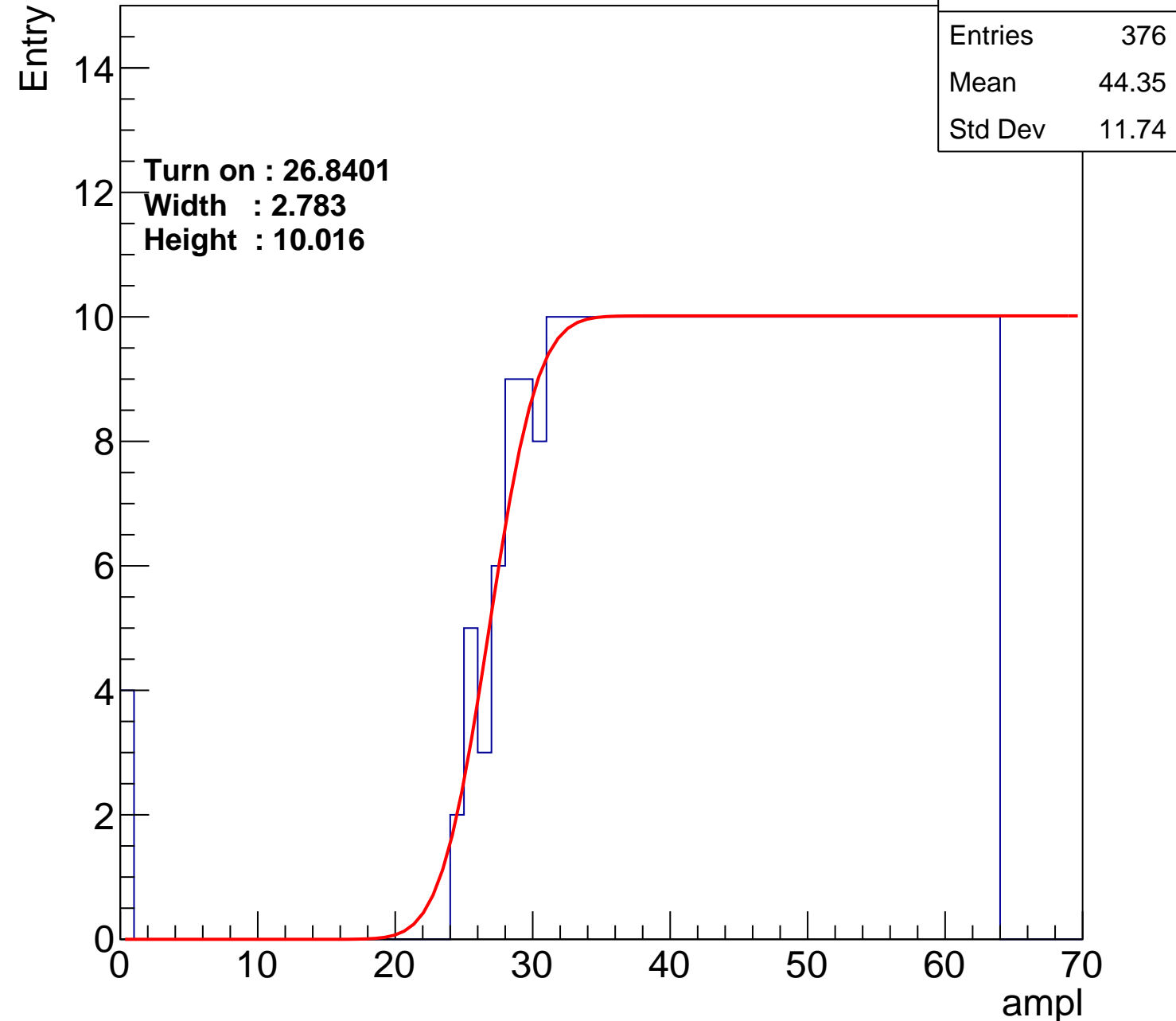
Width : 2.783

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 26.3306

Width : 2.949

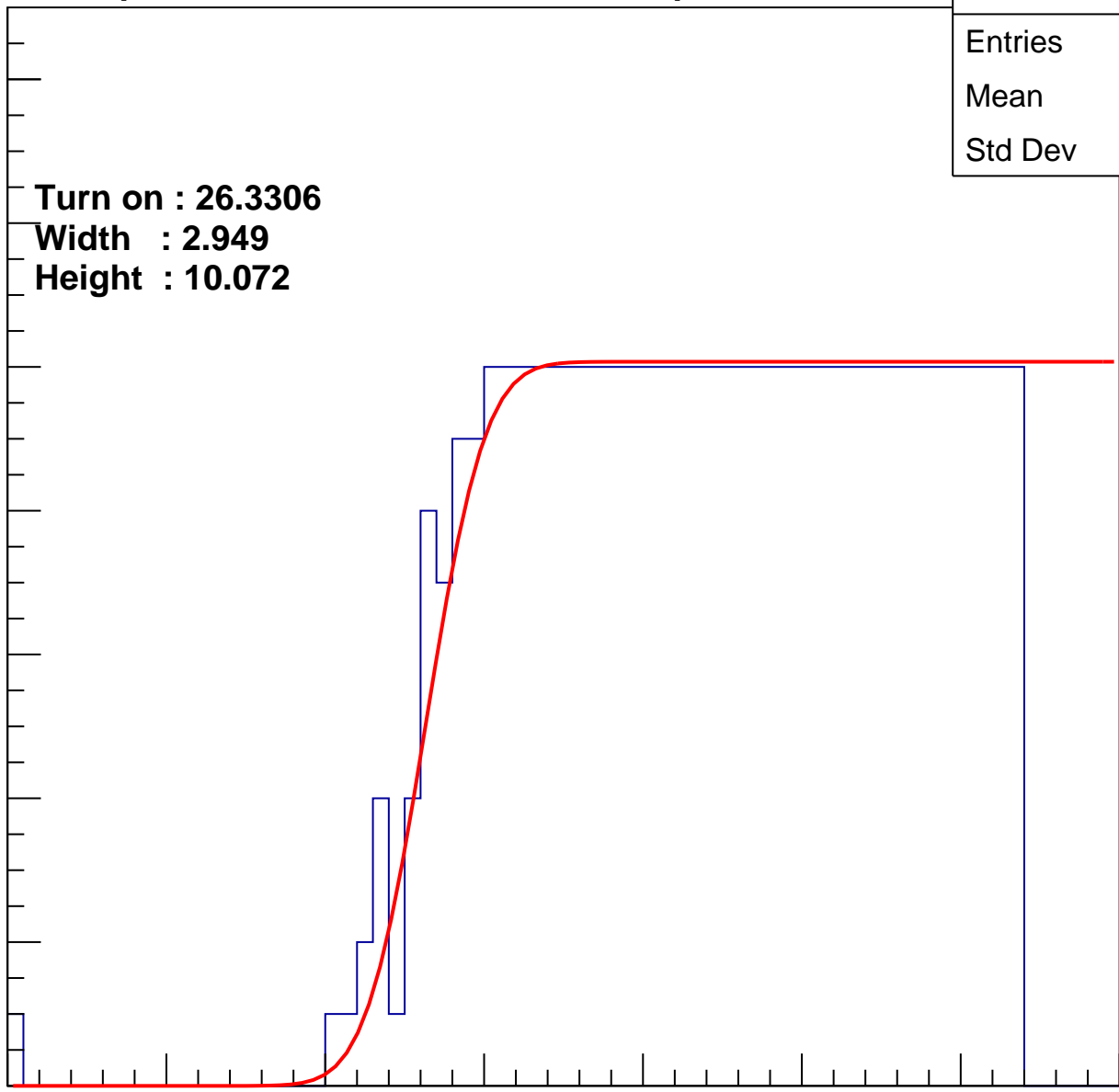
Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U10-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.6031

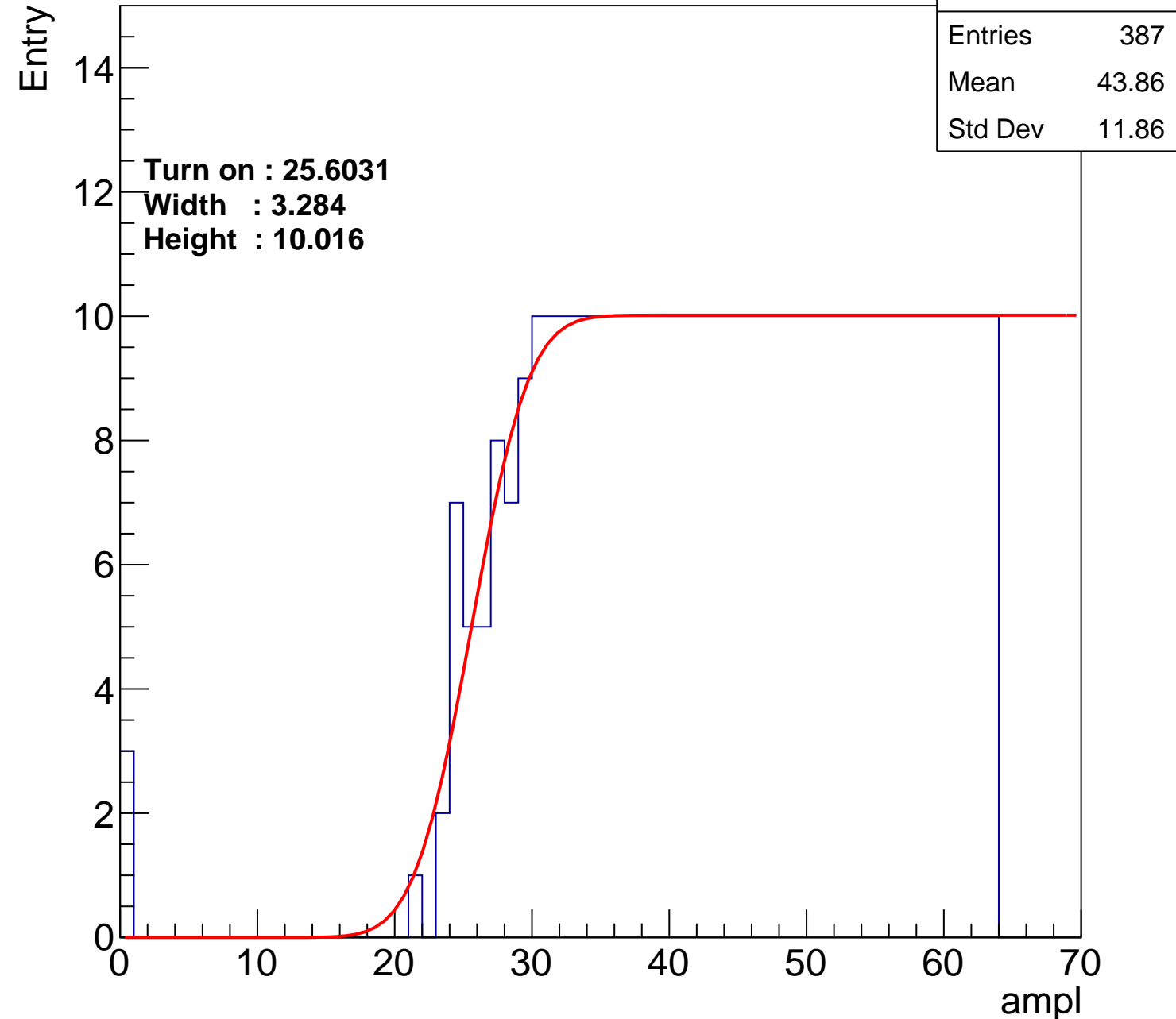
Width : 3.284

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch101

calib_packv5_042523_0143.root, FC#3, port B3

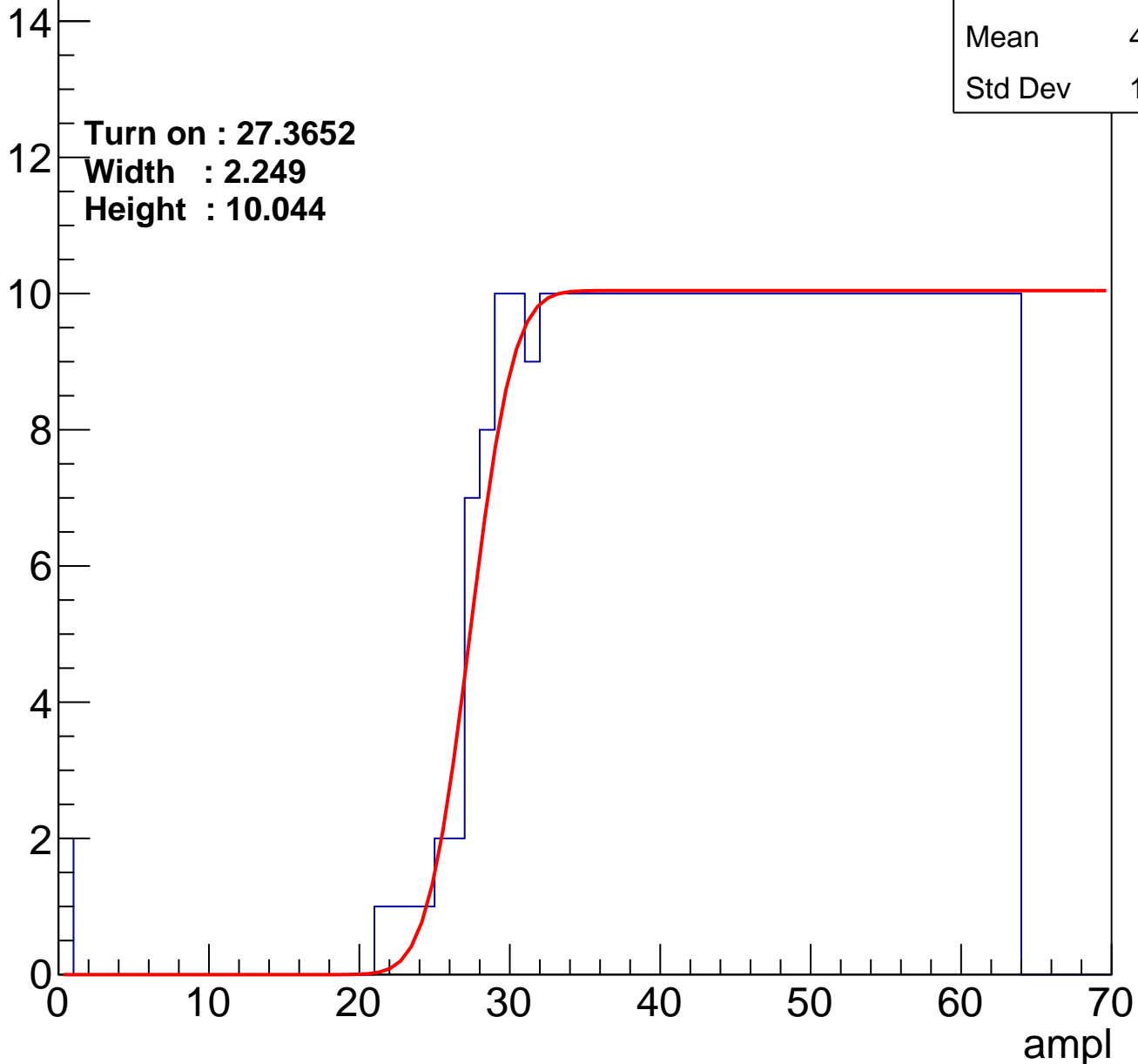
Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 27.3652

Width : 2.249

Height : 10.044

Entry



B1L000S, U10-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 27.2135

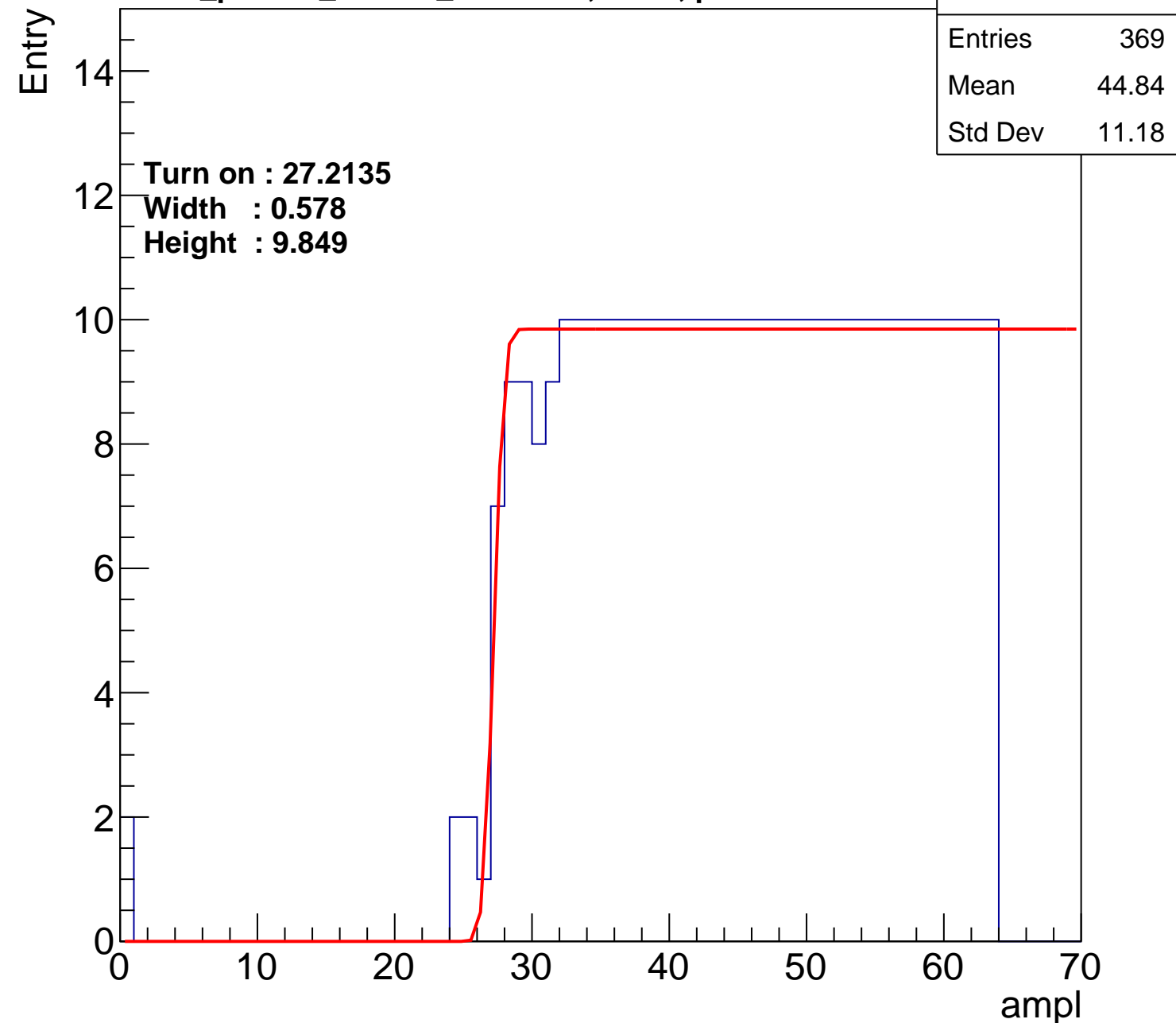
Width : 0.578

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch103

calib_packv5_042523_0143.root, FC#3, port B3

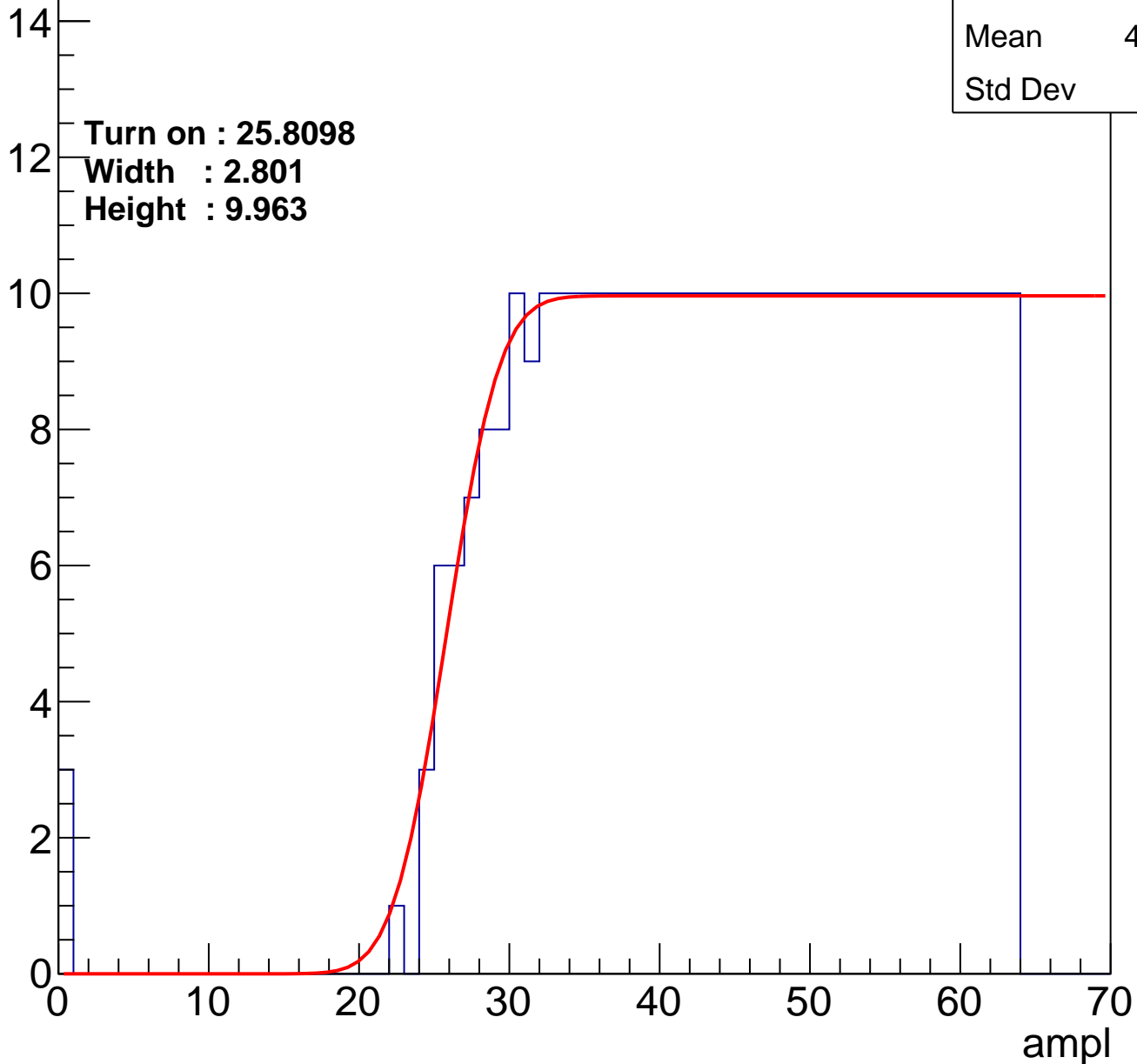
Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 25.8098

Width : 2.801

Height : 9.963

Entry



B1L000S, U10-ch104

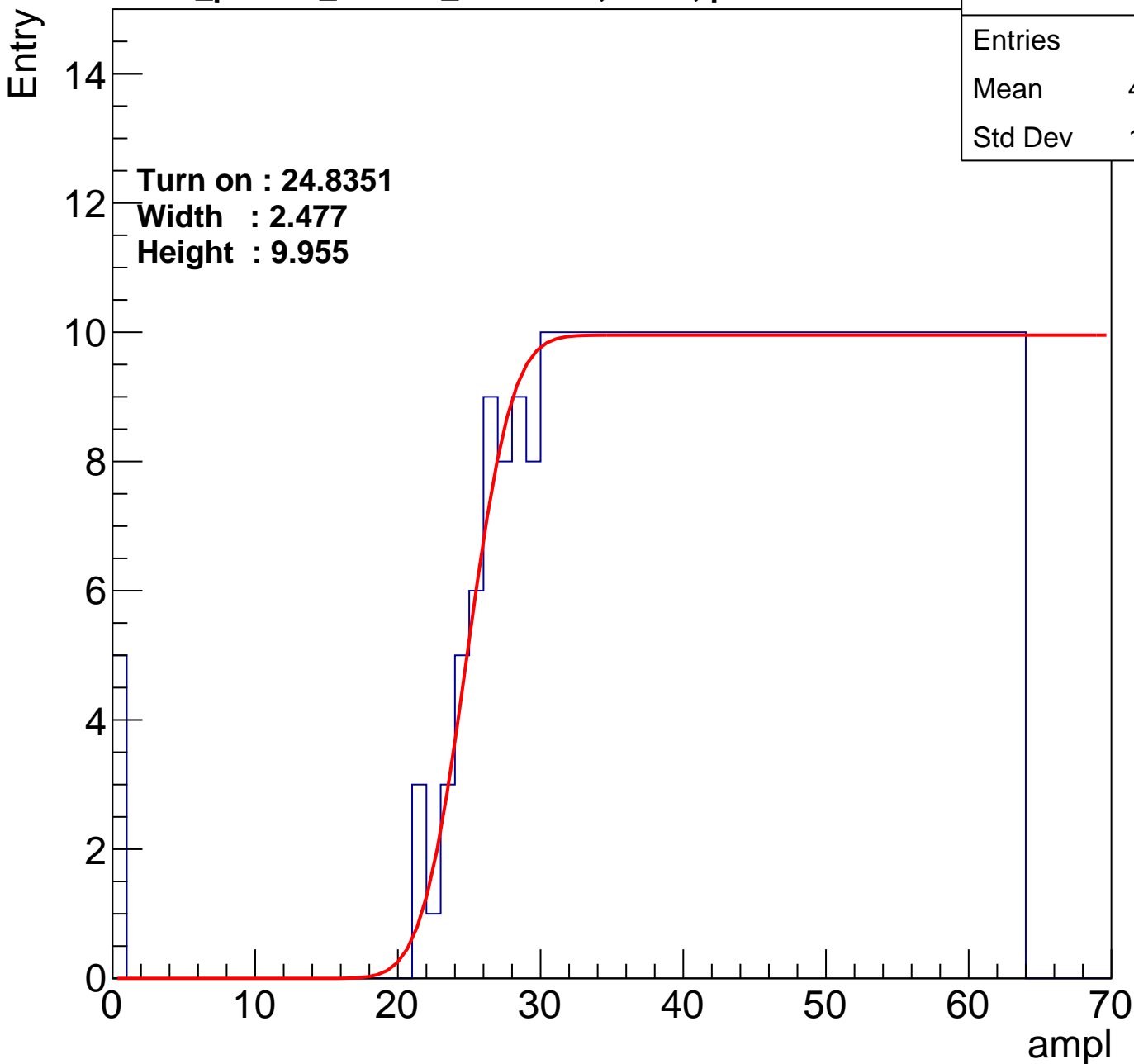
calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.24
Std Dev	12.42

Turn on : 24.8351

Width : 2.477

Height : 9.955



B1L000S, U10-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 26.7267

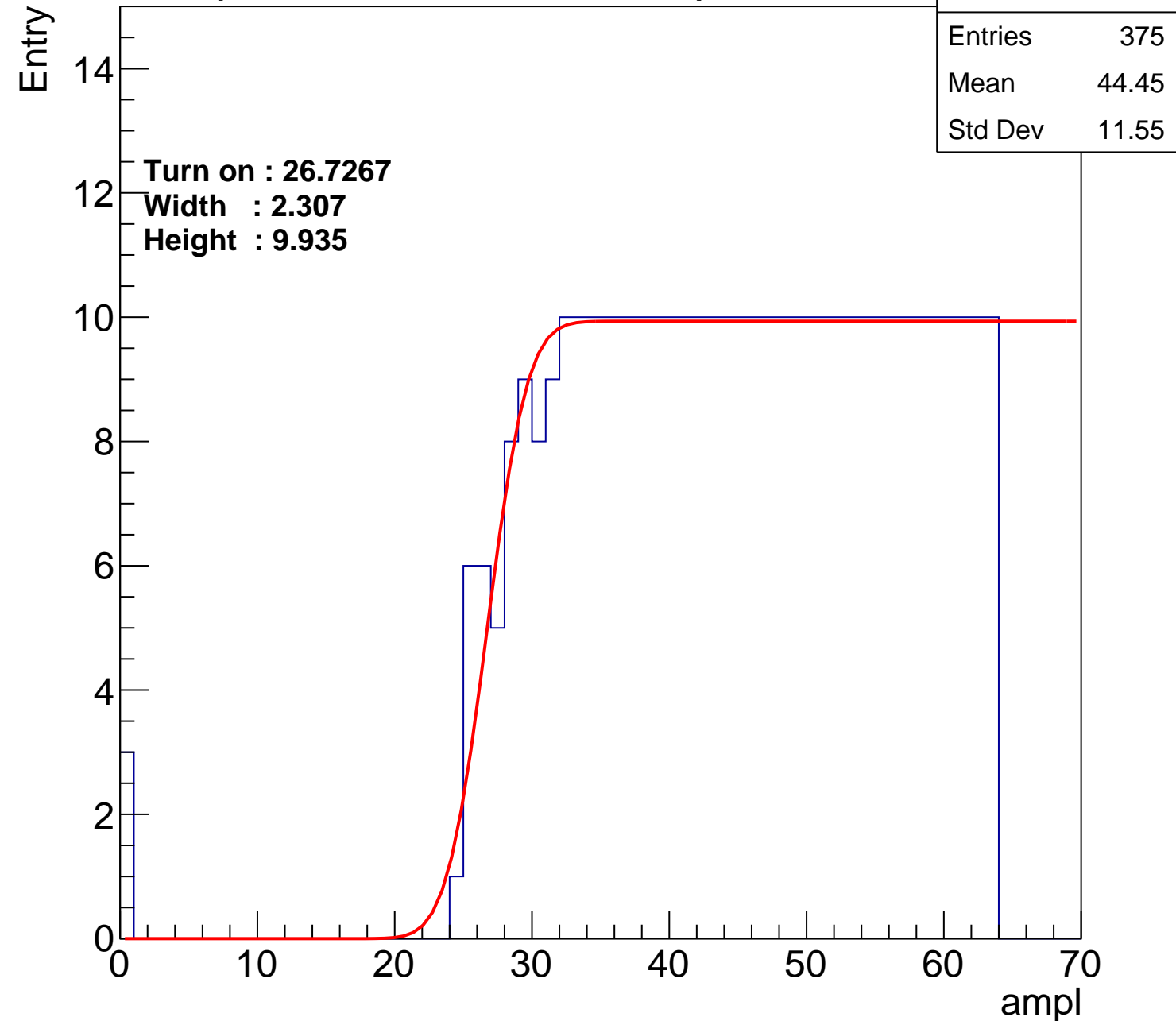
Width : 2.307

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch106

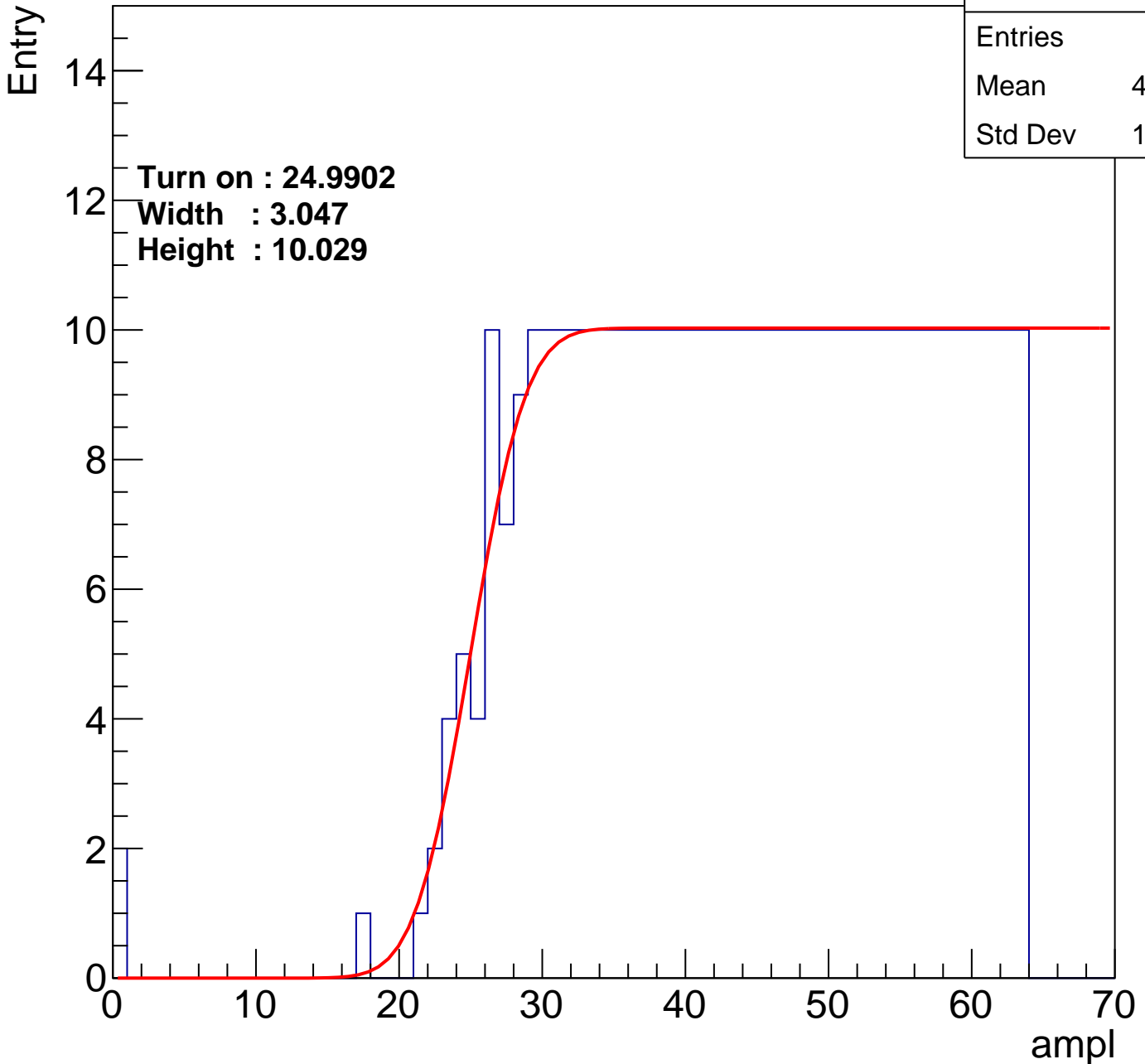
calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.53
Std Dev	11.89

Turn on : 24.9902

Width : 3.047

Height : 10.029



B1L000S, U10-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.7691

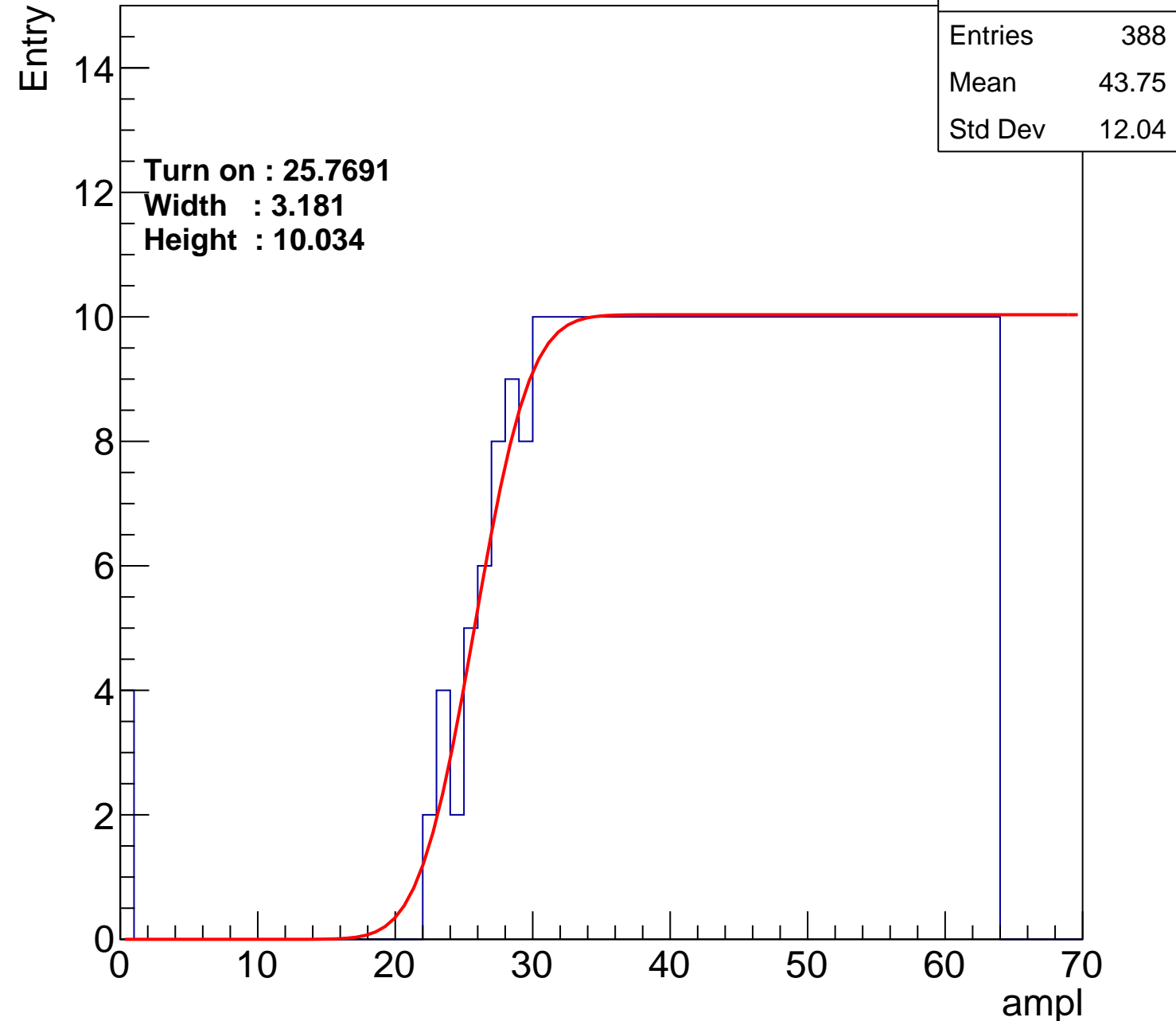
Width : 3.181

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch108

calib_packv5_042523_0143.root, FC#3, port B3

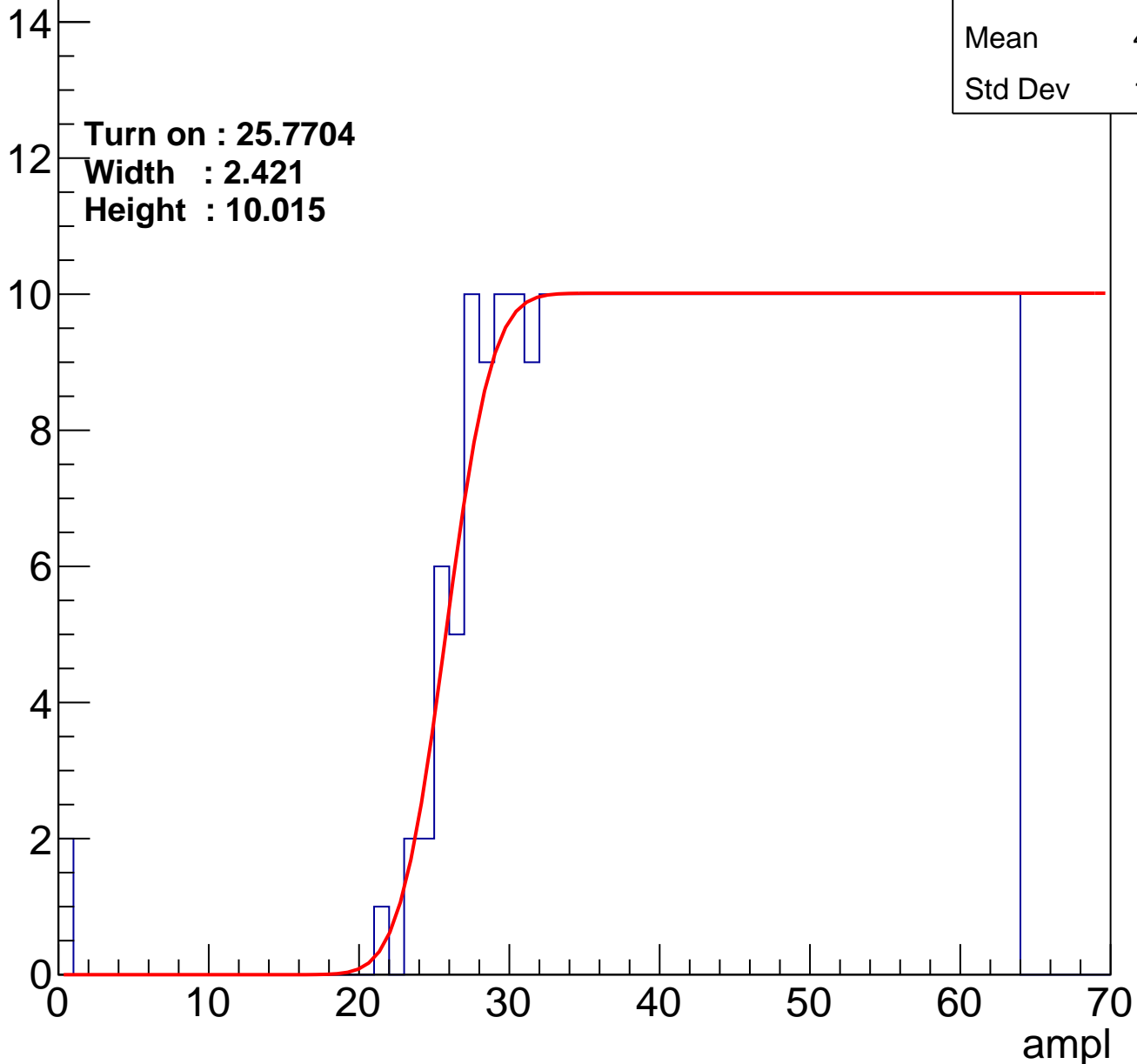
Entries	386
Mean	44.01
Std Dev	11.61

Turn on : 25.7704

Width : 2.421

Height : 10.015

Entry



B1L000S, U10-ch109

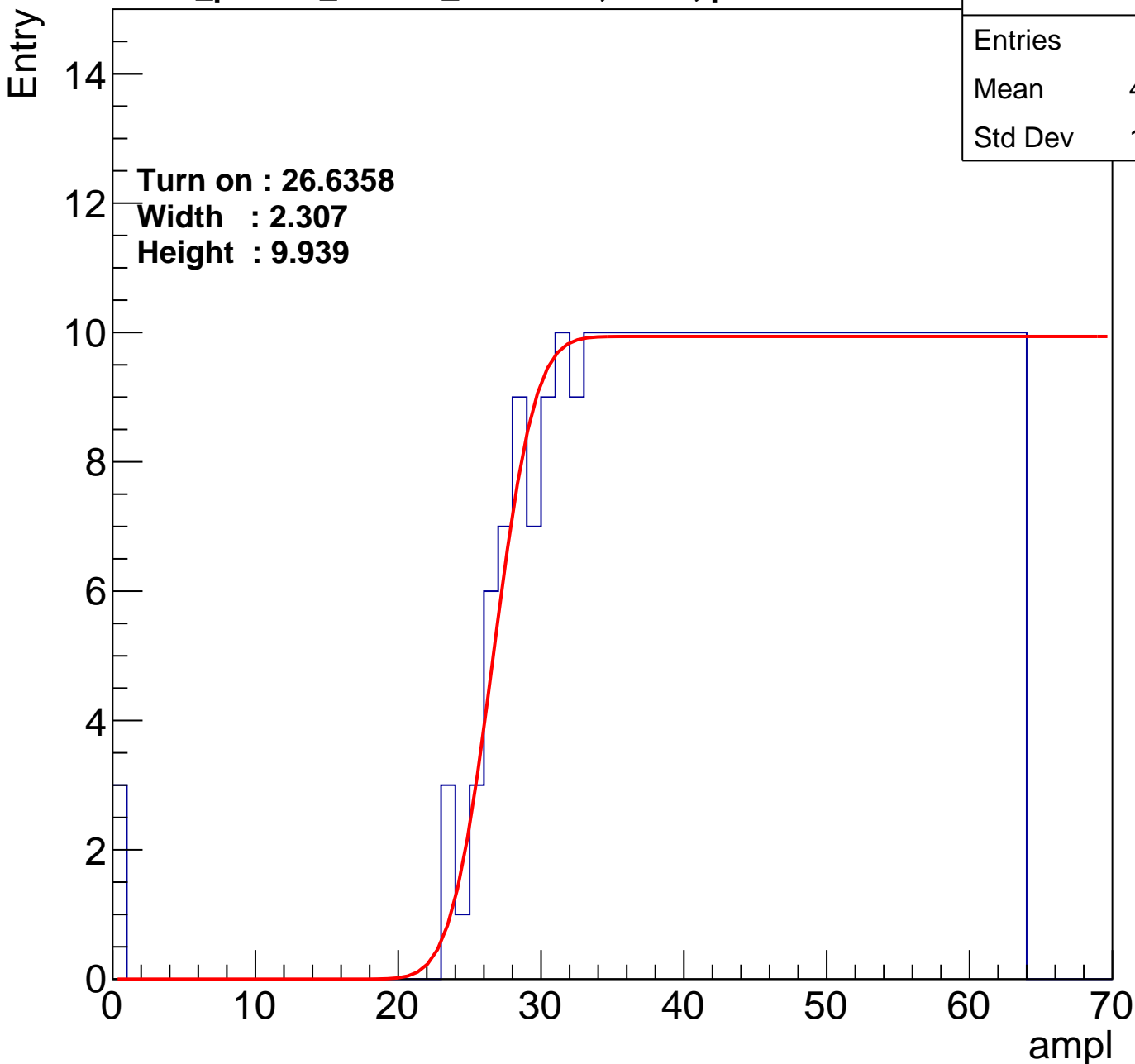
calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.6358

Width : 2.307

Height : 9.939



B1L000S, U10-ch110

calib_packv5_042523_0143.root, FC#3, port B3

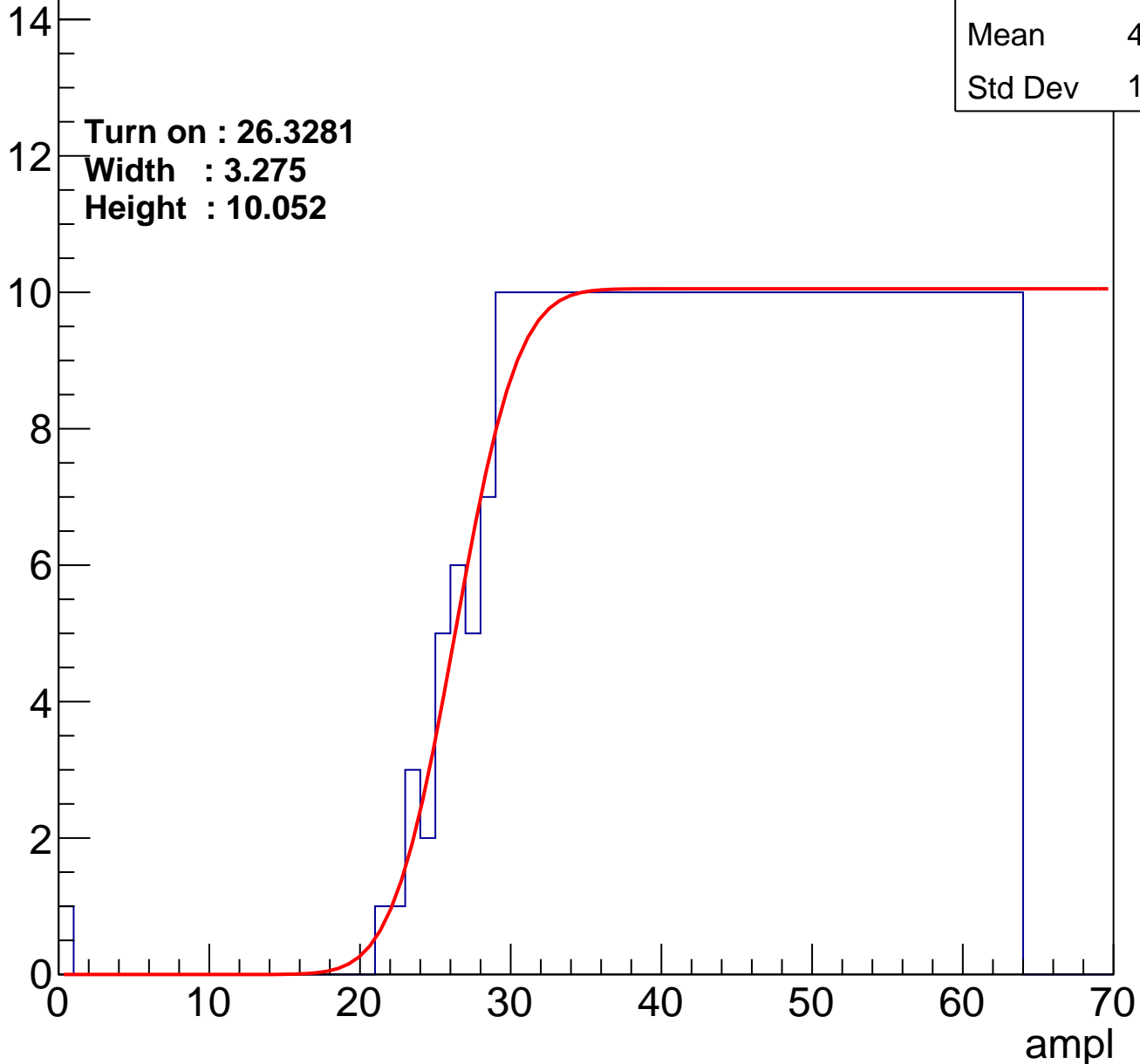
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.3281

Width : 3.275

Height : 10.052

Entry



B1L000S, U10-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 25.6360

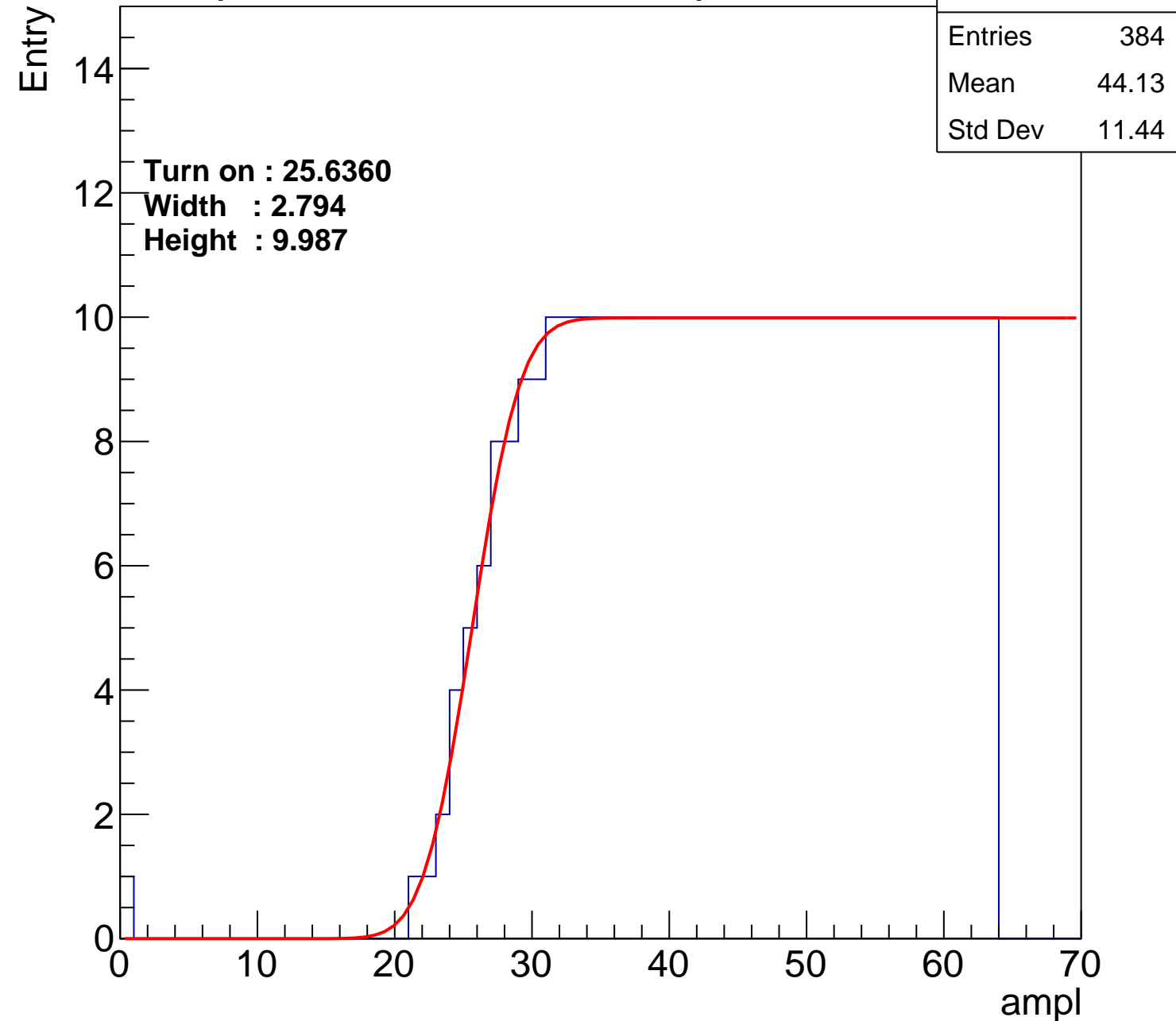
Width : 2.794

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 27.3288

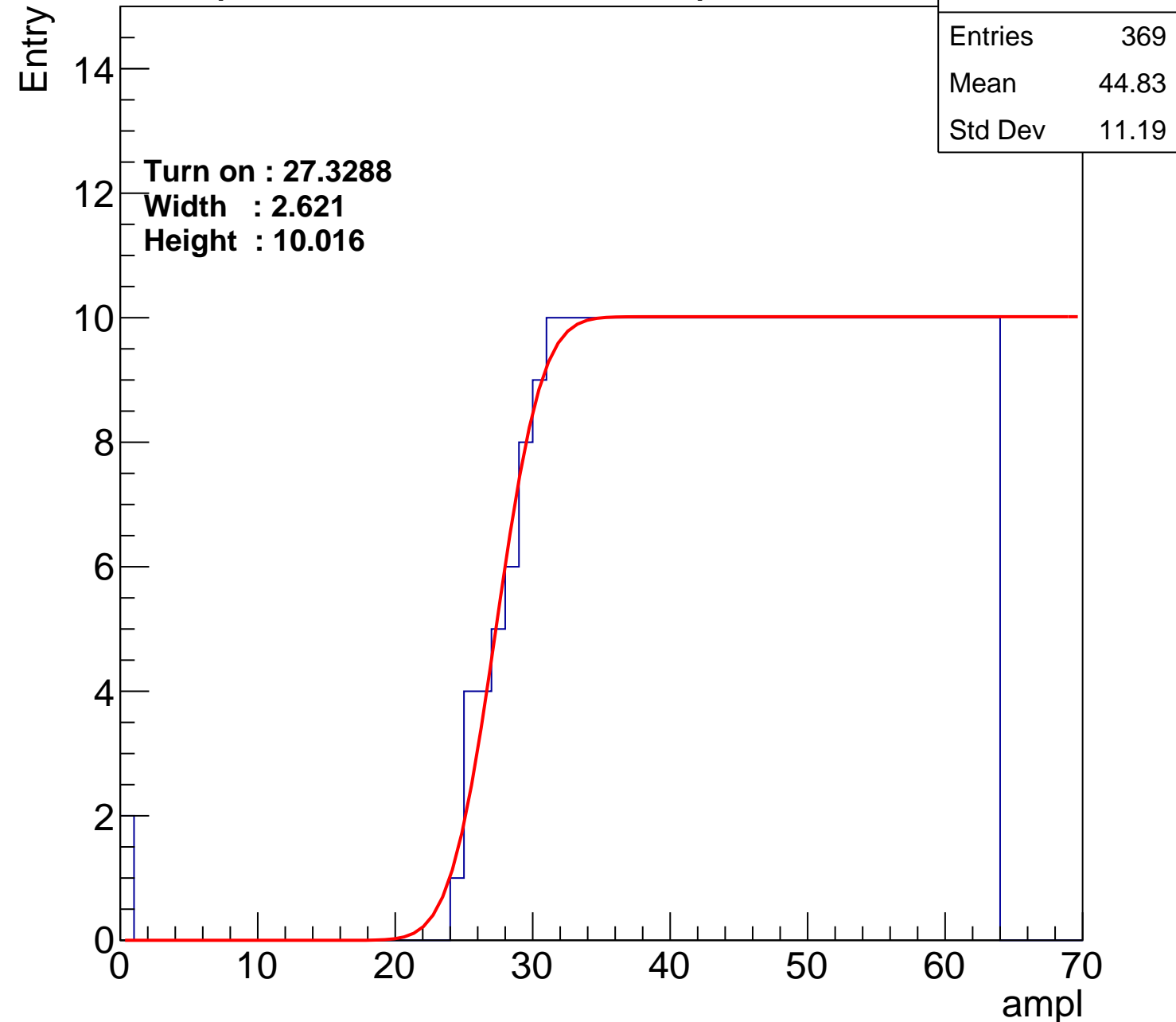
Width : 2.621

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.09
Std Dev	12.12

Turn on : 23.9294

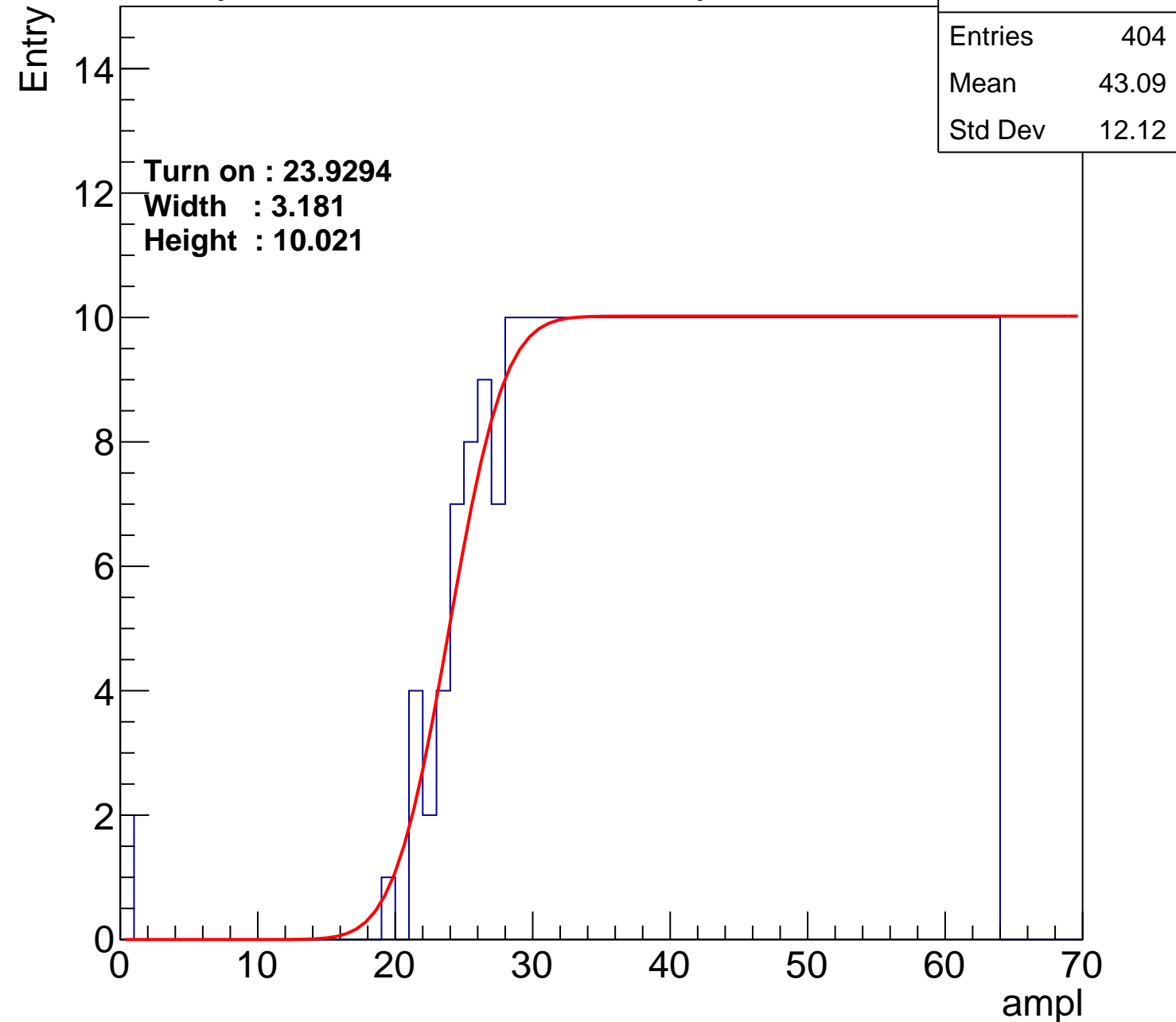
Width : 3.181

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.01
Std Dev	11.9

Turn on : 26.1966

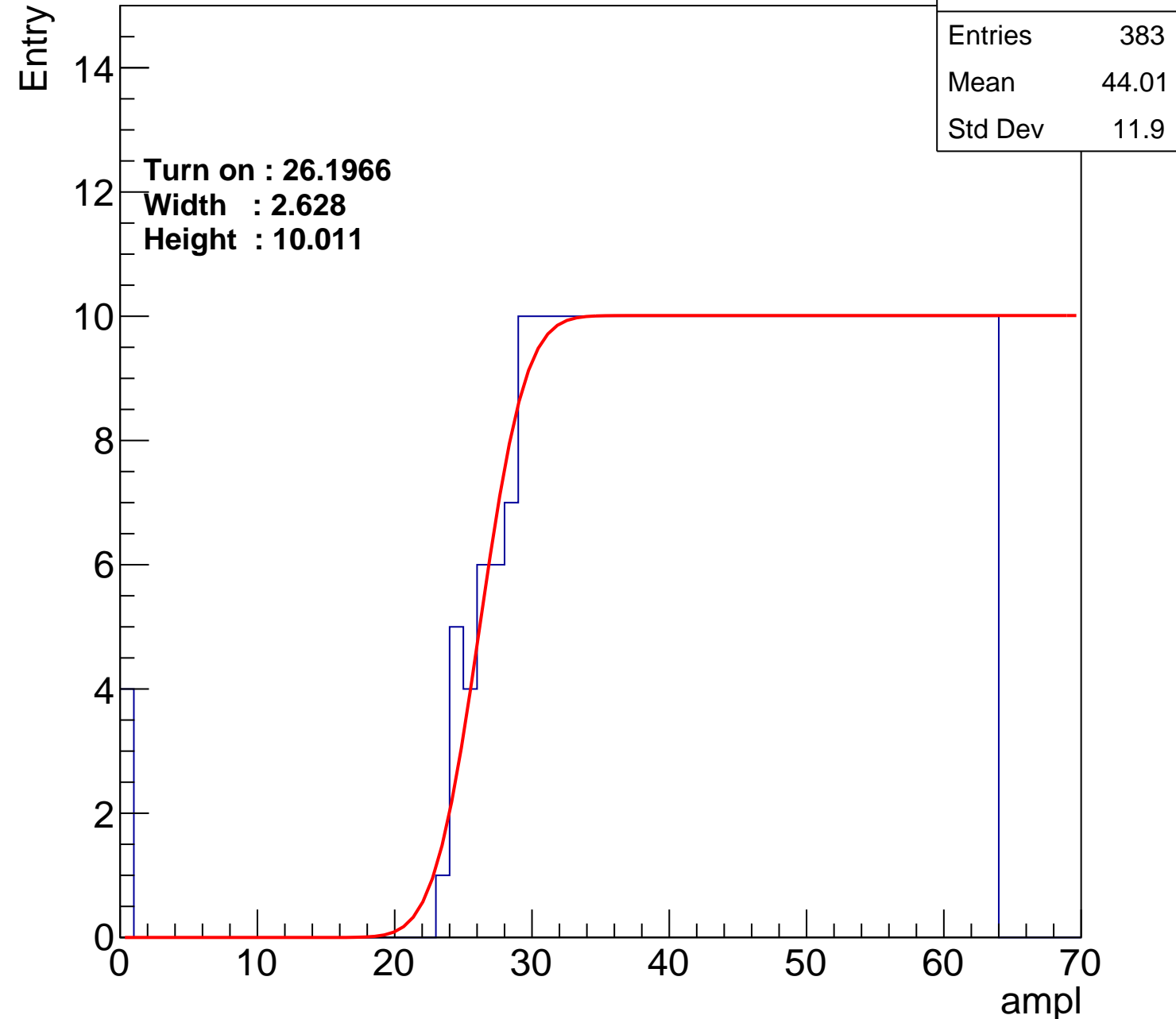
Width : 2.628

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44
Std Dev	11.8

Turn on : 26.1860

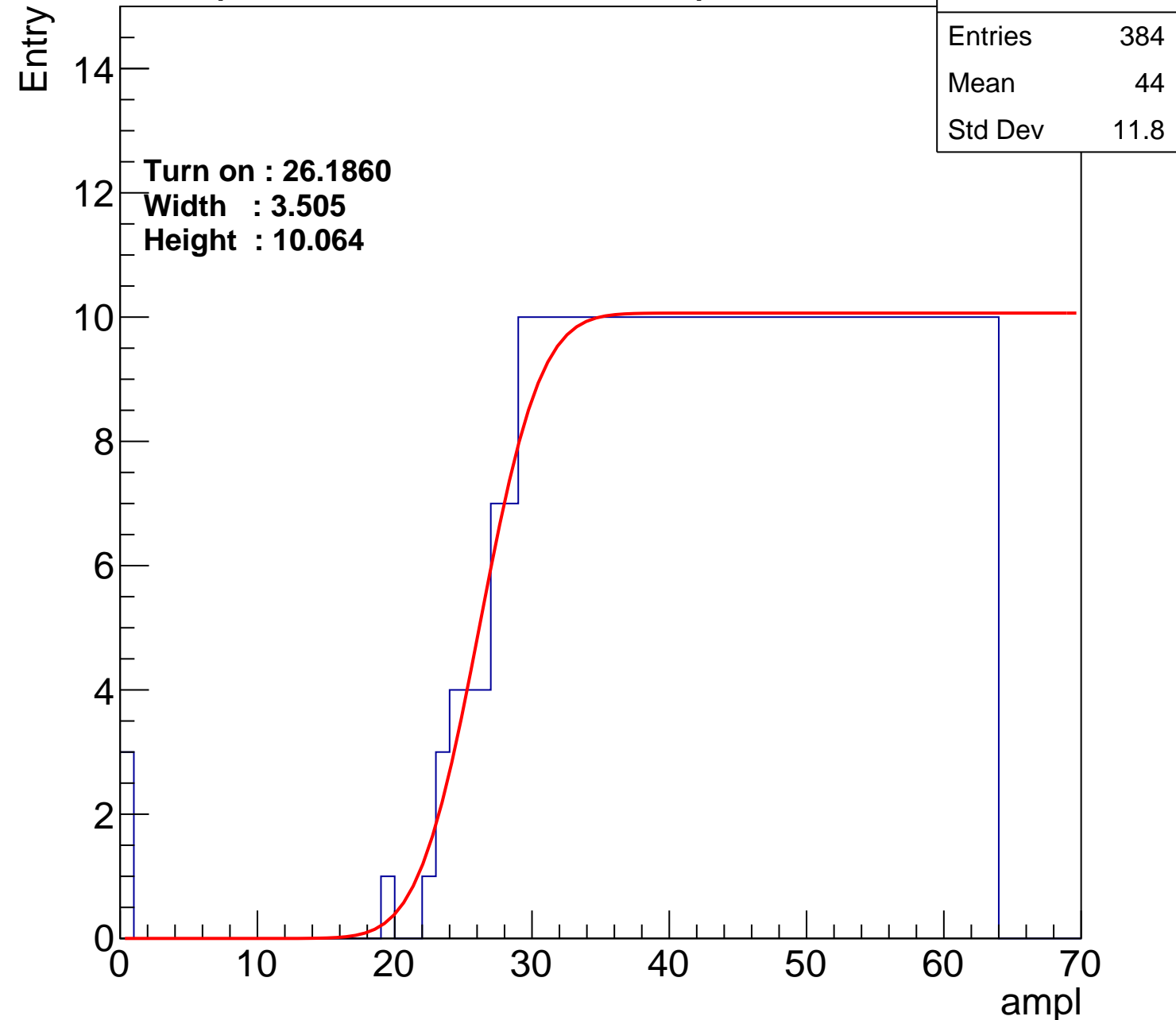
Width : 3.505

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.71
Std Dev	12.06

Turn on : 25.9148

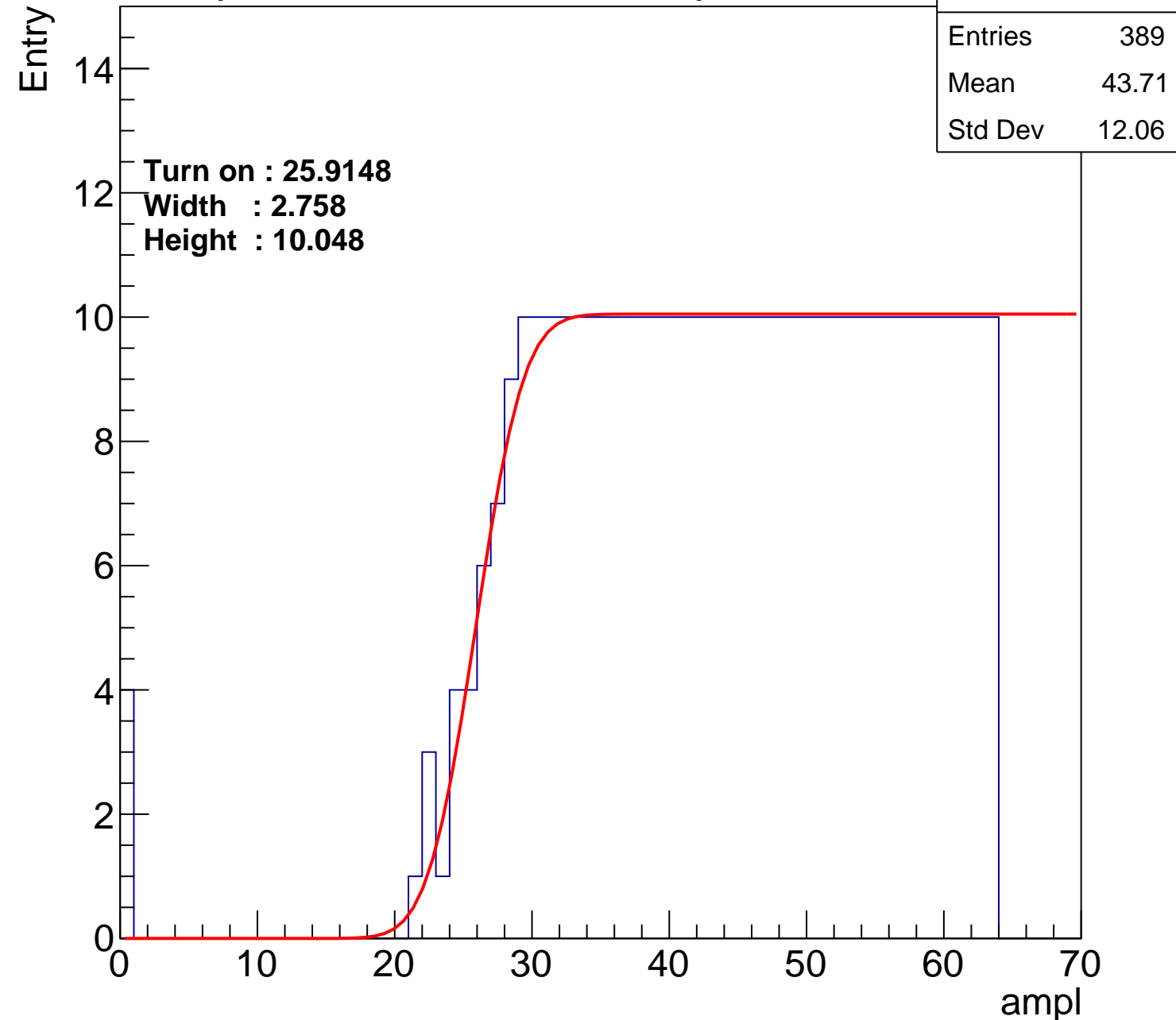
Width : 2.758

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.5606

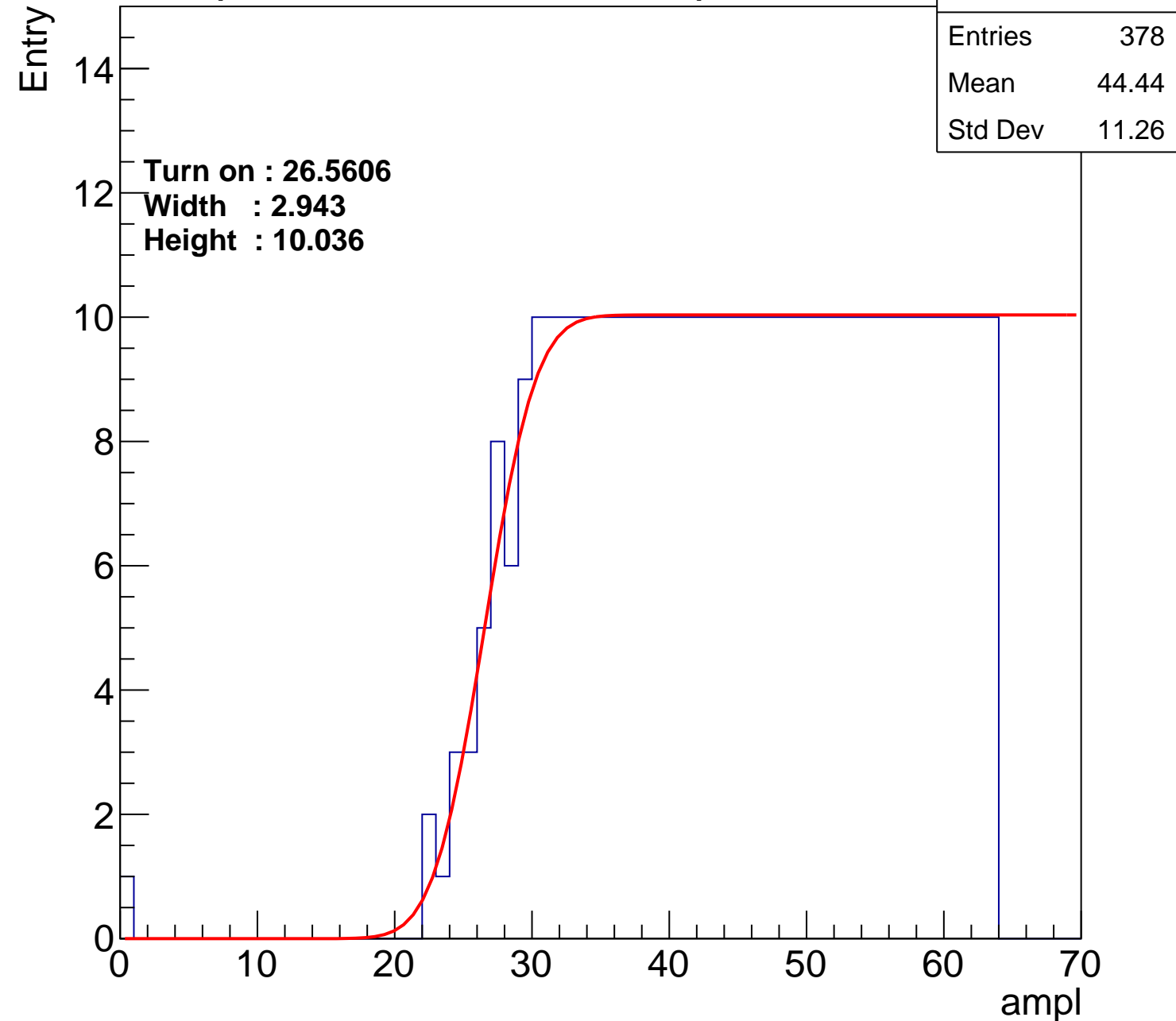
Width : 2.943

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.22
Std Dev	11.84

Turn on : 27.1132

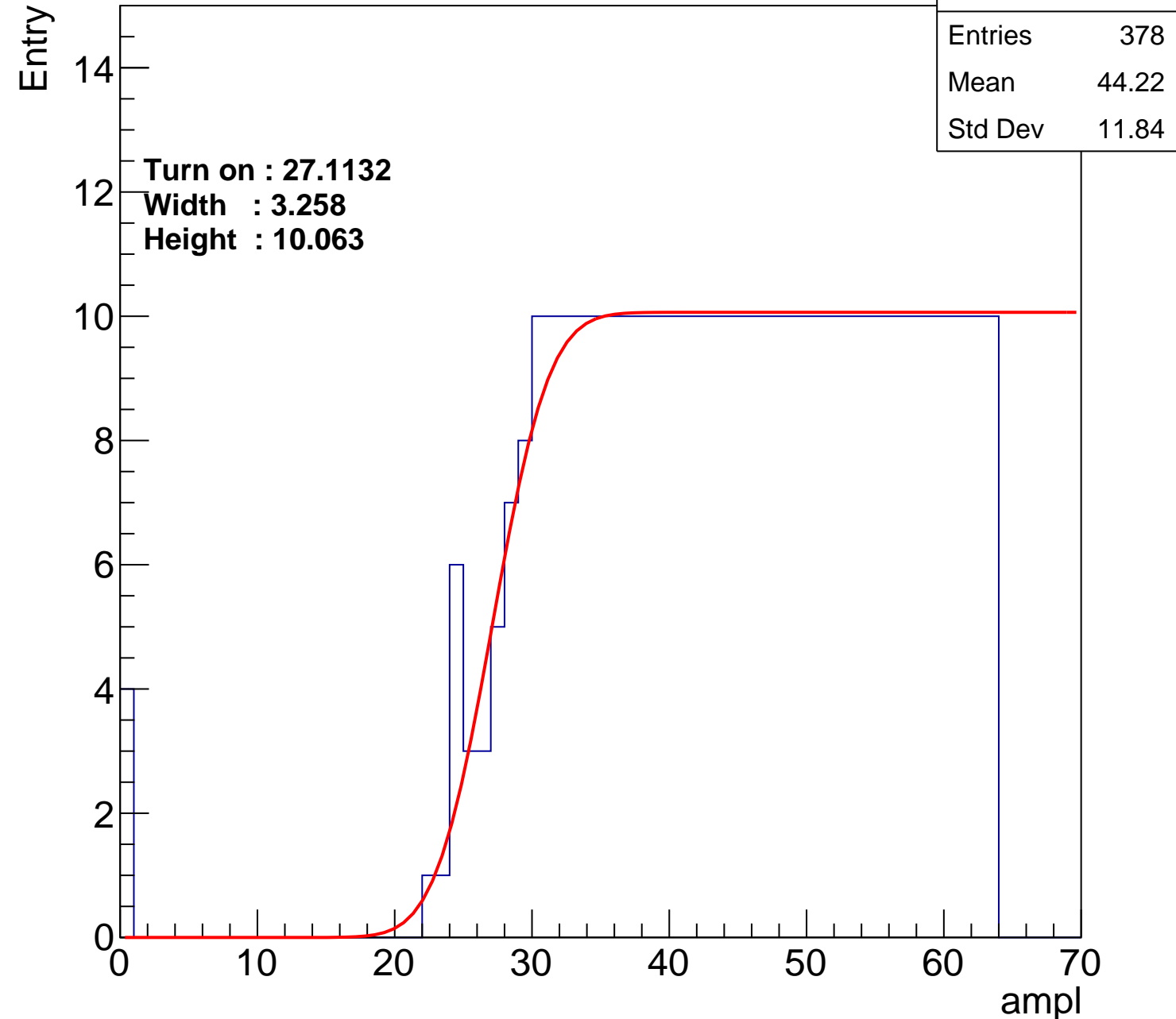
Width : 3.258

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch119

calib_packv5_042523_0143.root, FC#3, port B3

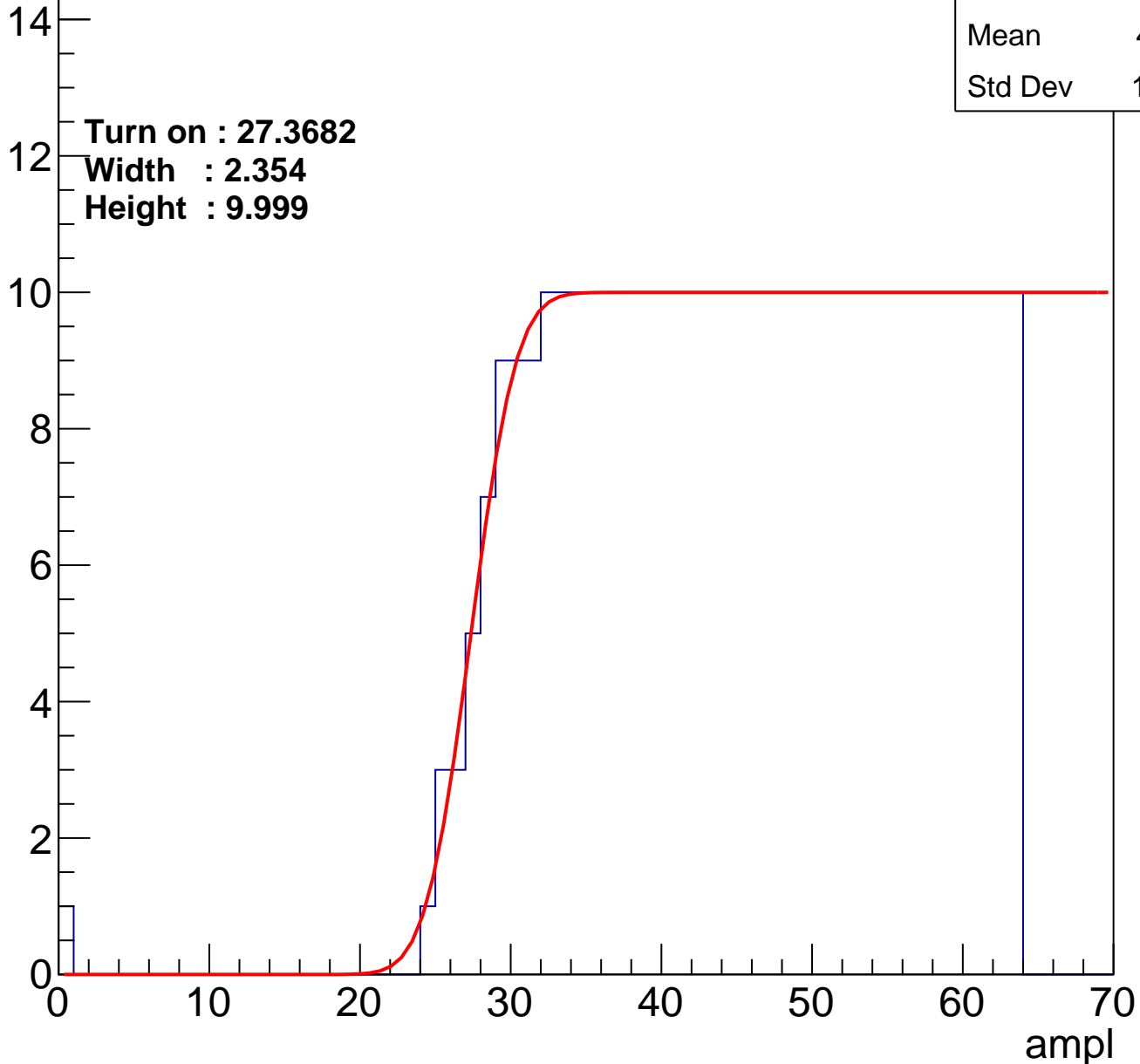
Entries	367
Mean	45.01
Std Dev	10.92

Turn on : 27.3682

Width : 2.354

Height : 9.999

Entry



B1L000S, U10-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 25.6123

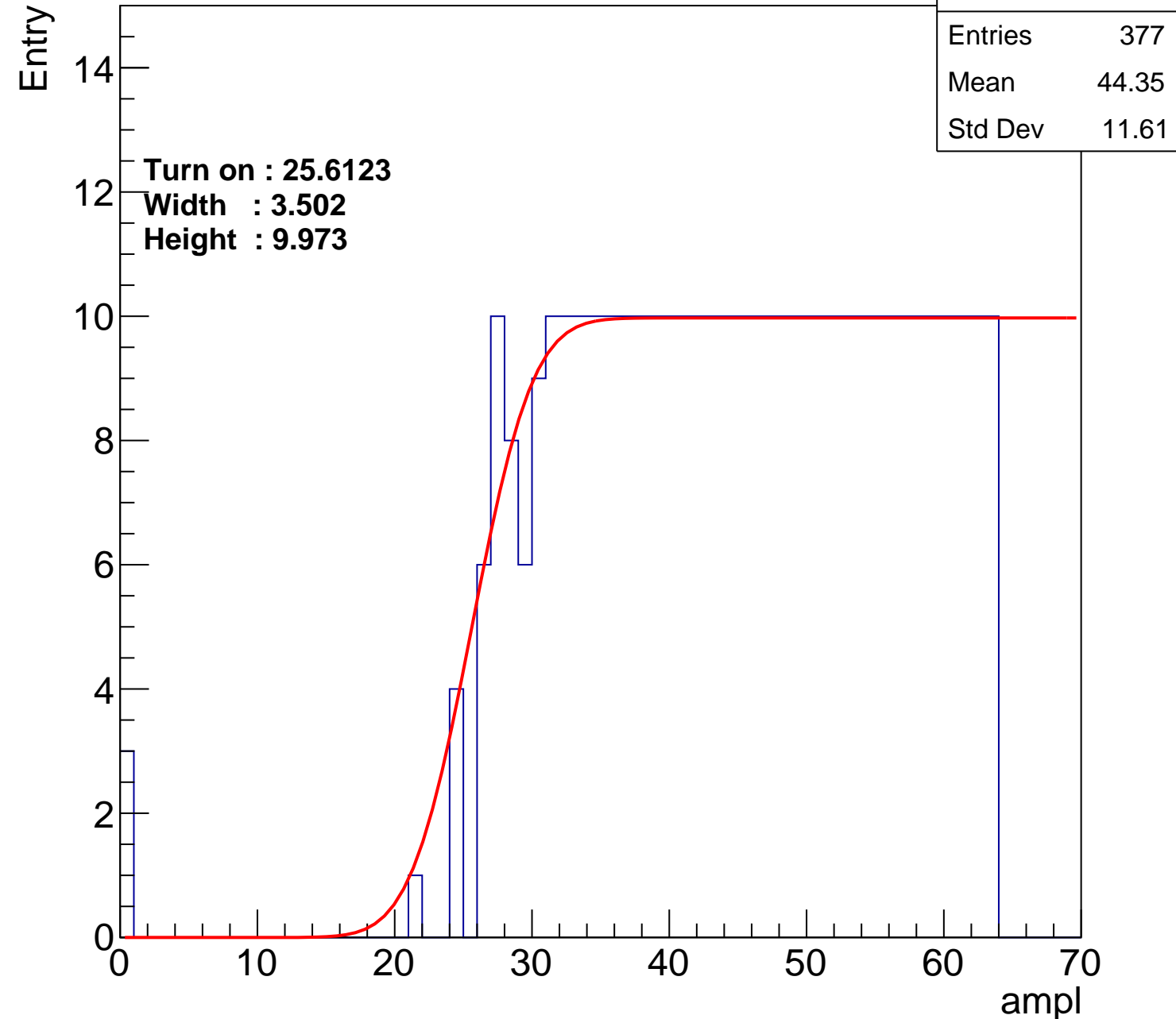
Width : 3.502

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.62
Std Dev	11.46

Turn on : 27.4356

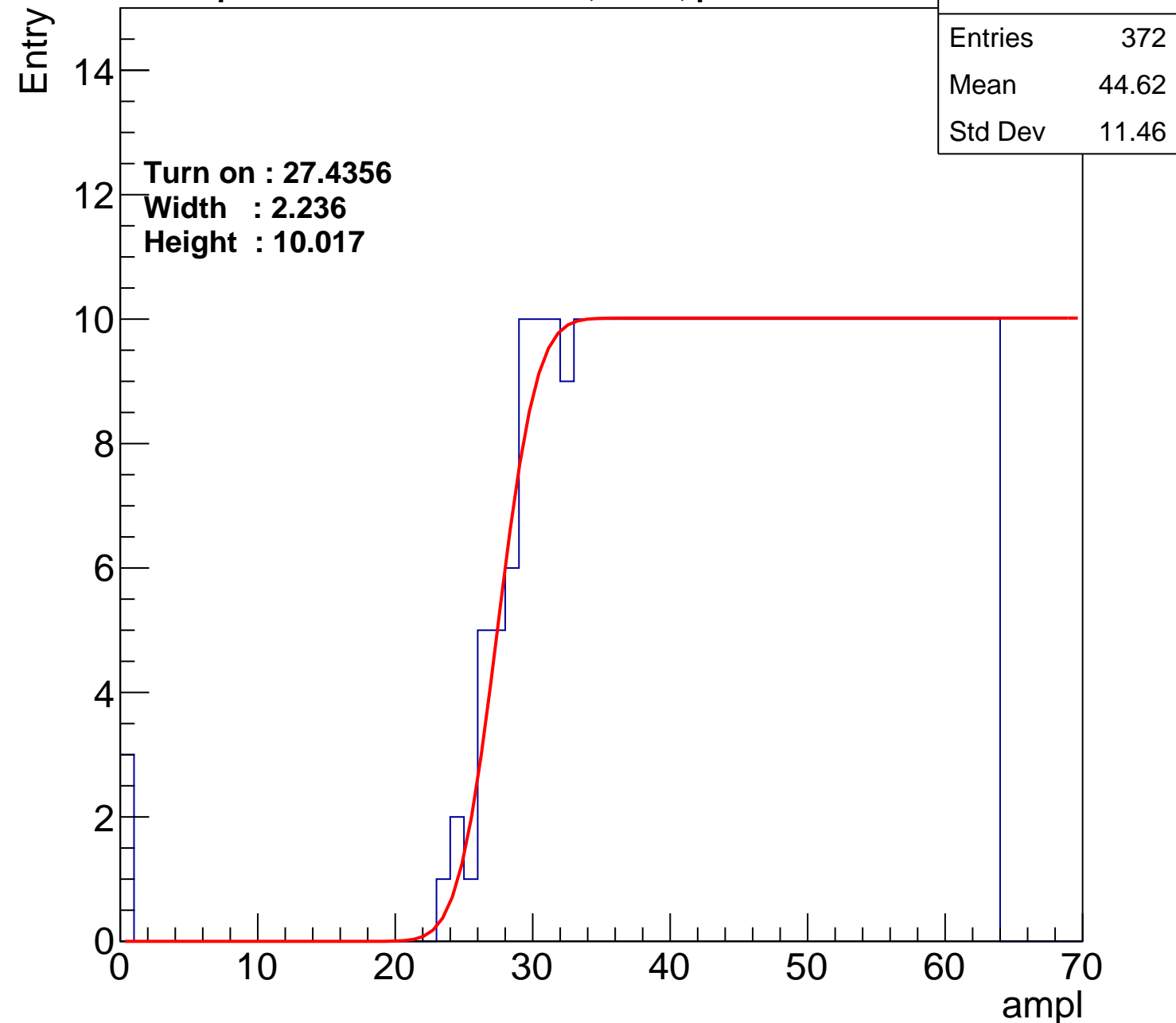
Width : 2.236

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch122

calib_packv5_042523_0143.root, FC#3, port B3

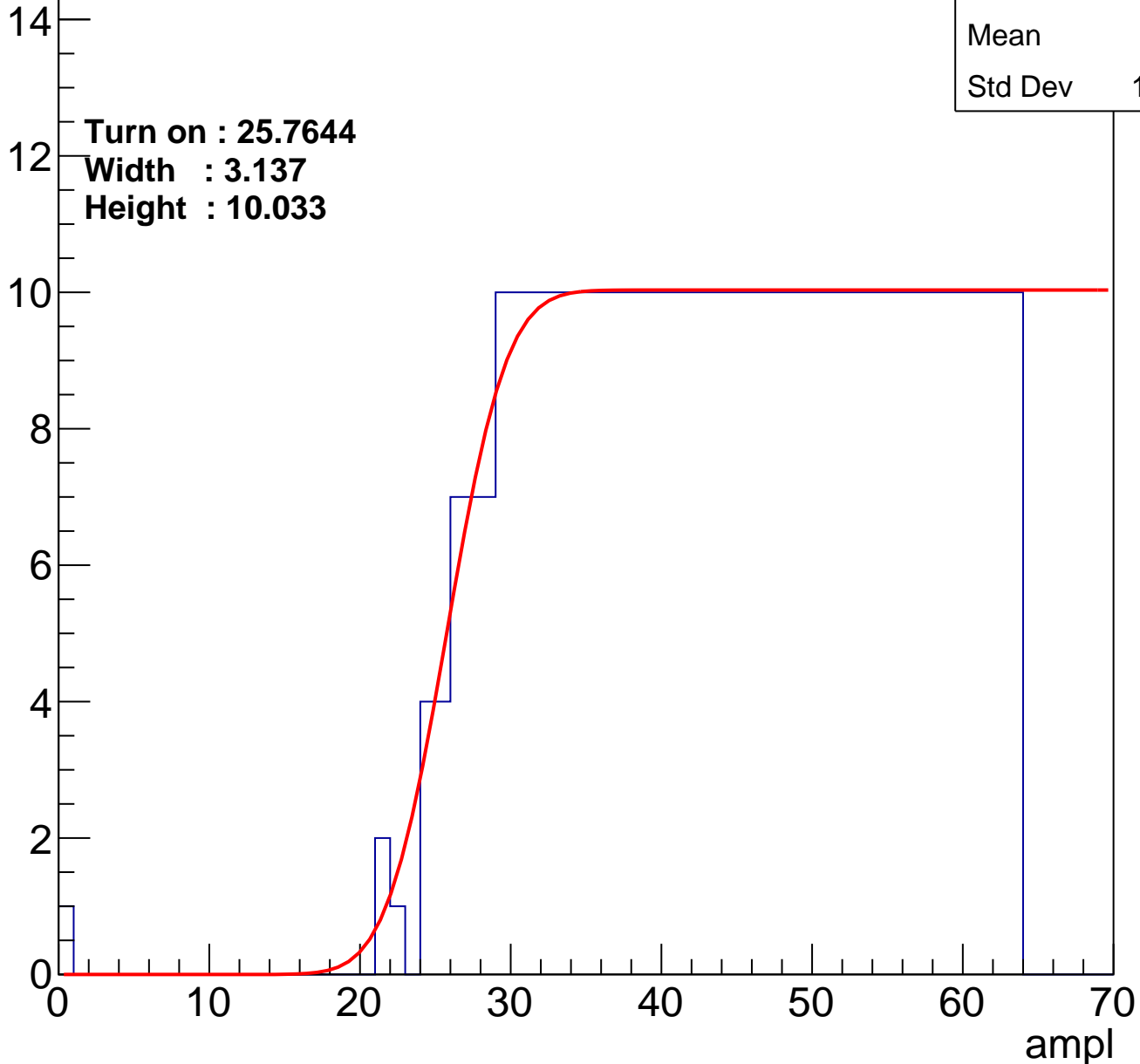
Entries	383
Mean	44.2
Std Dev	11.39

Turn on : 25.7644

Width : 3.137

Height : 10.033

Entry



B1L000S, U10-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.94
Std Dev	11.32

Turn on : 27.7812

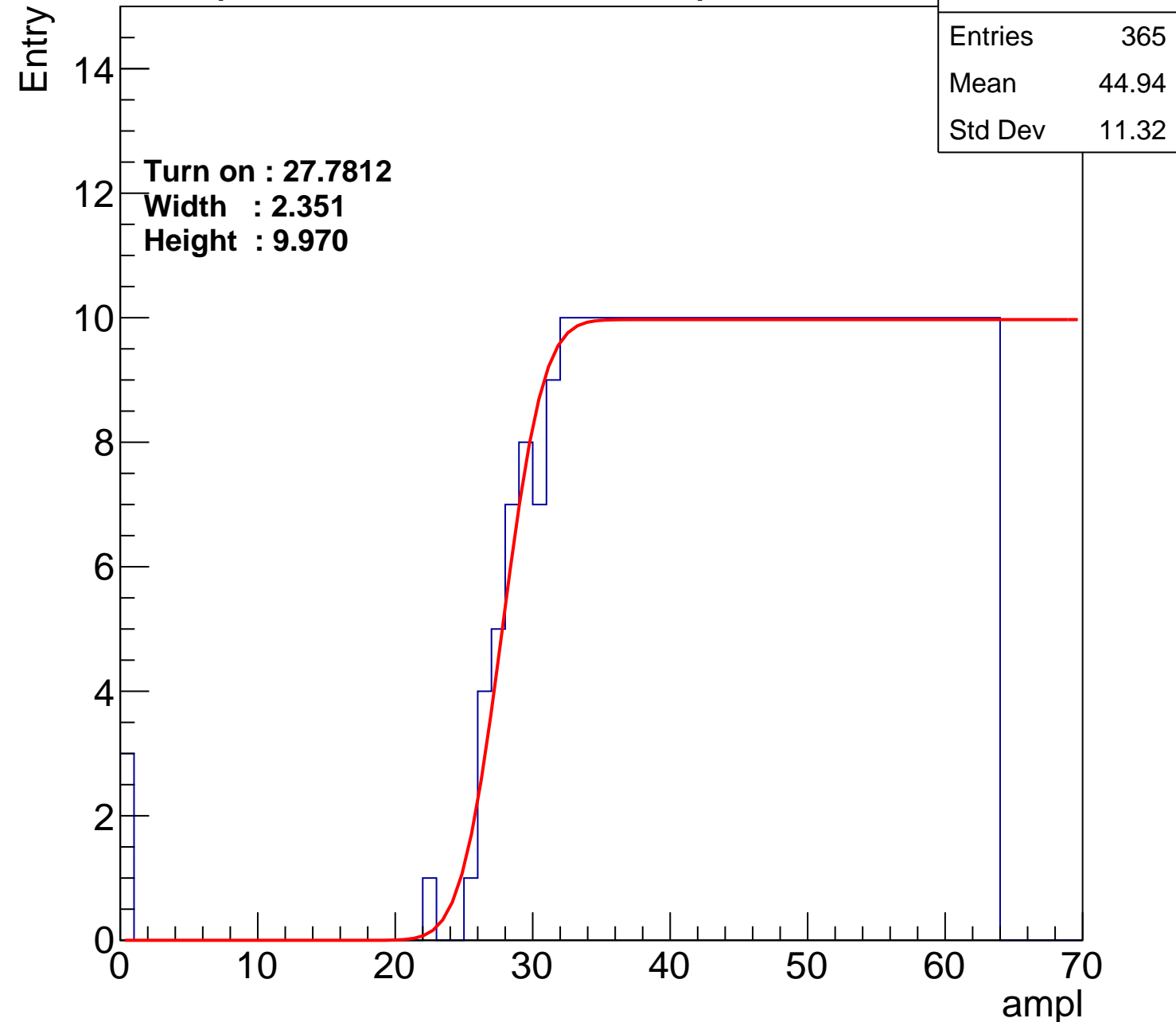
Width : 2.351

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	43.95
Std Dev	12.19

Turn on : 26.5367

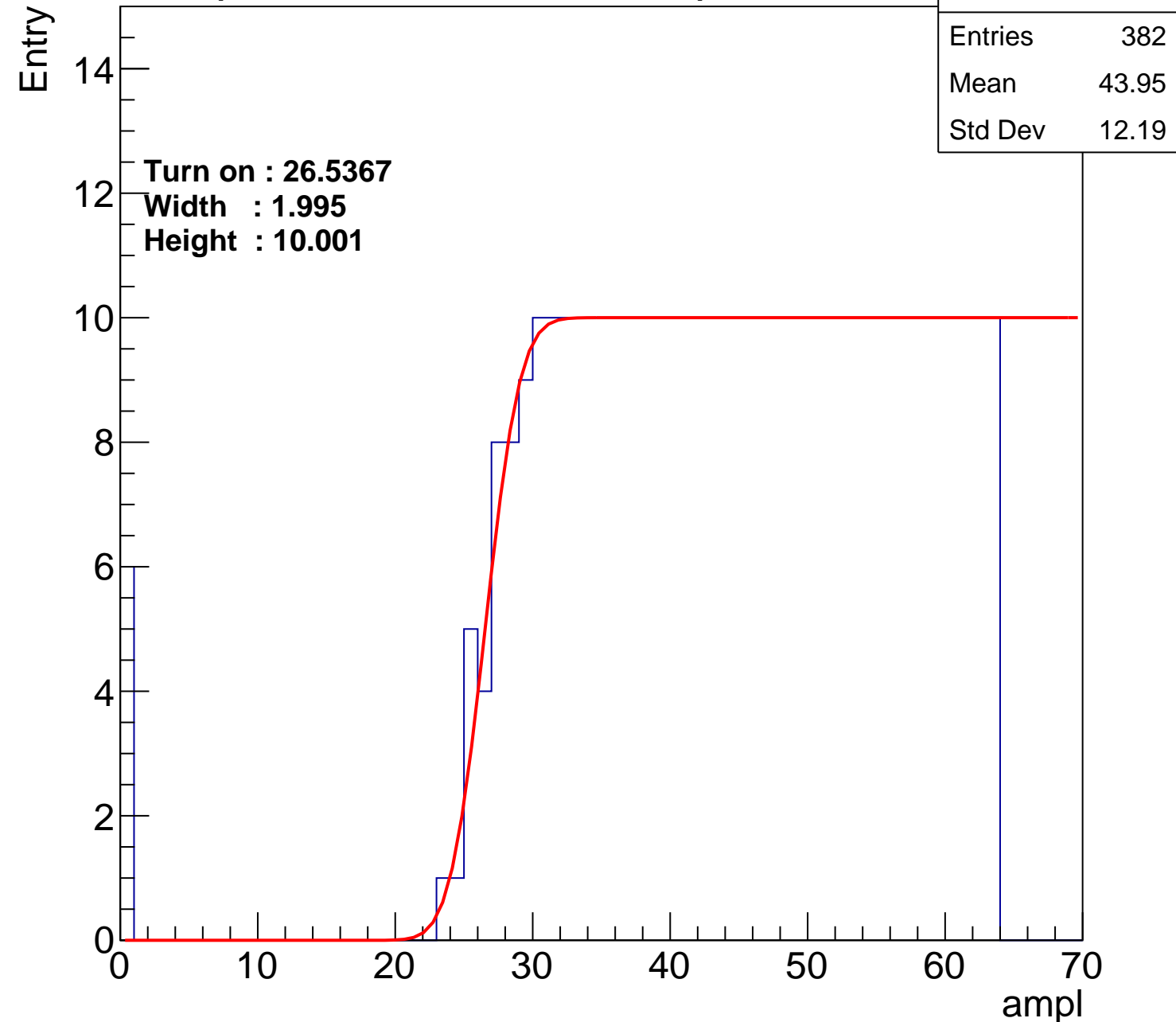
Width : 1.995

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.1625

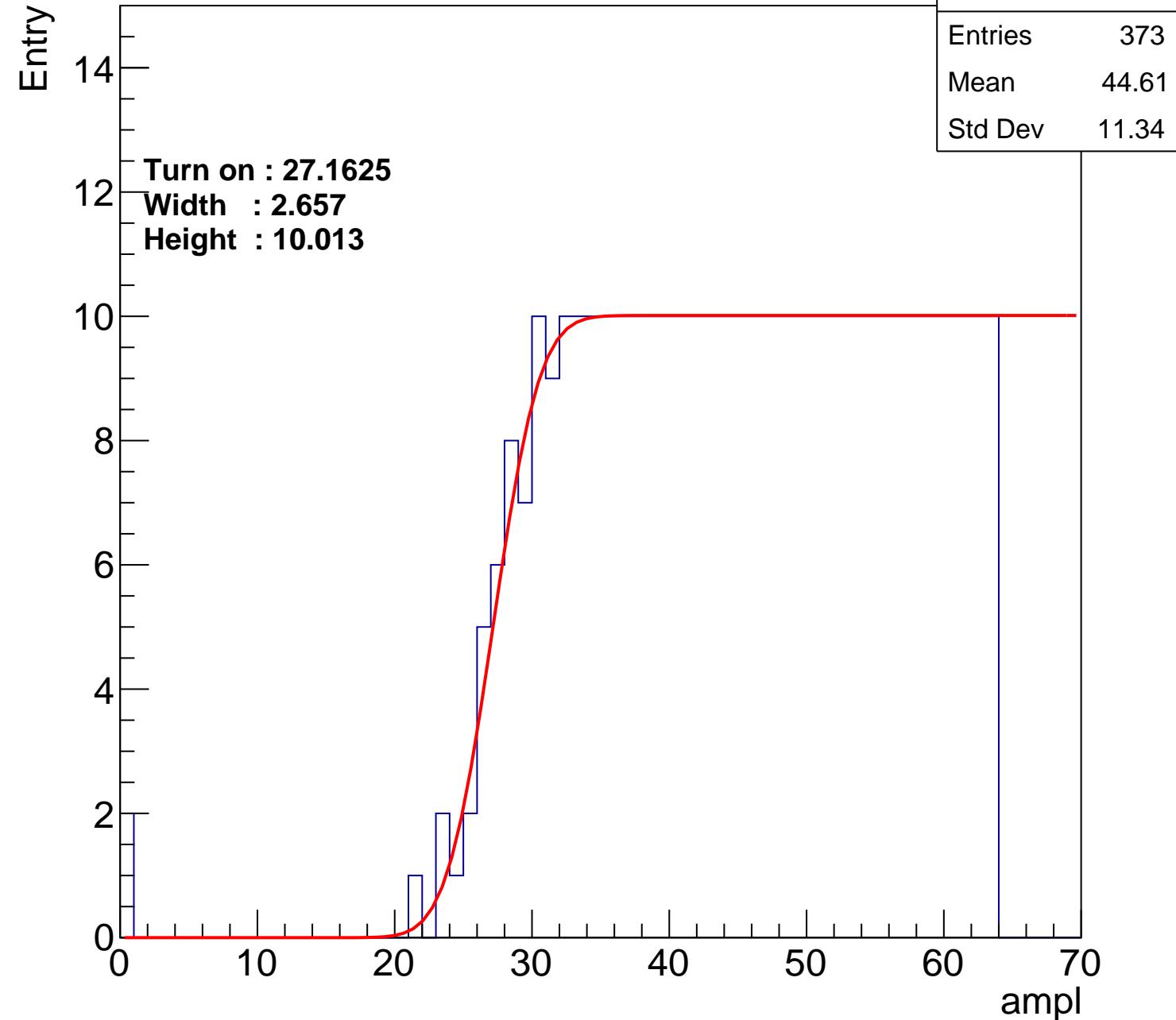
Width : 2.657

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U10-ch126

calib_packv5_042523_0143.root, FC#3, port B3

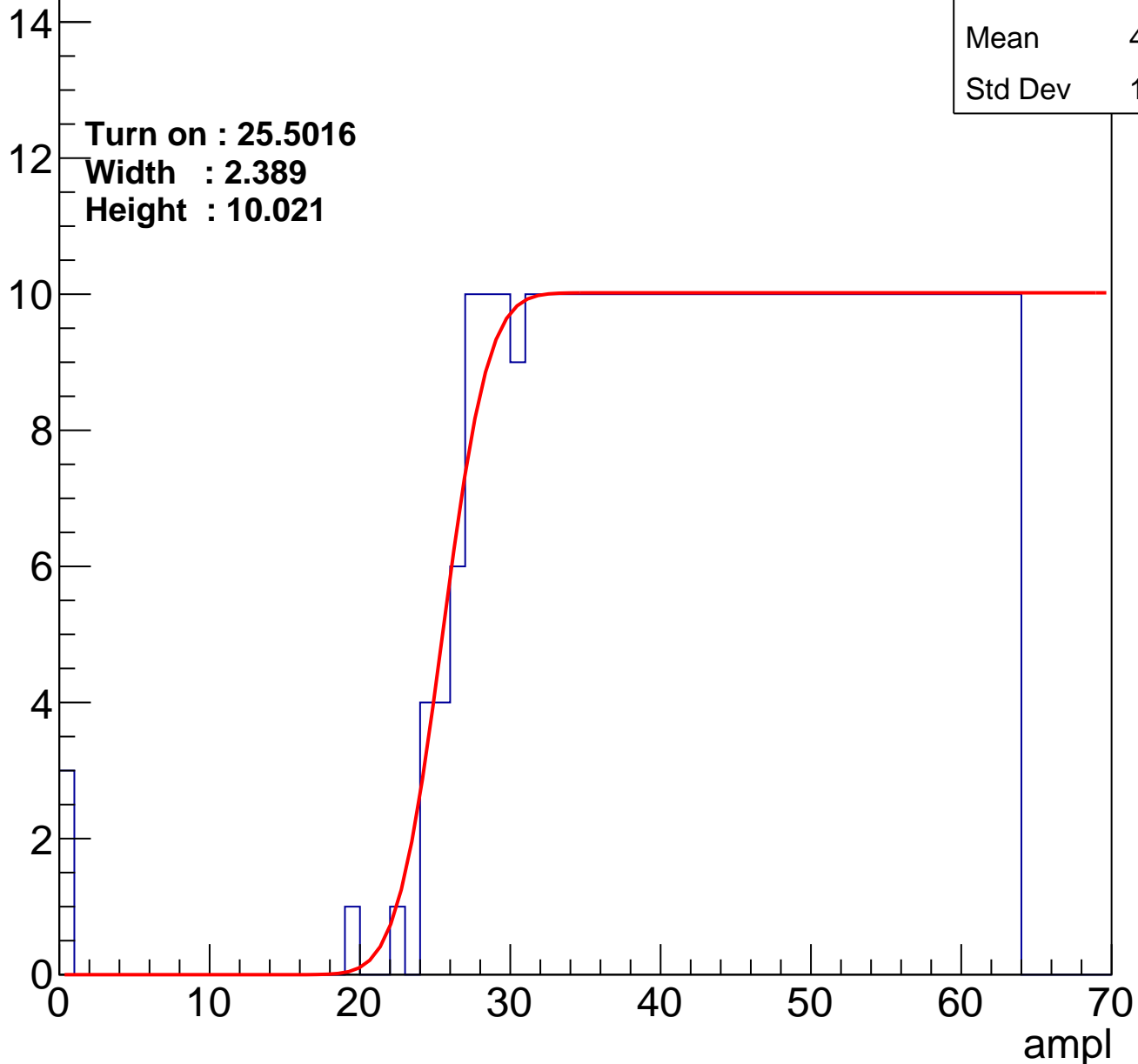
Entries	388
Mean	43.85
Std Dev	11.82

Turn on : 25.5016

Width : 2.389

Height : 10.021

Entry



B1L000S, U10-ch127

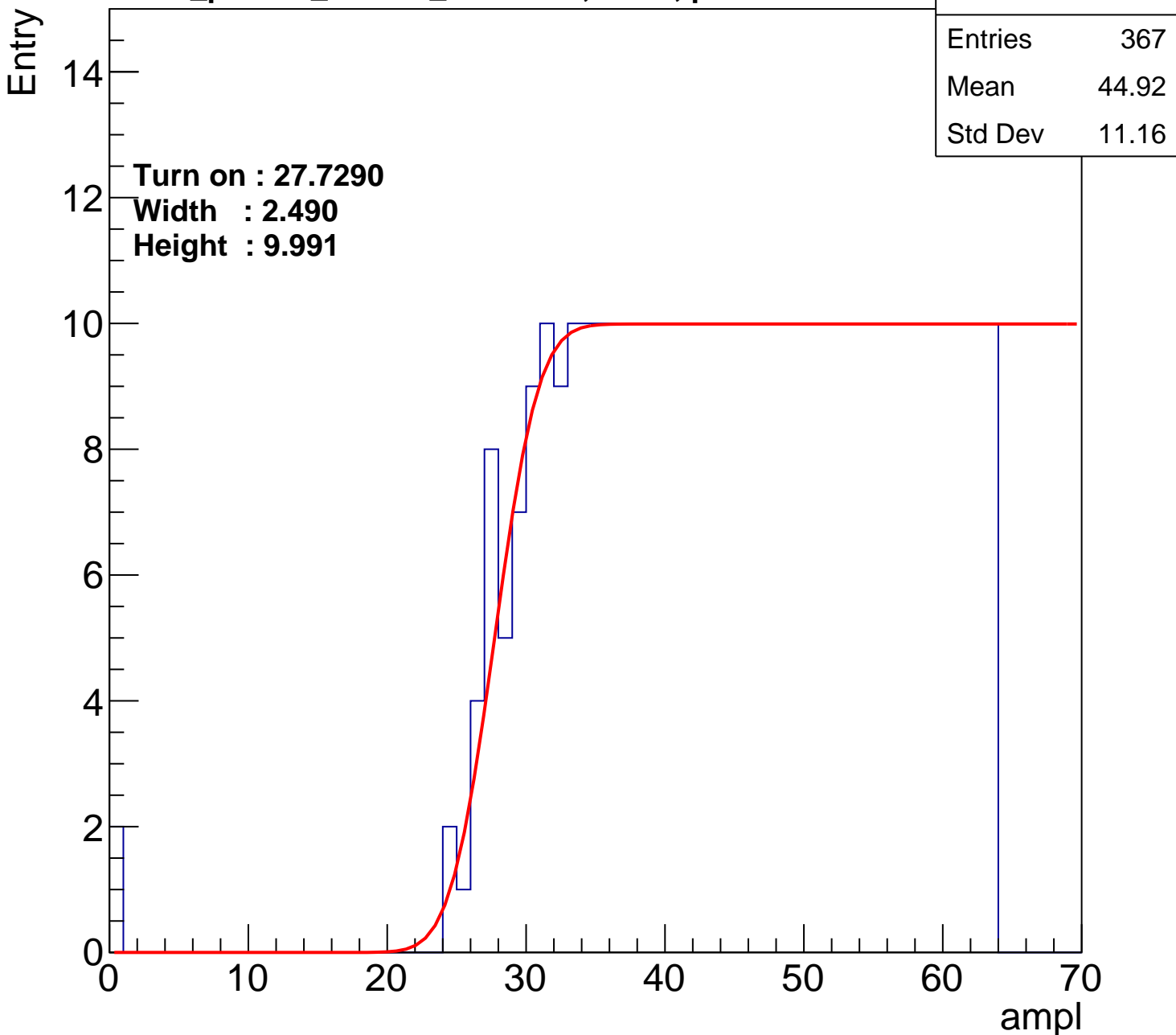
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.7290

Width : 2.490

Height : 9.991

Entries	367
Mean	44.92
Std Dev	11.16



B1L000S, U10-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.7290

Width : 2.490

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl

