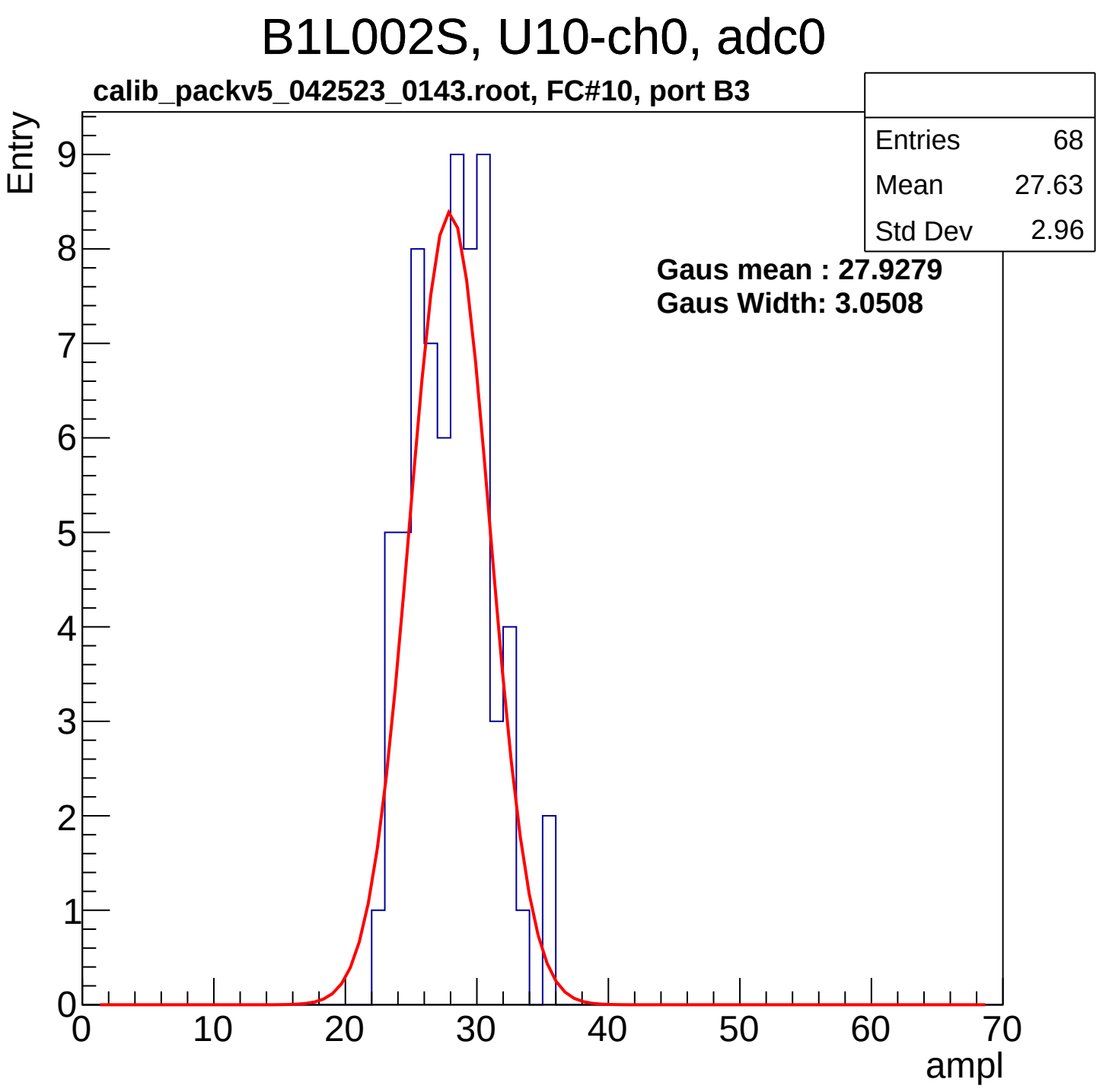
B1L002S, U10-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.63
Std Dev 2.96
Gaus mean : 27.9279
Gaus Width: 3.0508
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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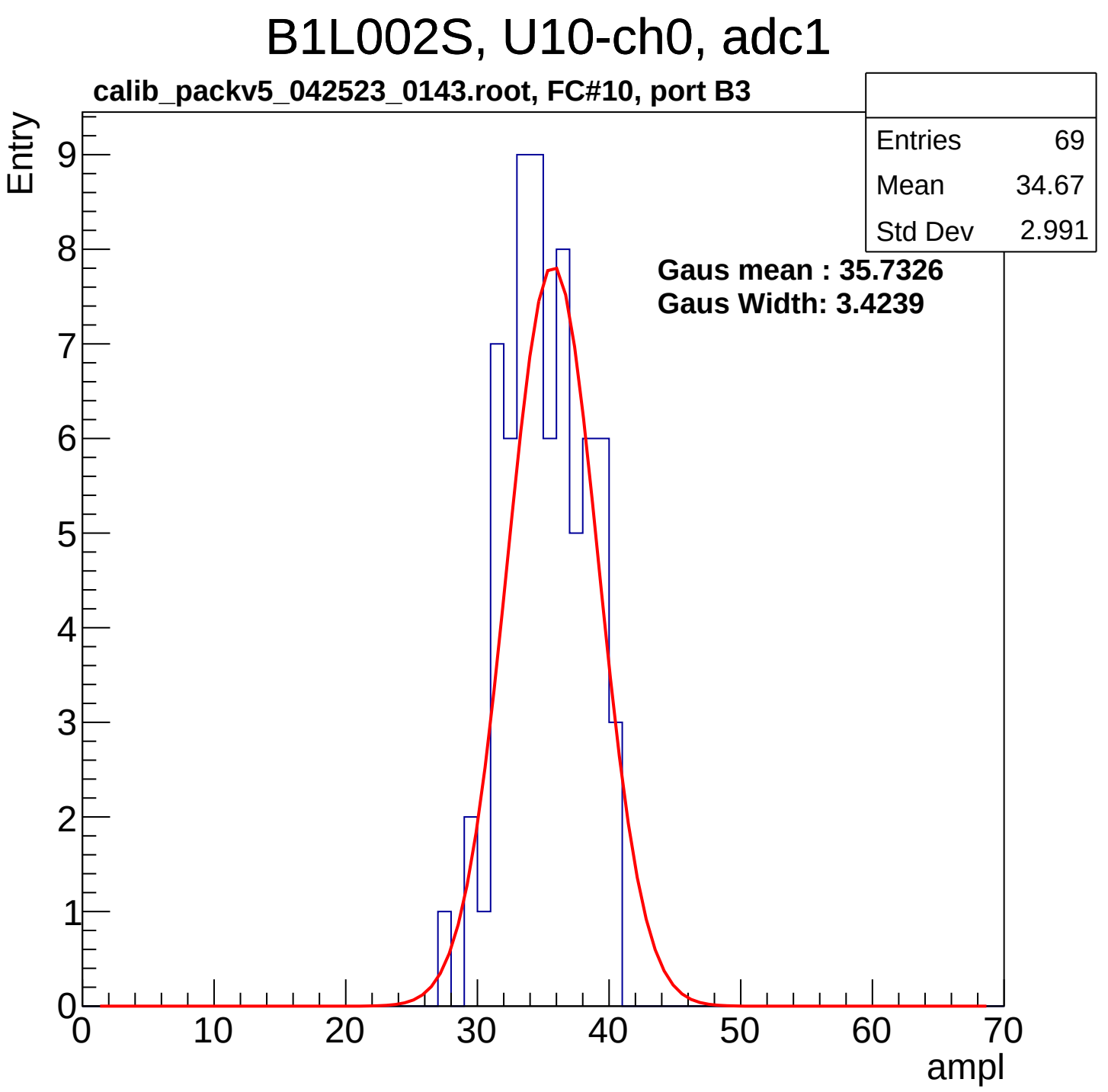
B1L002S, U10-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.67
Std Dev 2.991
Gaus mean : 35.7326
Gaus Width: 3.4239
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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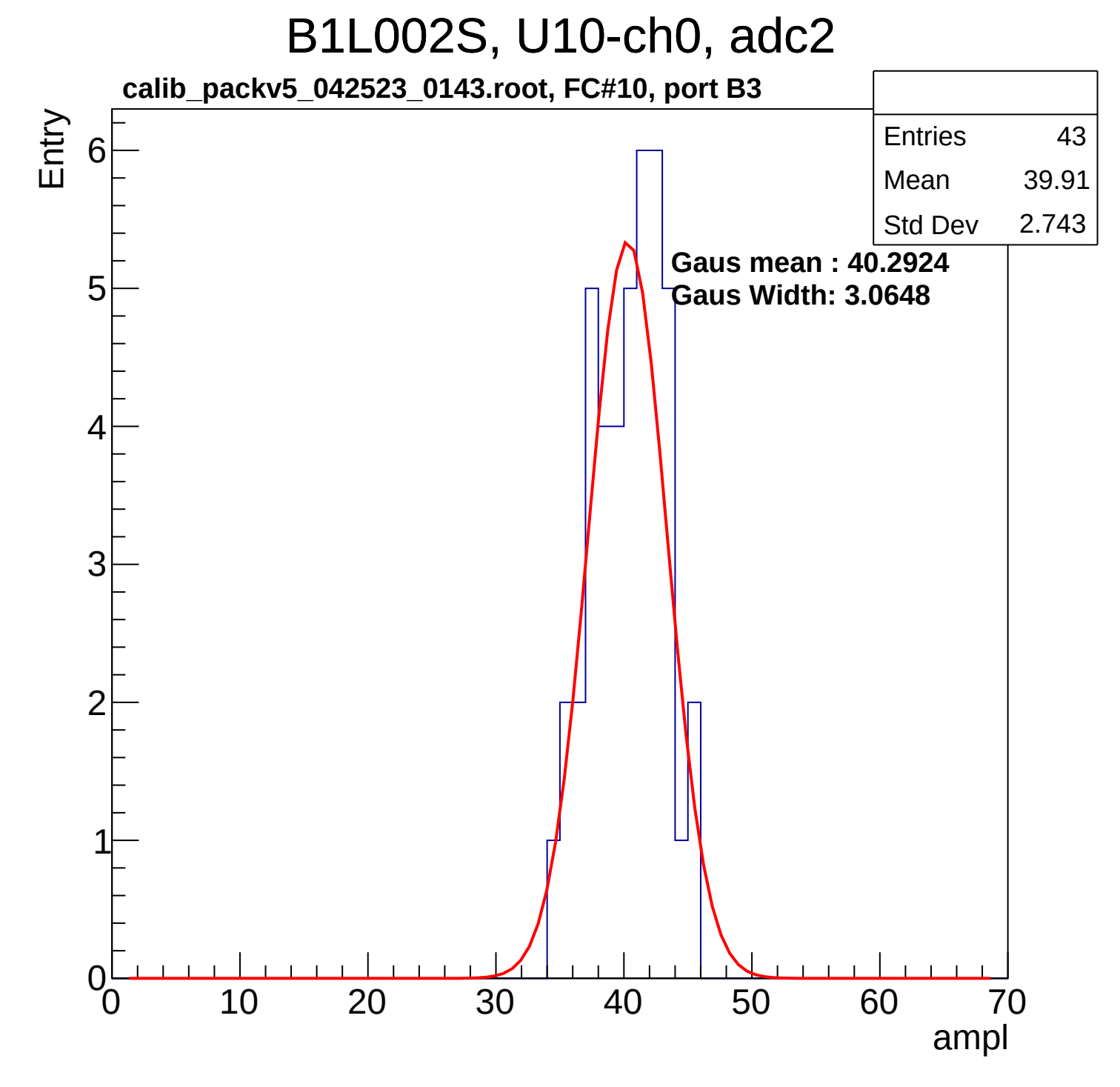
B1L002S, U10-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 39.91
Std Dev 2.743
Gaus mean : 40.2924
Gaus Width: 3.0648
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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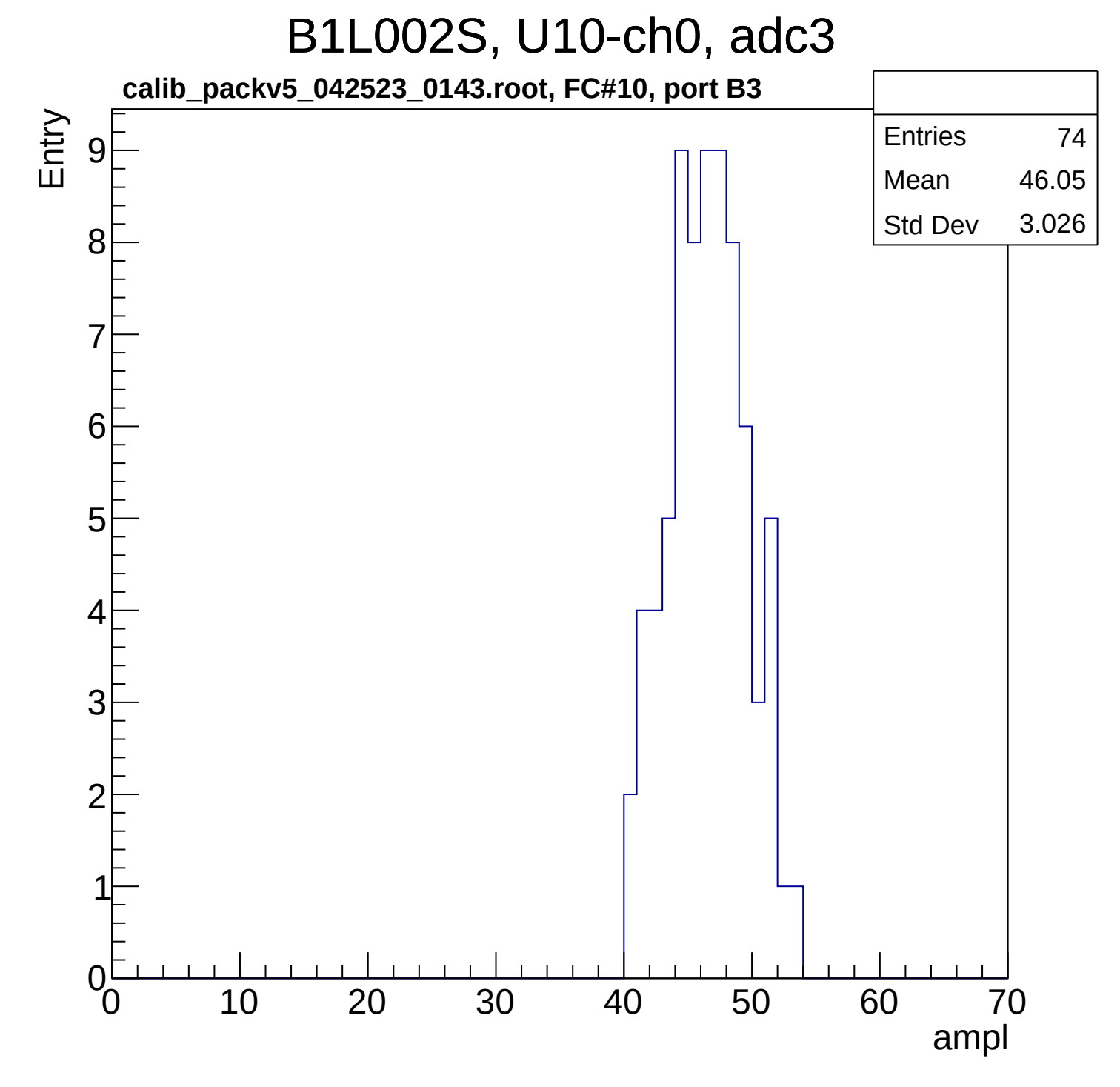
B1L002S, U10-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.05
Std Dev 3.026
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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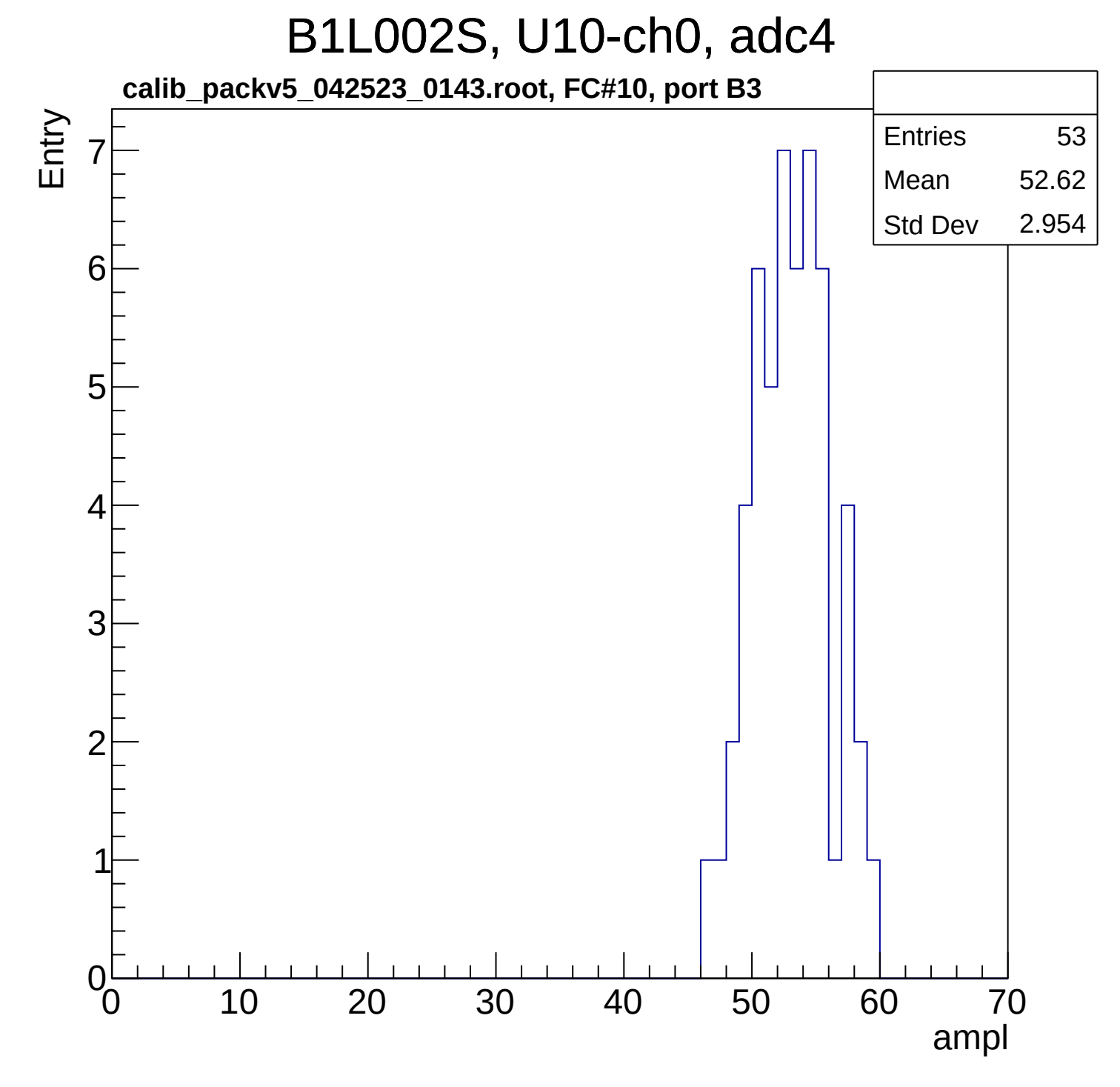
B1L002S, U10-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.62
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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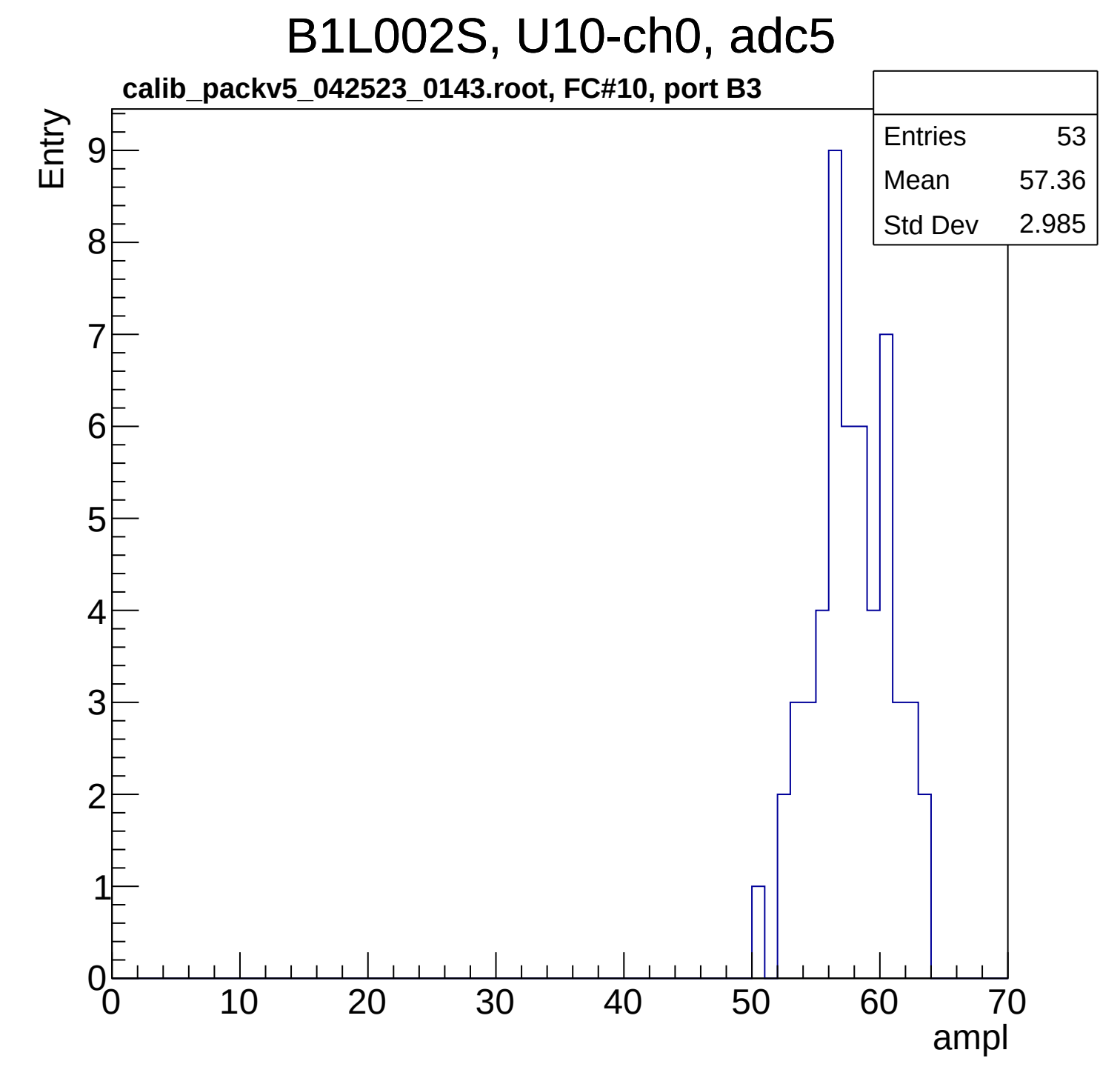
B1L002S, U10-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.36
Std Dev 2.985
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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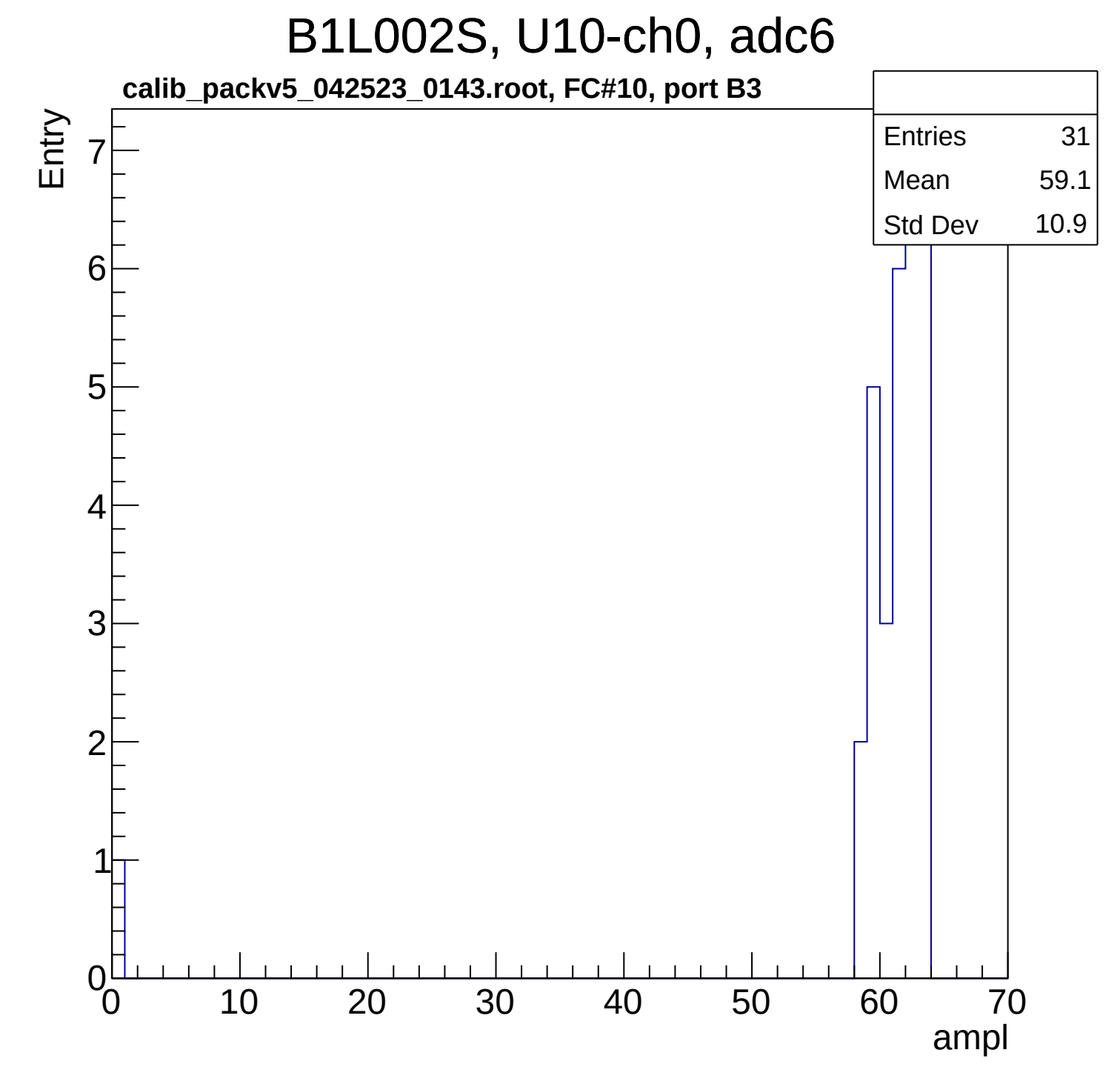
B1L002S, U10-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.1
Std Dev 10.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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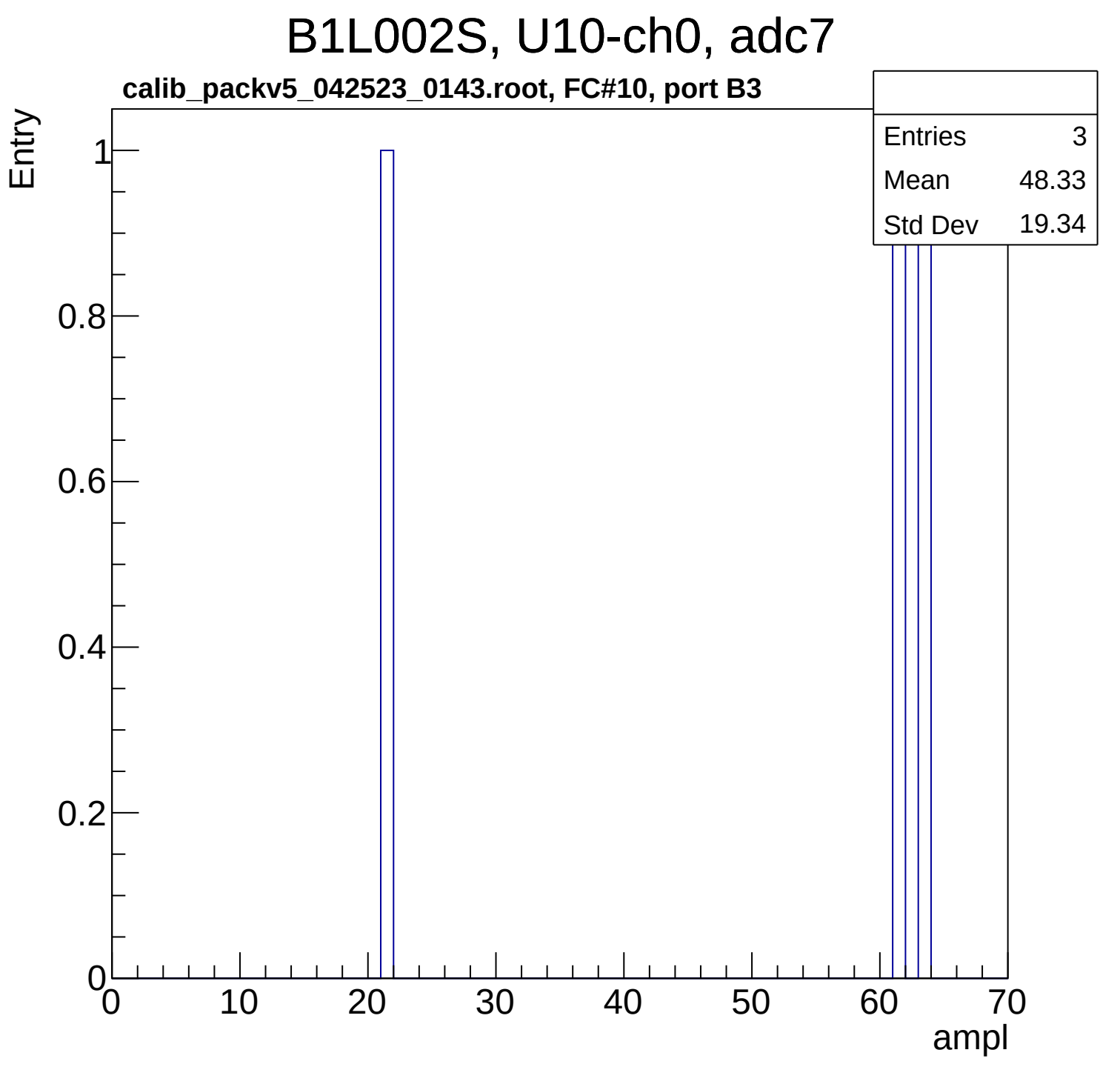
B1L002S, U10-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 19.34
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
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60
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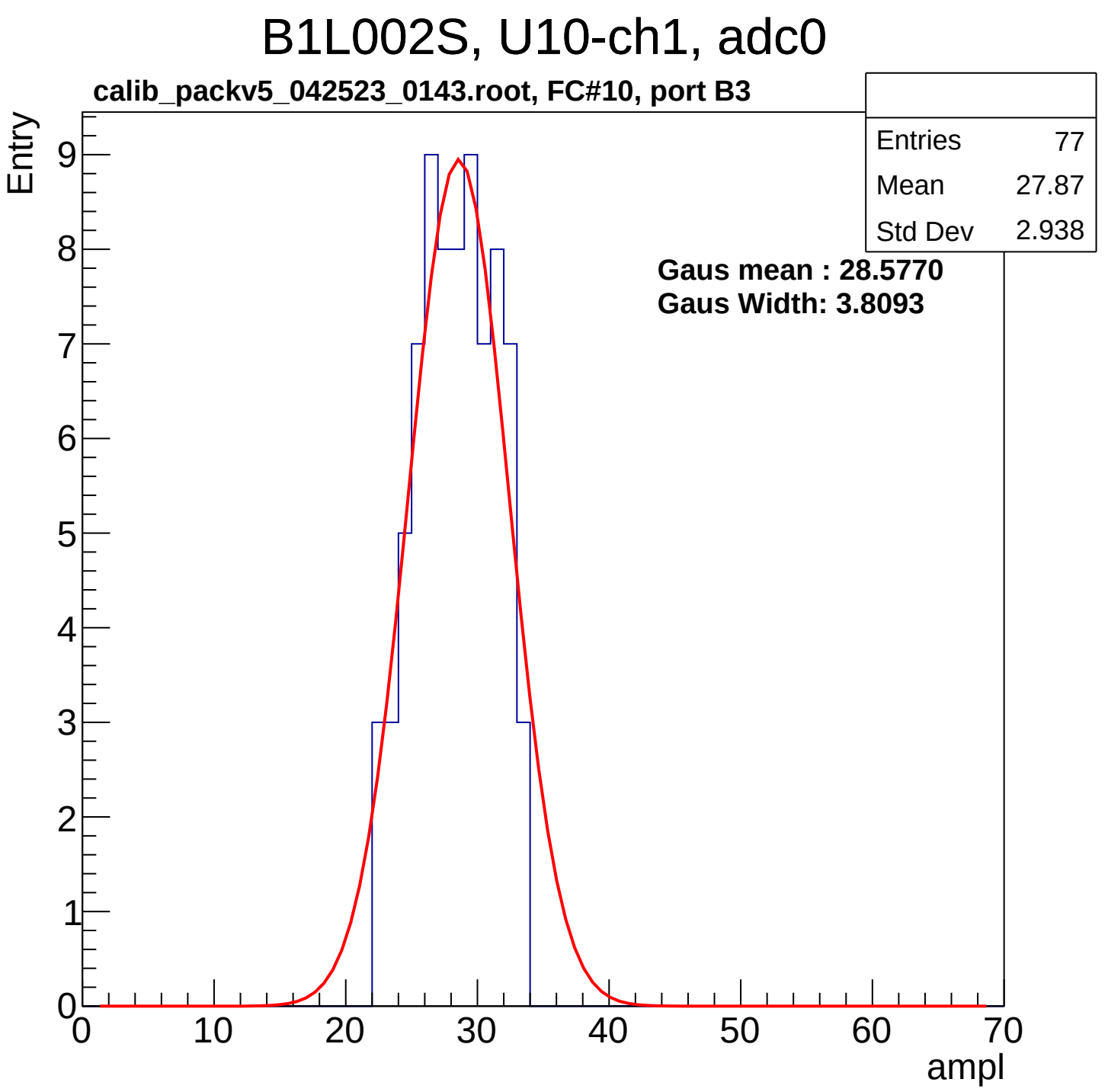
B1L002S, U10-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.87
Std Dev 2.938
Gaus mean : 28.5770
Gaus Width: 3.8093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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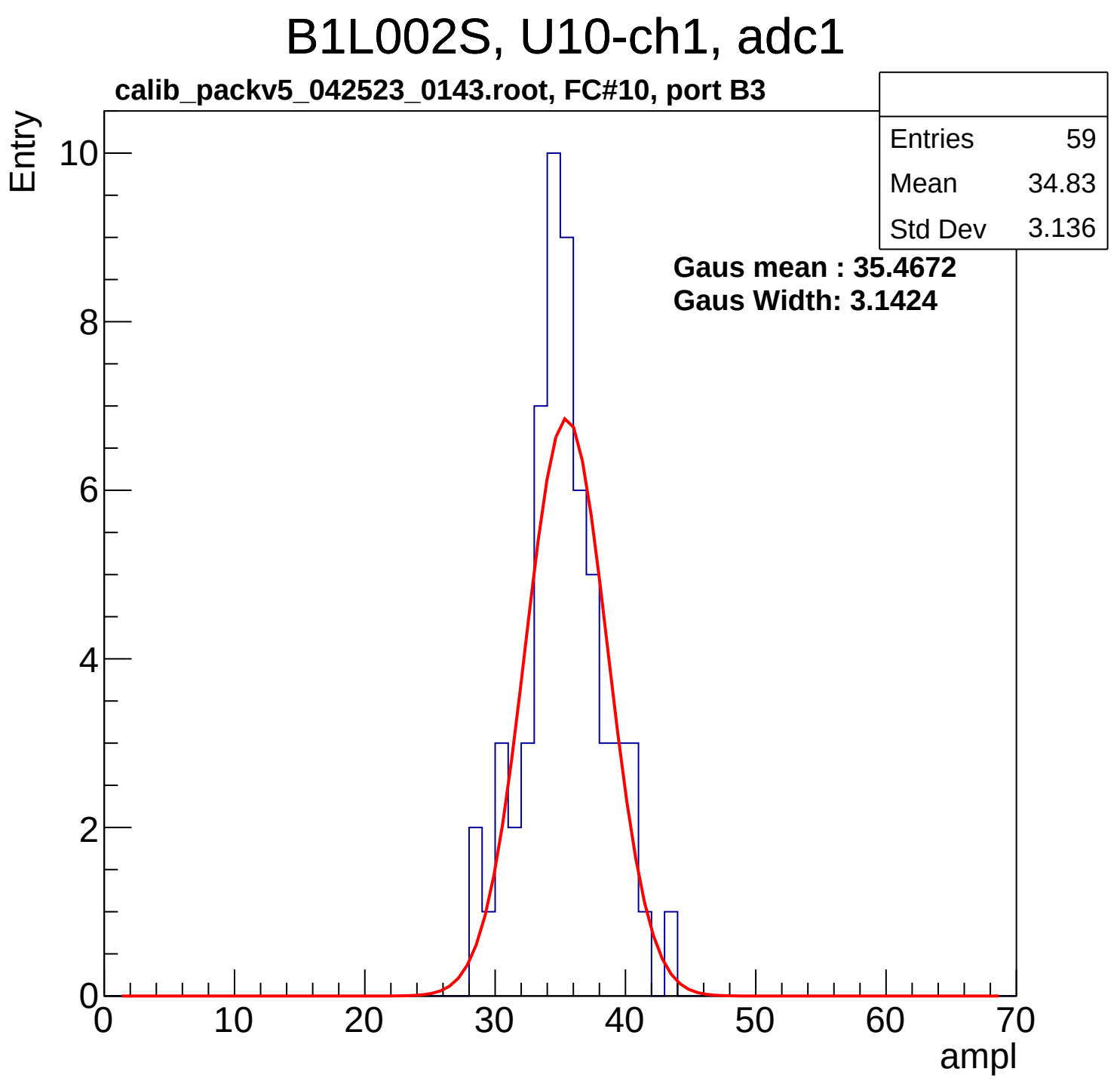
B1L002S, U10-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.83
Std Dev 3.136
Gaus mean : 35.4672
Gaus Width: 3.1424
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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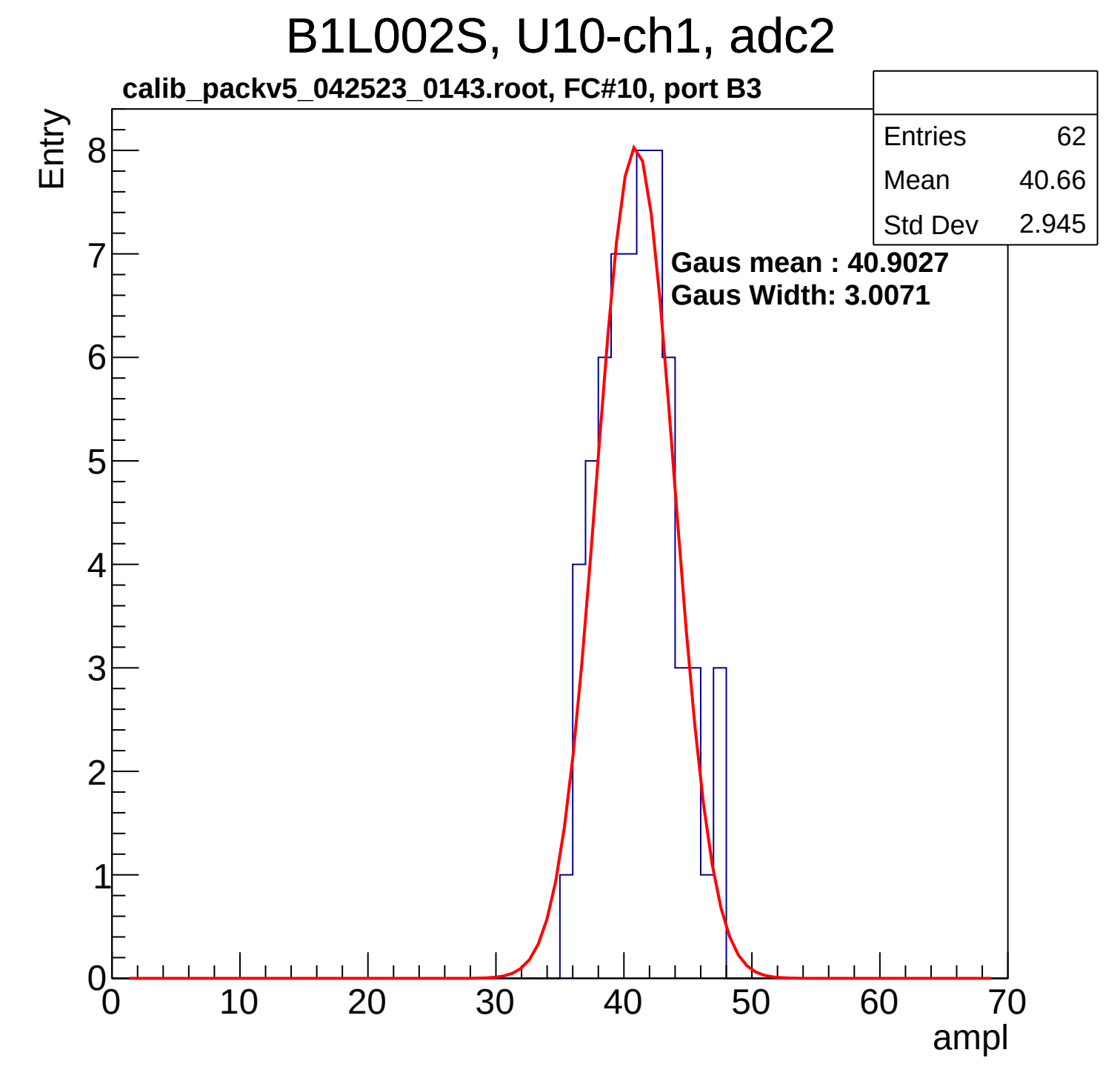
B1L002S, U10-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.66
Std Dev 2.945
Gaus mean : 40.9027
Gaus Width: 3.0071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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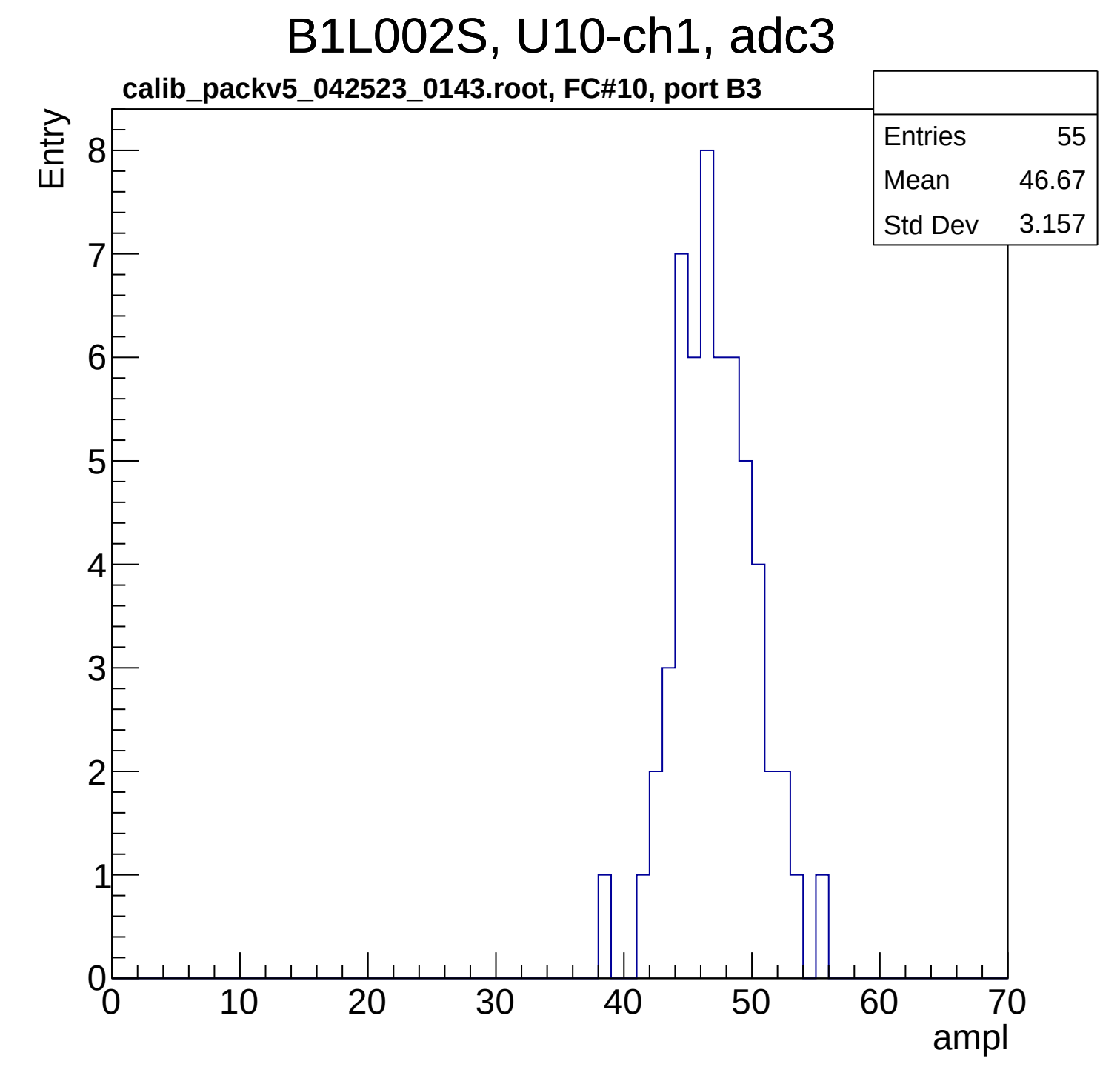
B1L002S, U10-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.67
Std Dev 3.157
Entry
ampl
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1
2
3
4
5
6
7
8
0
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50
60
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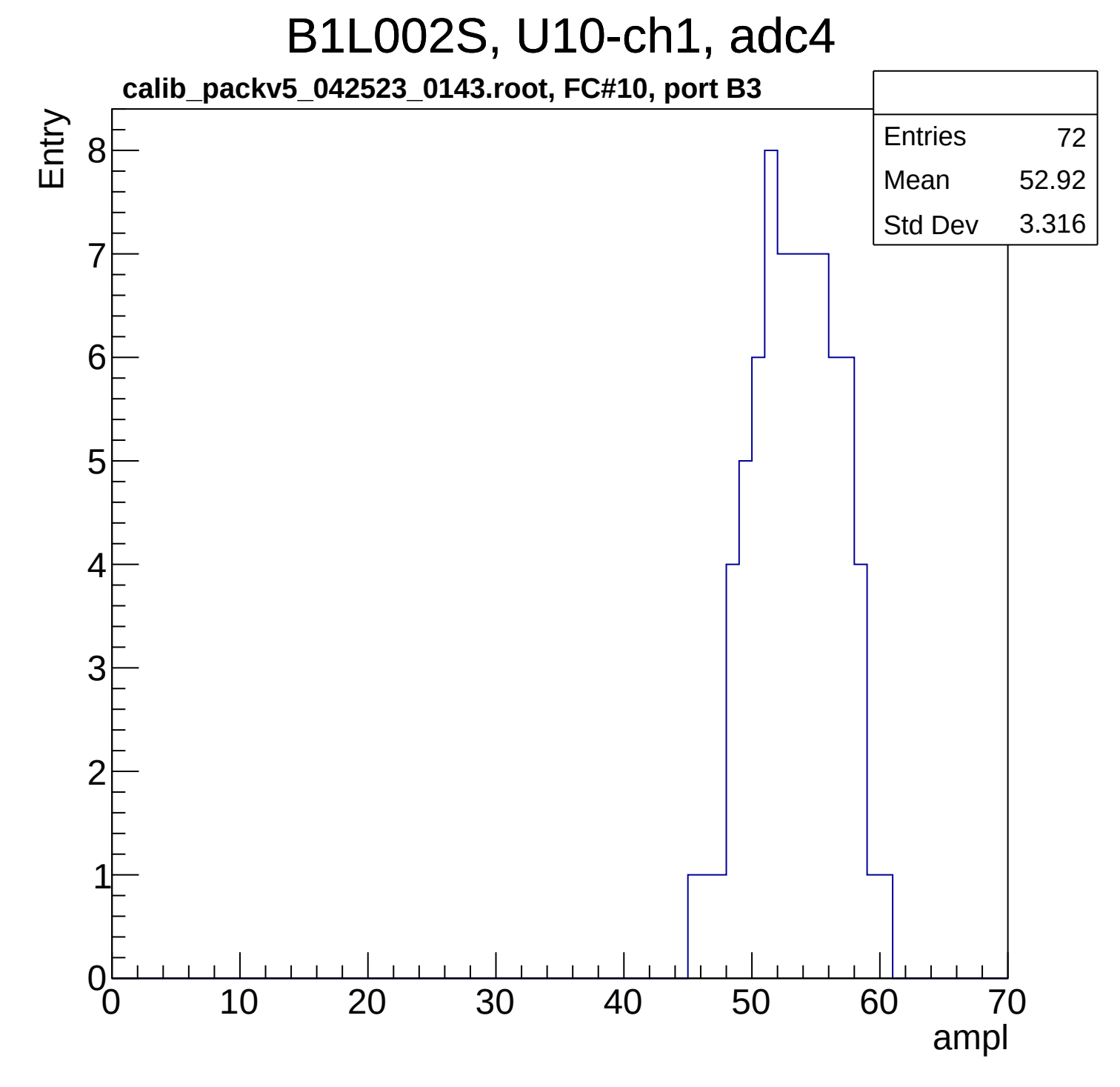
B1L002S, U10-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 52.92
Std Dev 3.316
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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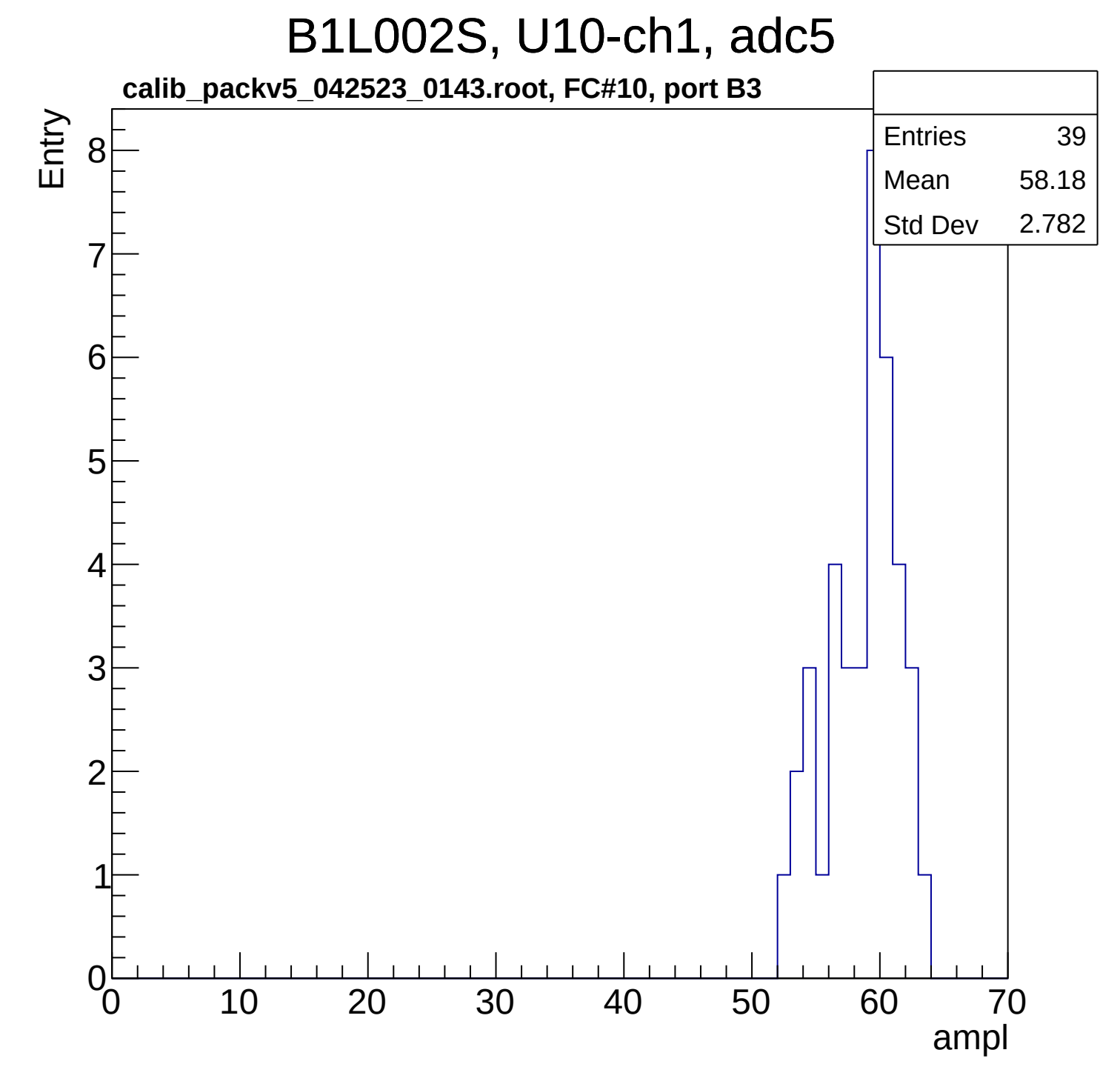
B1L002S, U10-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 58.18
Std Dev 2.782
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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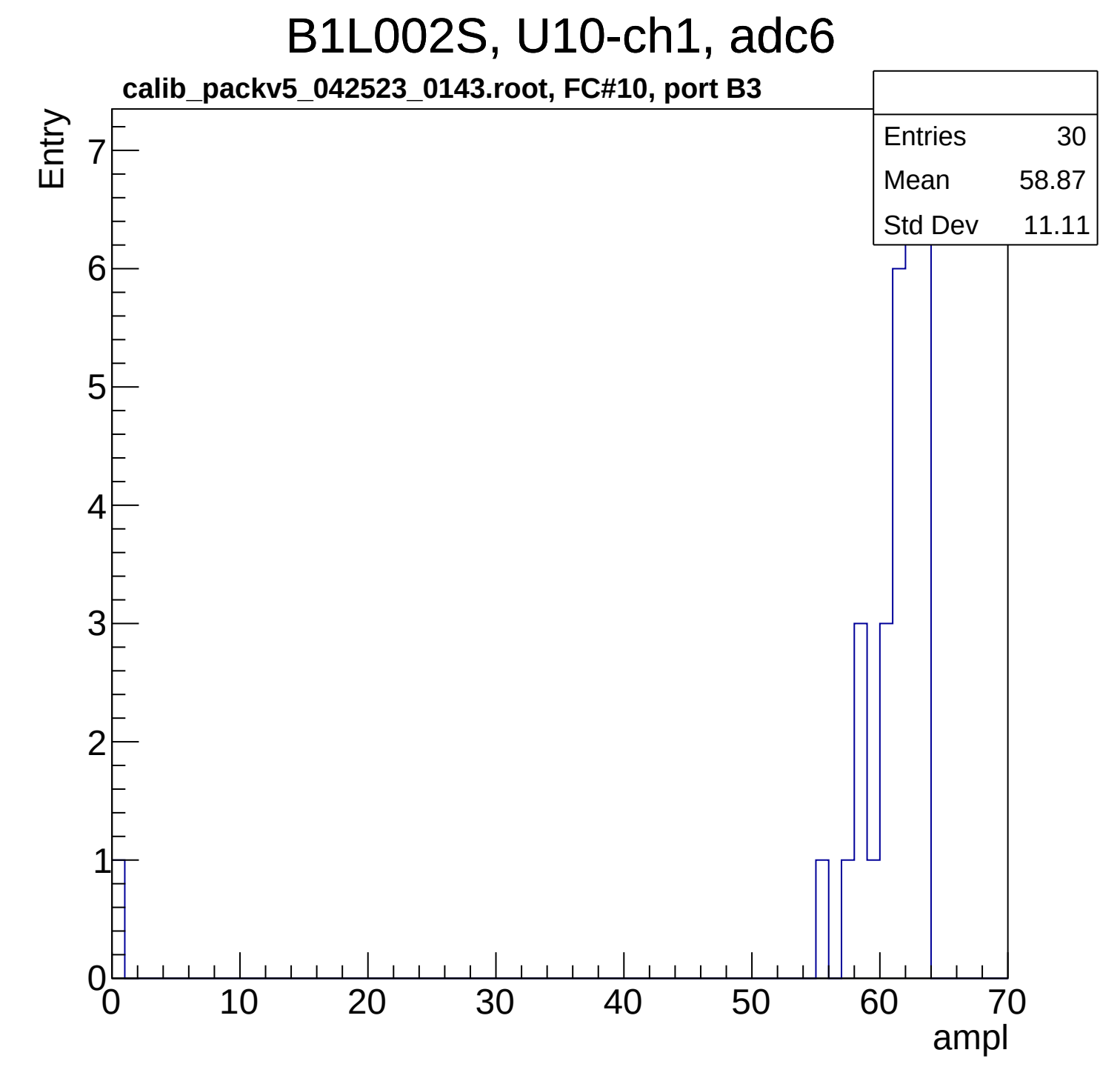
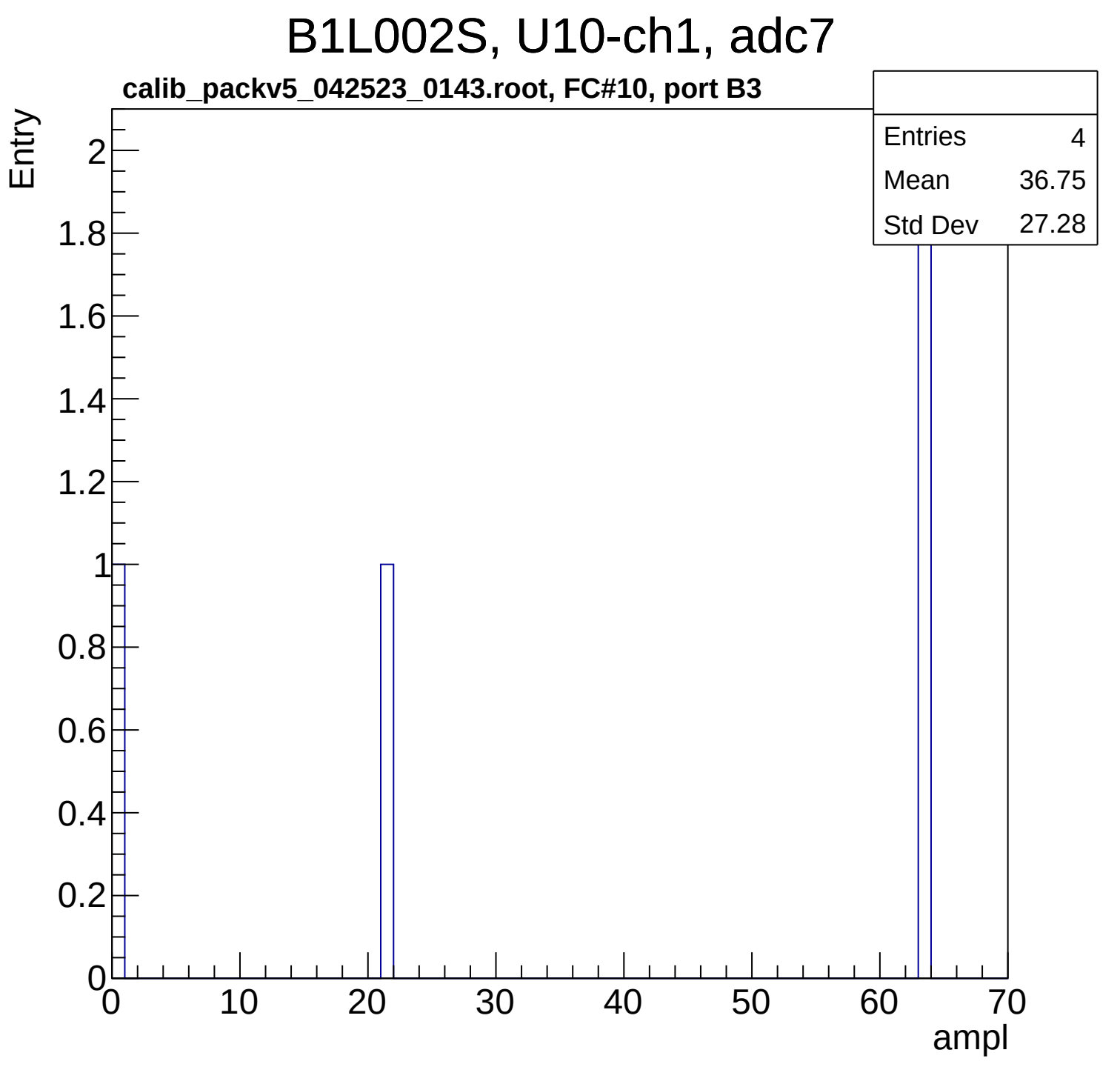

B1L002S, U10-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 58.87
Std Dev 11.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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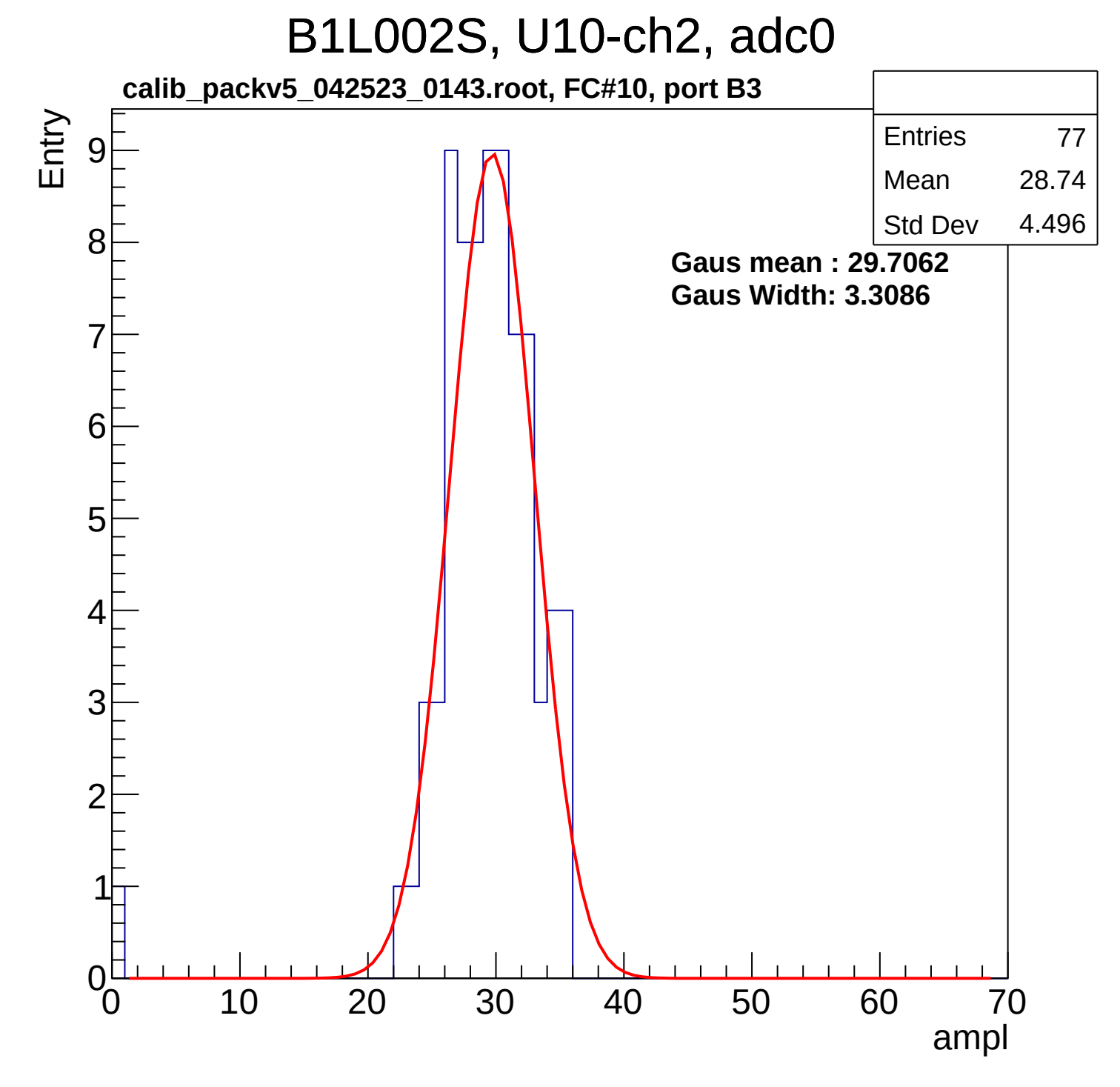
B1L002S, U10-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.74
Std Dev 4.496
Gaus mean : 29.7062
Gaus Width: 3.3086
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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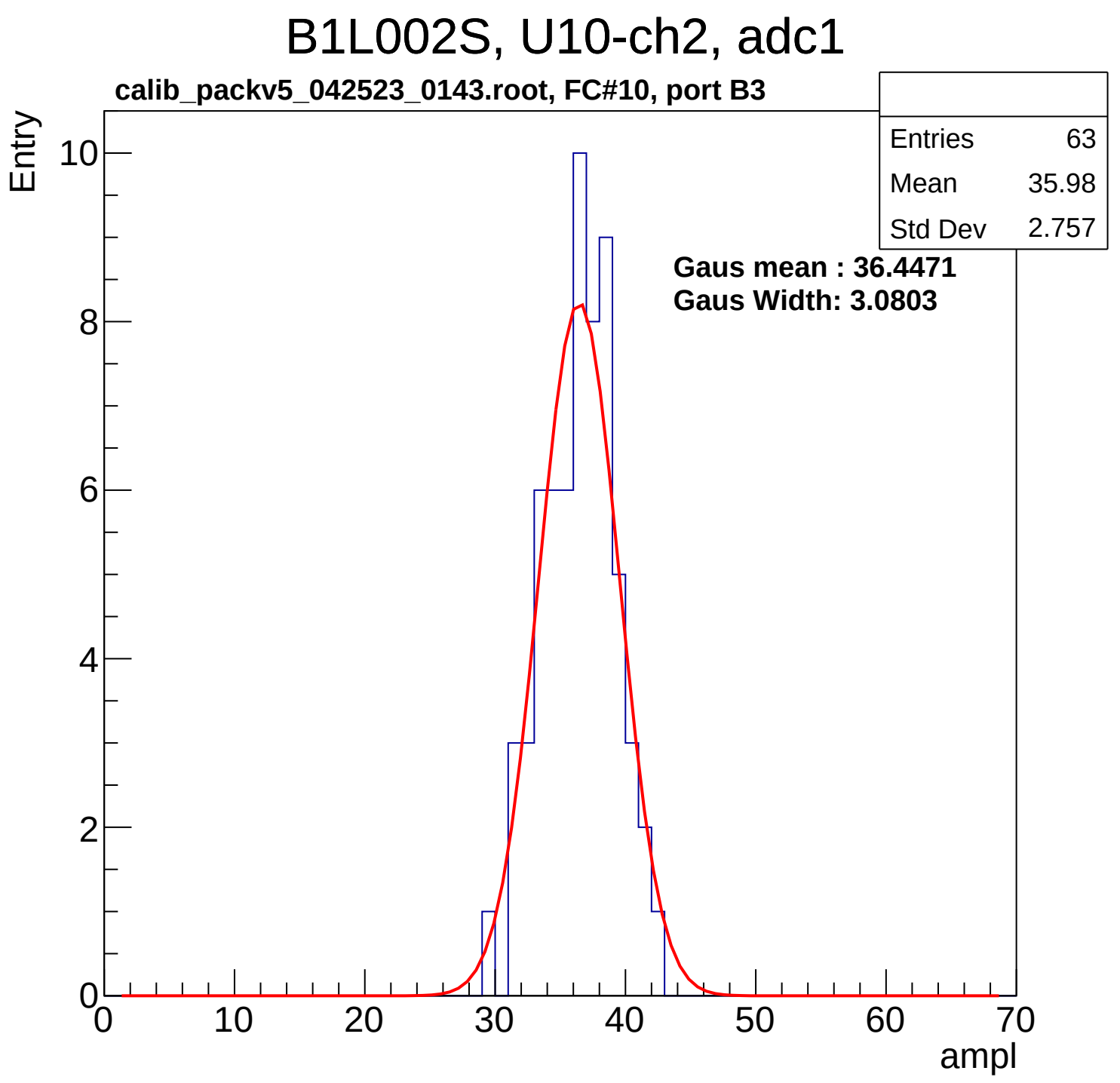
B1L002S, U10-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.98
Std Dev 2.757
Gaus mean : 36.4471
Gaus Width: 3.0803
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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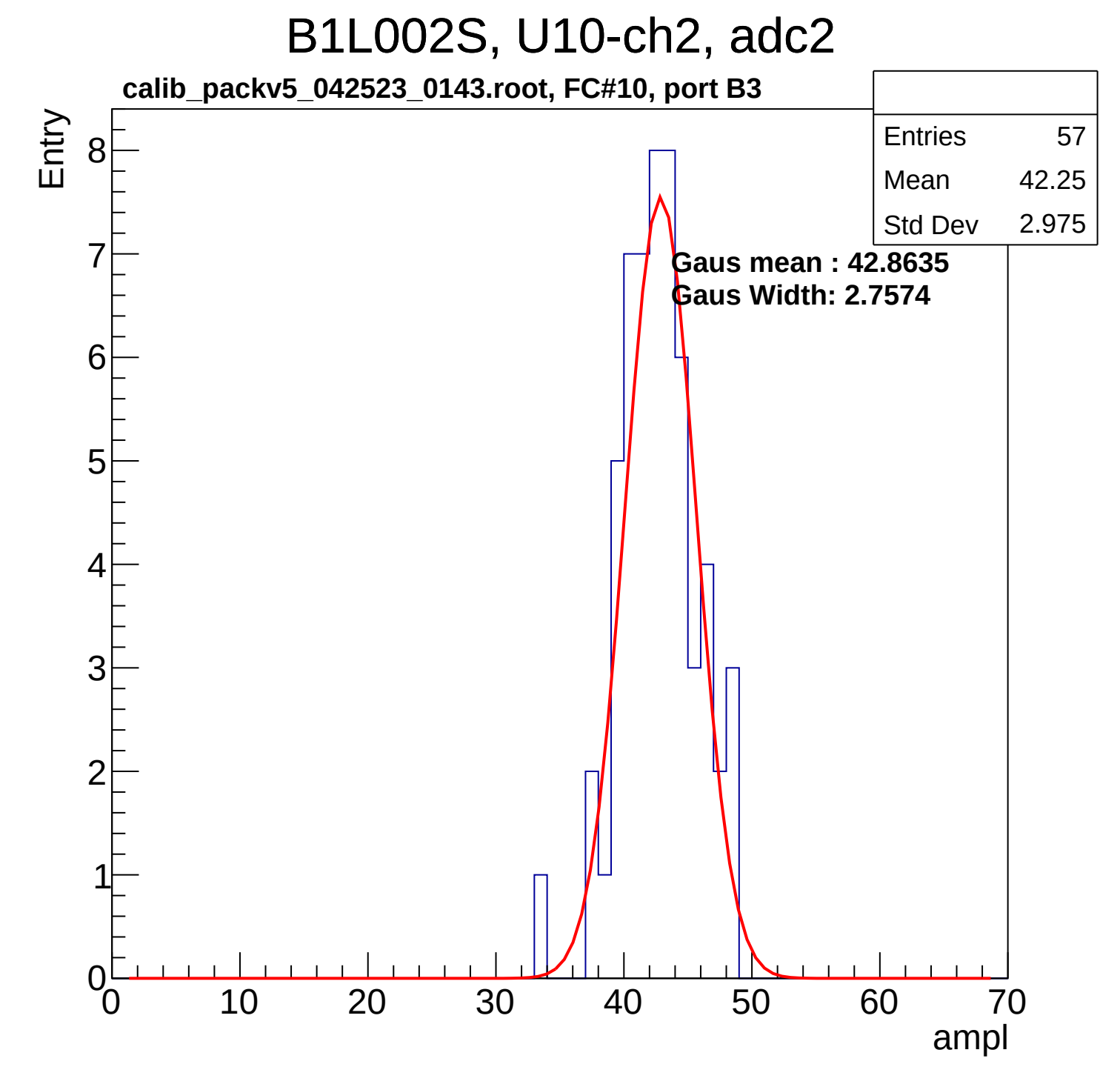
B1L002S, U10-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.25
Std Dev 2.975
Gaus mean : 42.8635
Gaus Width: 2.7574
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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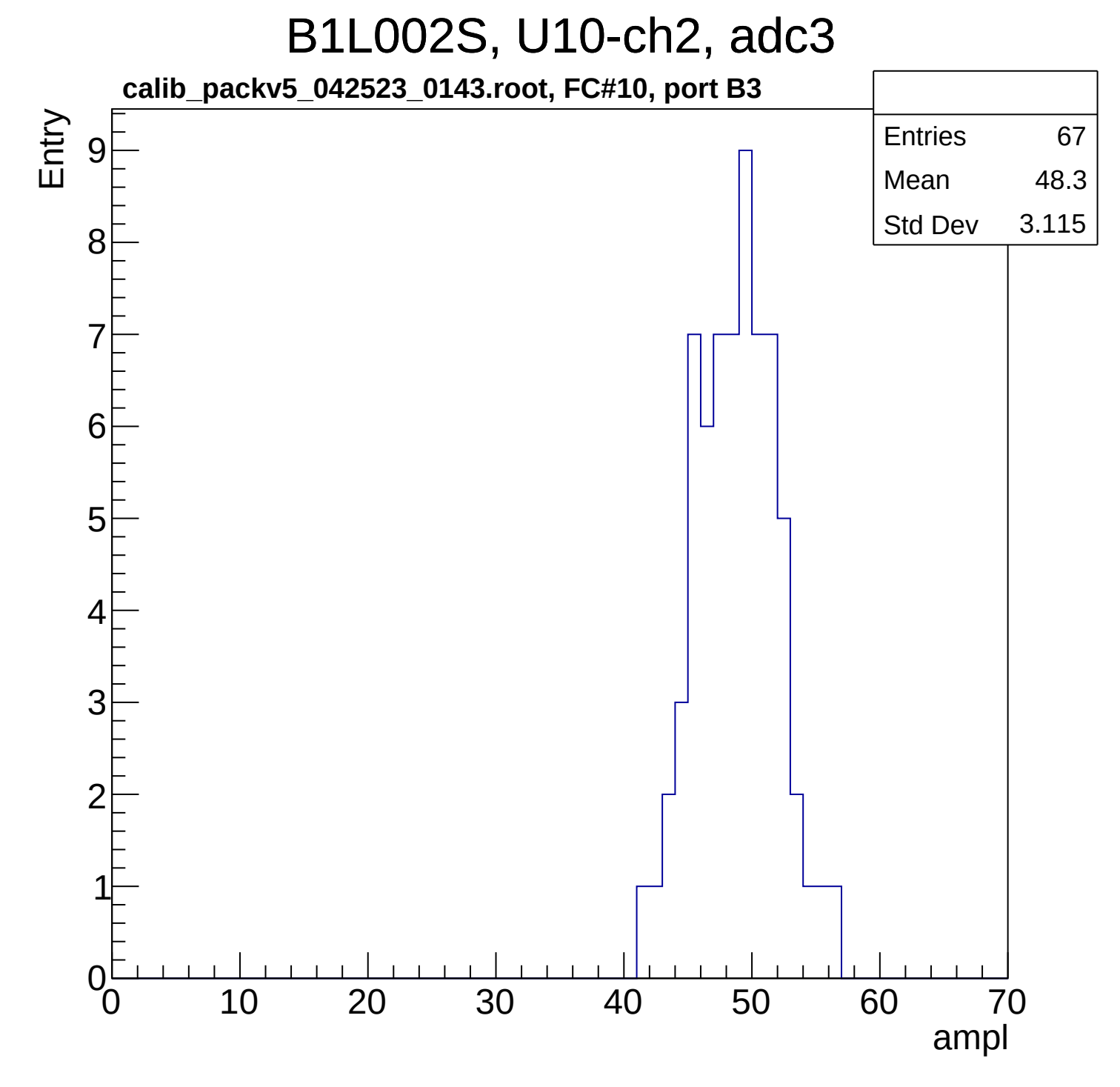
B1L002S, U10-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.3
Std Dev 3.115
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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50
60
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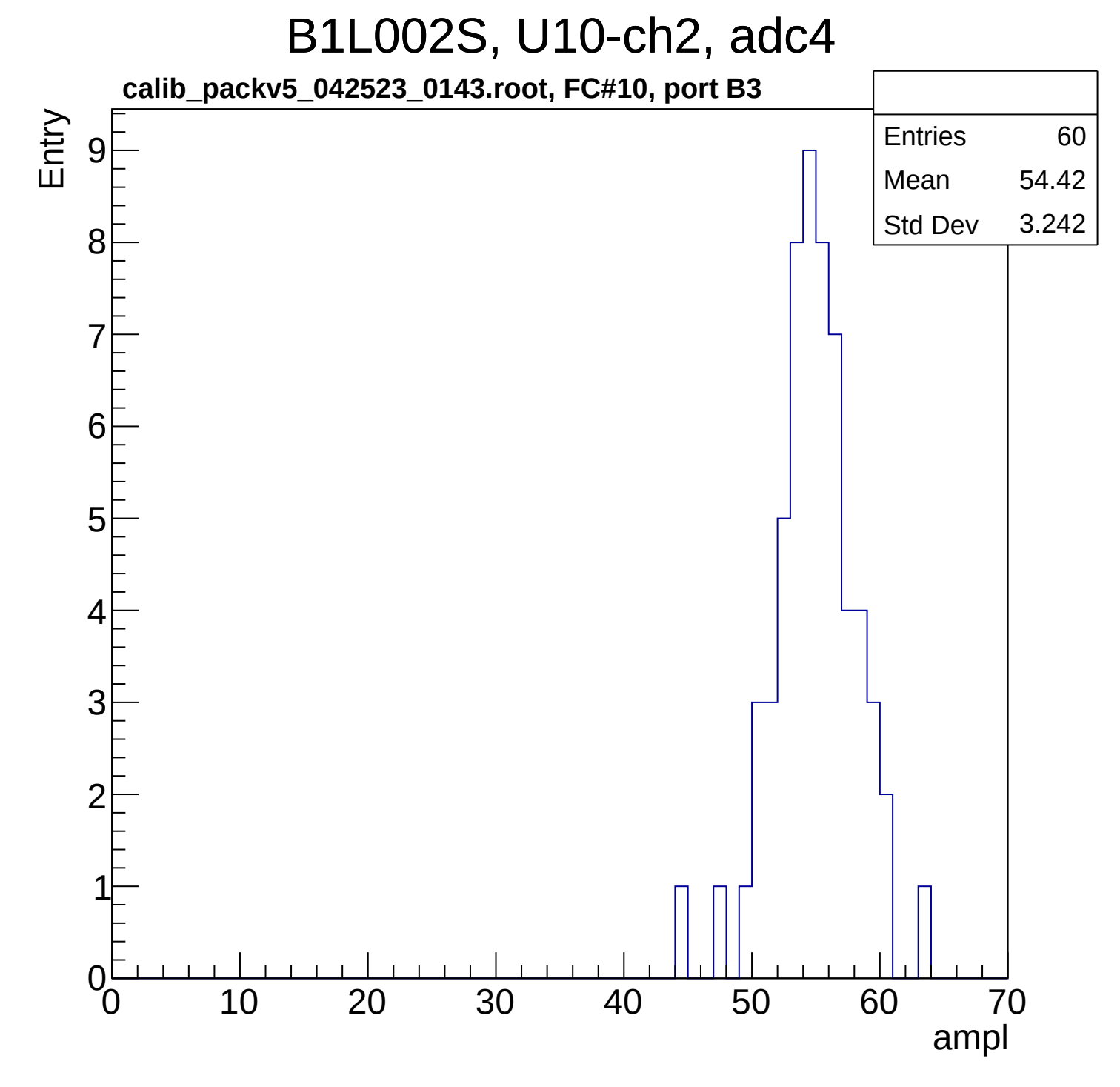
B1L002S, U10-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.42
Std Dev 3.242
Entry
ampl
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5
6
7
8
9
0
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20
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40
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60
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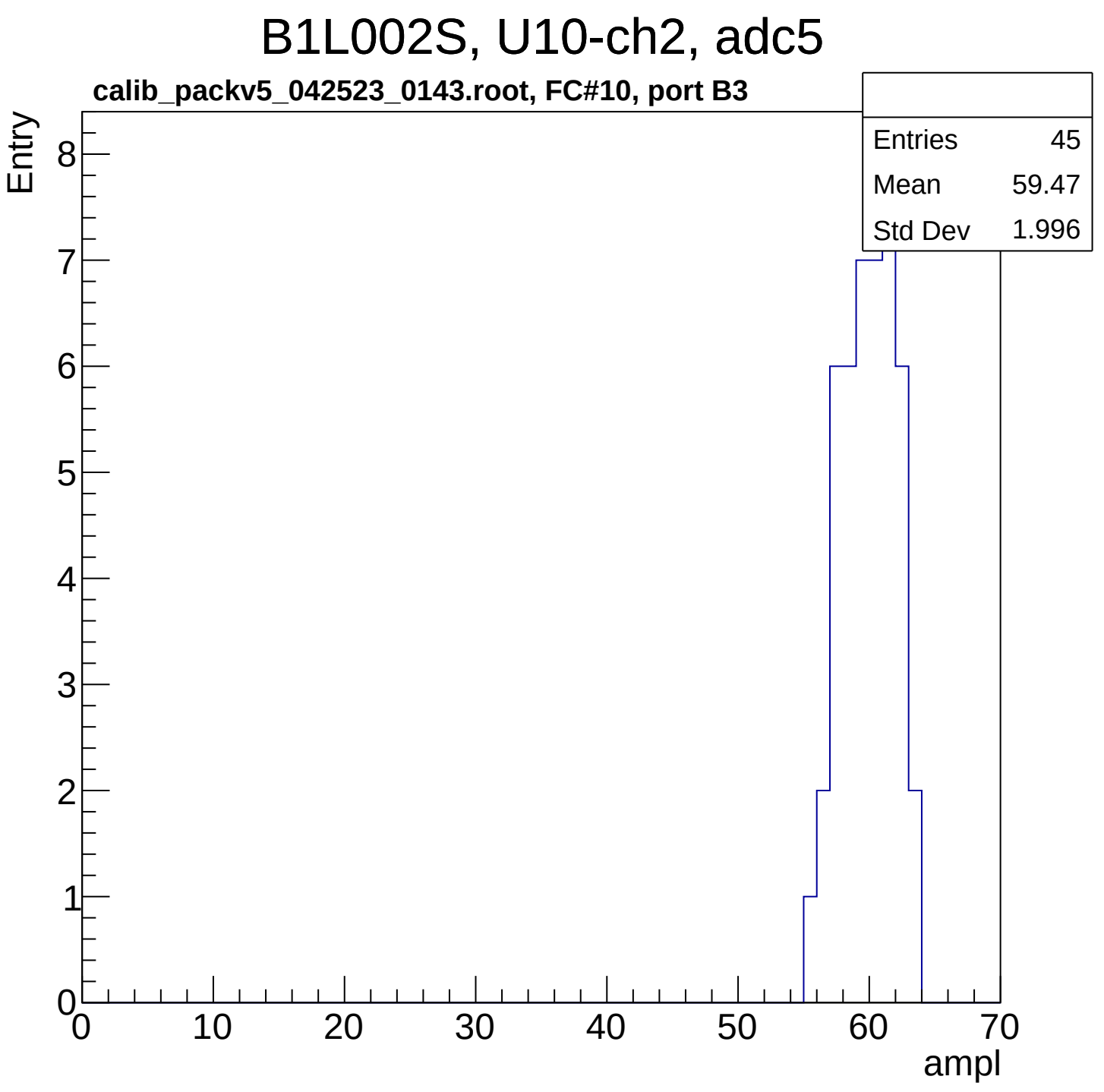
B1L002S, U10-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 59.47
Std Dev 1.996
Entry
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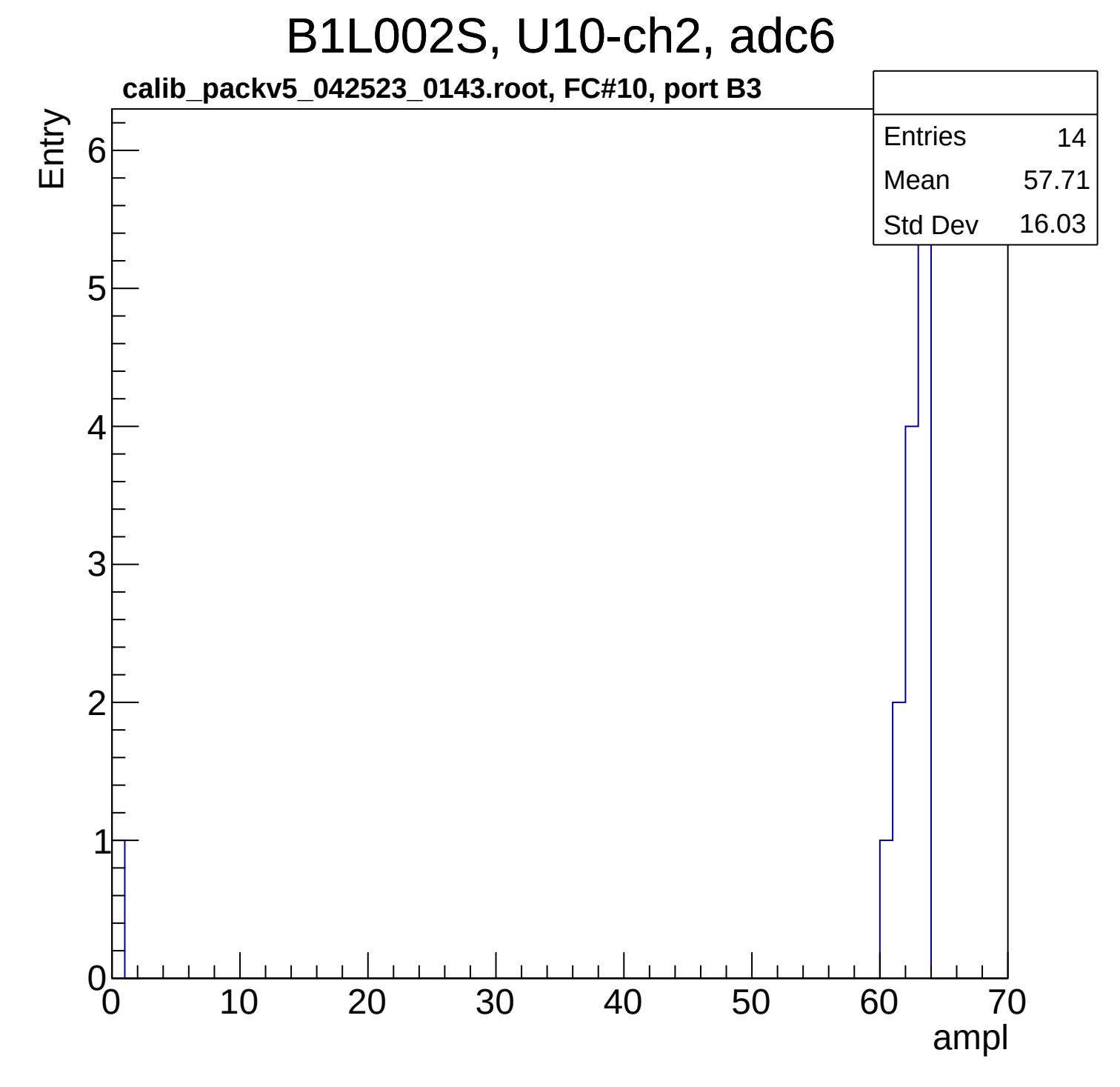
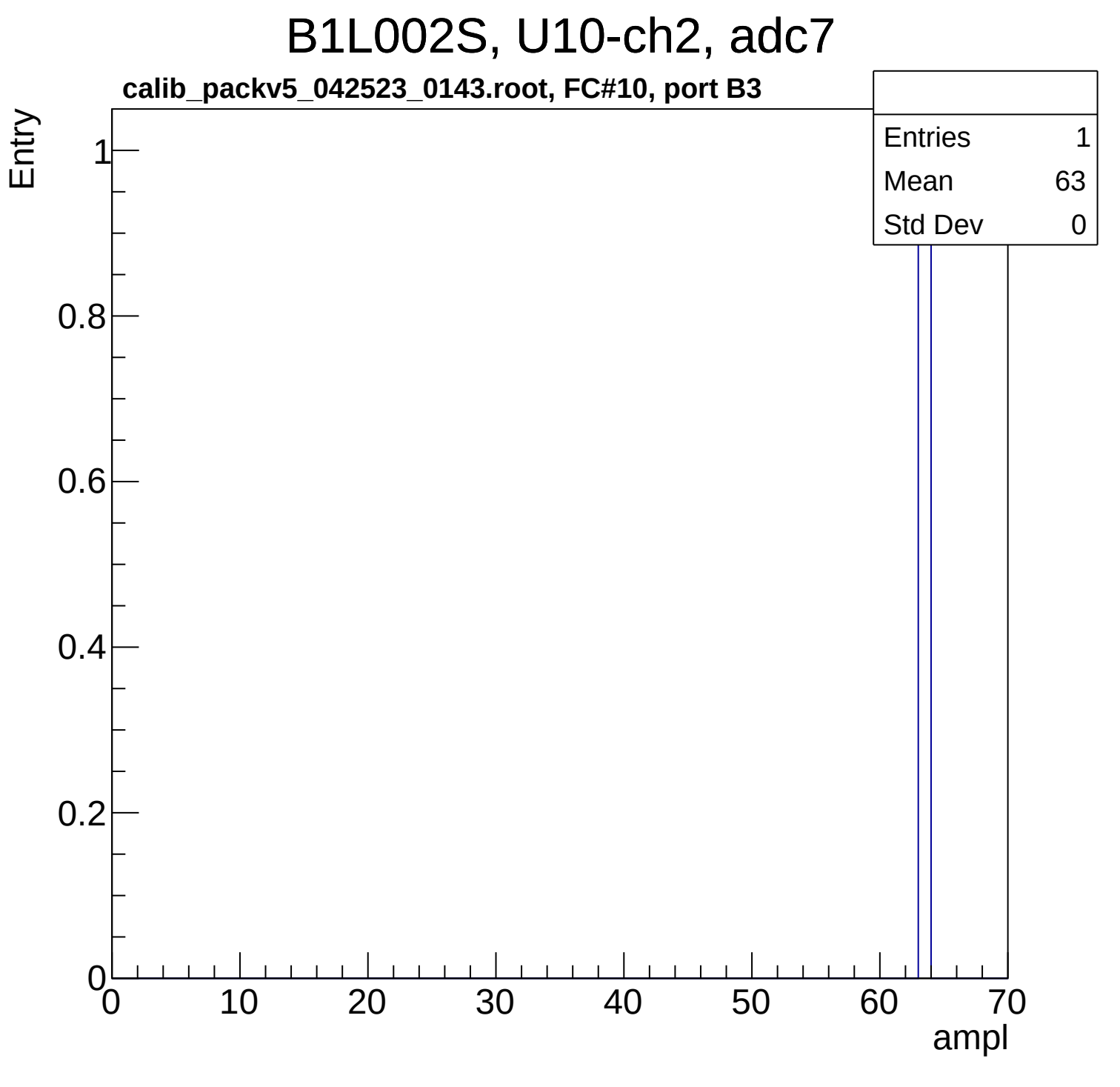
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.71
Std Dev 16.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
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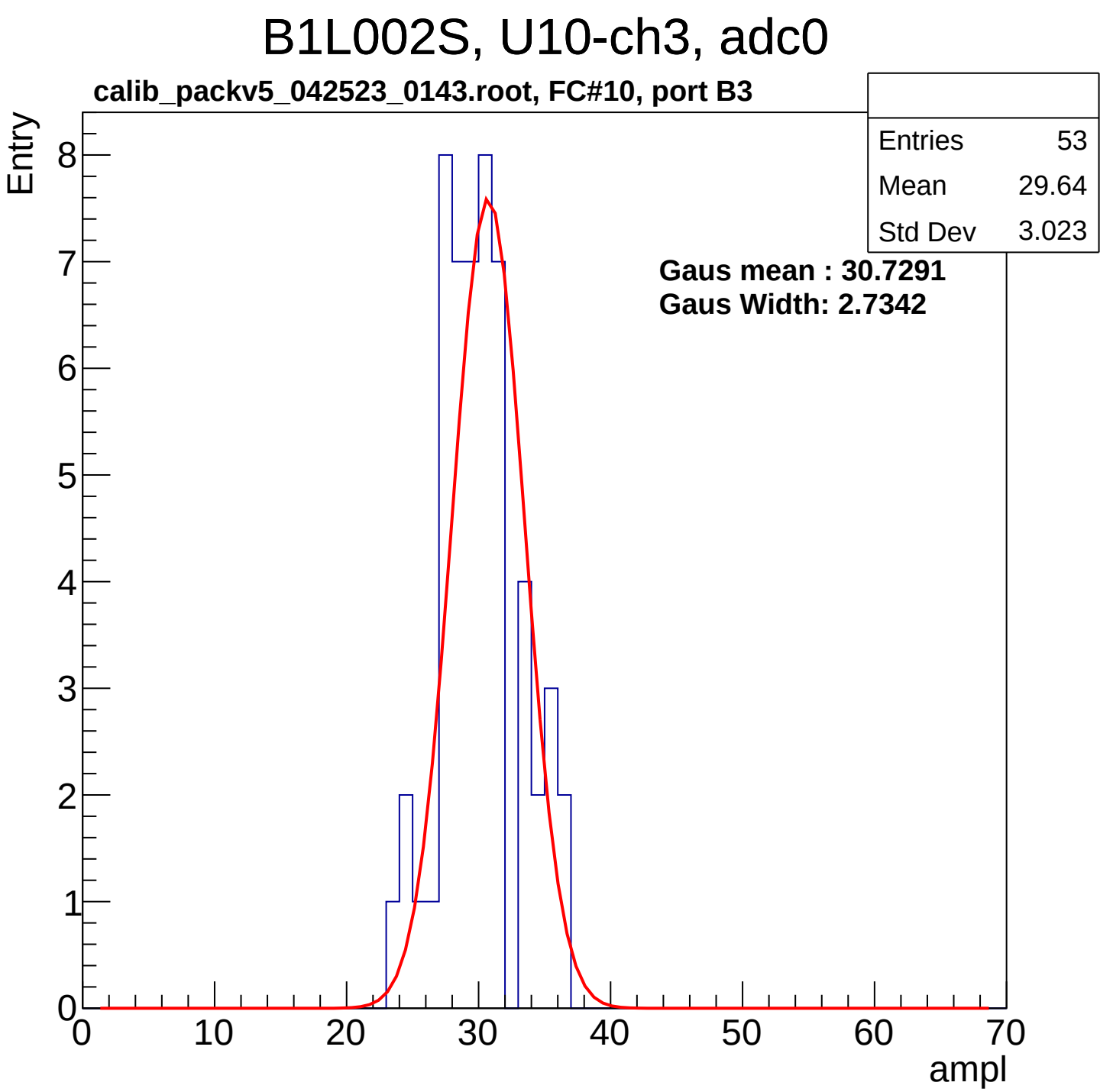
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 29.64
Std Dev 3.023
Gaus mean : 30.7291
Gaus Width: 2.7342
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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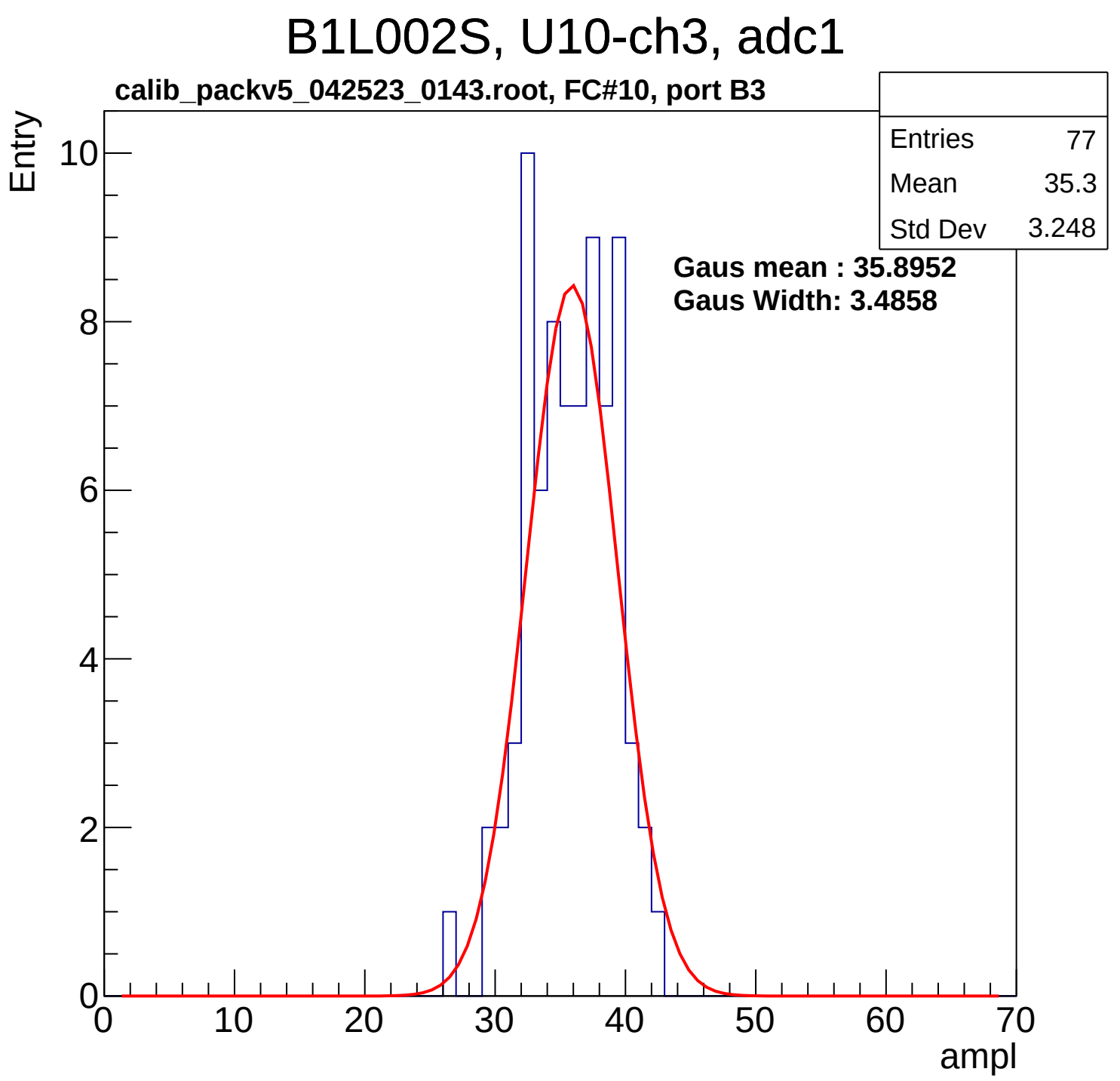
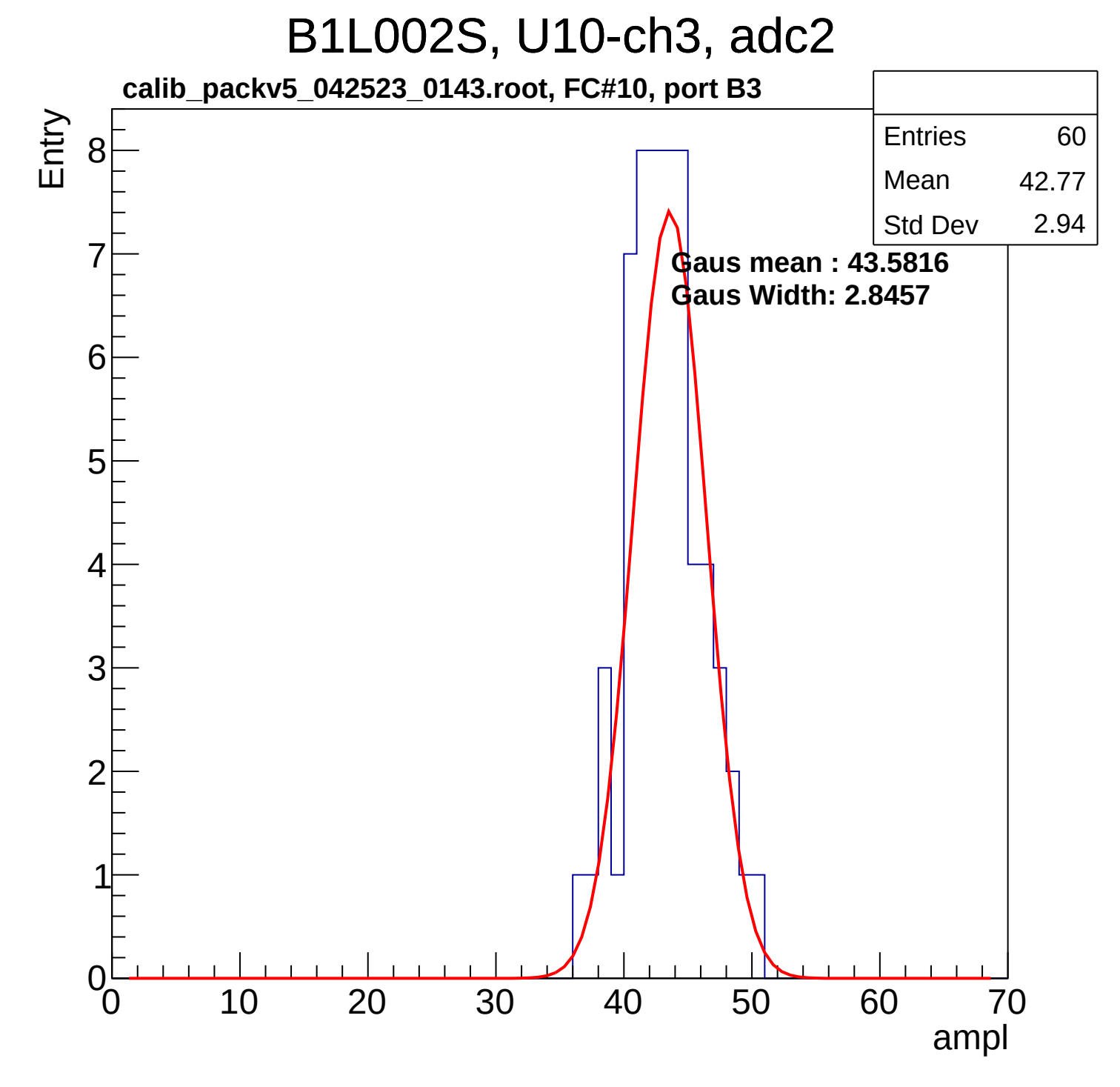

B1L002S, U10-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.3
Std Dev 3.248
Gaus mean : 35.8952
Gaus Width: 3.4858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.77
Std Dev 2.94
Gaus mean : 43.5816
Gaus Width: 2.8457
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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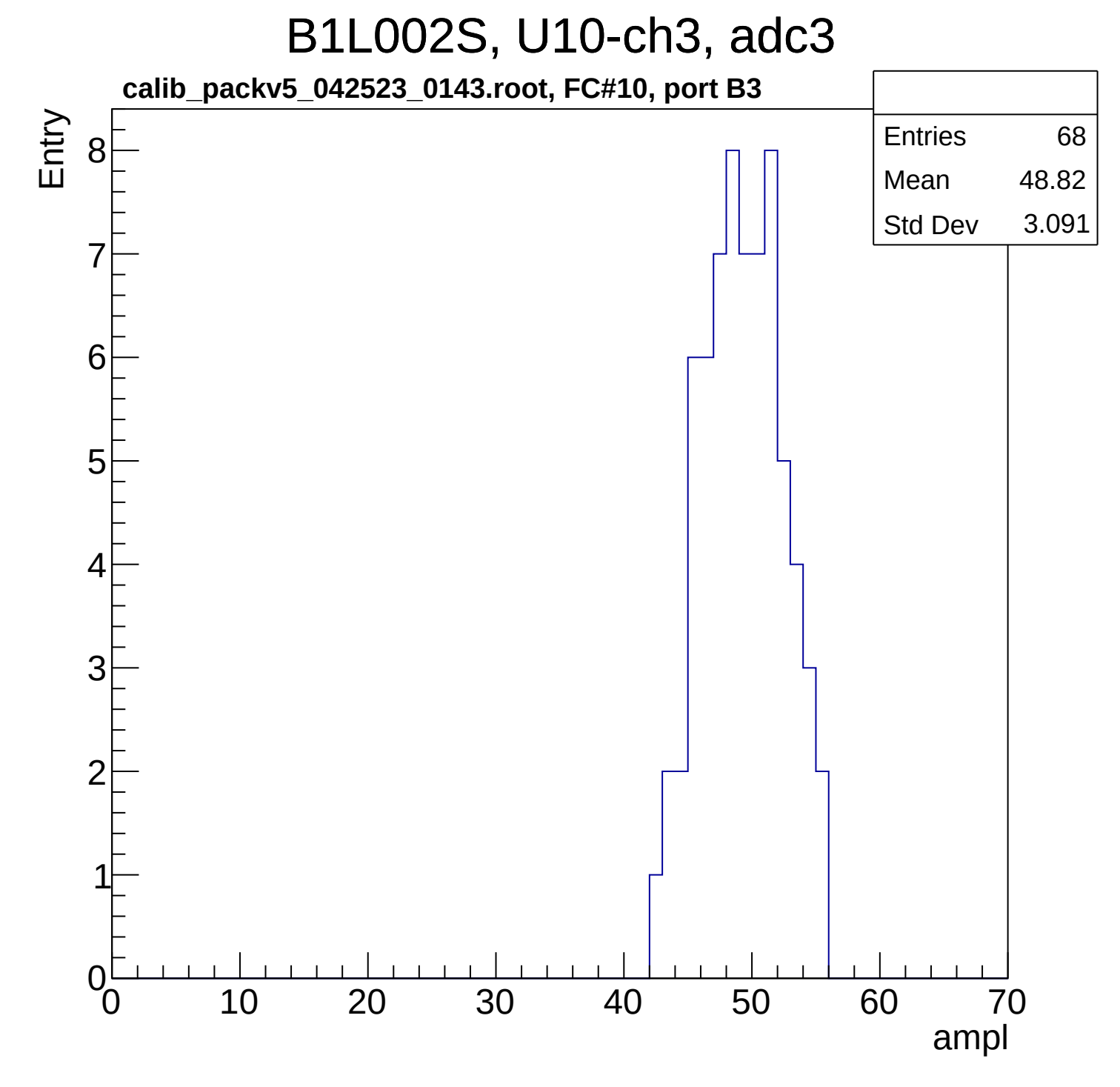
B1L002S, U10-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.82
Std Dev 3.091
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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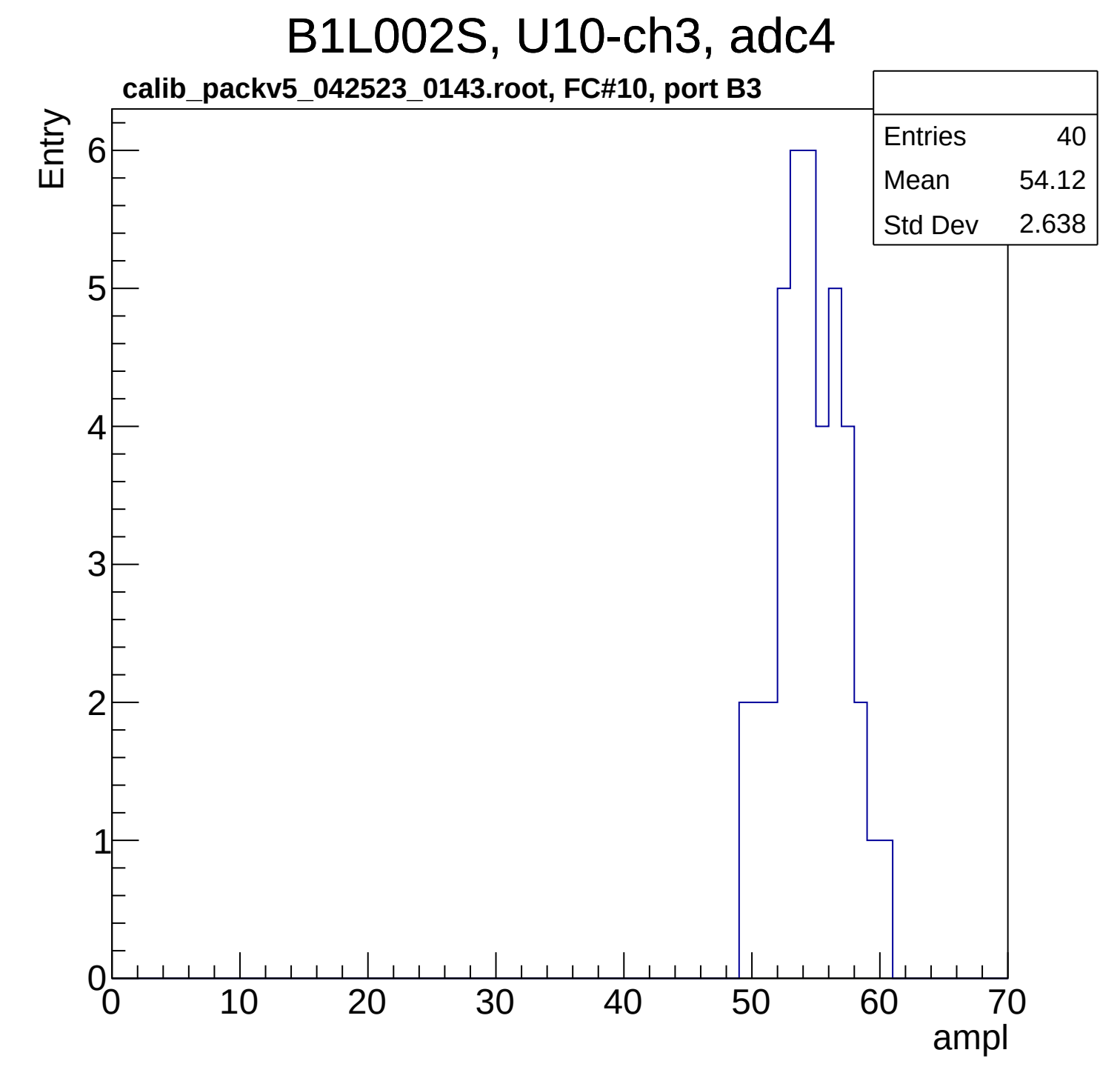
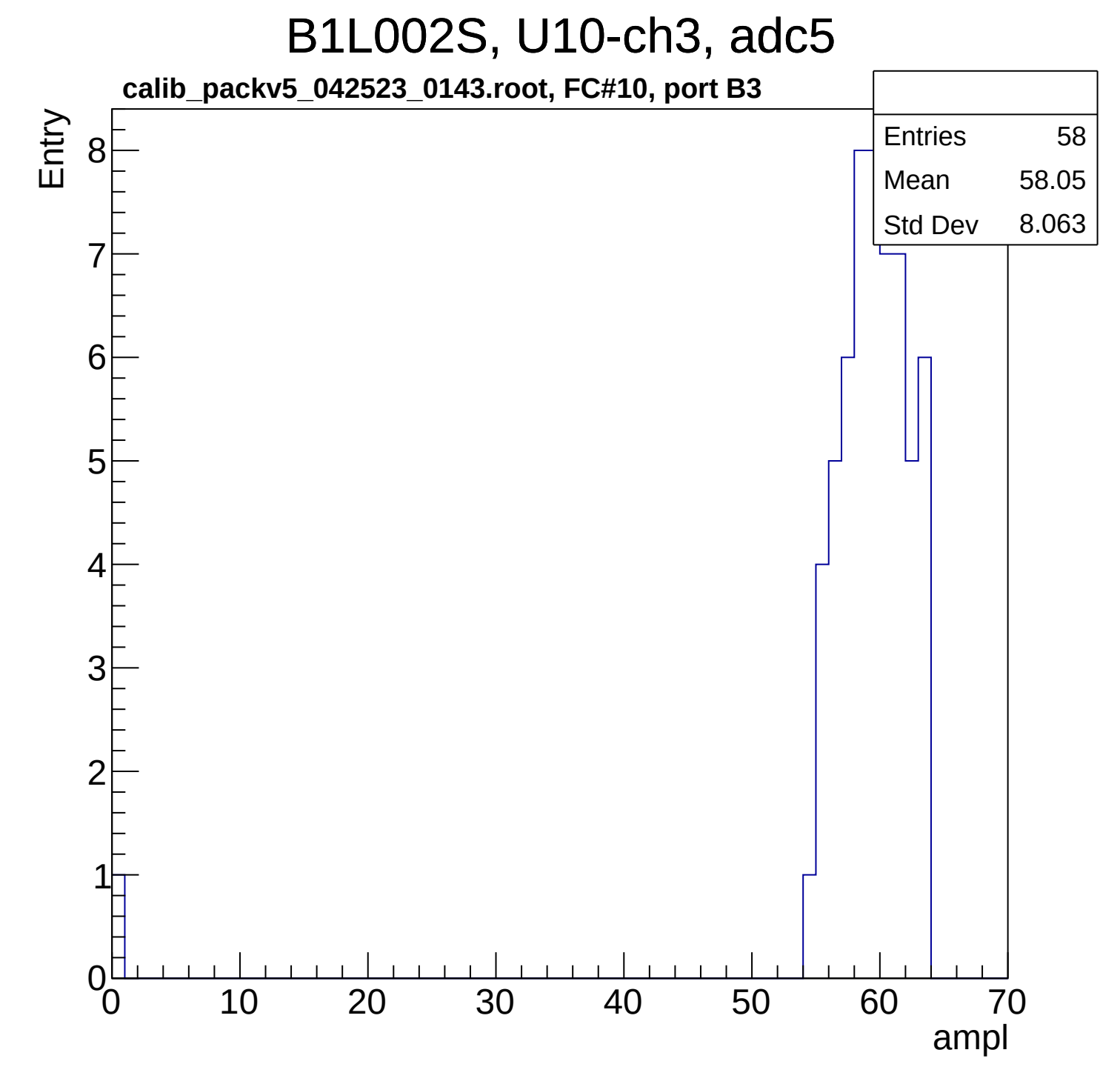

B1L002S, U10-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 54.12
Std Dev 2.638
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.05
Std Dev 8.063
Entry
ampl
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1
2
3
4
5
6
7
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10
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60
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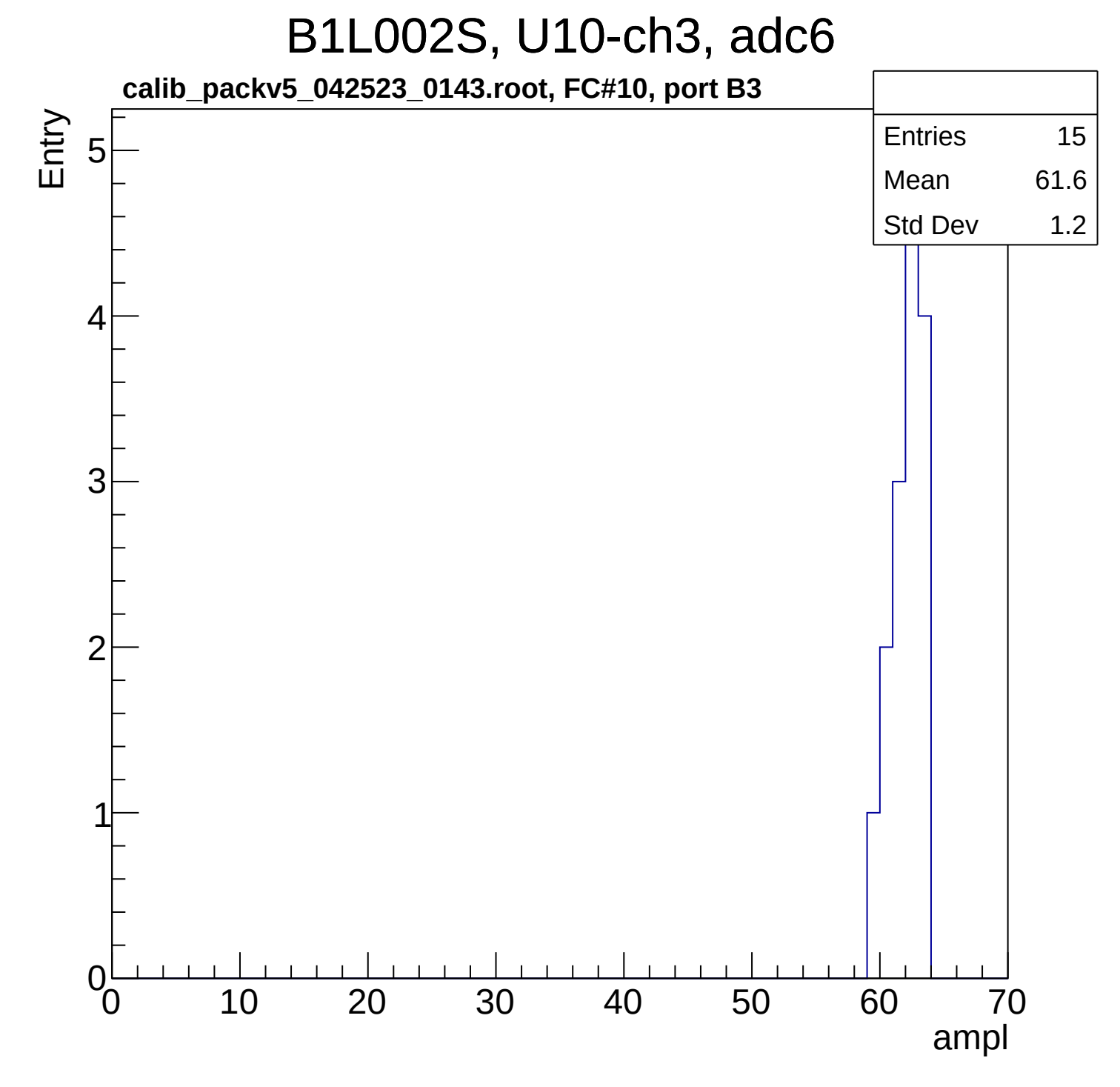
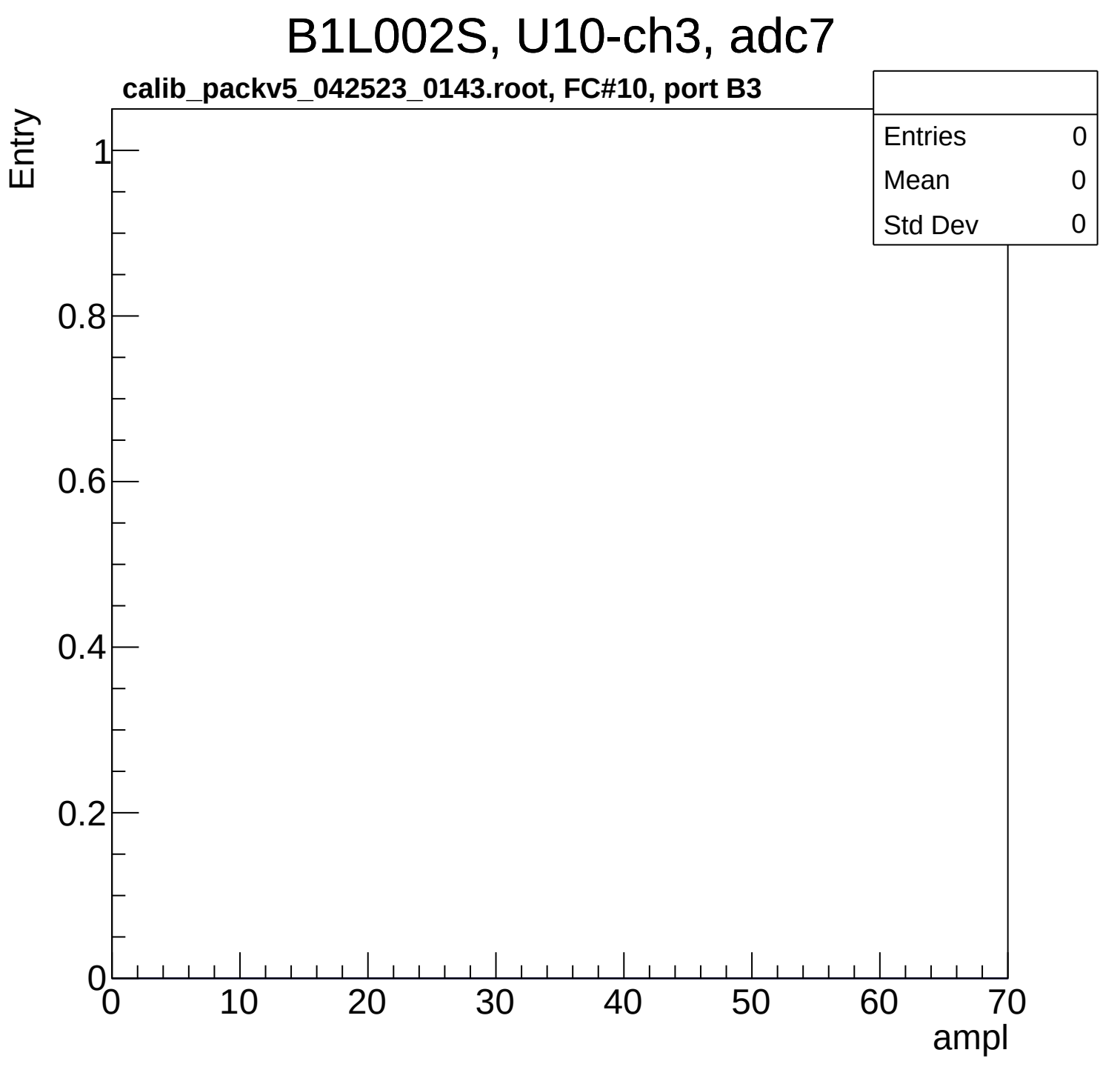

B1L002S, U10-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.6
Std Dev 1.2
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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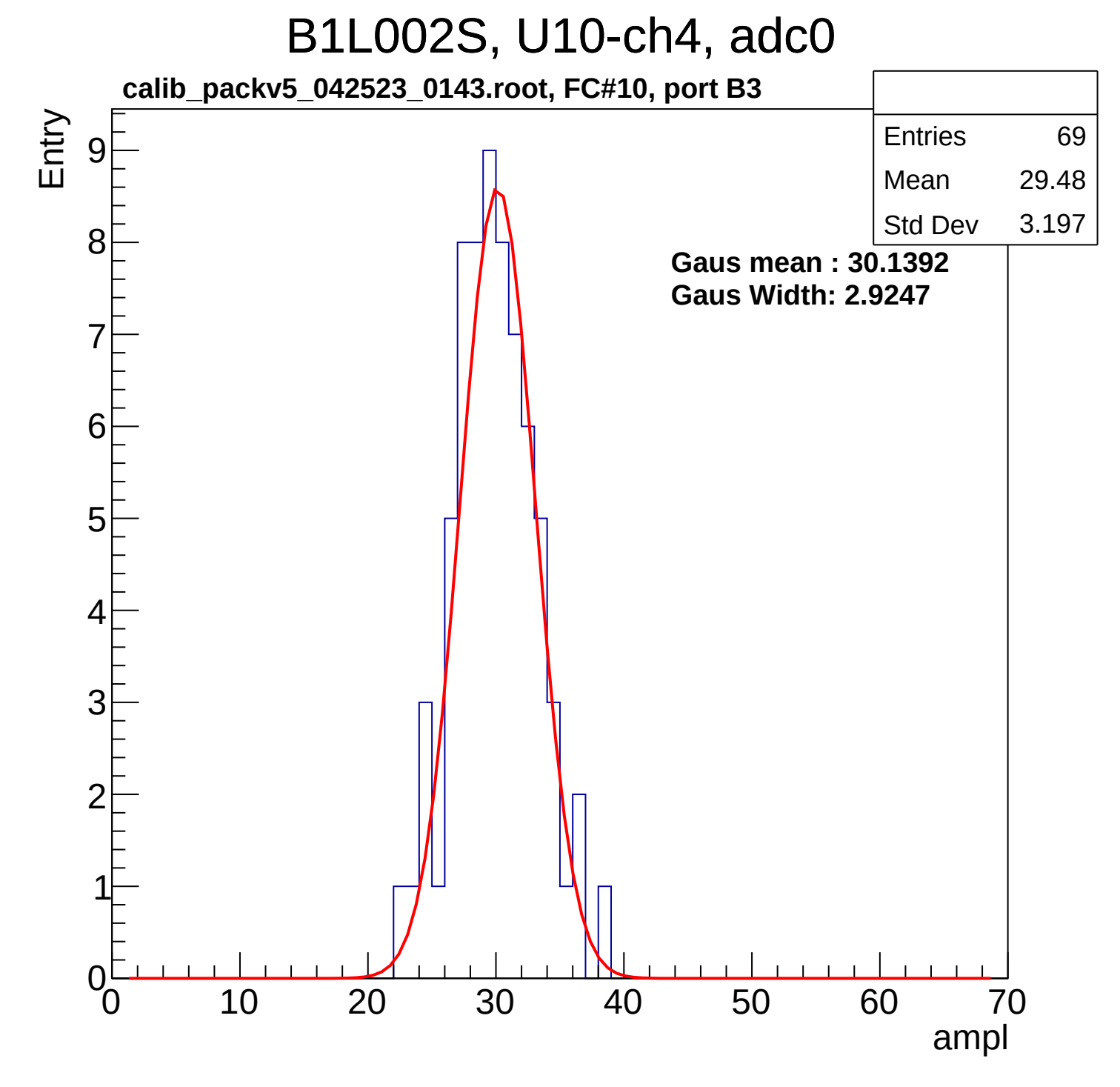
50
60
70
ampl


B1L002S, U10-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 29.48
Std Dev 3.197
Gaus mean : 30.1392
Gaus Width: 2.9247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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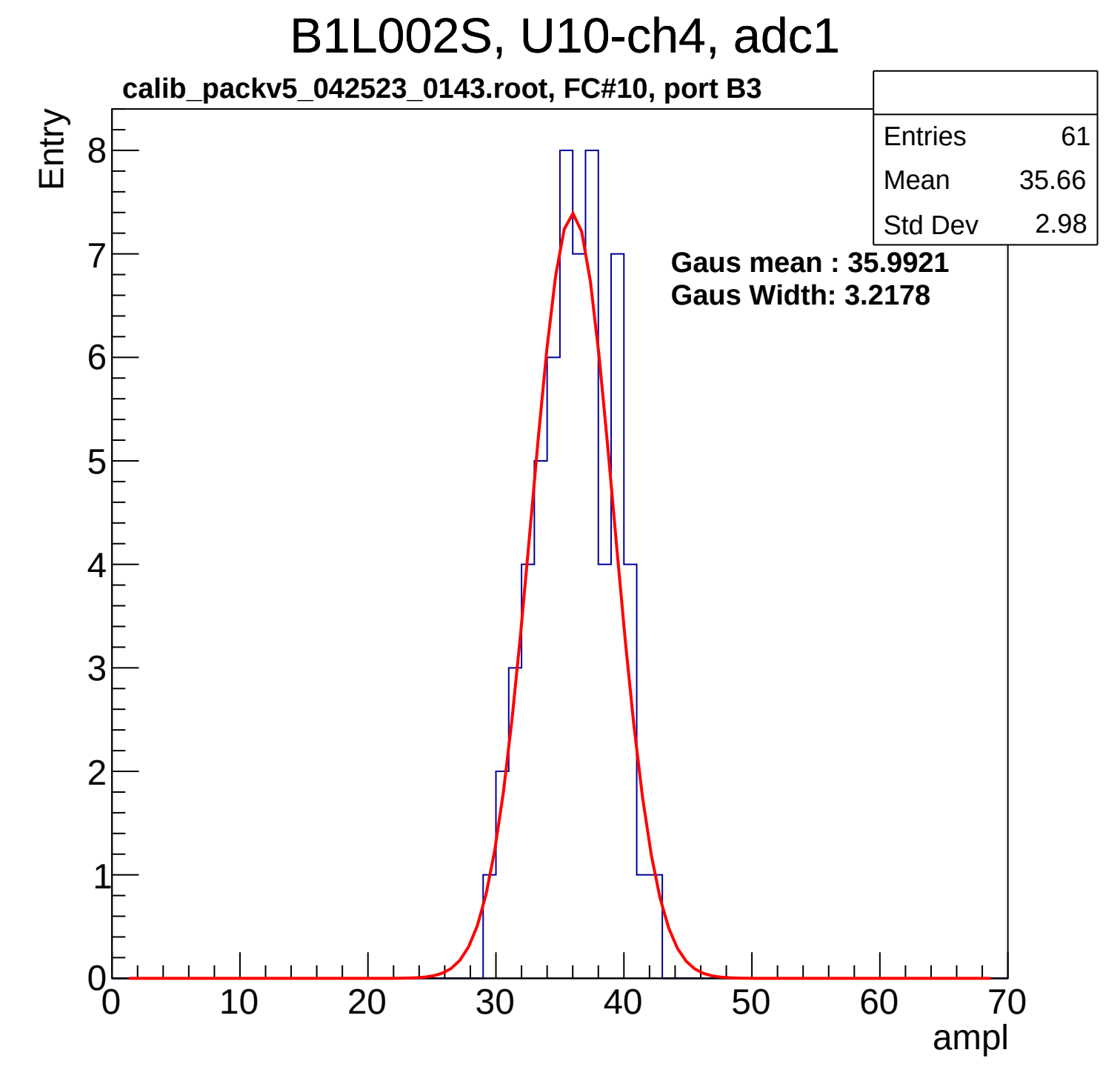
B1L002S, U10-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.66
Std Dev 2.98
Gaus mean : 35.9921
Gaus Width: 3.2178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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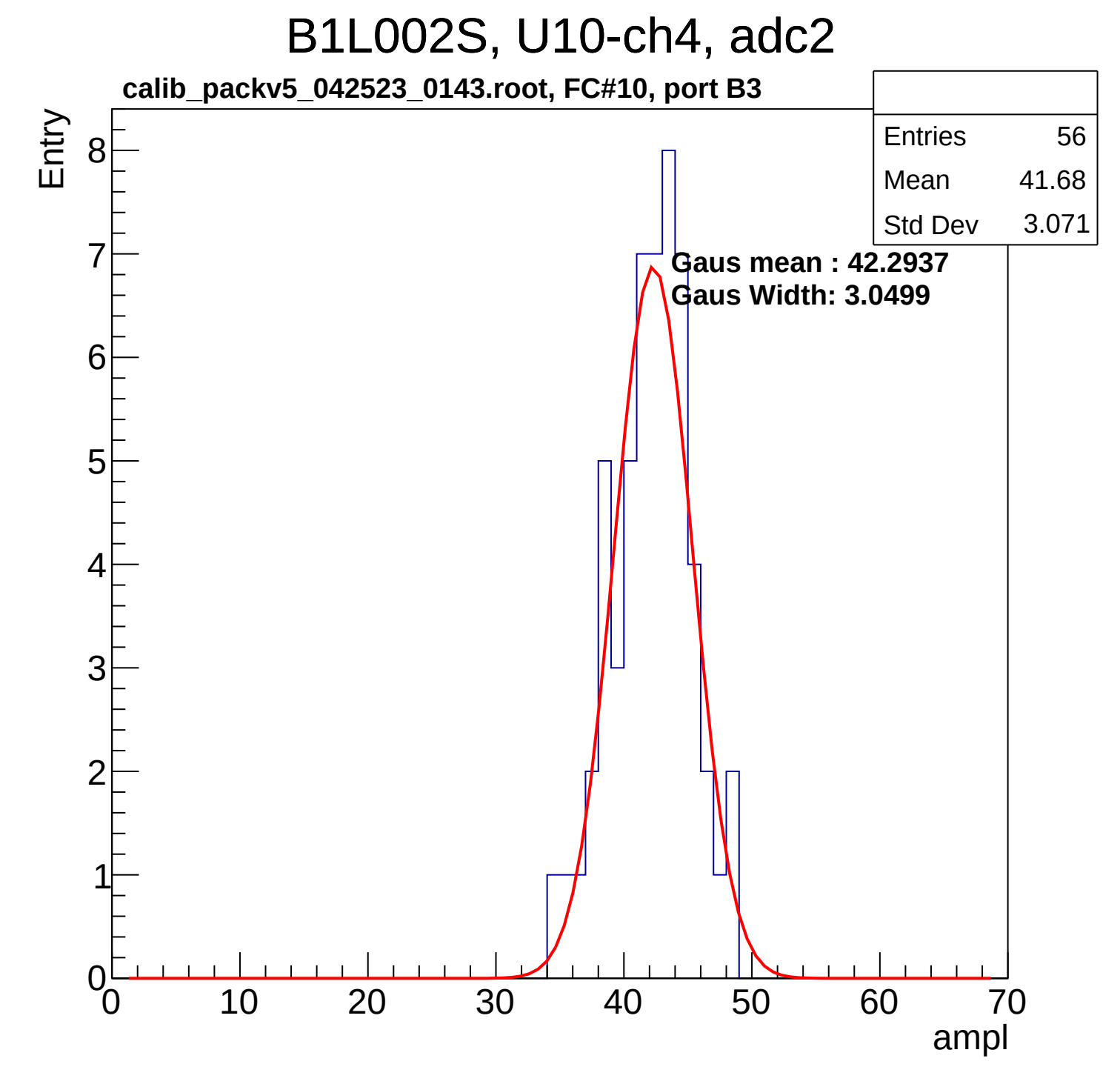
B1L002S, U10-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.68
Std Dev 3.071
Gaus mean : 42.2937
Gaus Width: 3.0499
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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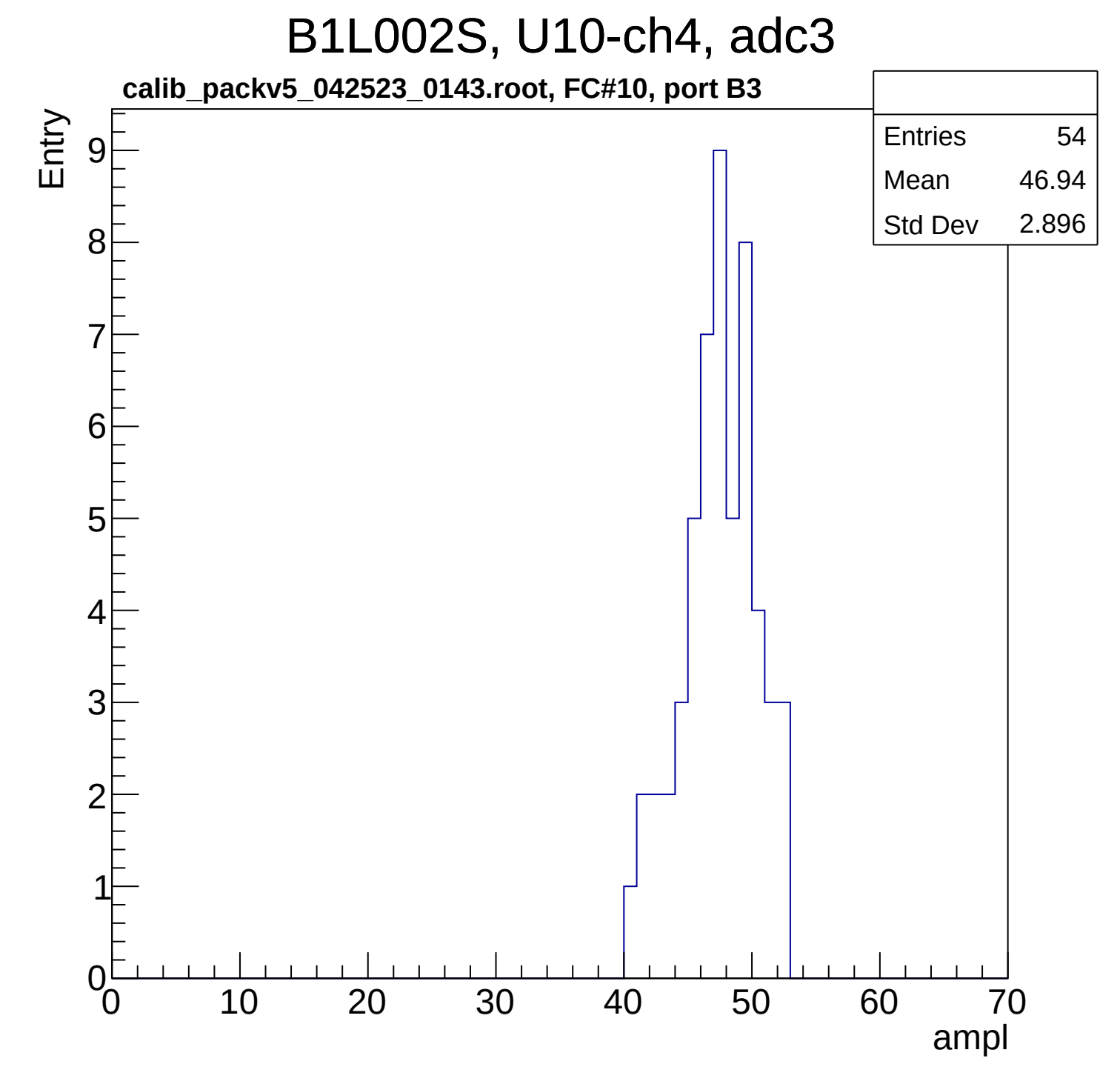
B1L002S, U10-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.94
Std Dev 2.896
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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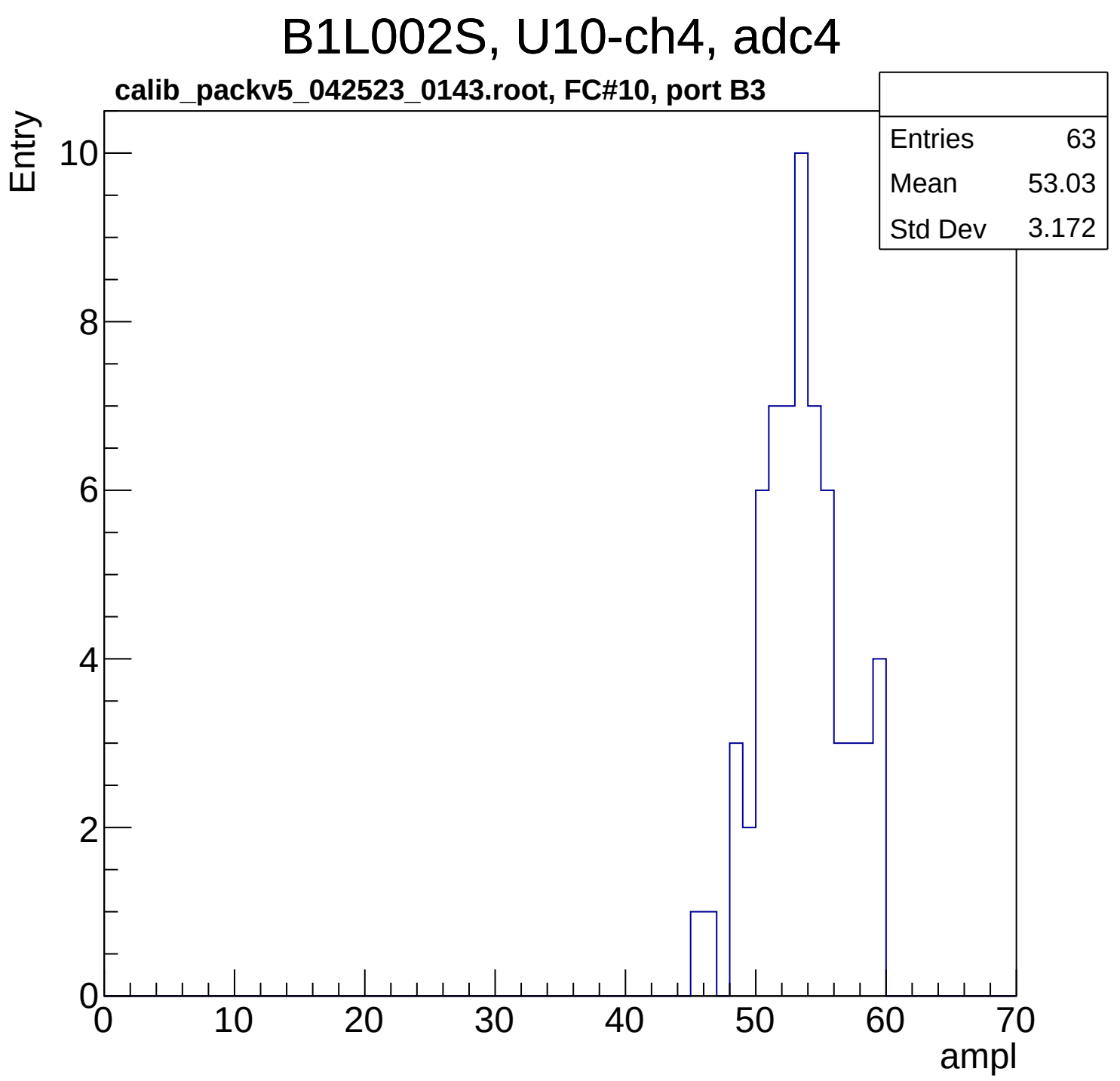
B1L002S, U10-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.03
Std Dev 3.172
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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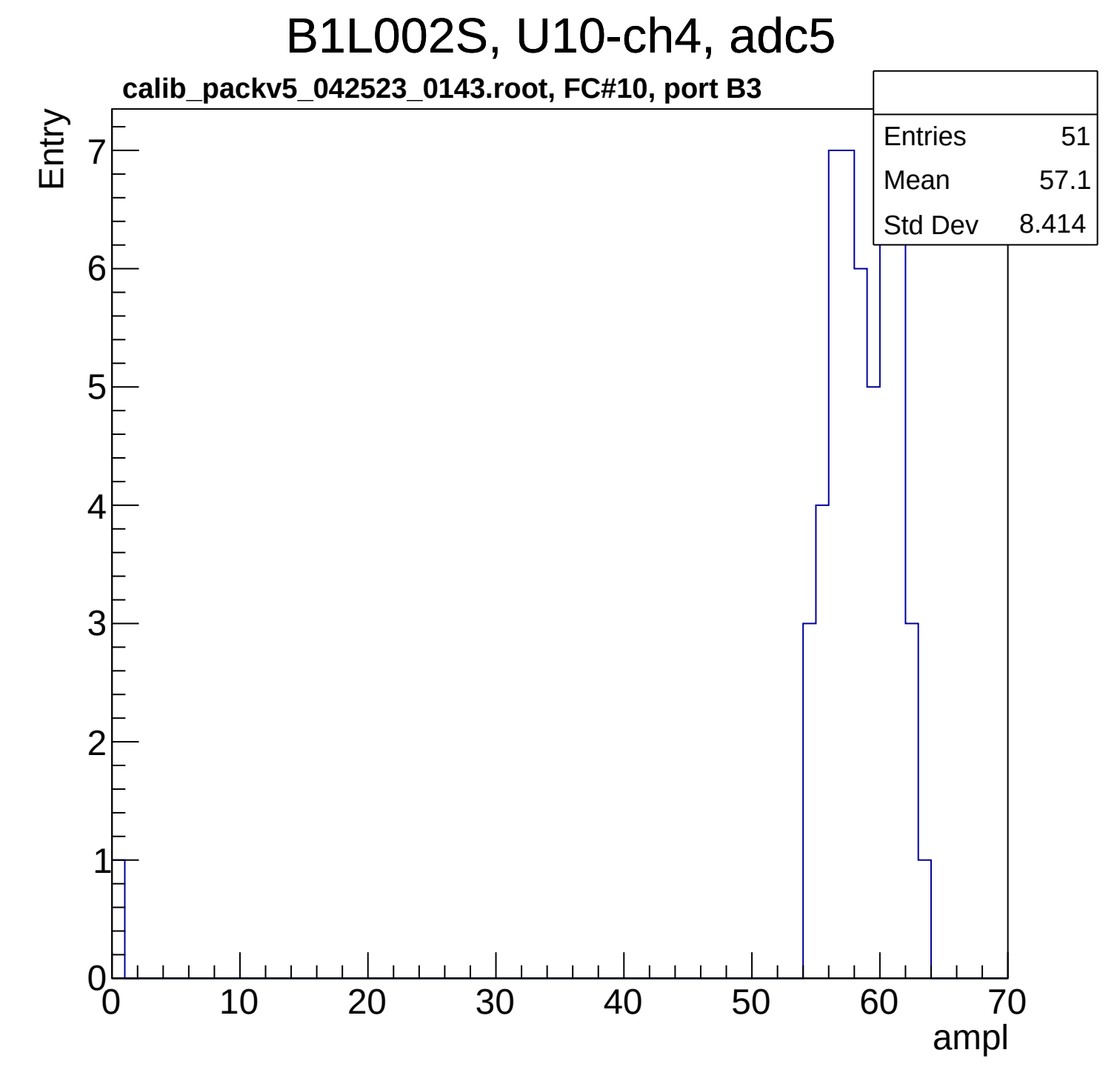
60
70


B1L002S, U10-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.1
Std Dev 8.414
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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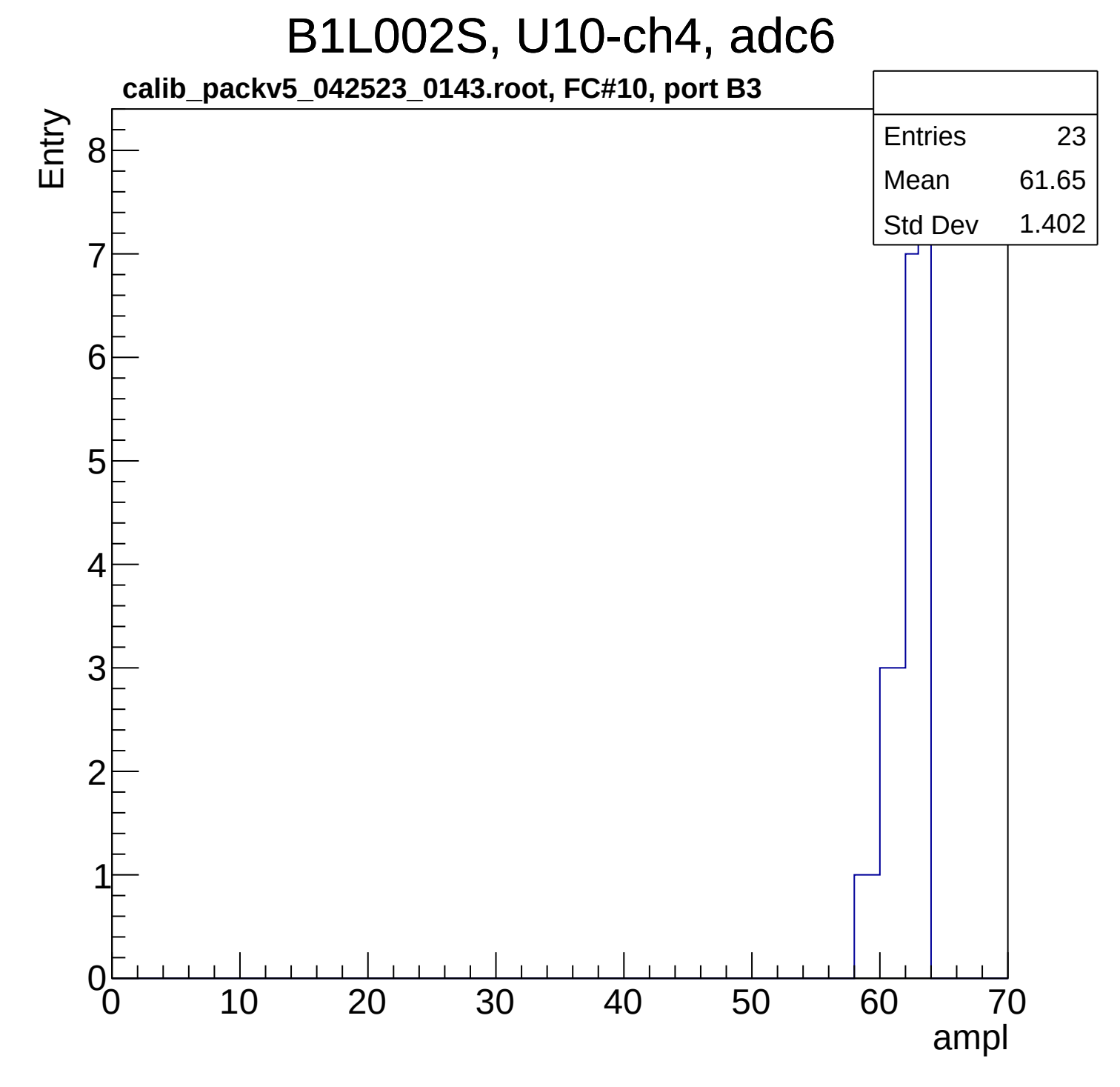
B1L002S, U10-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.65
Std Dev 1.402
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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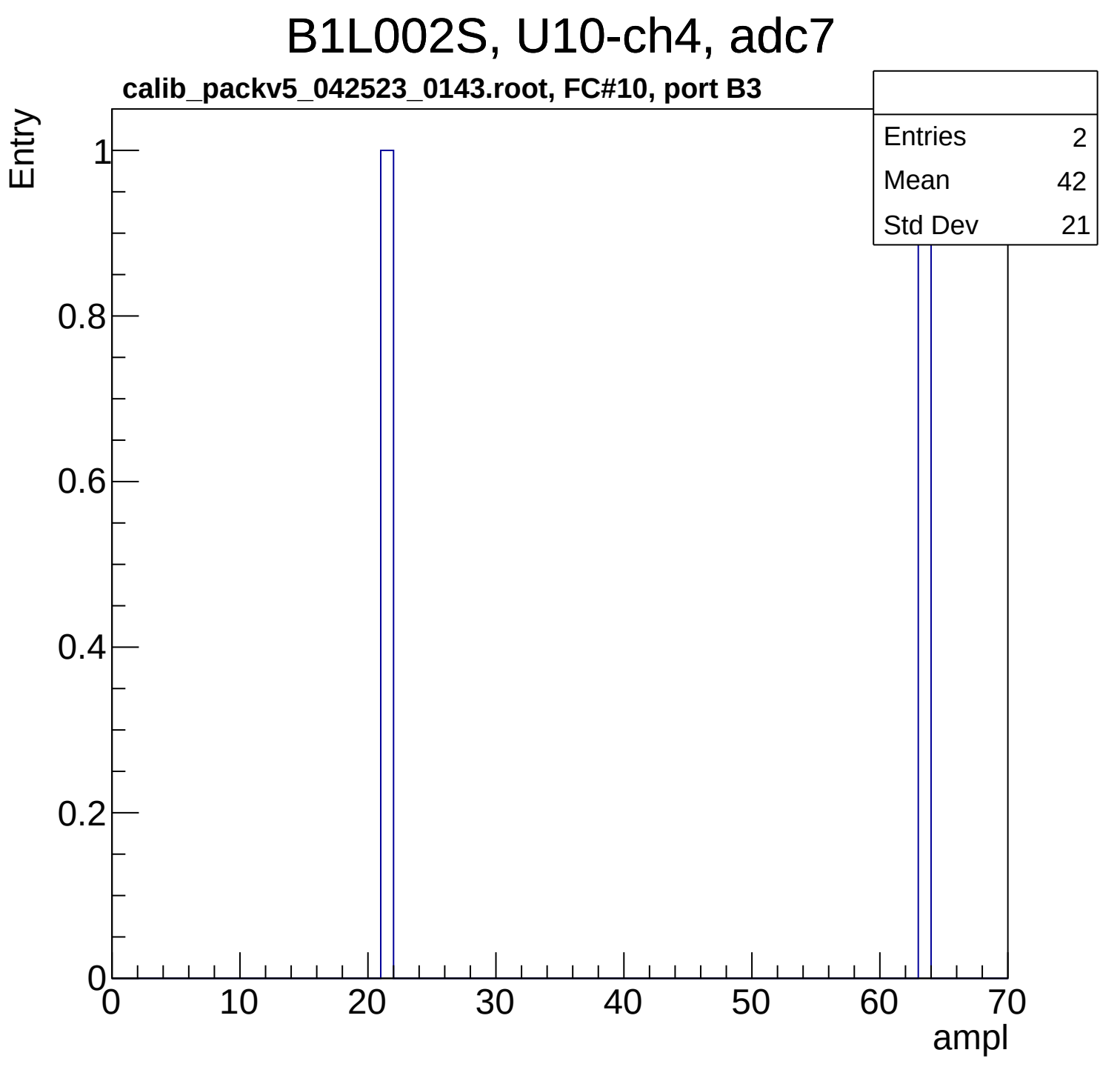
B1L002S, U10-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
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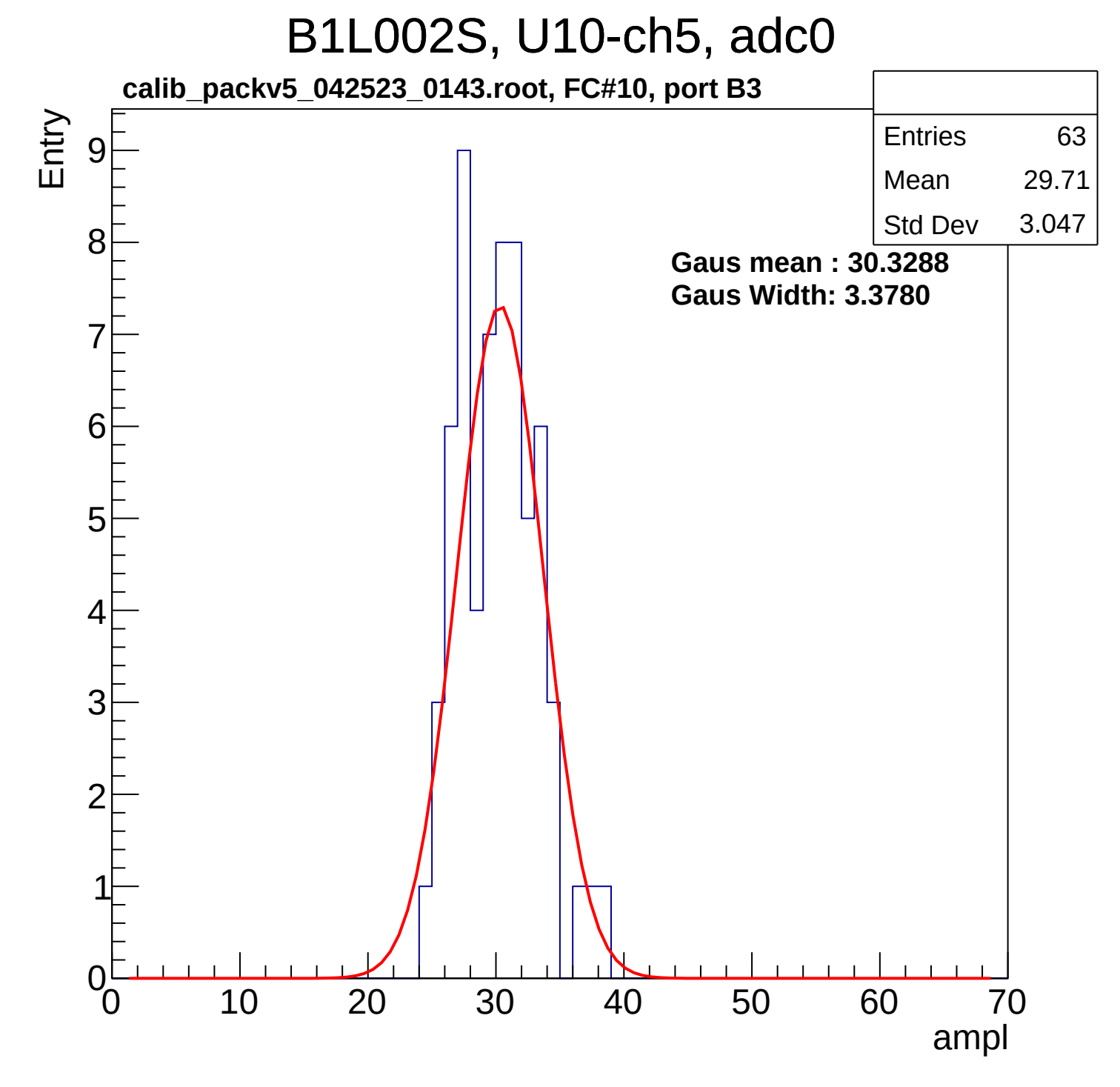
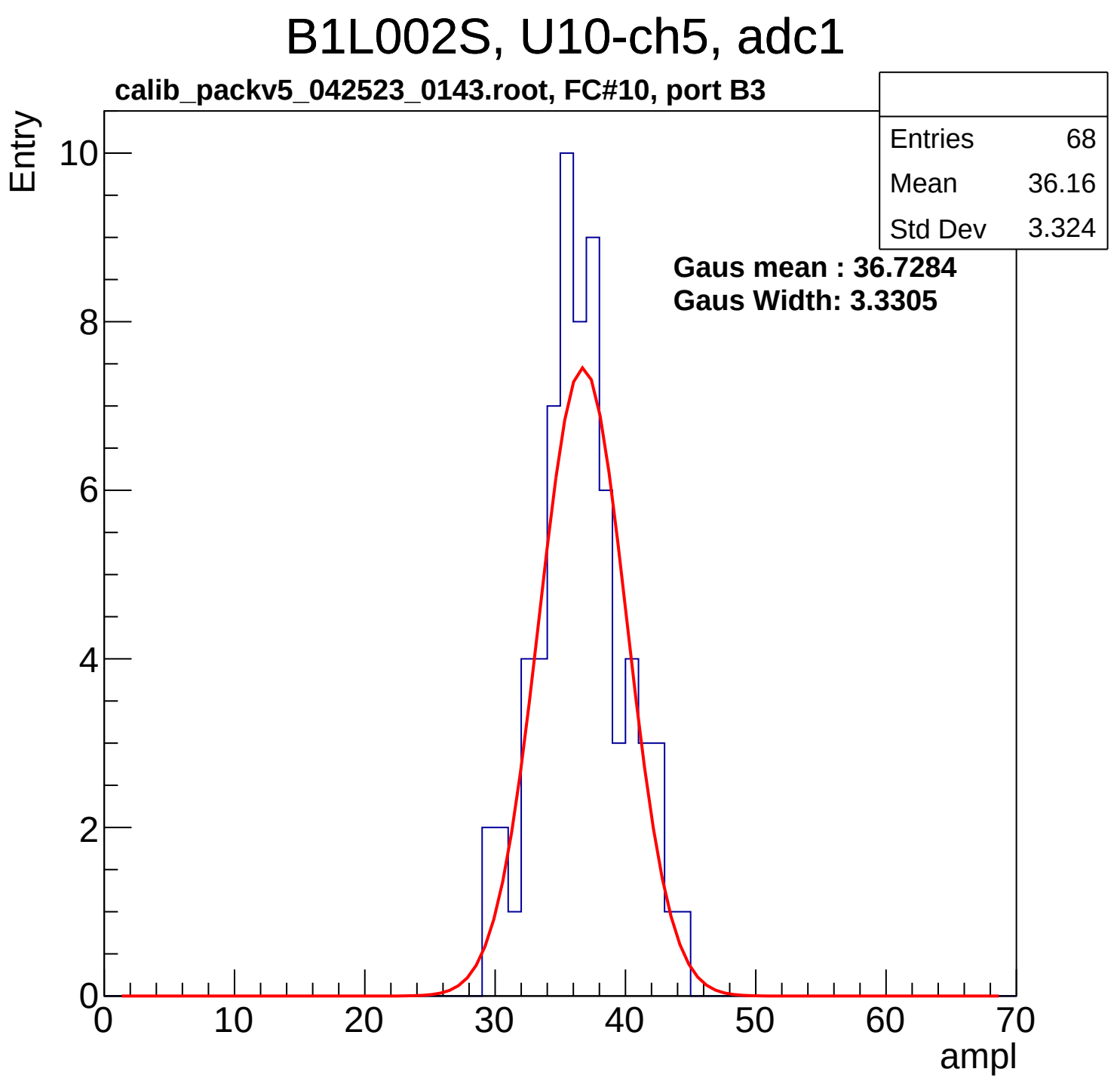
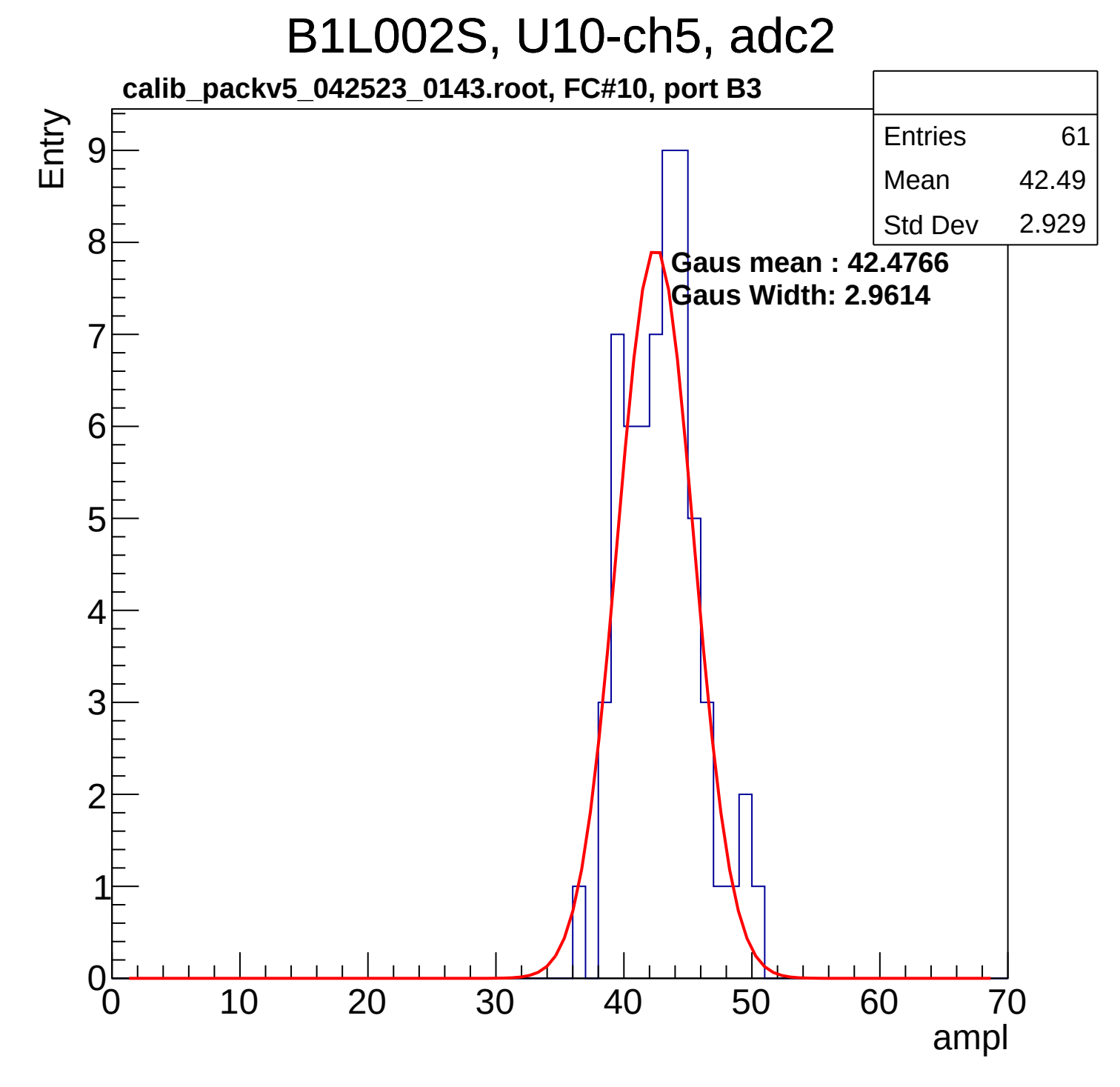
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 29.71
Std Dev 3.047
Gaus mean : 30.3288
Gaus Width: 3.3780
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.16
Std Dev 3.324
Gaus mean : 36.7284
Gaus Width: 3.3305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 42.49
Std Dev 2.929
Gaus mean : 42.4766
Gaus Width: 2.9614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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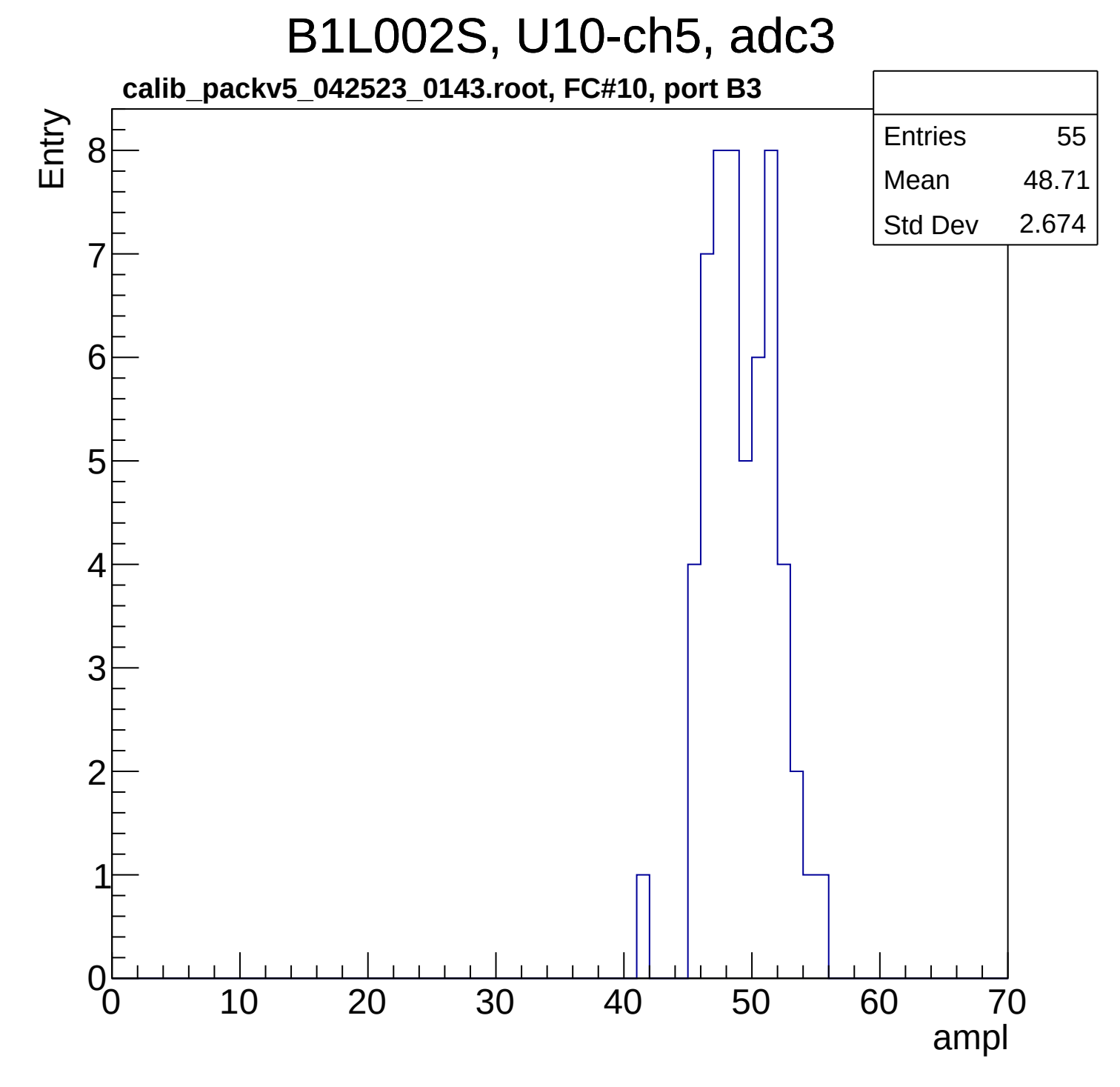
B1L002S, U10-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.71
Std Dev 2.674
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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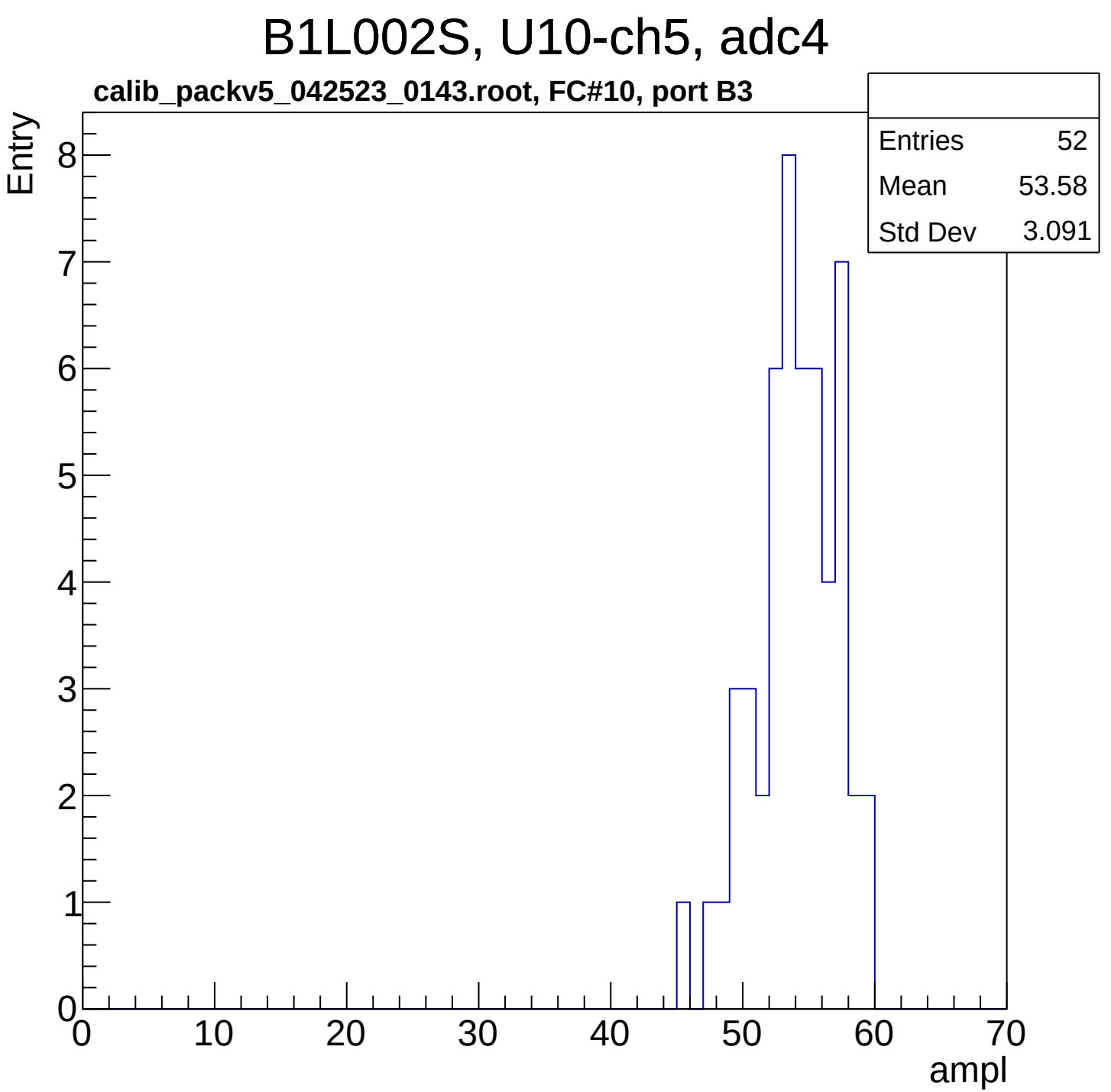
B1L002S, U10-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.58
Std Dev 3.091
Entry
ampl
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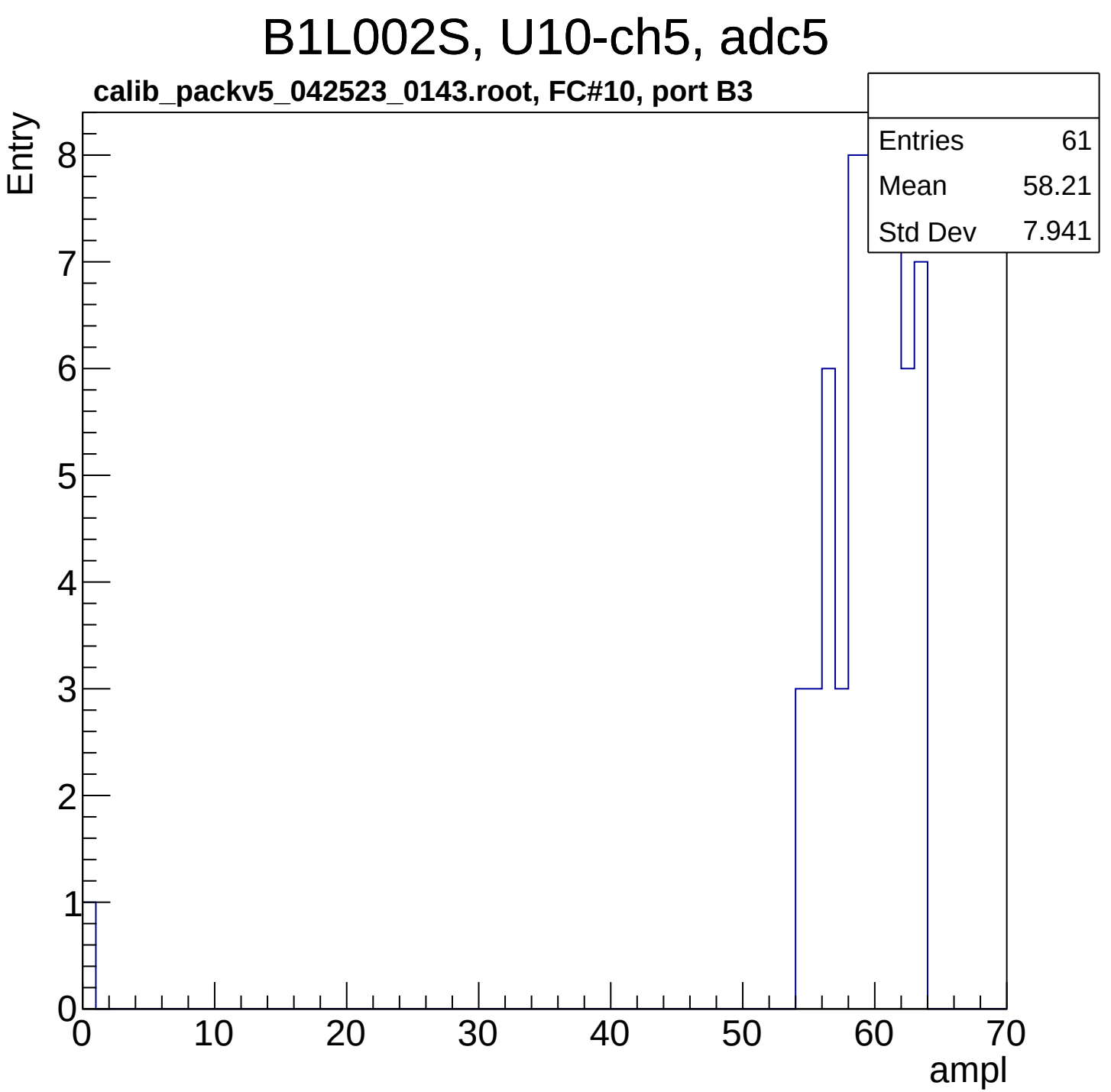
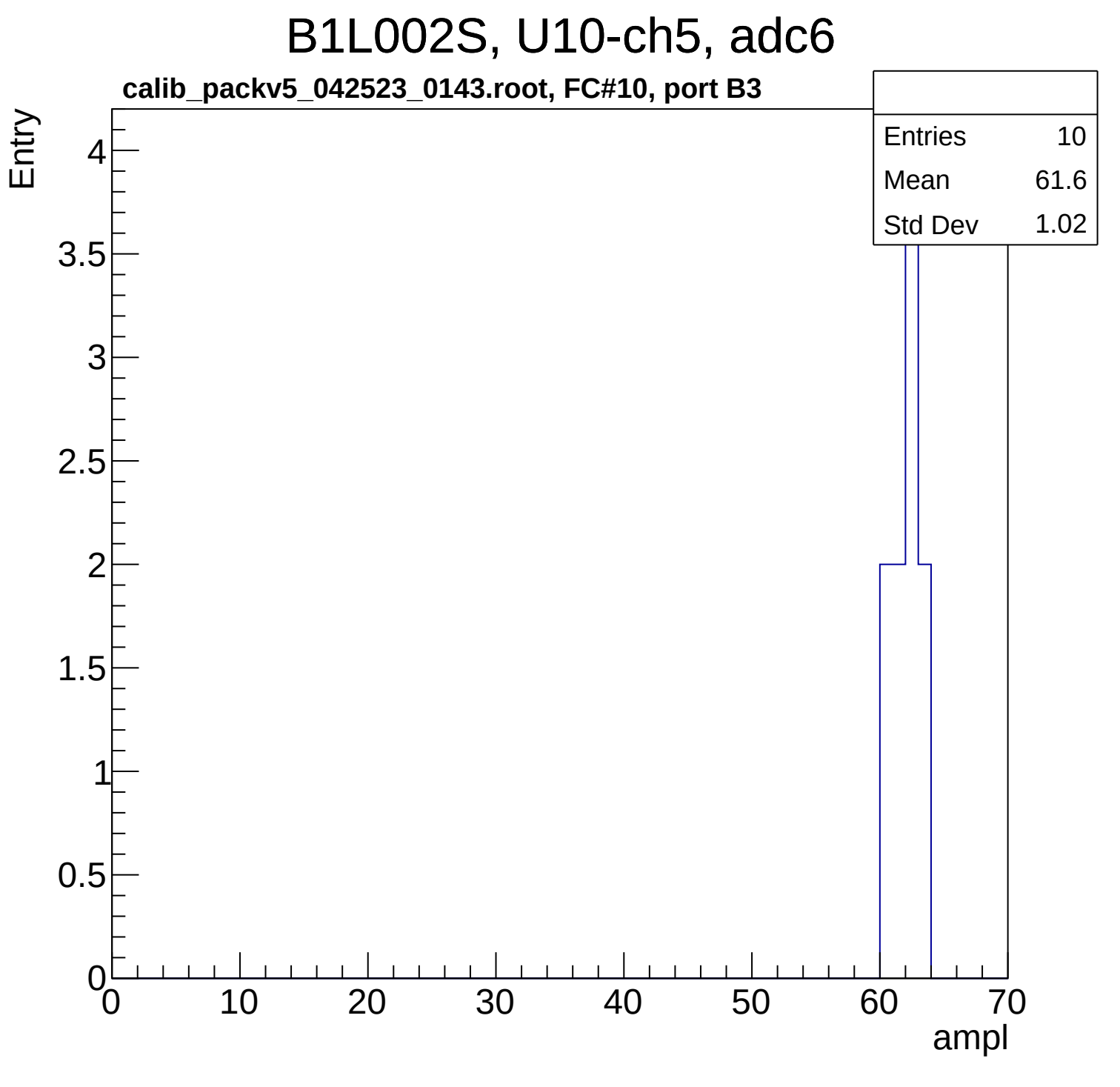
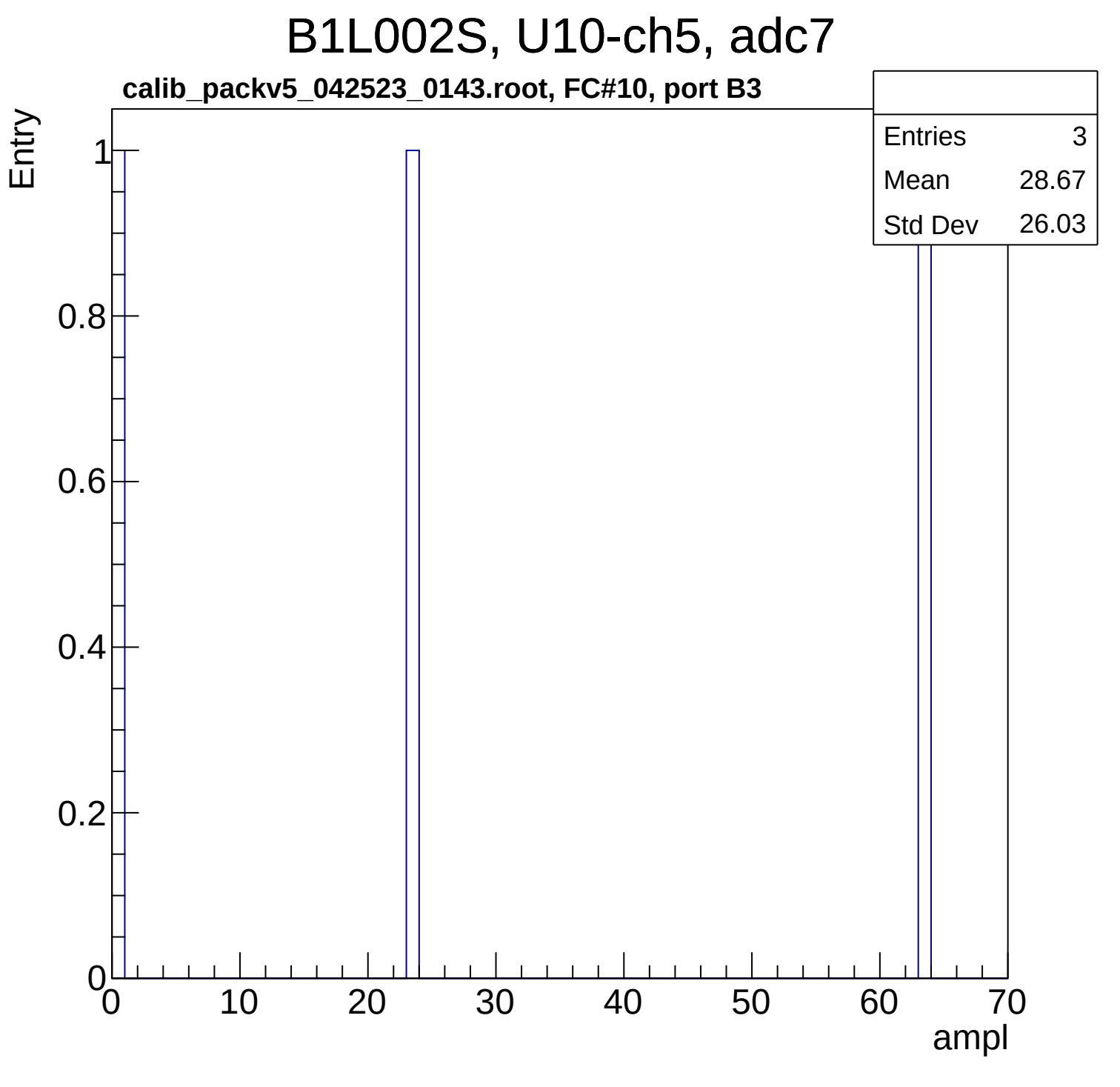
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.21
Std Dev 7.941
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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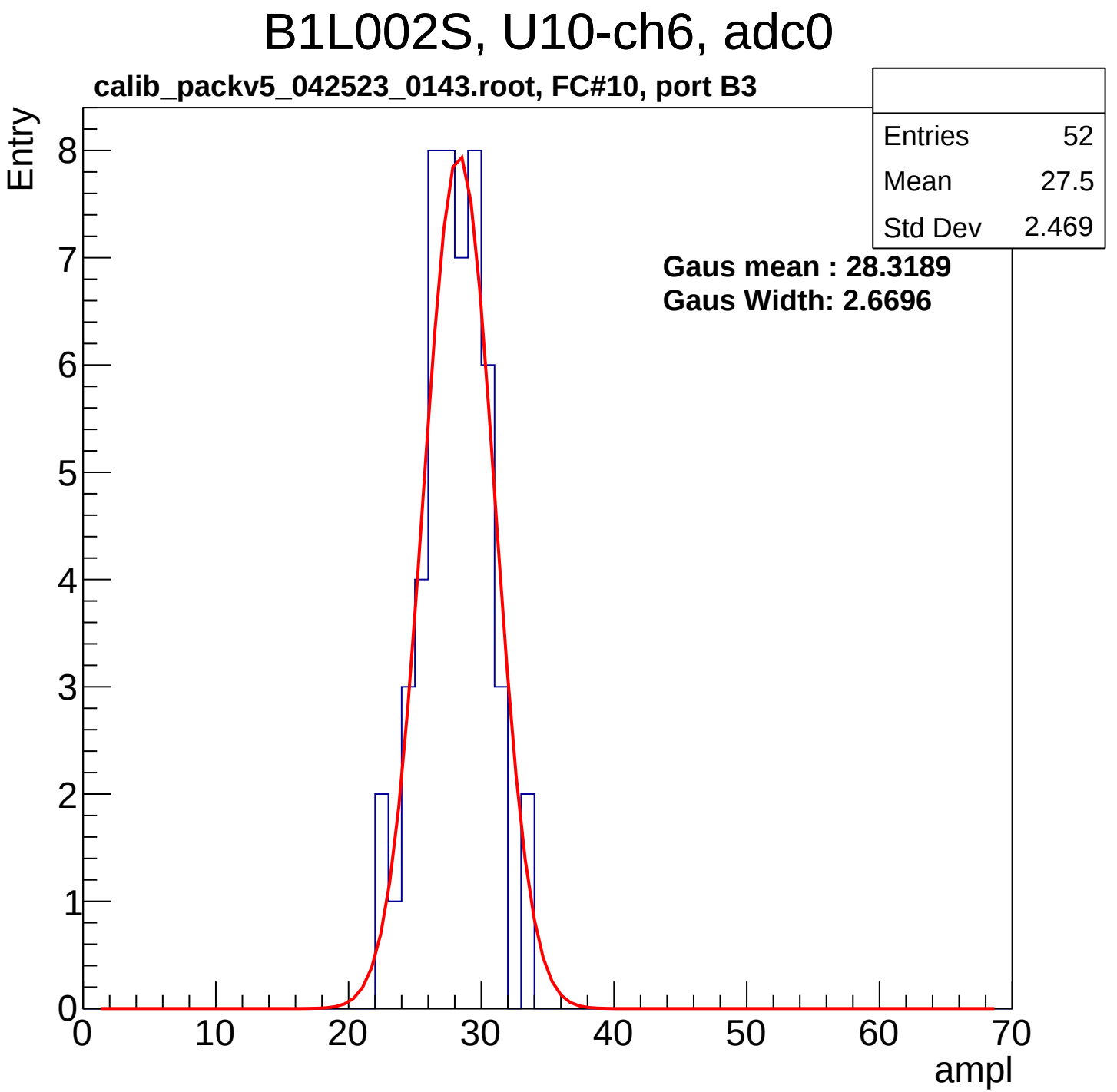
B1L002S, U10-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.5
Std Dev 2.469
Gaus mean : 28.3189
Gaus Width: 2.6696
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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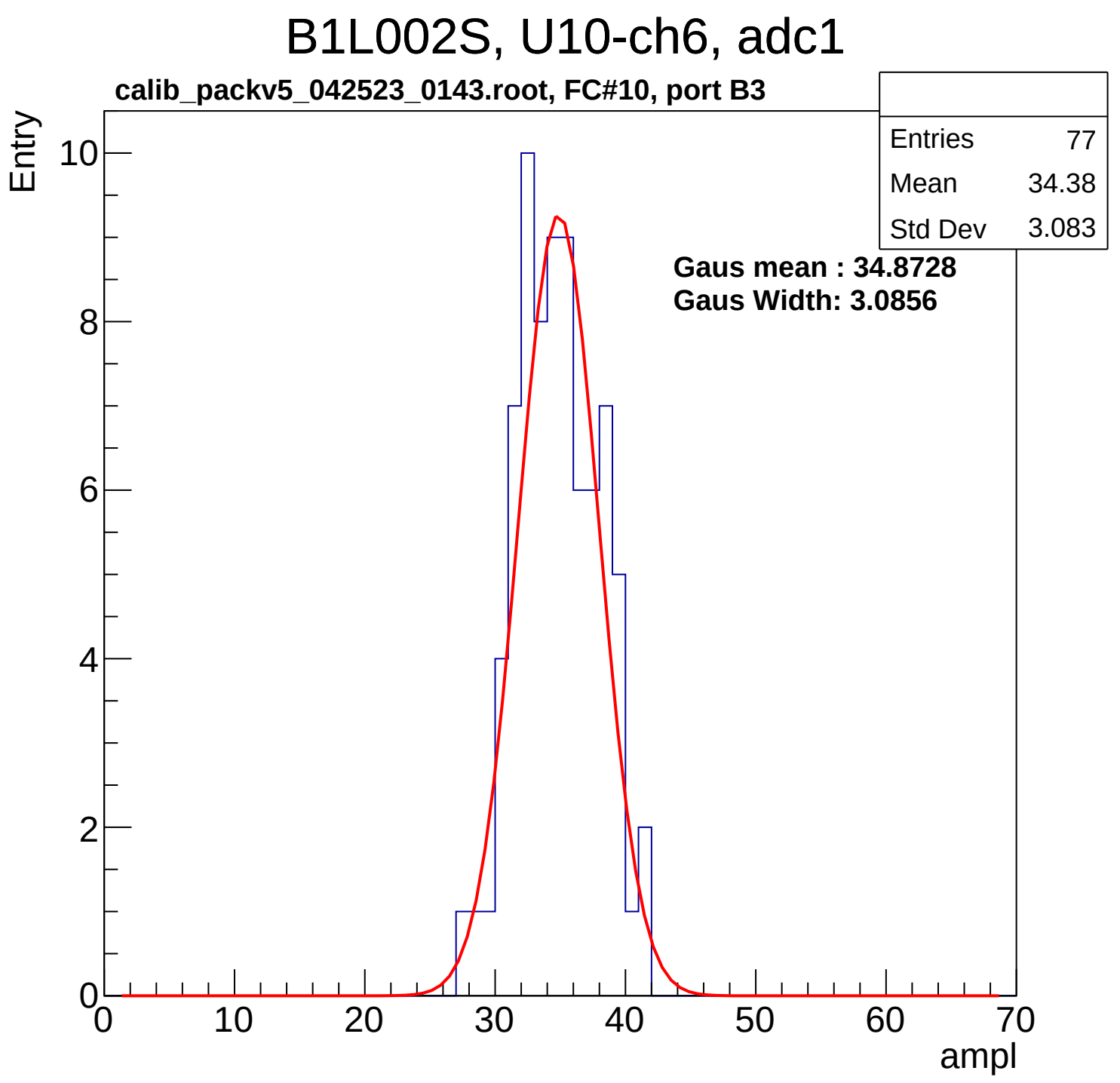
40
50
60
70


B1L002S, U10-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.38
Std Dev 3.083
Gaus mean : 34.8728
Gaus Width: 3.0856
Entry
ampl
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2
4
6
8
10
0
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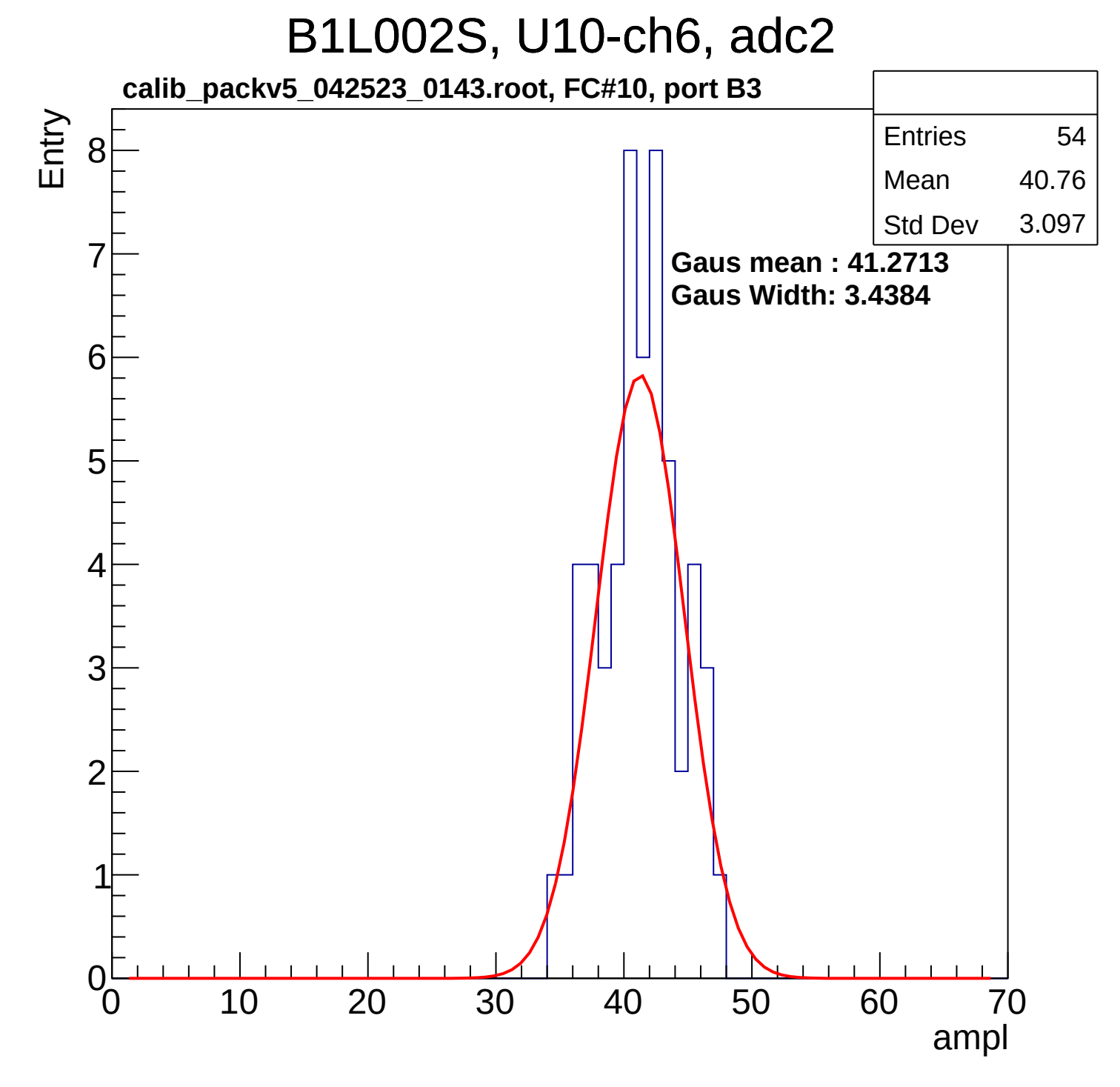
20
30
40
50
60
70


B1L002S, U10-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.76
Std Dev 3.097
Gaus mean : 41.2713
Gaus Width: 3.4384
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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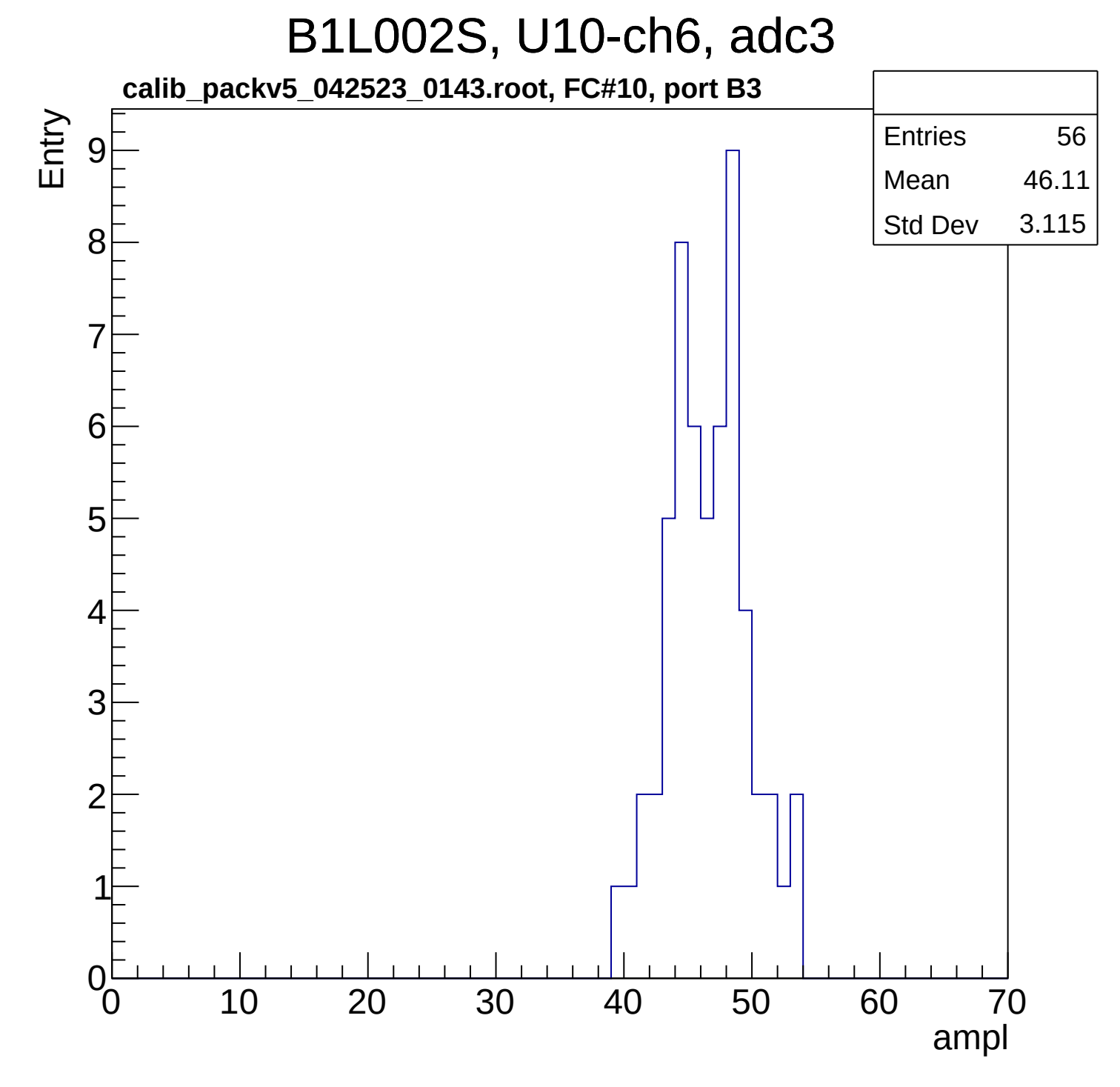
B1L002S, U10-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.11
Std Dev 3.115
Entry
ampl
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3
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5
6
7
8
9
0
10
20
30
40
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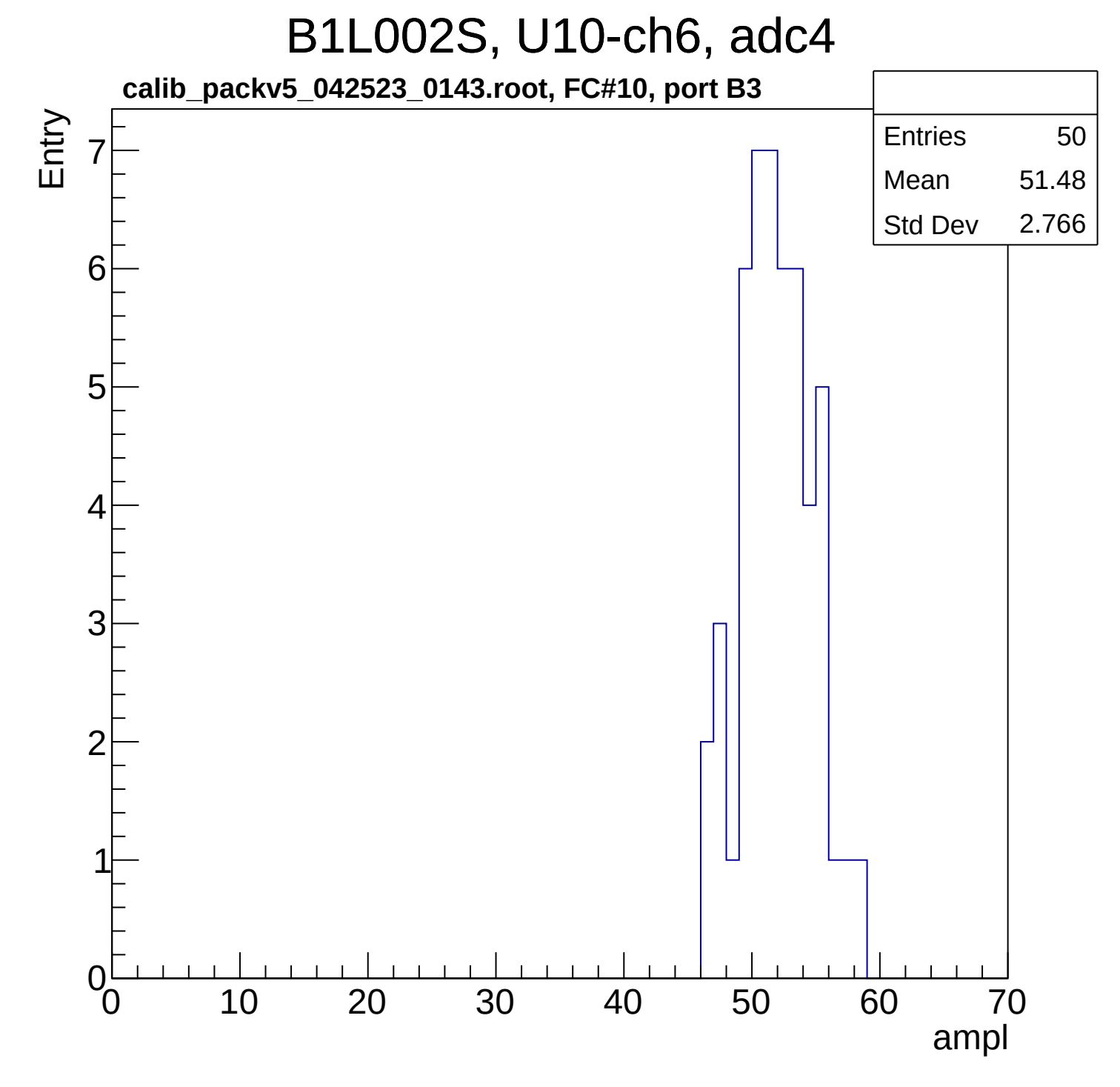
60
70


B1L002S, U10-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.48
Std Dev 2.766
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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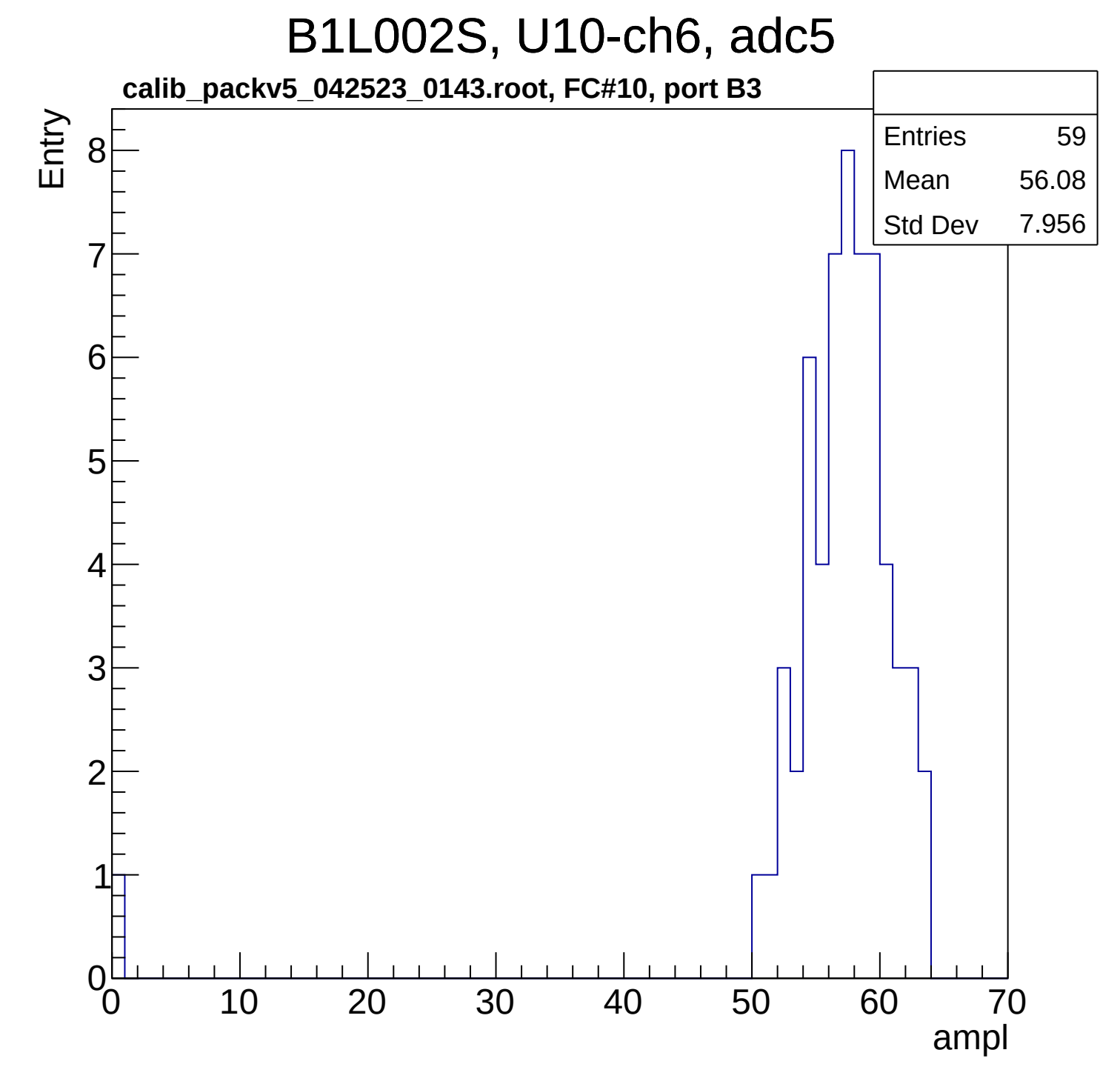
B1L002S, U10-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 56.08
Std Dev 7.956
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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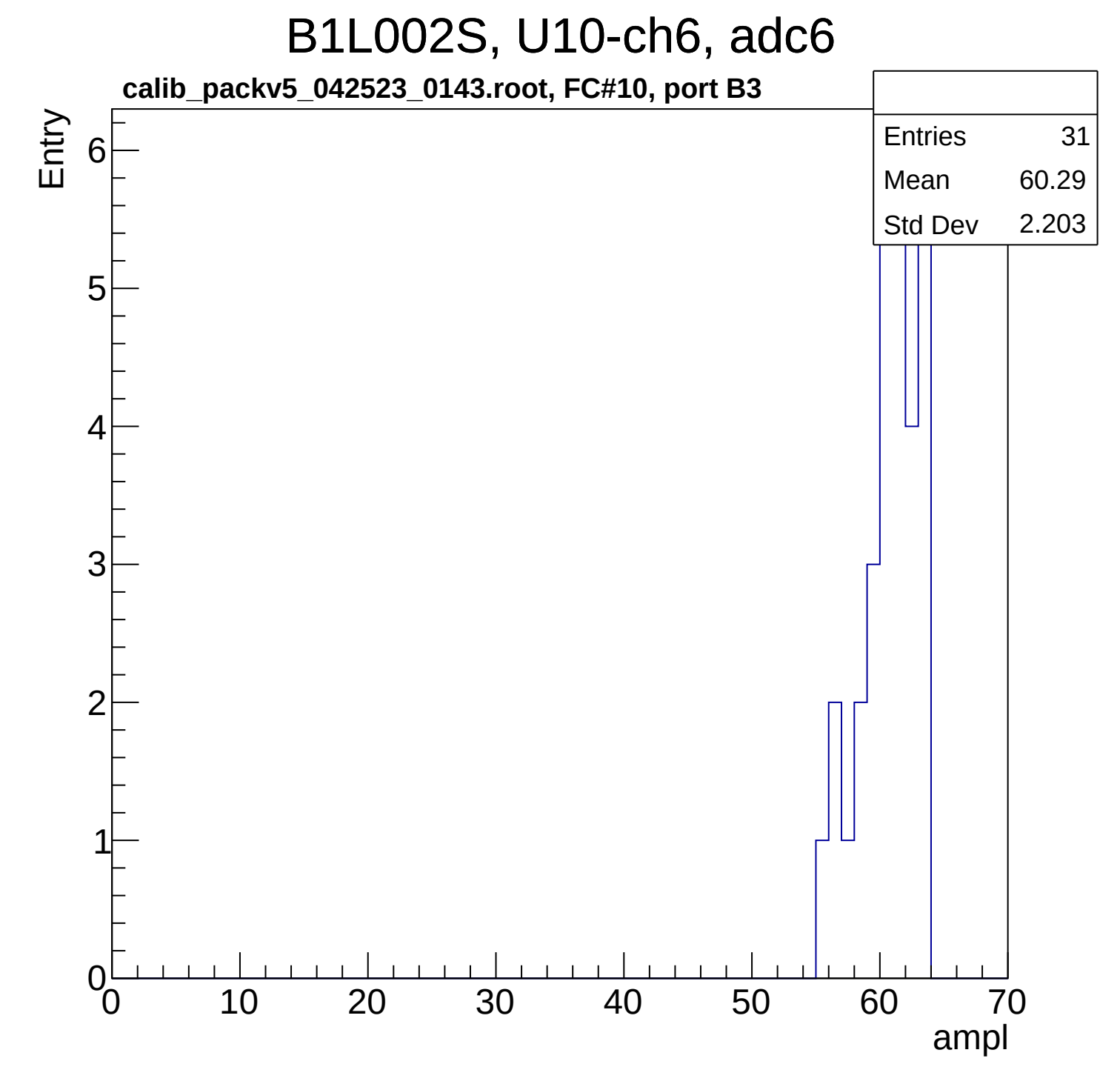
B1L002S, U10-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 60.29
Std Dev 2.203
Entry
ampl
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2
3
4
5
6
0
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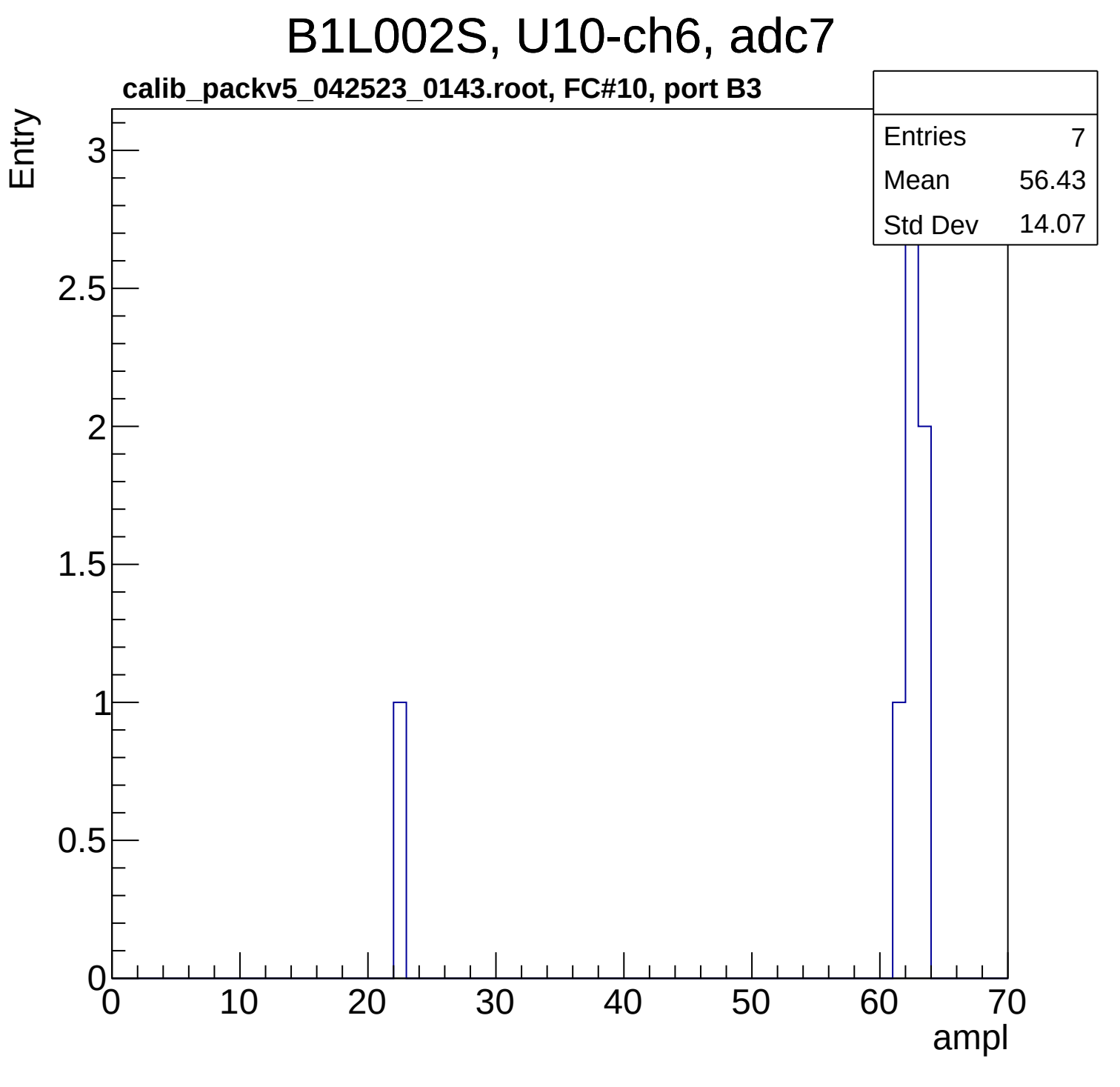
20
30
40
50
60
70


B1L002S, U10-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 56.43
Std Dev 14.07
Entry
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1
1.5
2
2.5
3
0
10
20
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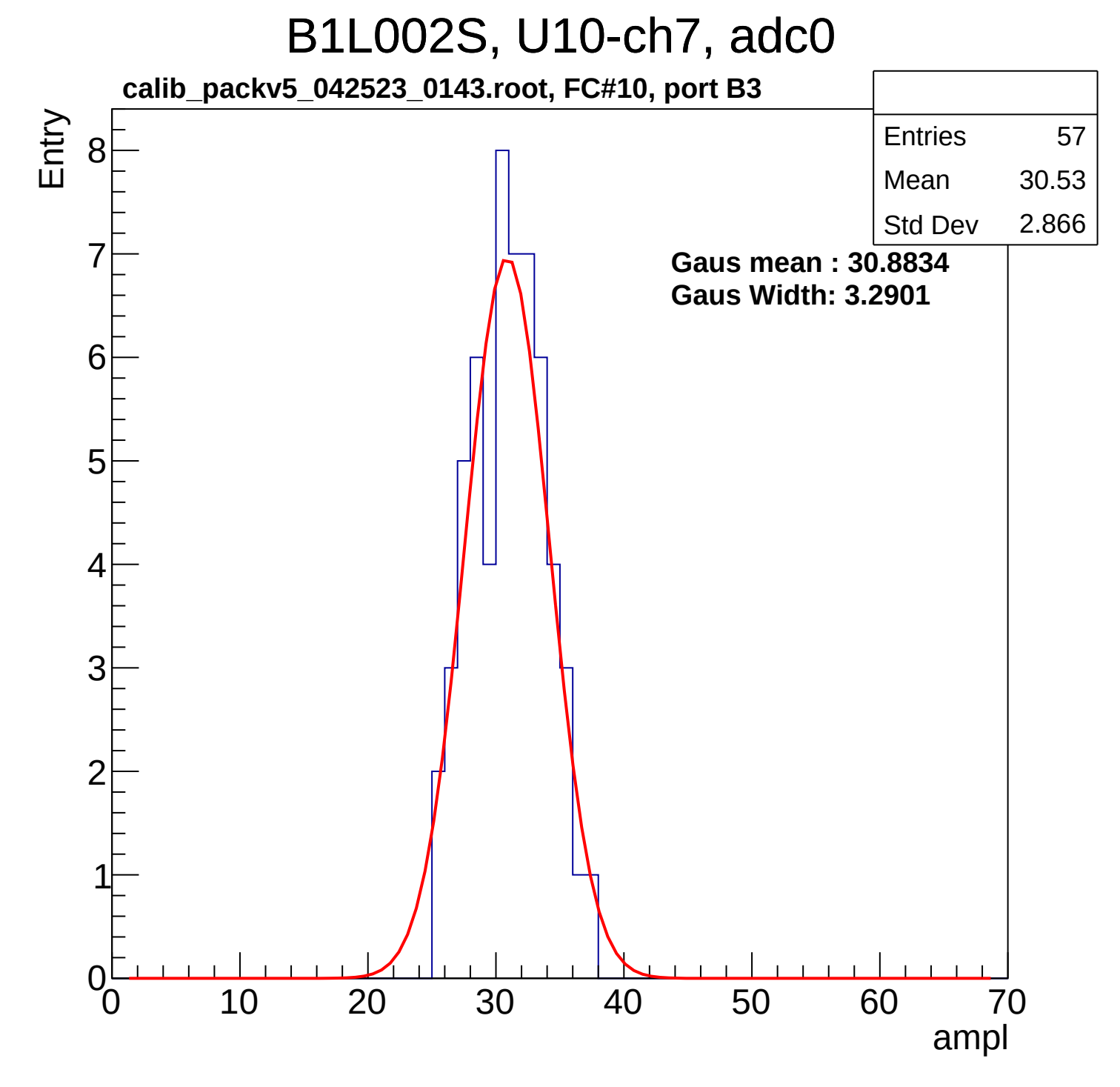
40
50
60
70
ampl


B1L002S, U10-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 30.53
Std Dev 2.866
Gaus mean : 30.8834
Gaus Width: 3.2901
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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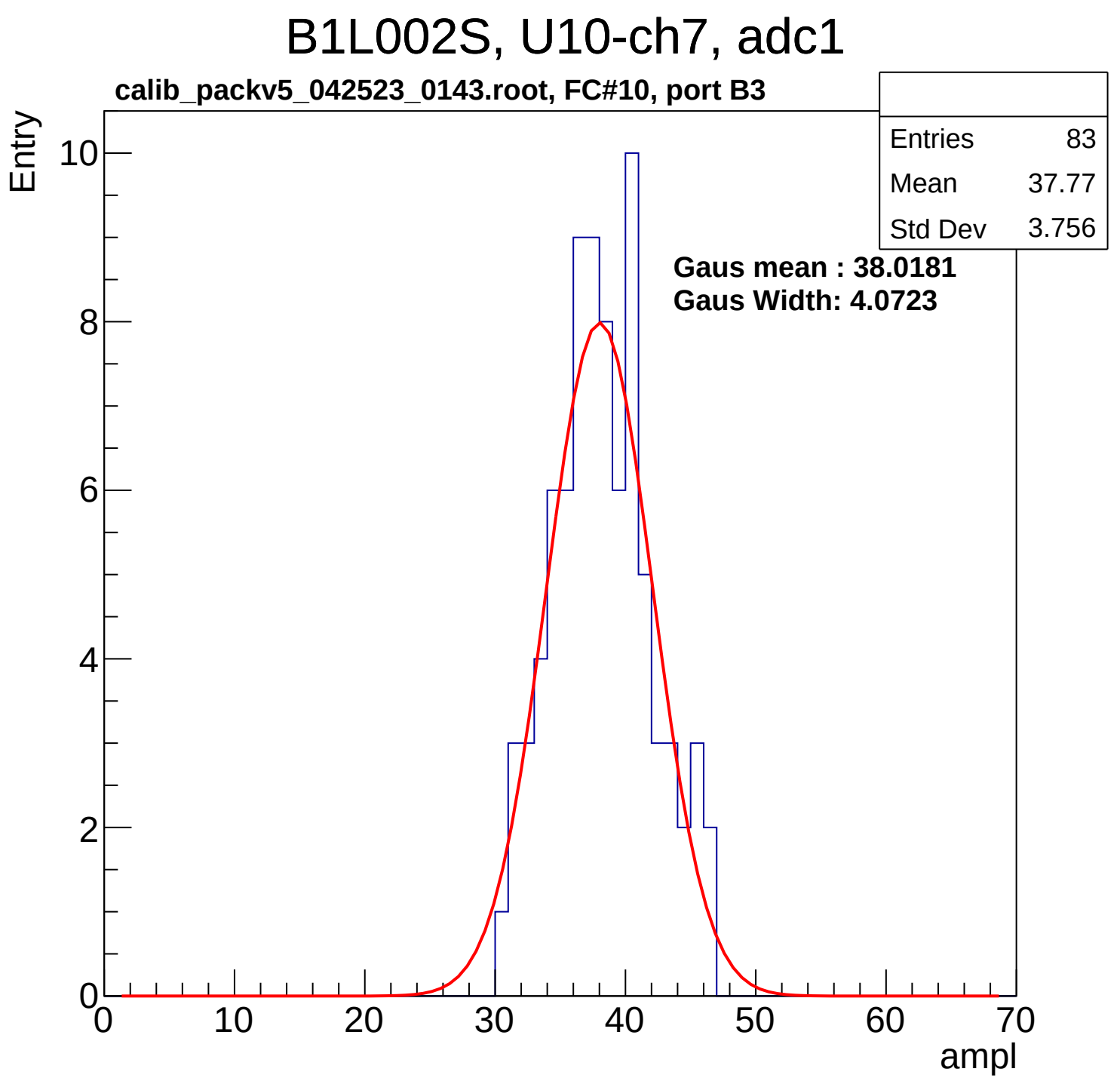
B1L002S, U10-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 37.77
Std Dev 3.756
Gaus mean : 38.0181
Gaus Width: 4.0723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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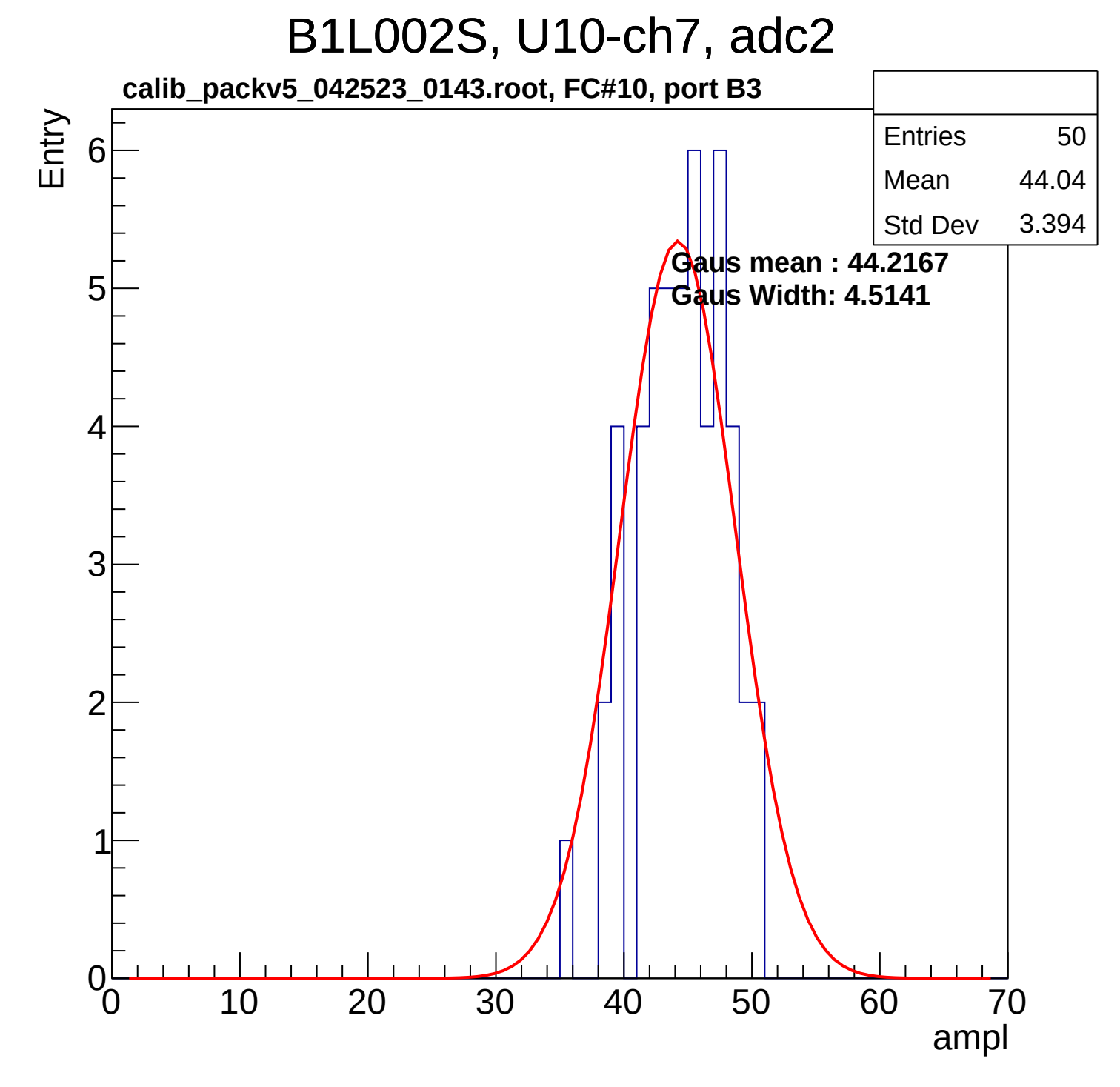
B1L002S, U10-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 44.04
Std Dev 3.394
Gaus mean : 44.2167
Gaus Width: 4.5141
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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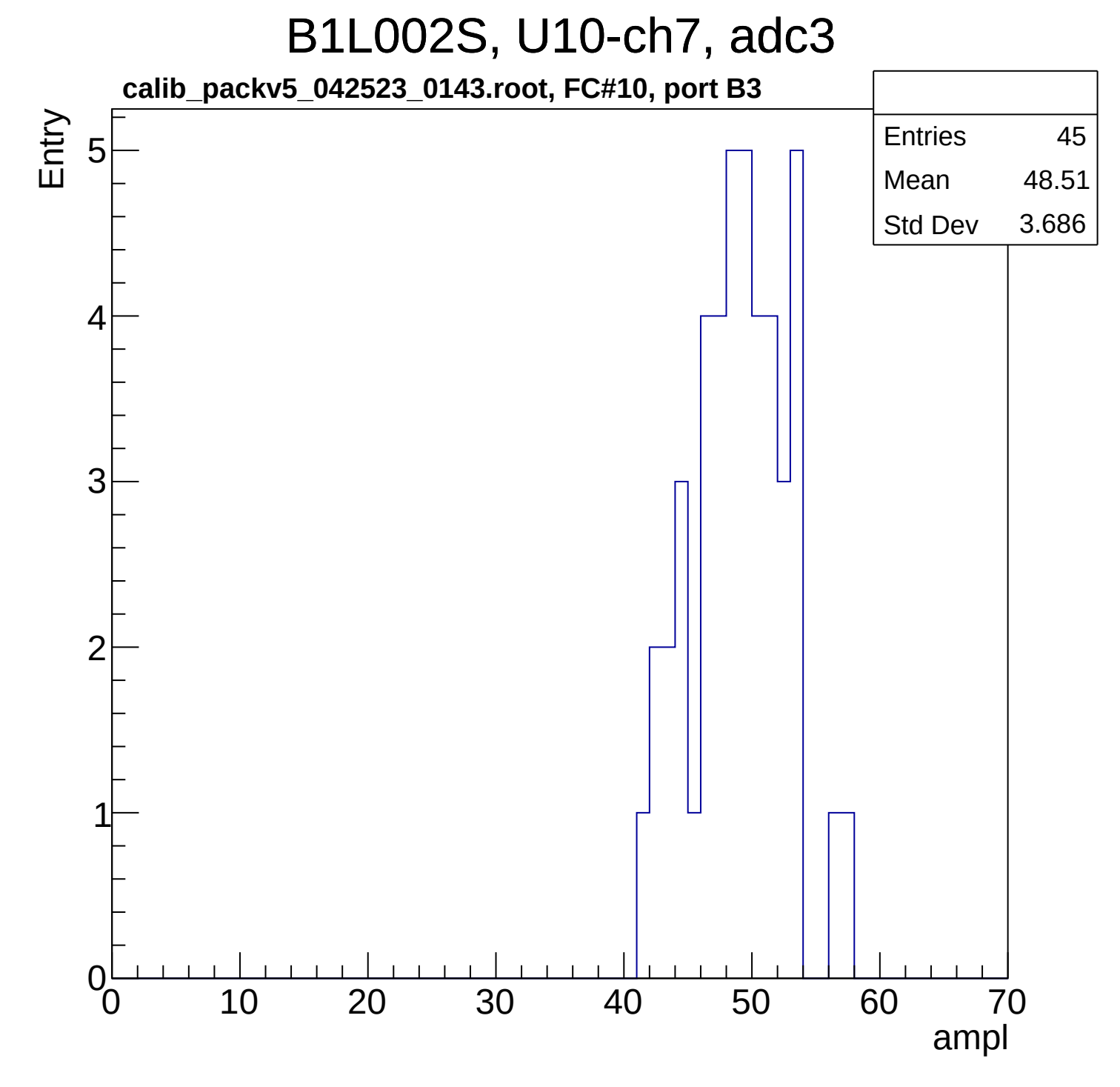
40
50
60
70


B1L002S, U10-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 48.51
Std Dev 3.686
Entry
ampl
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3
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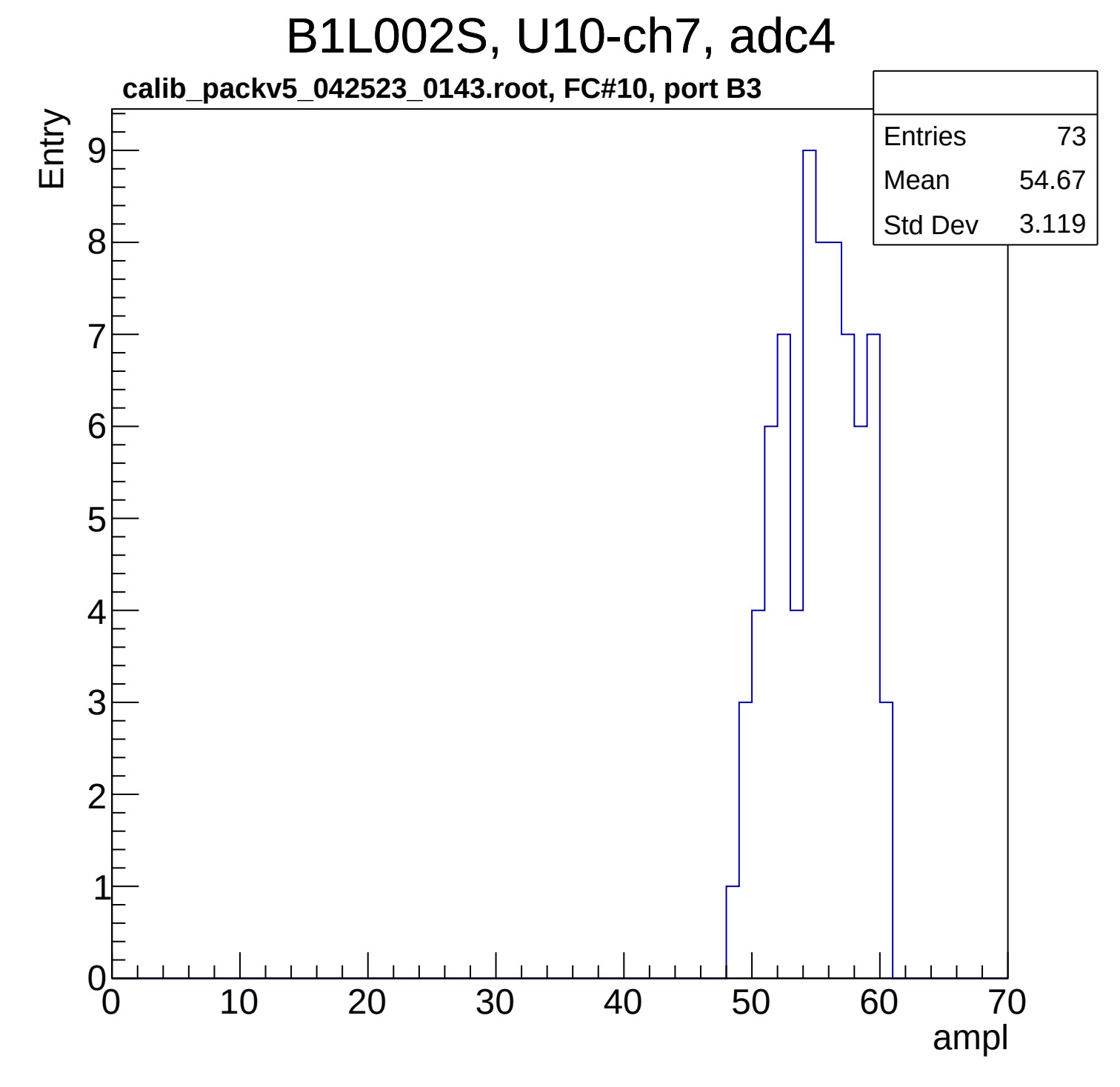
B1L002S, U10-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 54.67
Std Dev 3.119
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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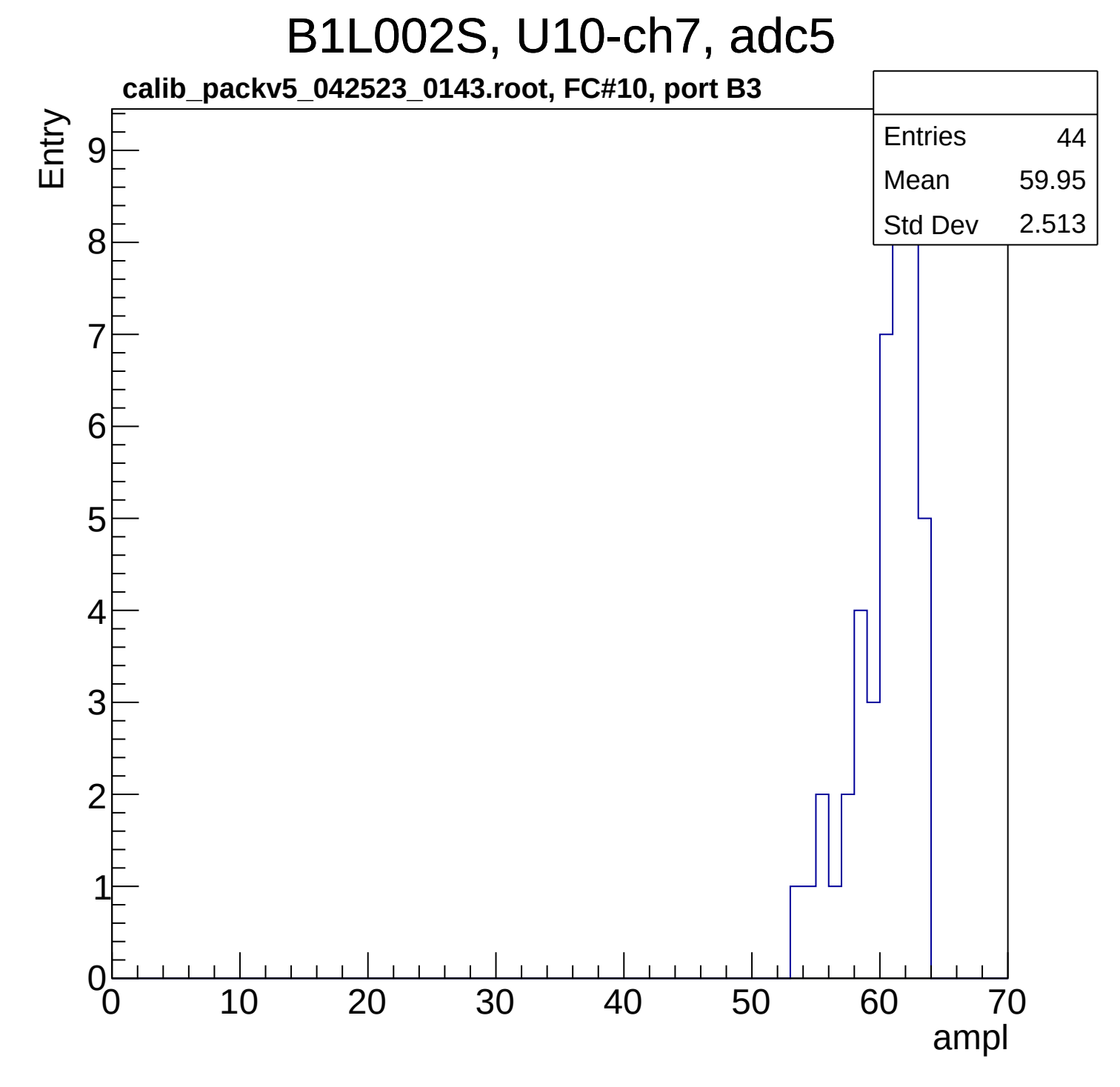
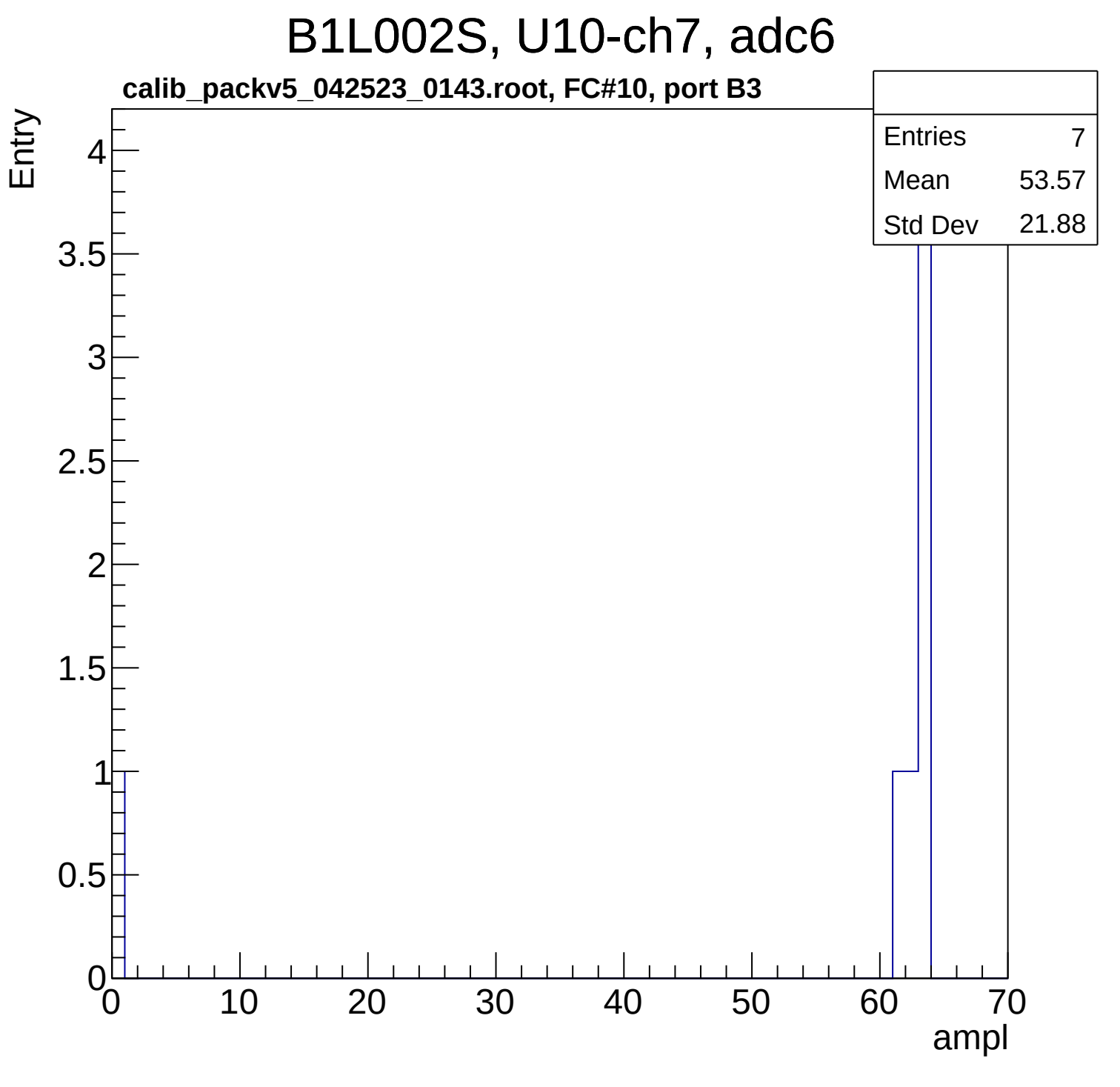

B1L002S, U10-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.95
Std Dev 2.513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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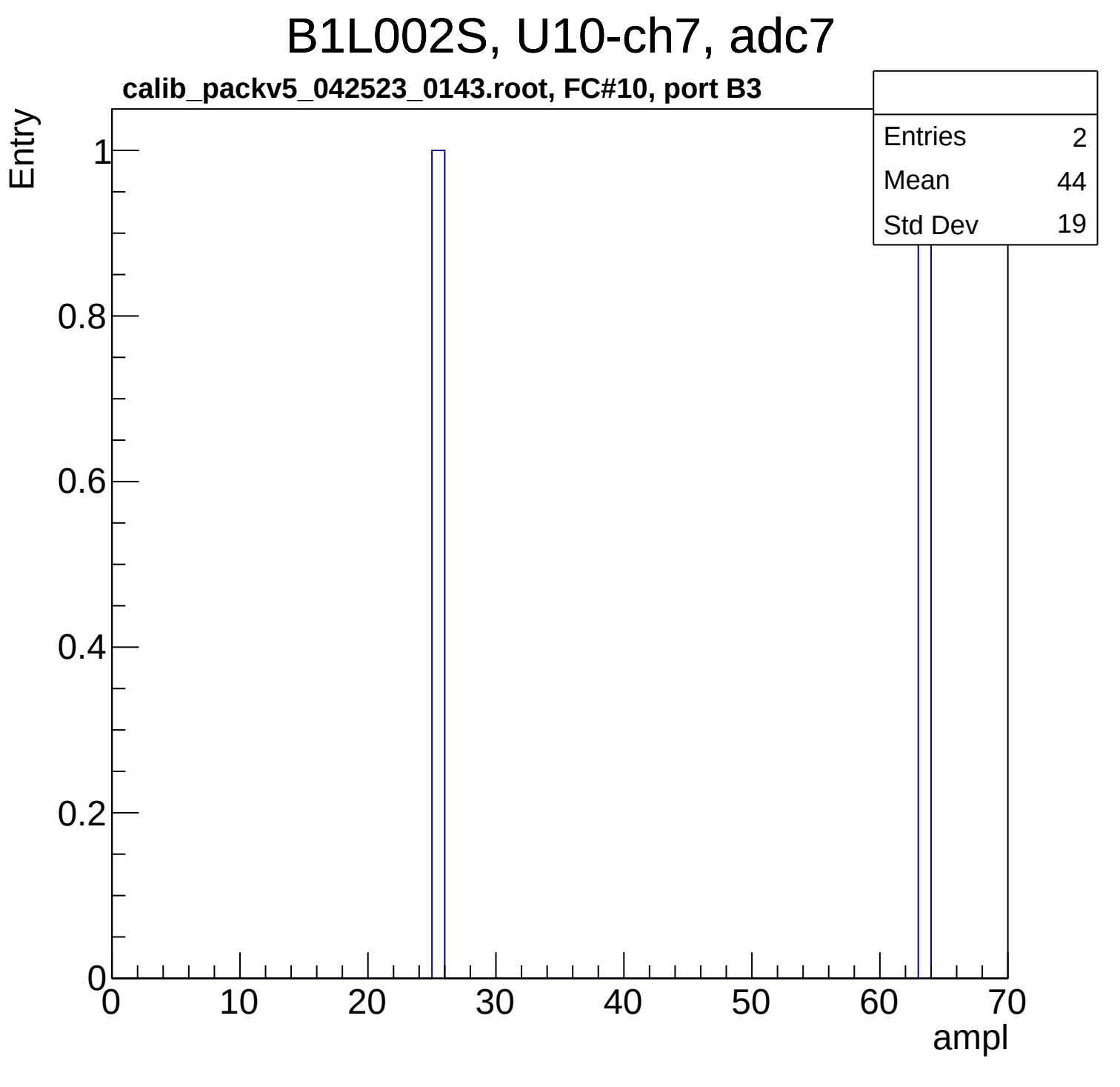
B1L002S, U10-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 44
Std Dev 19
Entry
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0.6
0.4
0.2
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10
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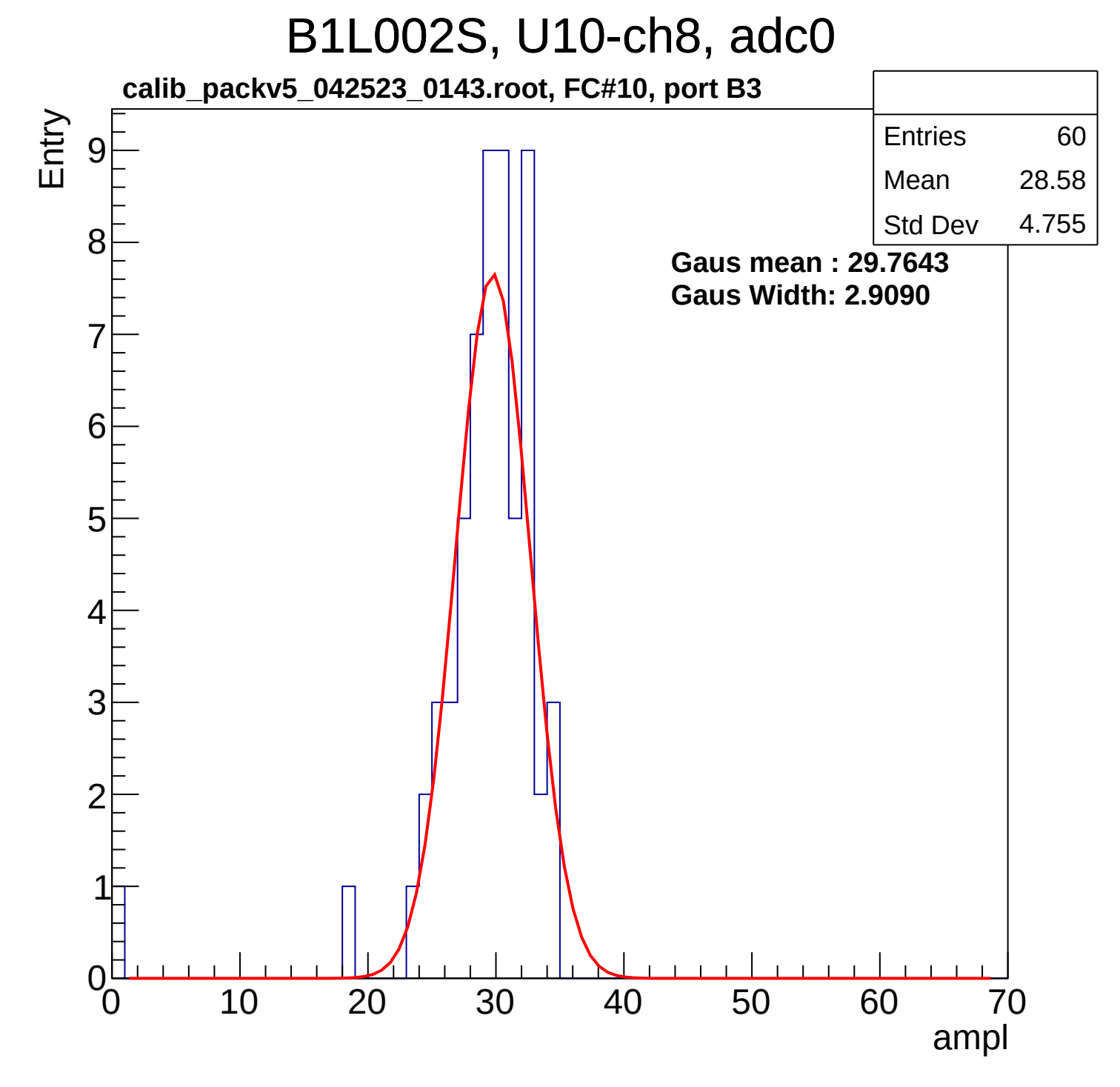
40
50
60
70
ampl


B1L002S, U10-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.58
Std Dev 4.755
Gaus mean : 29.7643
Gaus Width: 2.9090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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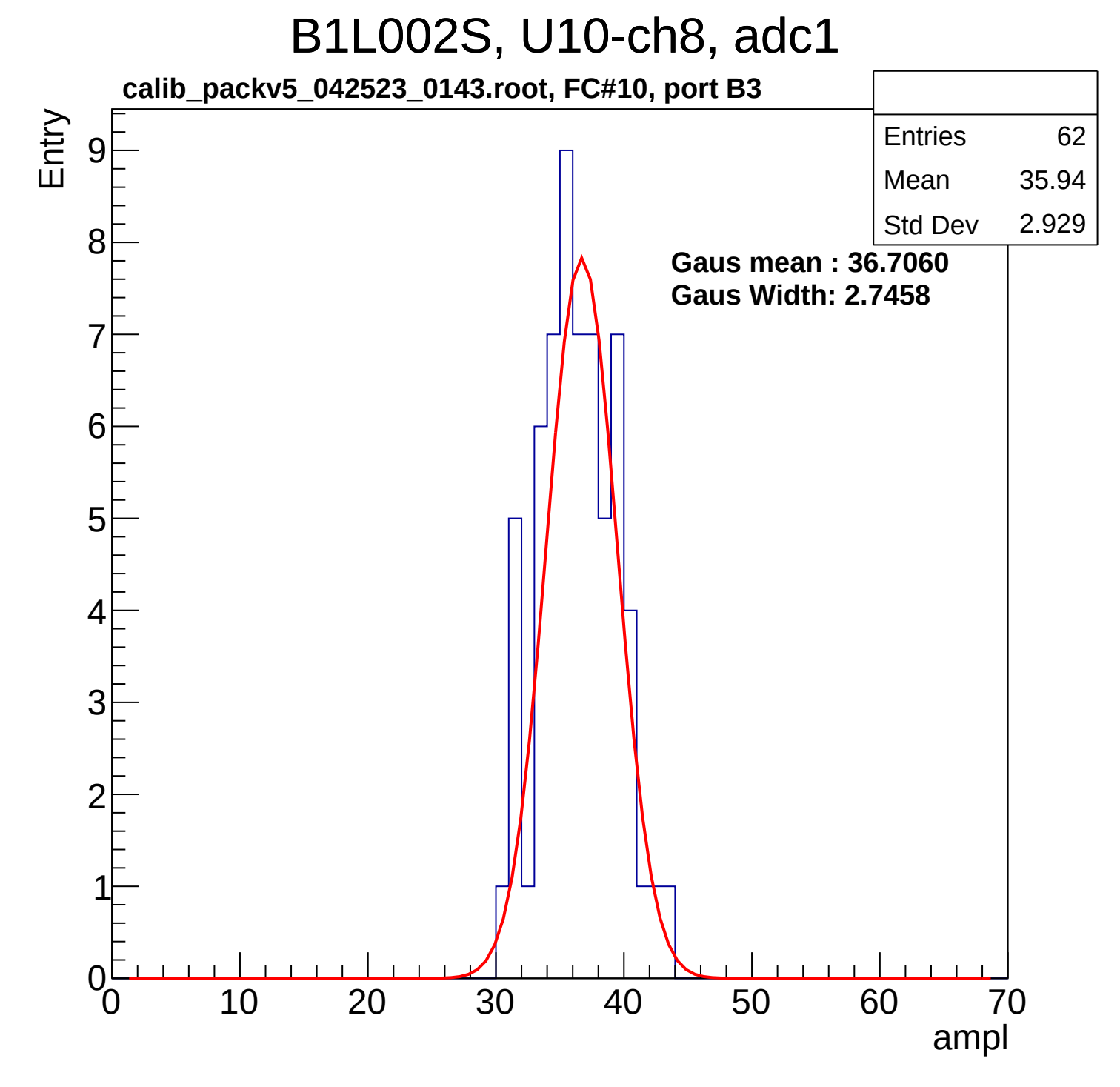
B1L002S, U10-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.94
Std Dev 2.929
Gaus mean : 36.7060
Gaus Width: 2.7458
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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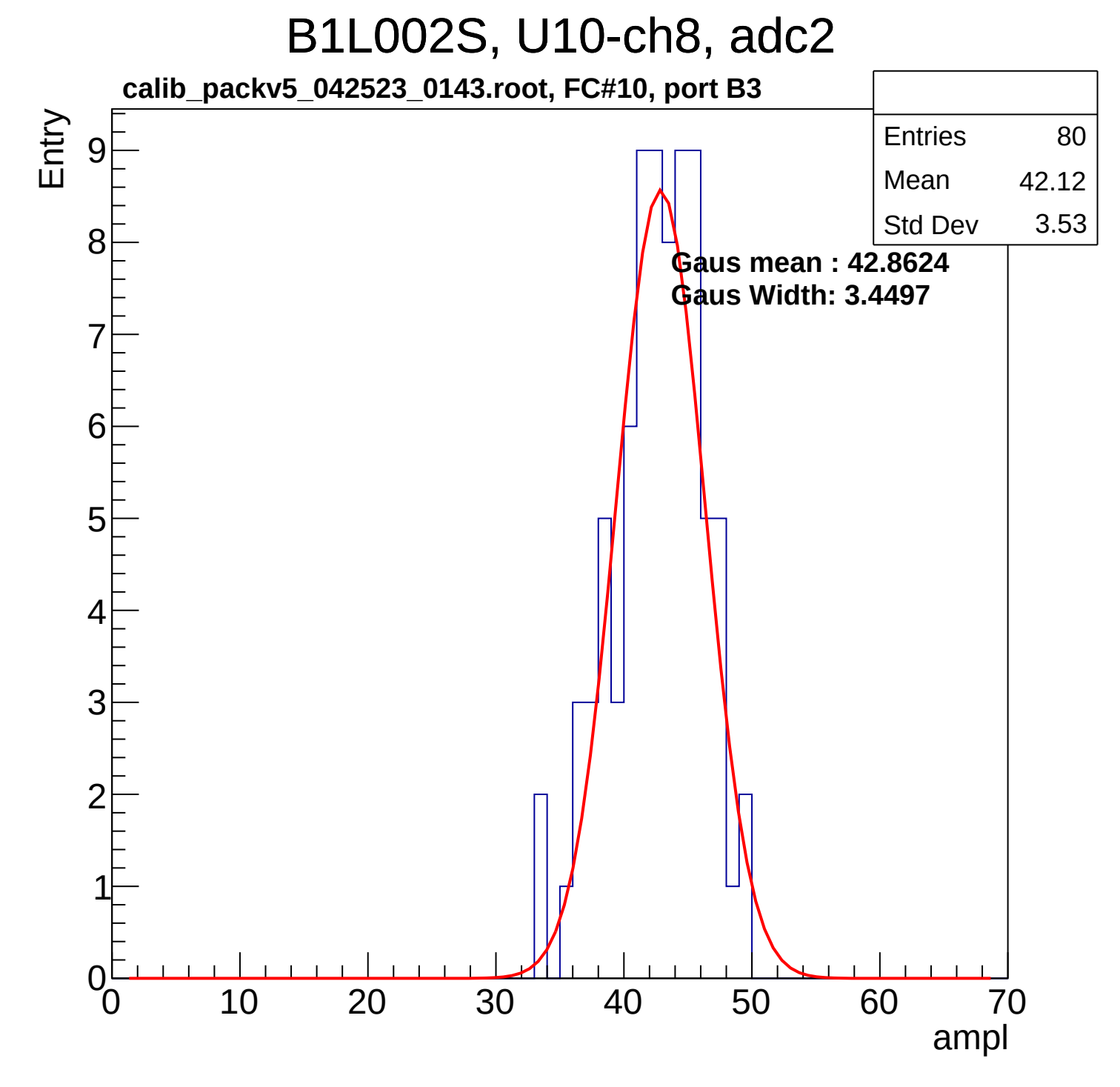
B1L002S, U10-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 42.12
Std Dev 3.53
Gaus mean : 42.8624
Gaus Width: 3.4497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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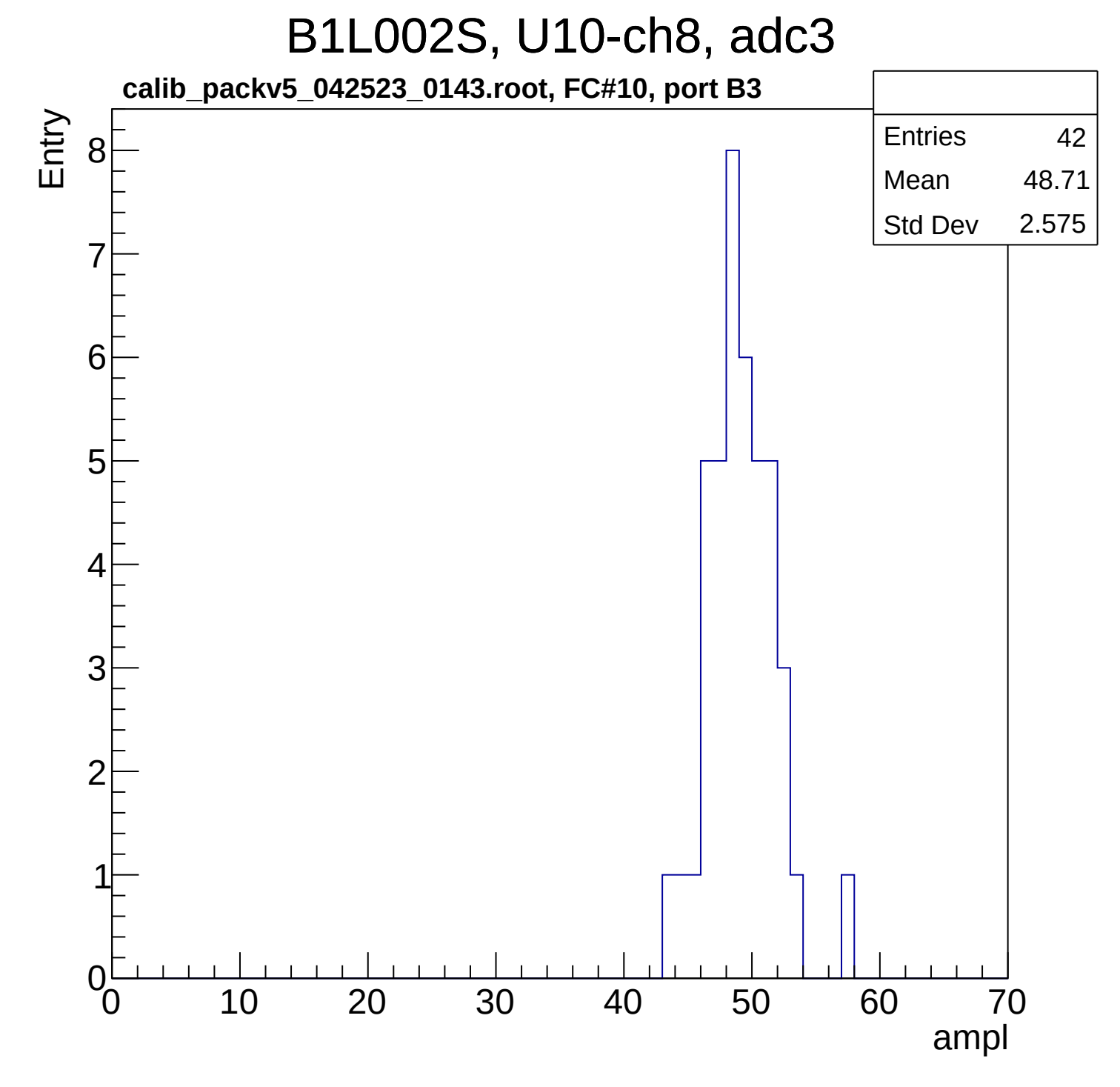
B1L002S, U10-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 48.71
Std Dev 2.575
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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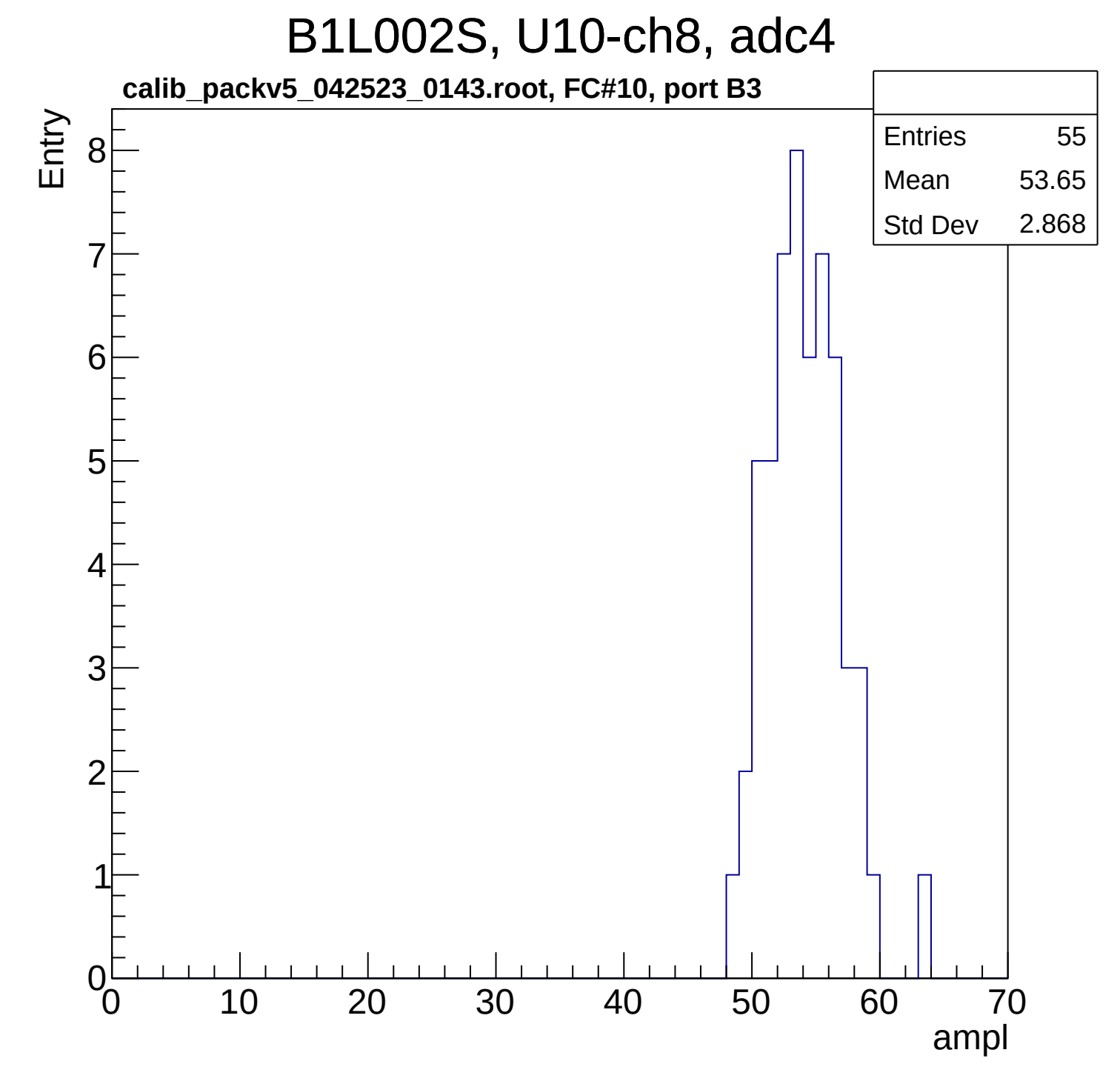
B1L002S, U10-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.65
Std Dev 2.868
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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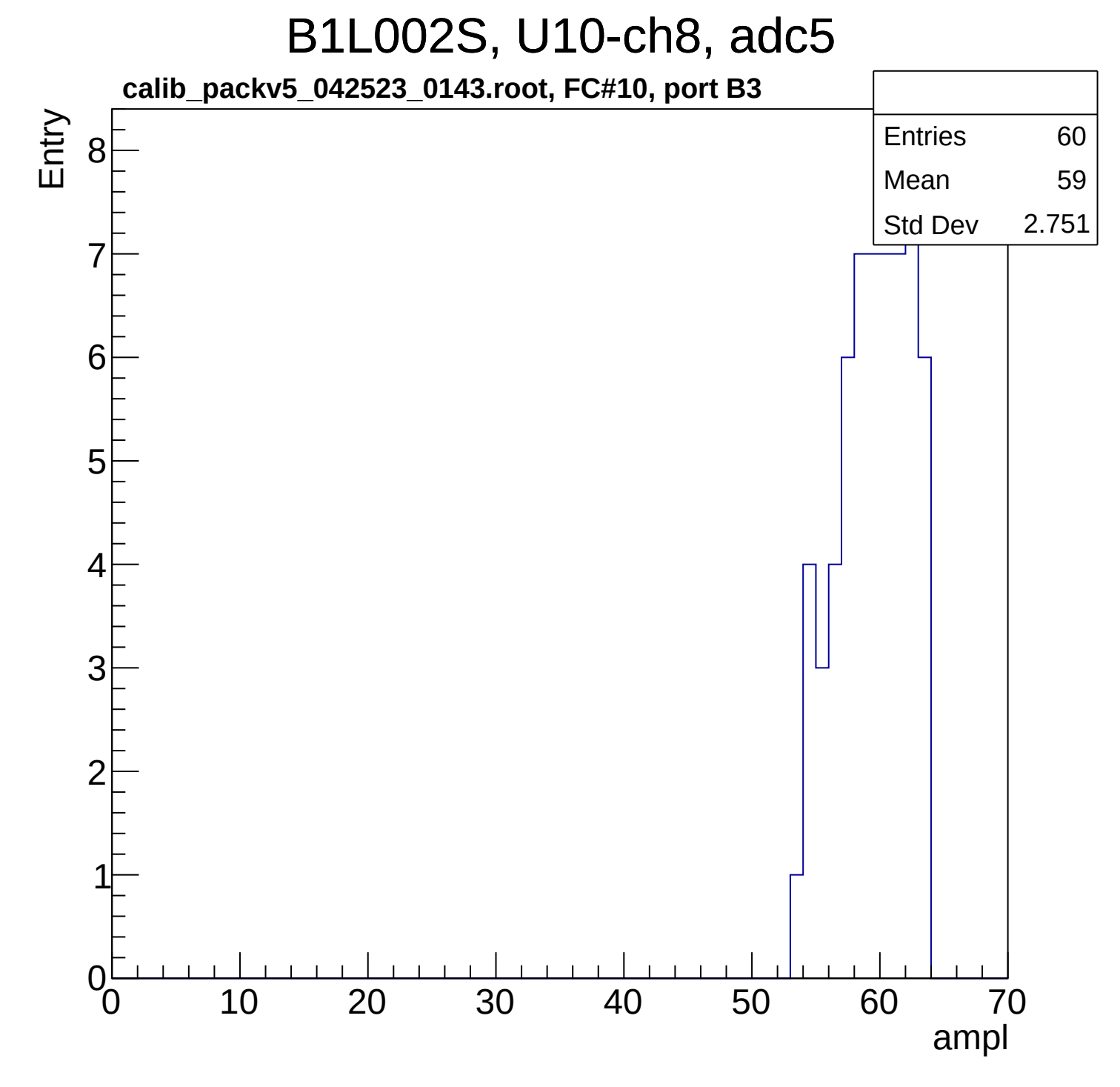
B1L002S, U10-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 59
Std Dev 2.751
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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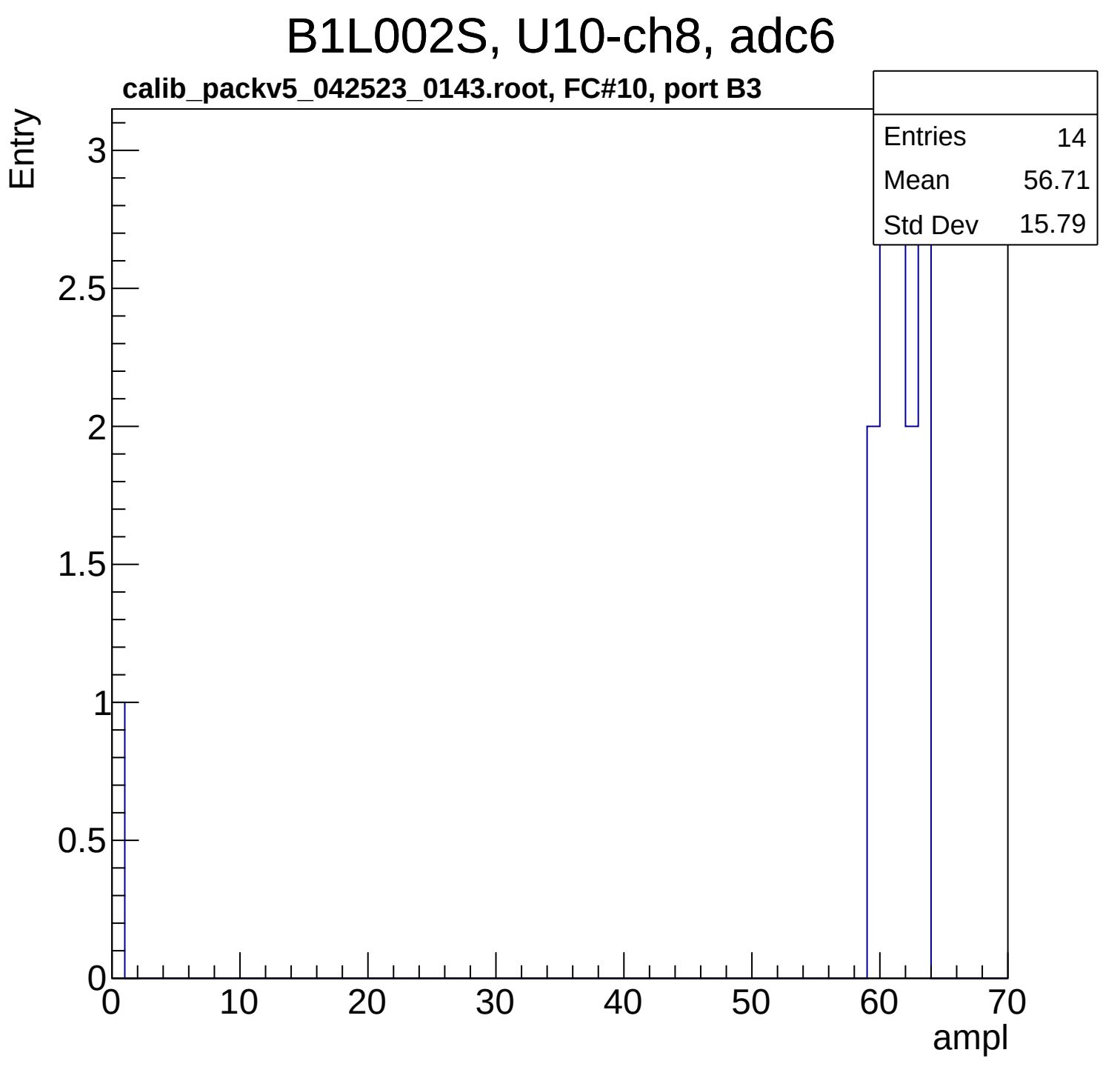
B1L002S, U10-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 56.71
Std Dev 15.79
Entry
ampl
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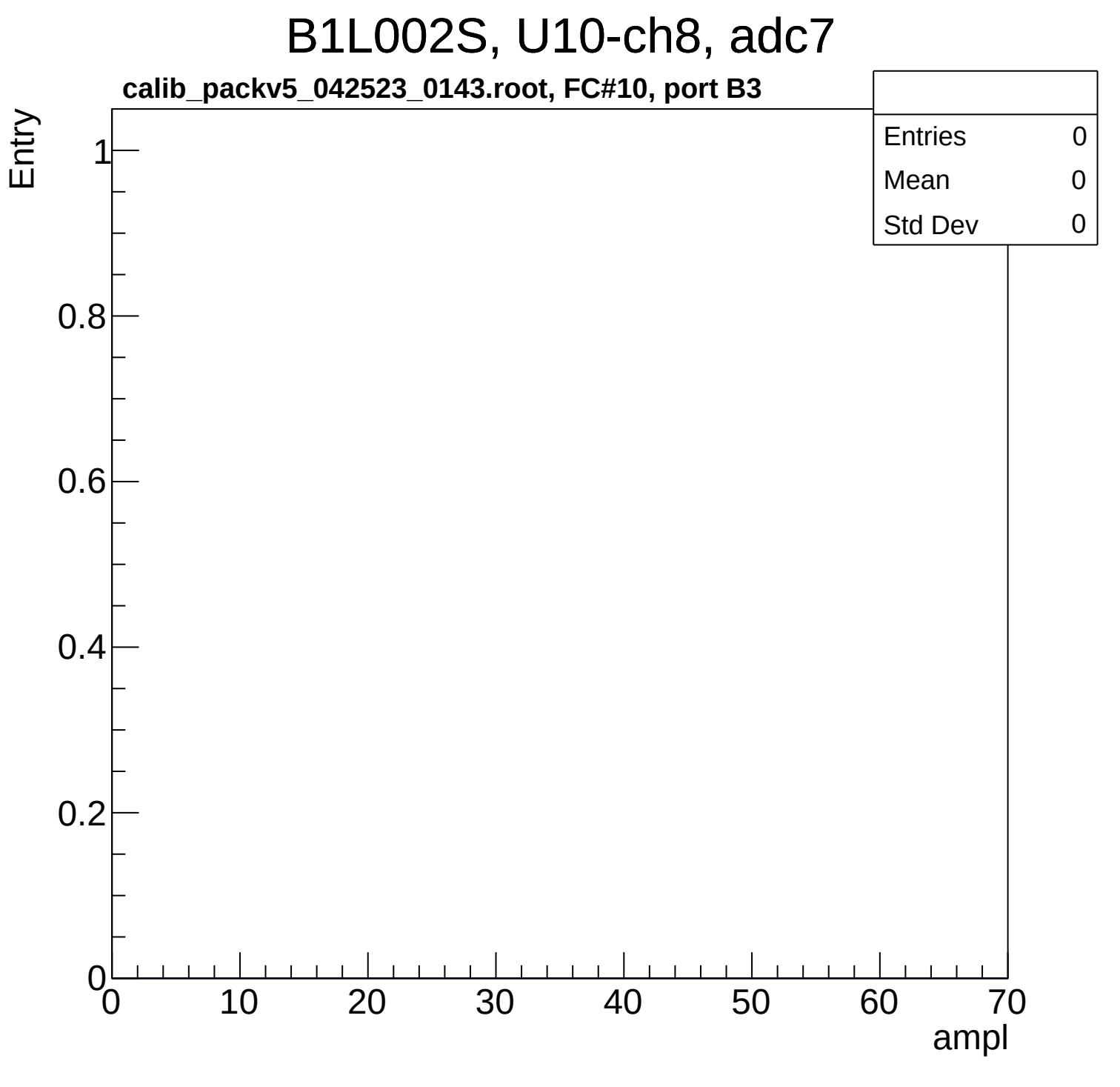
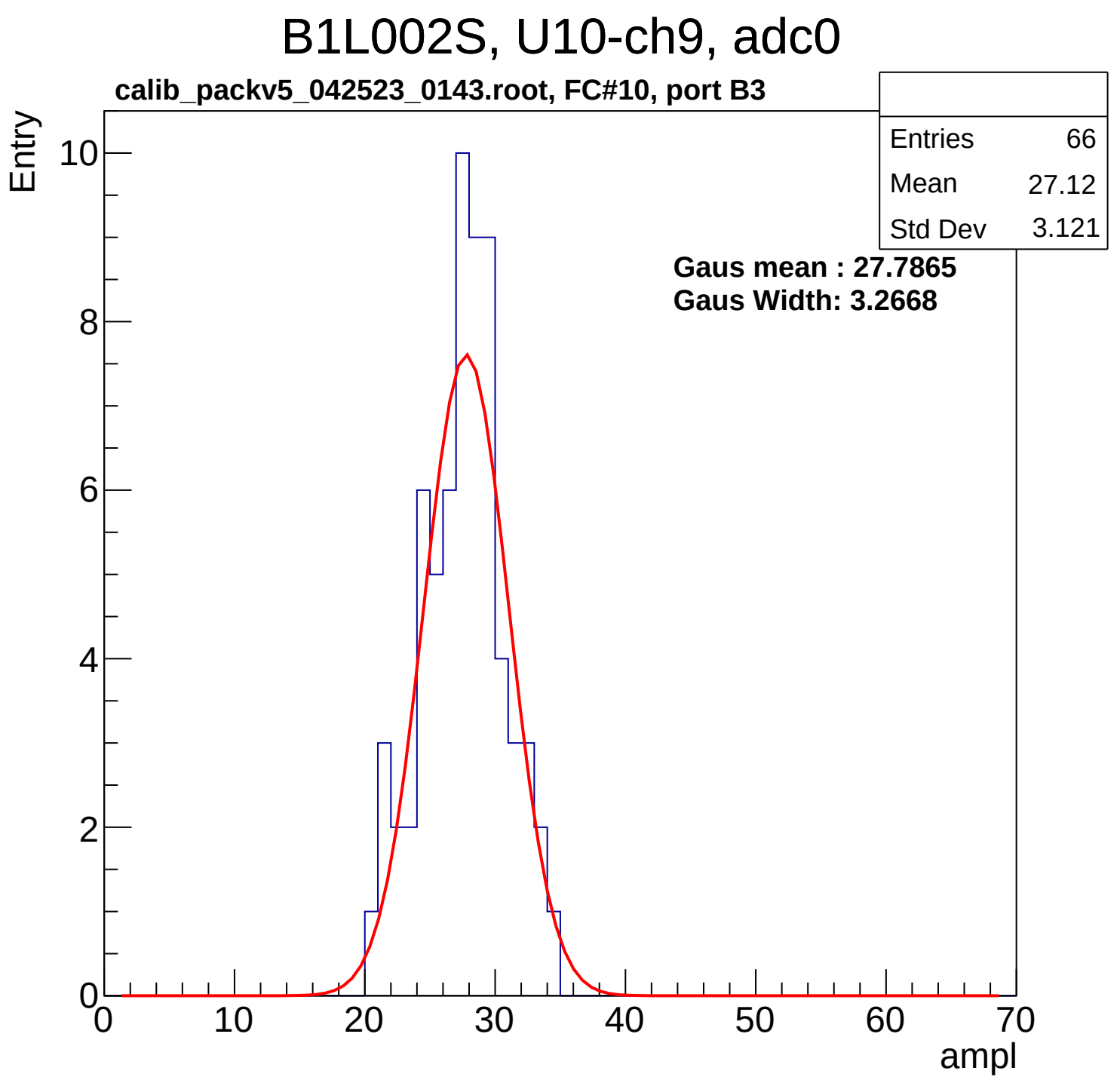
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.12
Std Dev 3.121
Gaus mean : 27.7865
Gaus Width: 3.2668
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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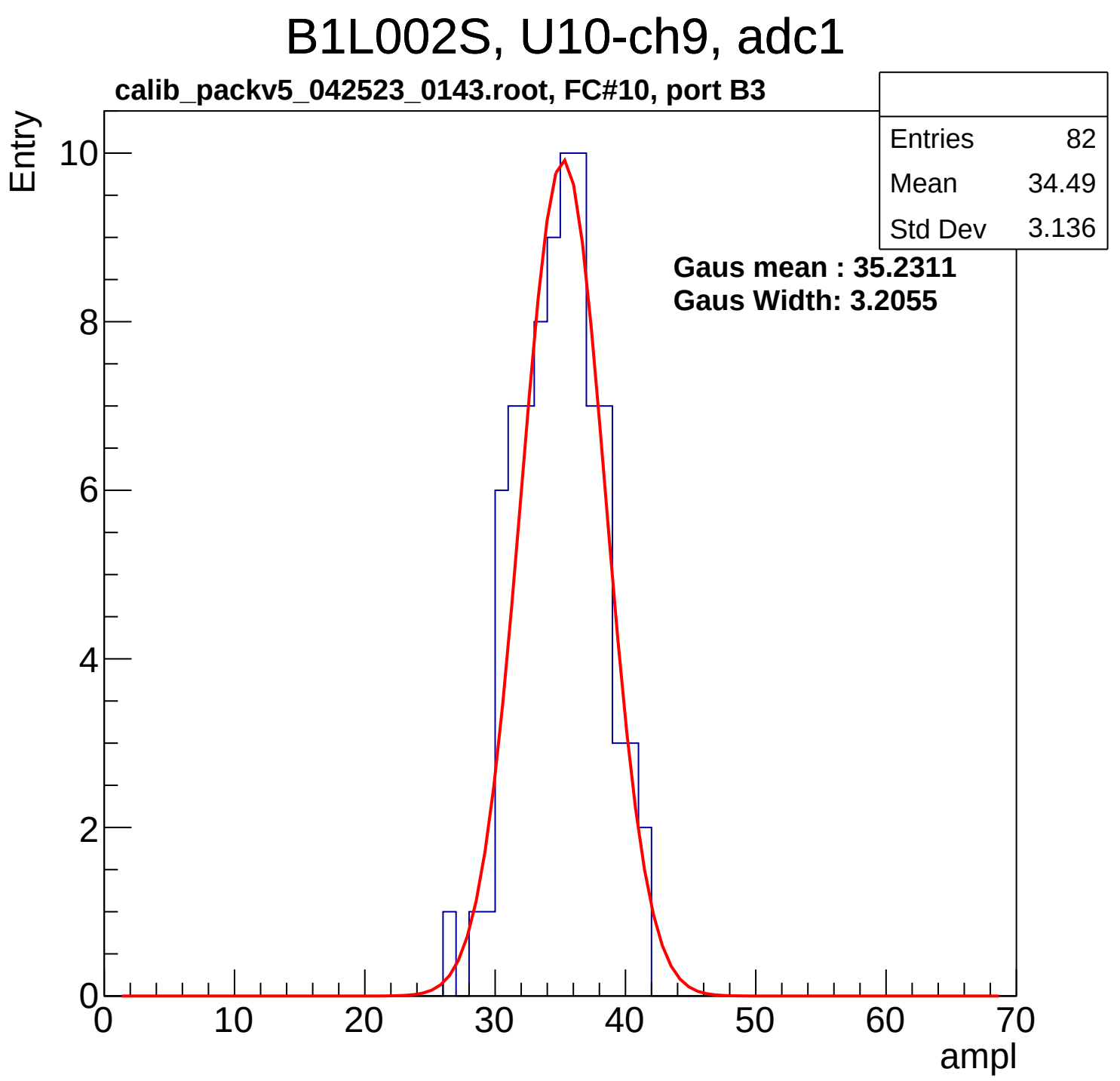
B1L002S, U10-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 34.49
Std Dev 3.136
Gaus mean : 35.2311
Gaus Width: 3.2055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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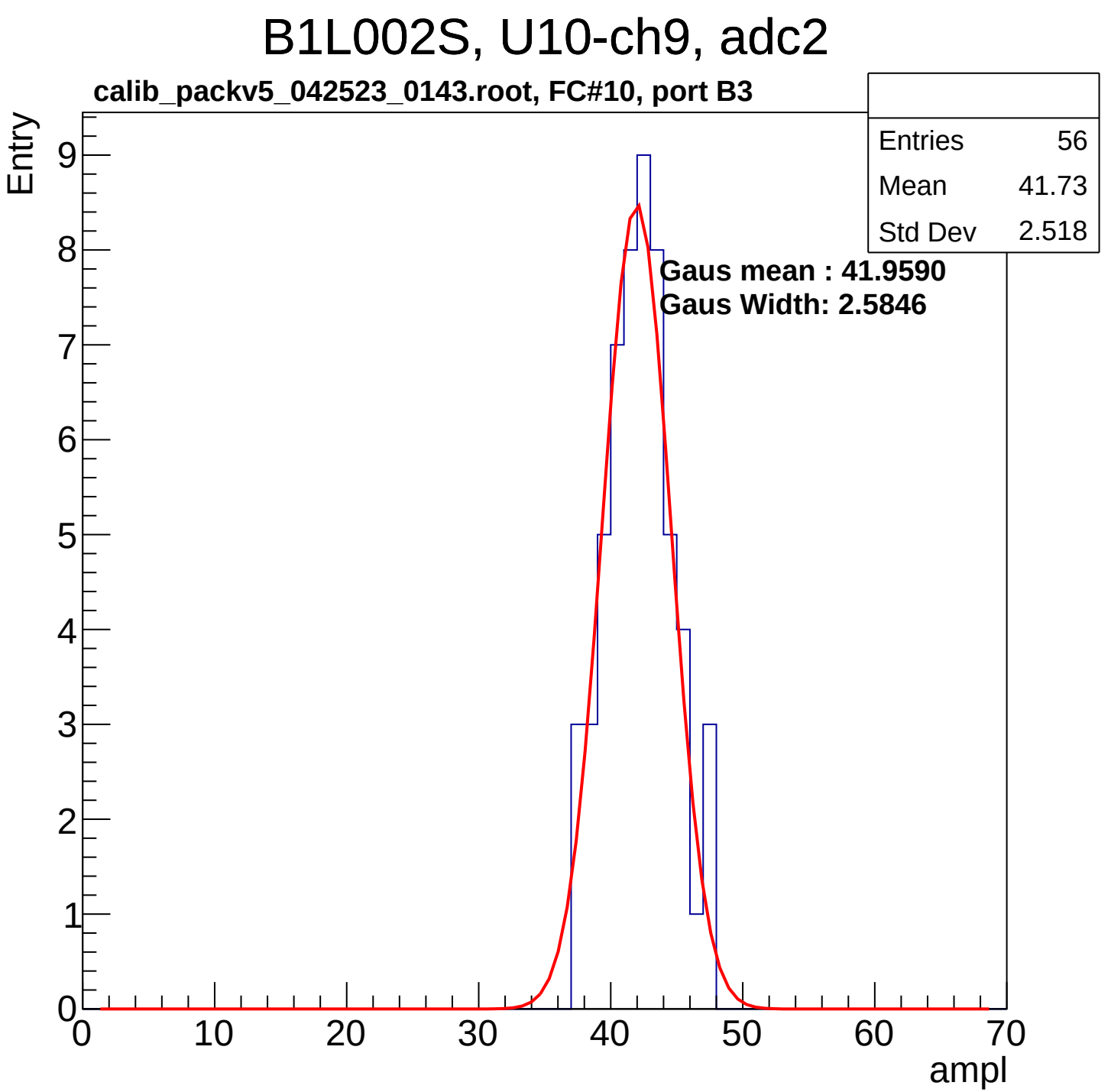
B1L002S, U10-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.73
Std Dev 2.518
Gaus mean : 41.9590
Gaus Width: 2.5846
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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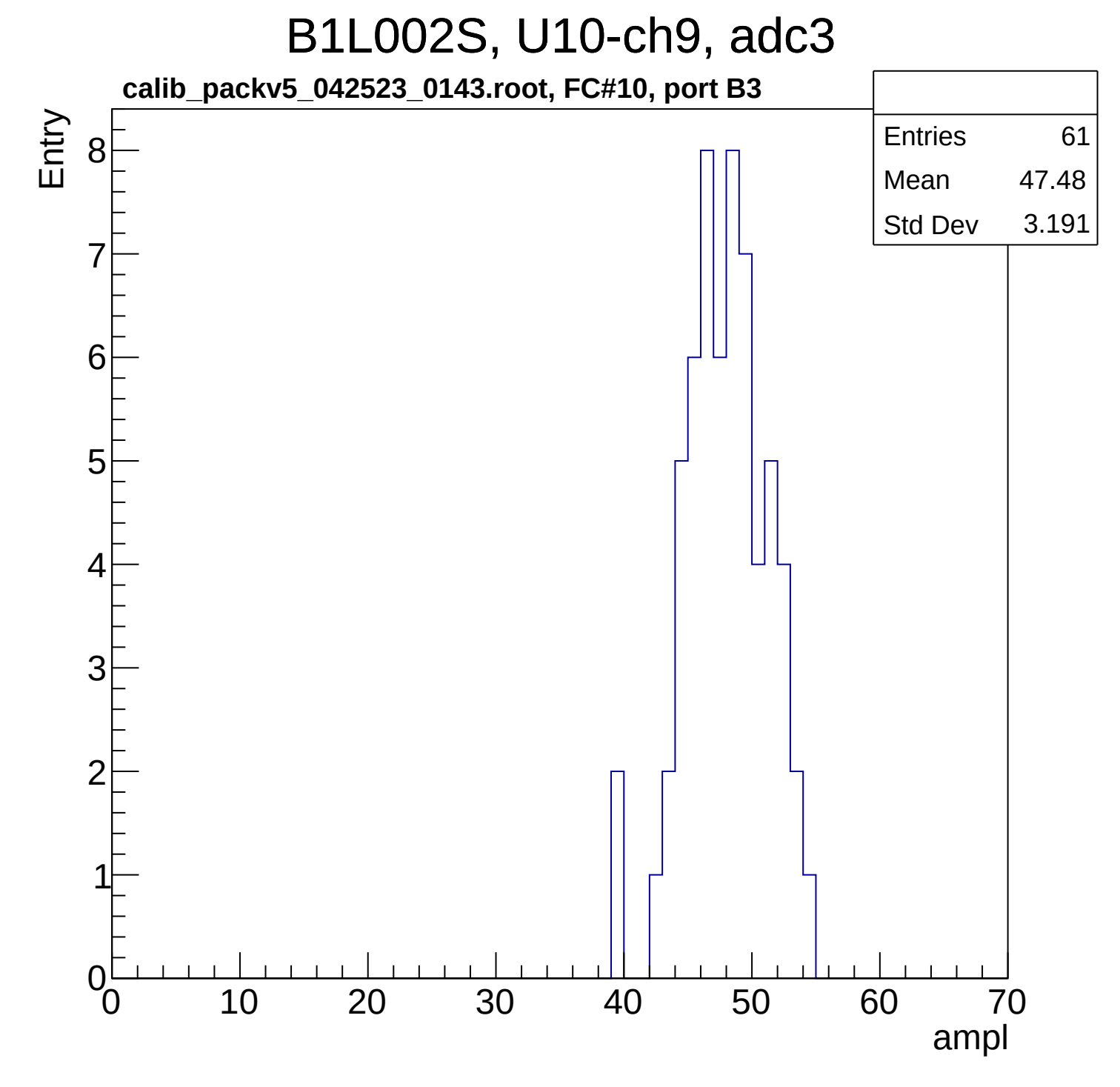
B1L002S, U10-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.48
Std Dev 3.191
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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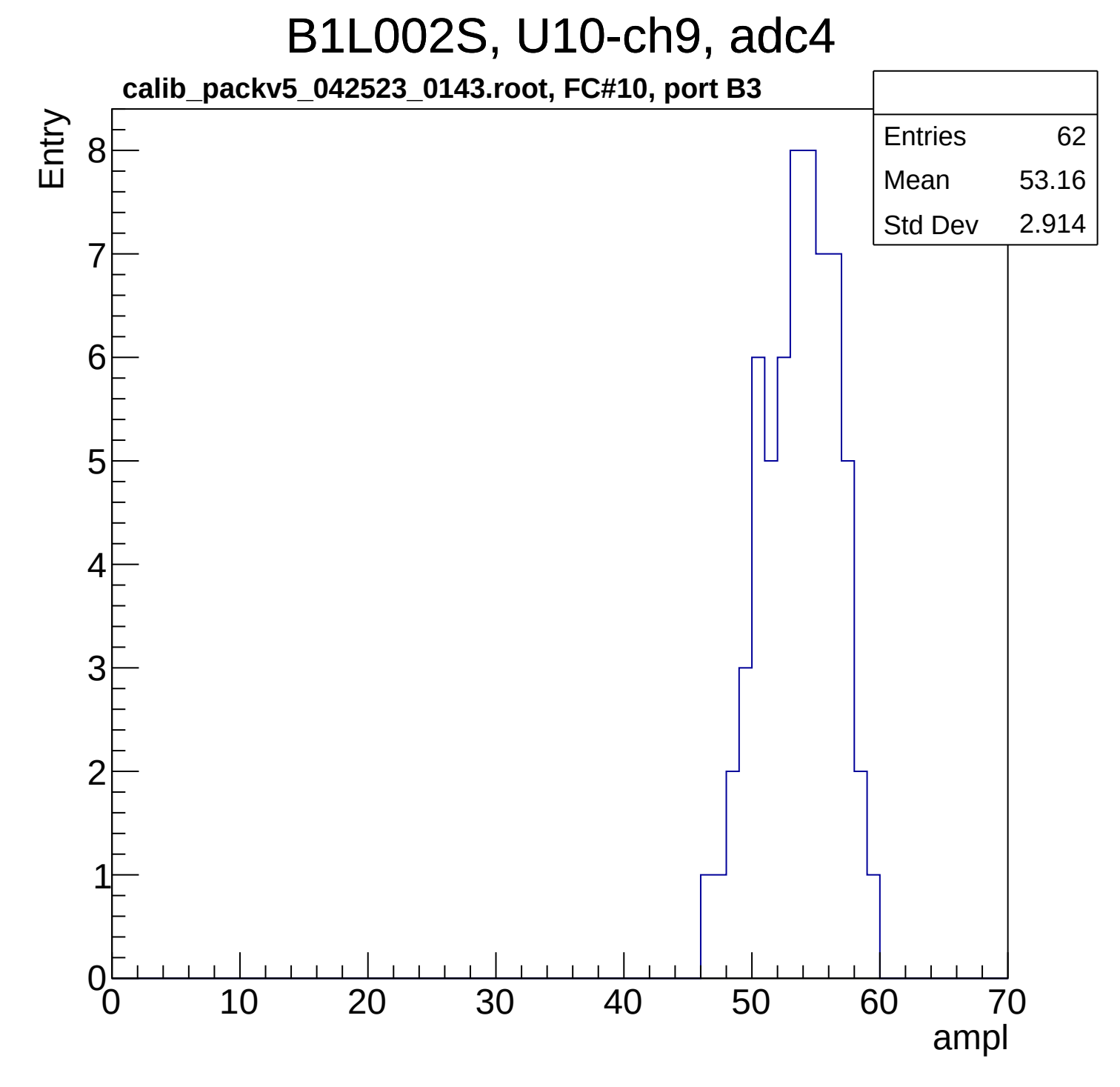
B1L002S, U10-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.16
Std Dev 2.914
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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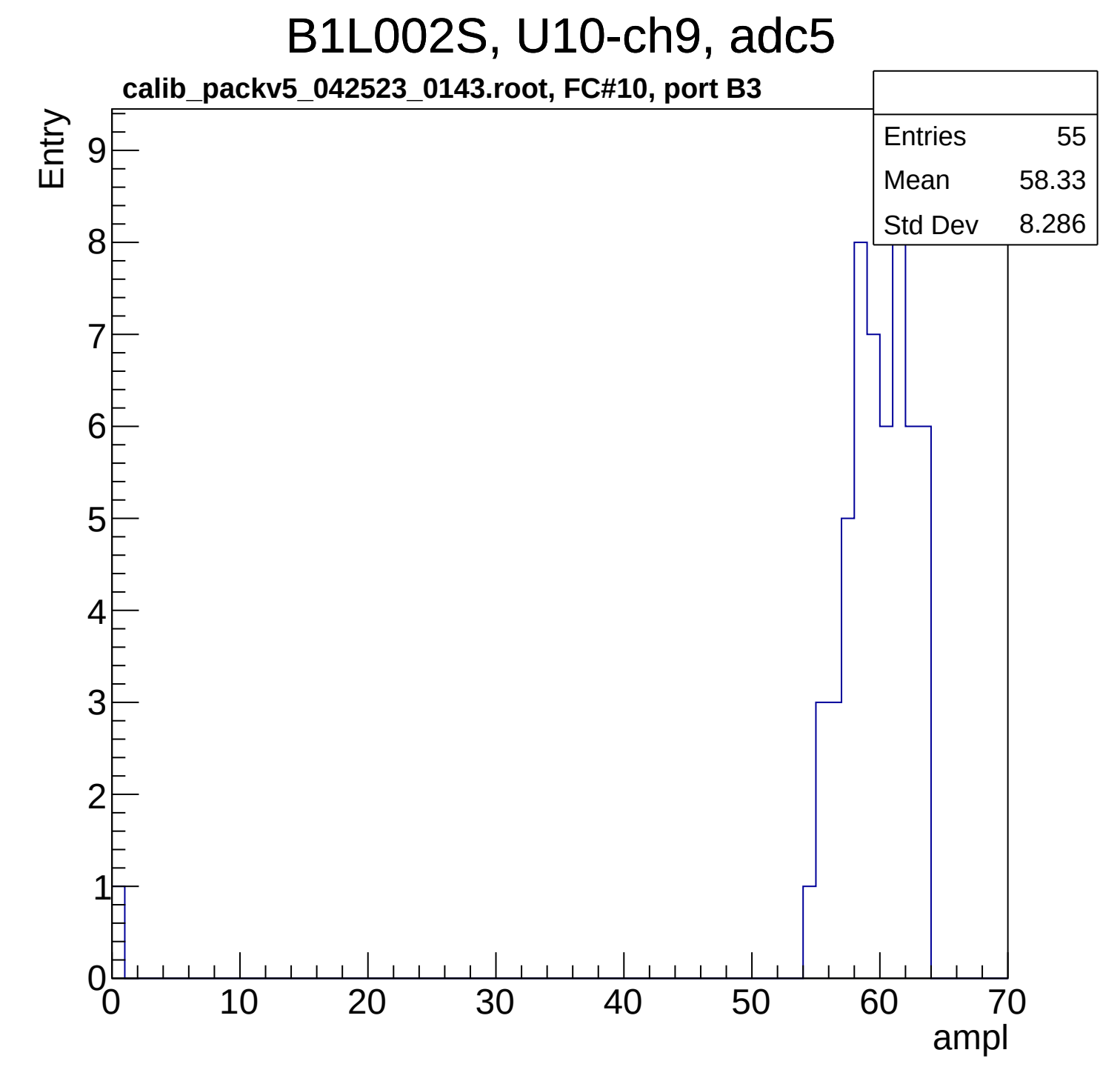
B1L002S, U10-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.33
Std Dev 8.286
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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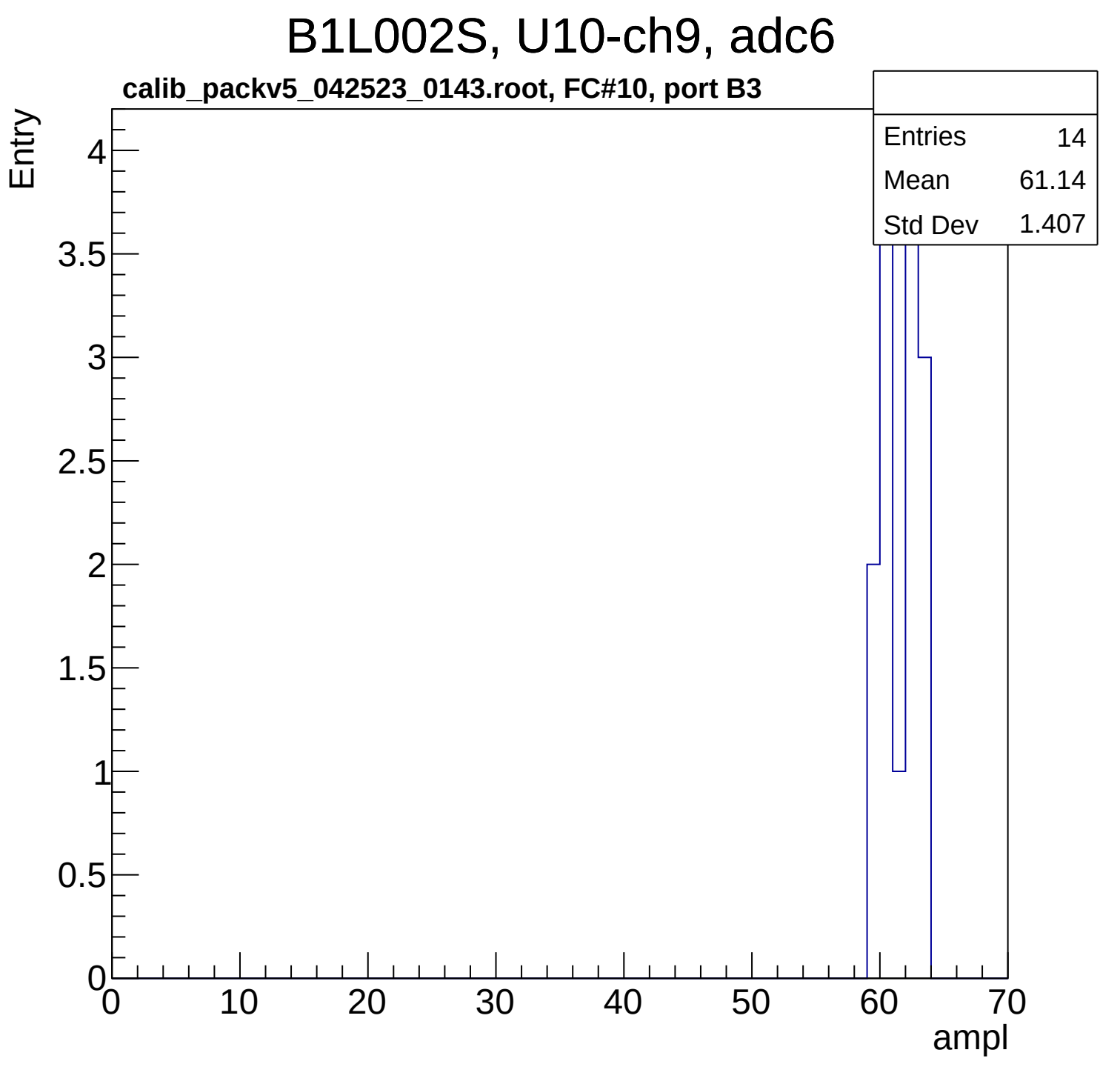
B1L002S, U10-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.14
Std Dev 1.407
Entry
ampl
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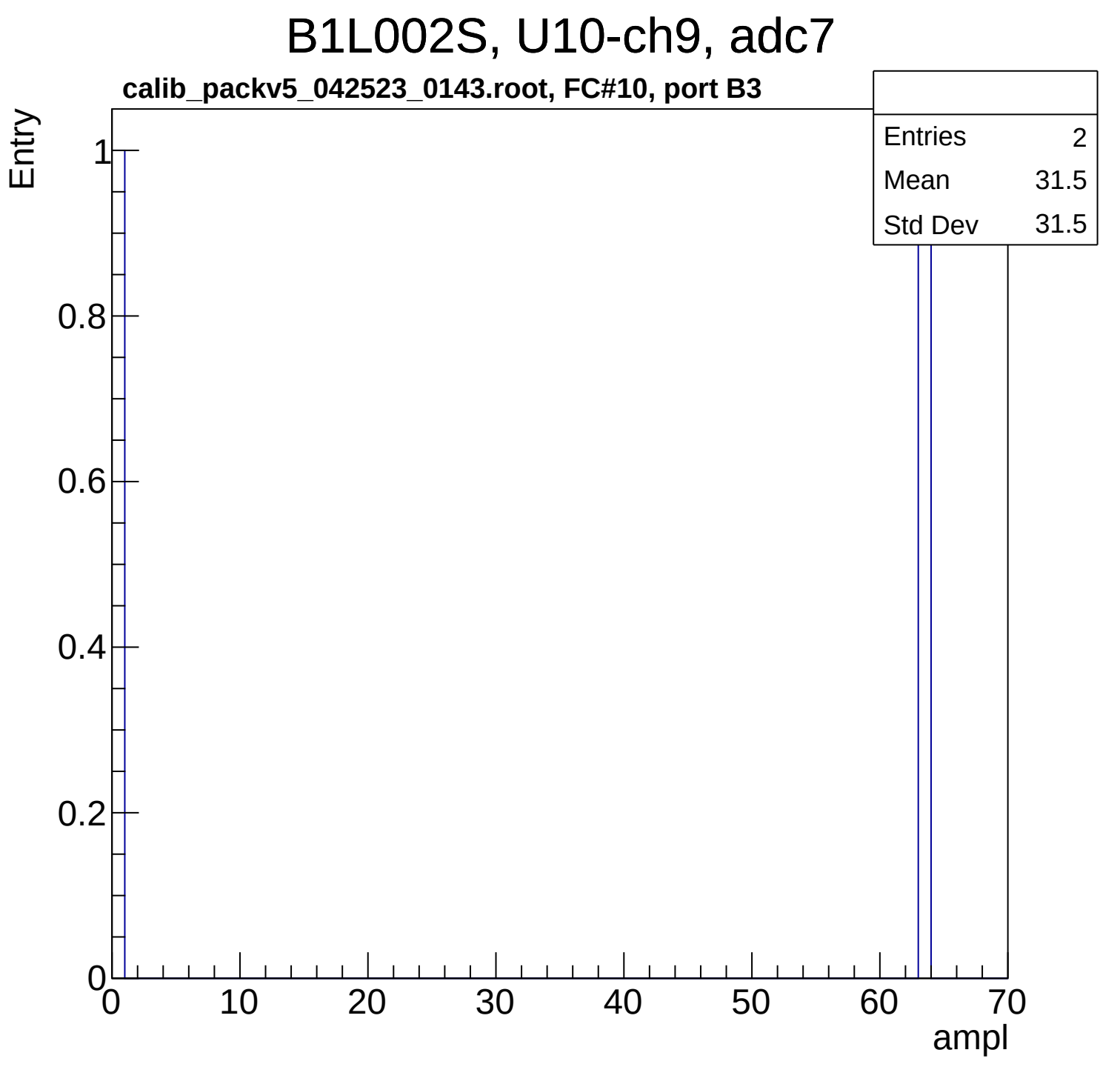
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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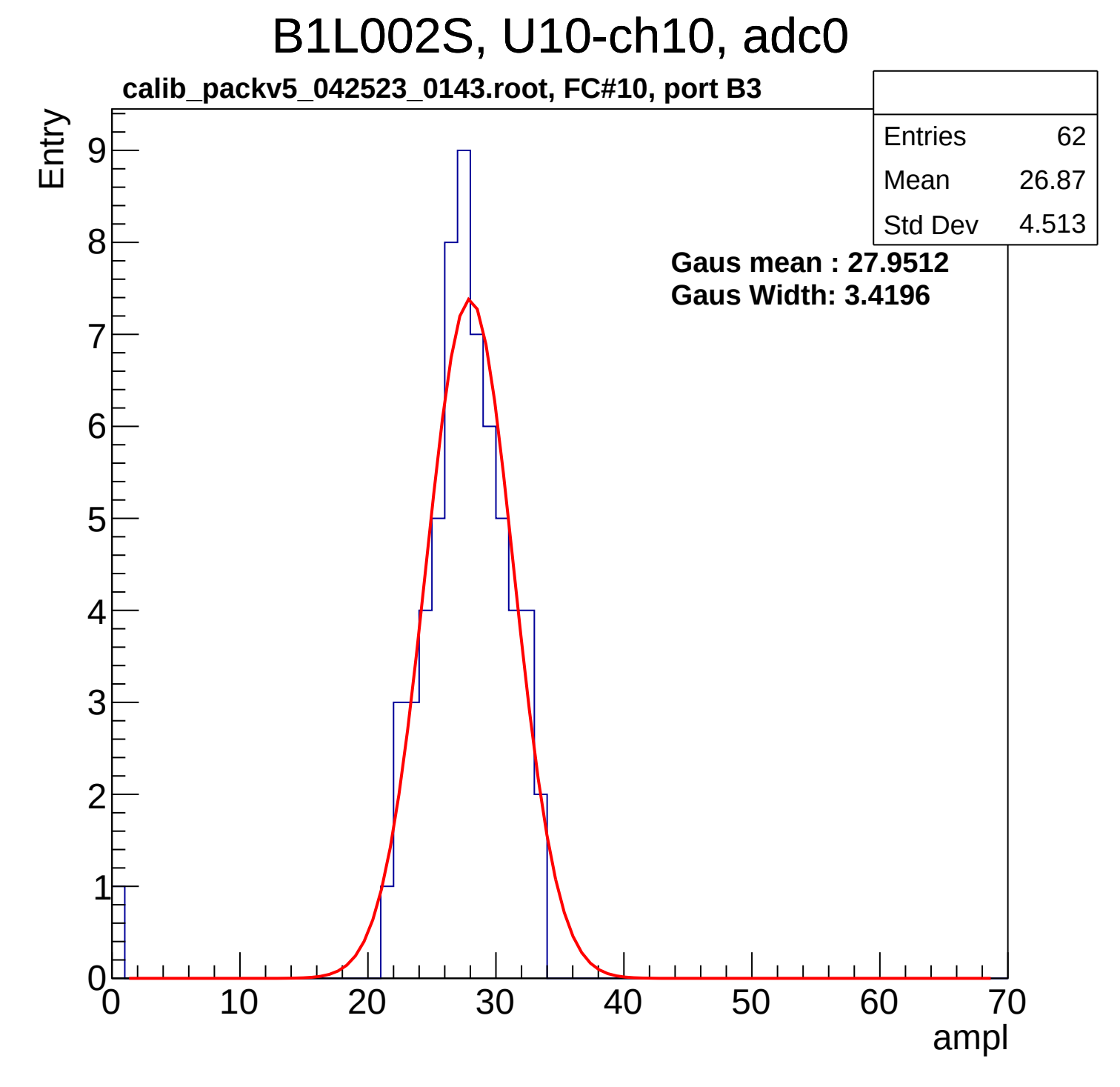
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.87
Std Dev 4.513
Gaus mean : 27.9512
Gaus Width: 3.4196
Entry
ampl
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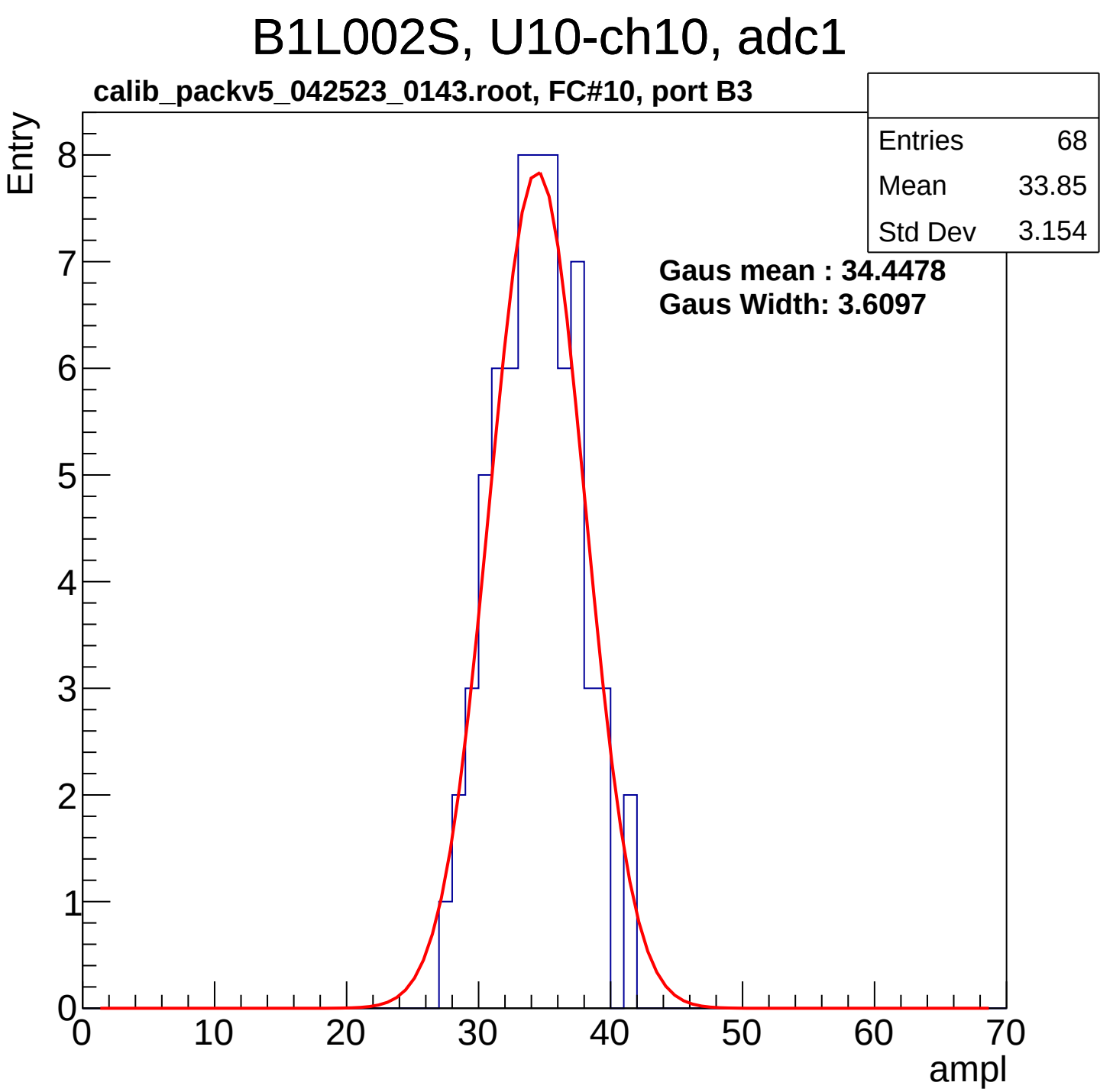
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.85
Std Dev 3.154
Gaus mean : 34.4478
Gaus Width: 3.6097
Entry
ampl
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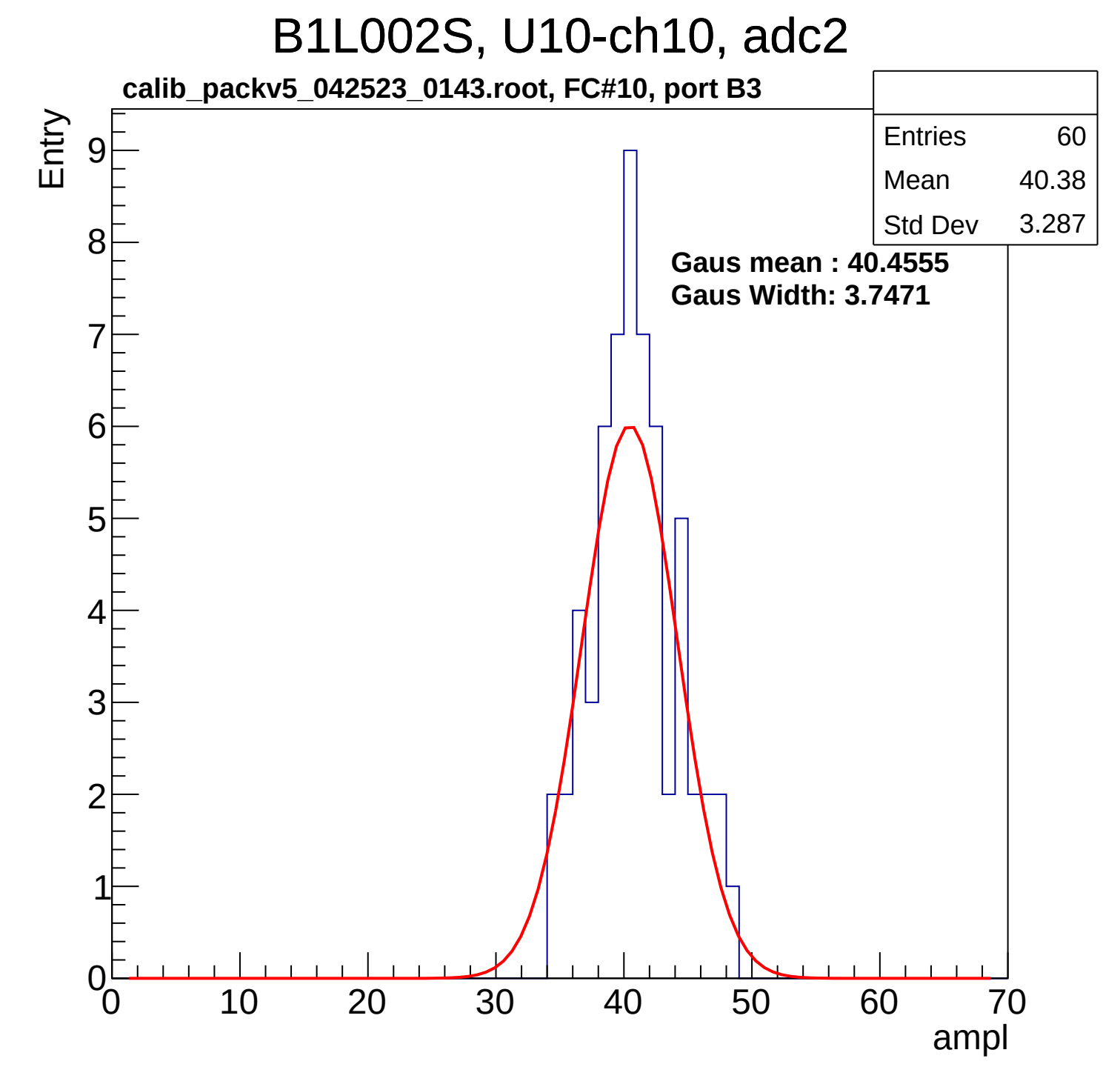
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.38
Std Dev 3.287
Gaus mean : 40.4555
Gaus Width: 3.7471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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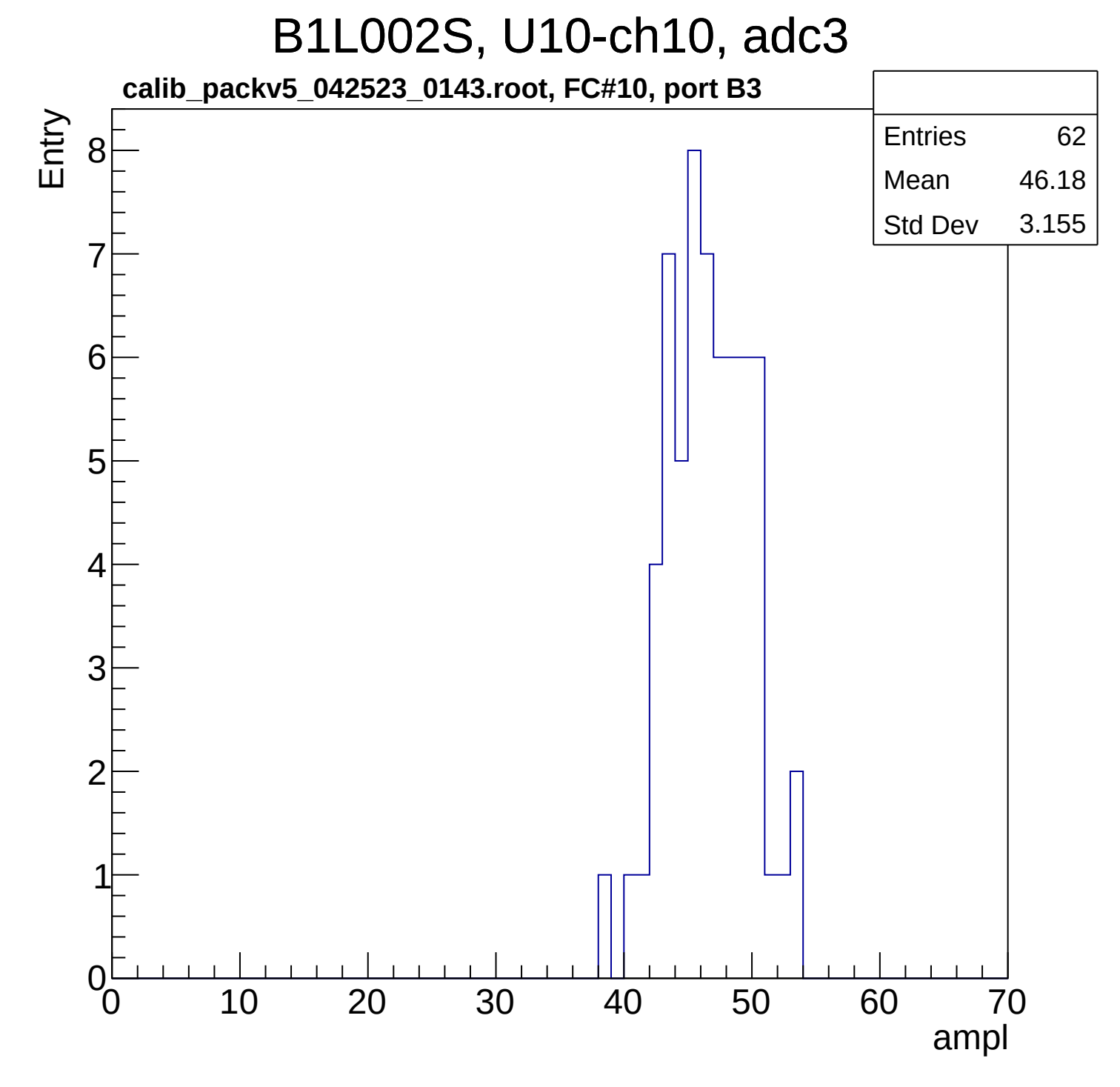
B1L002S, U10-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.18
Std Dev 3.155
Entry
ampl
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2
3
4
5
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0
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60
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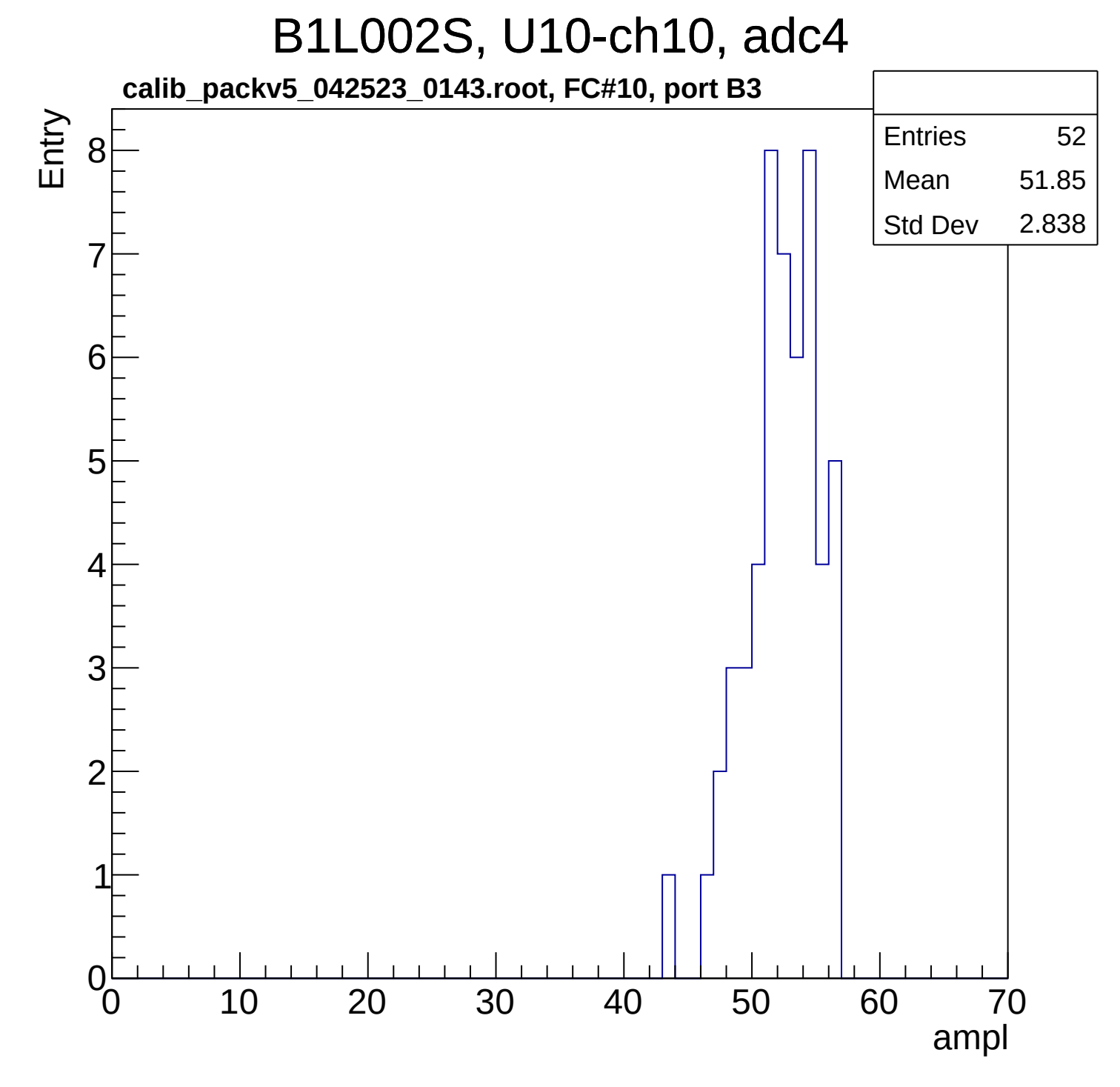
B1L002S, U10-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.85
Std Dev 2.838
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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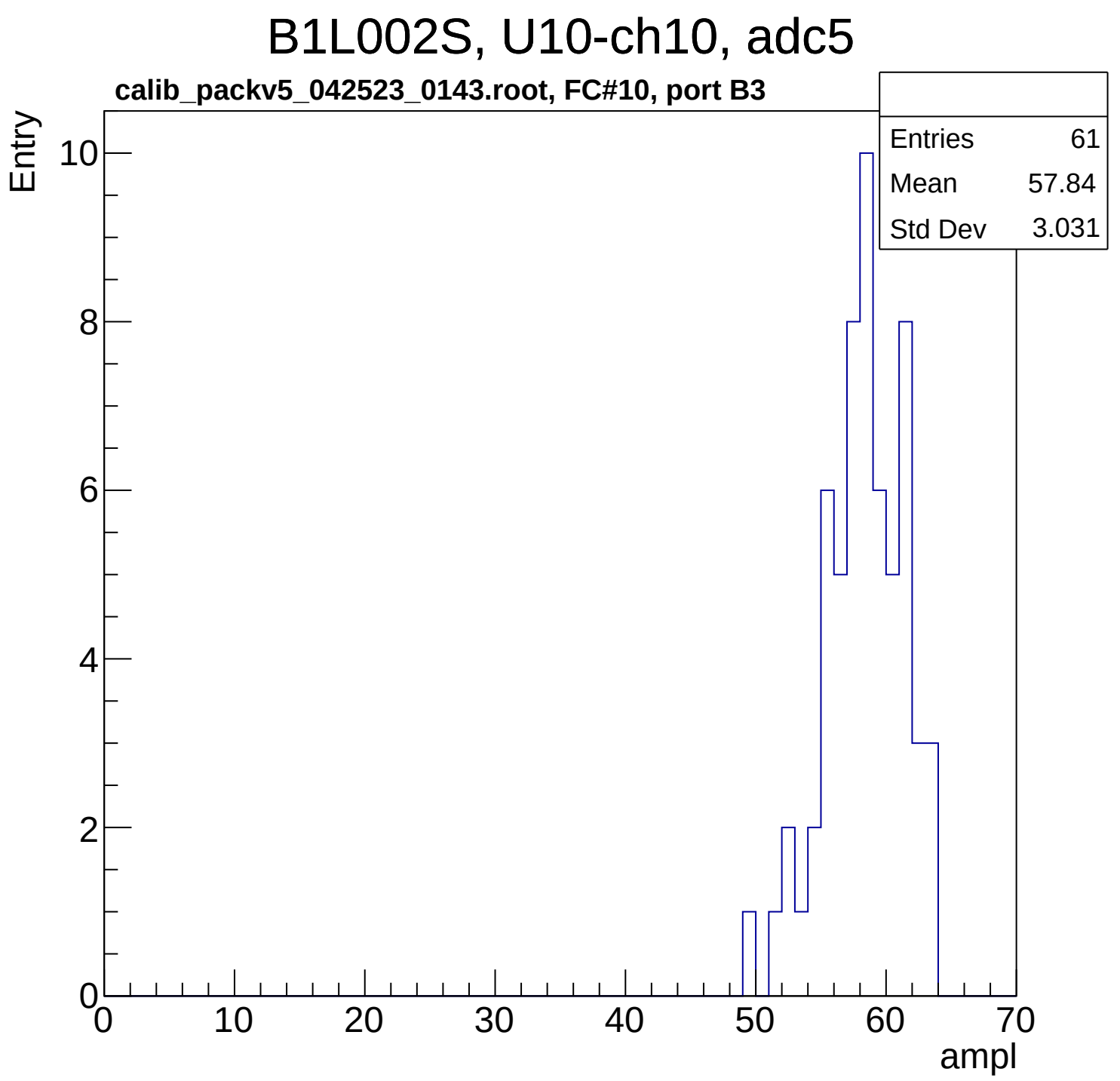
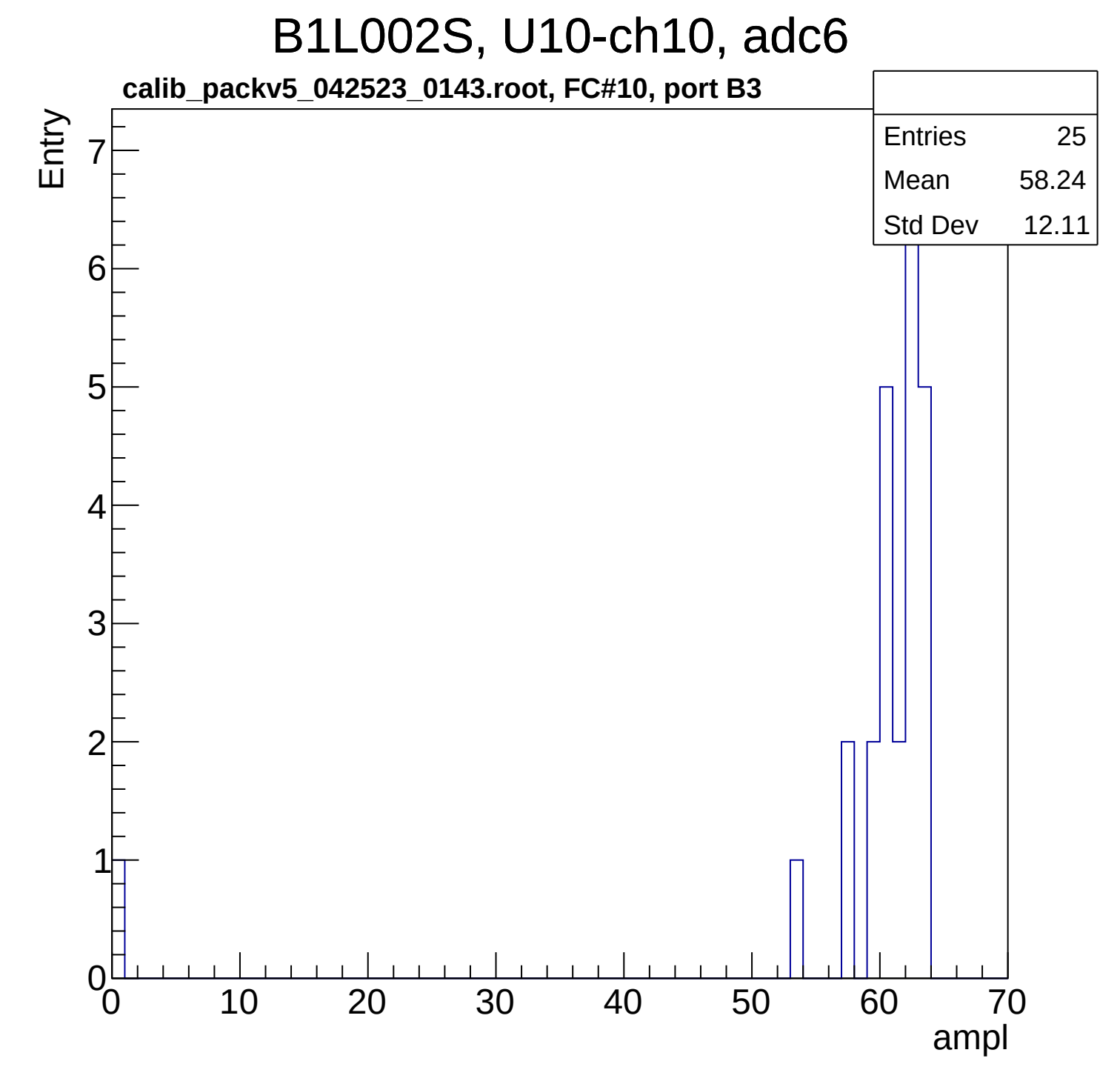

B1L002S, U10-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.84
Std Dev 3.031
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.24
Std Dev 12.11
Entry
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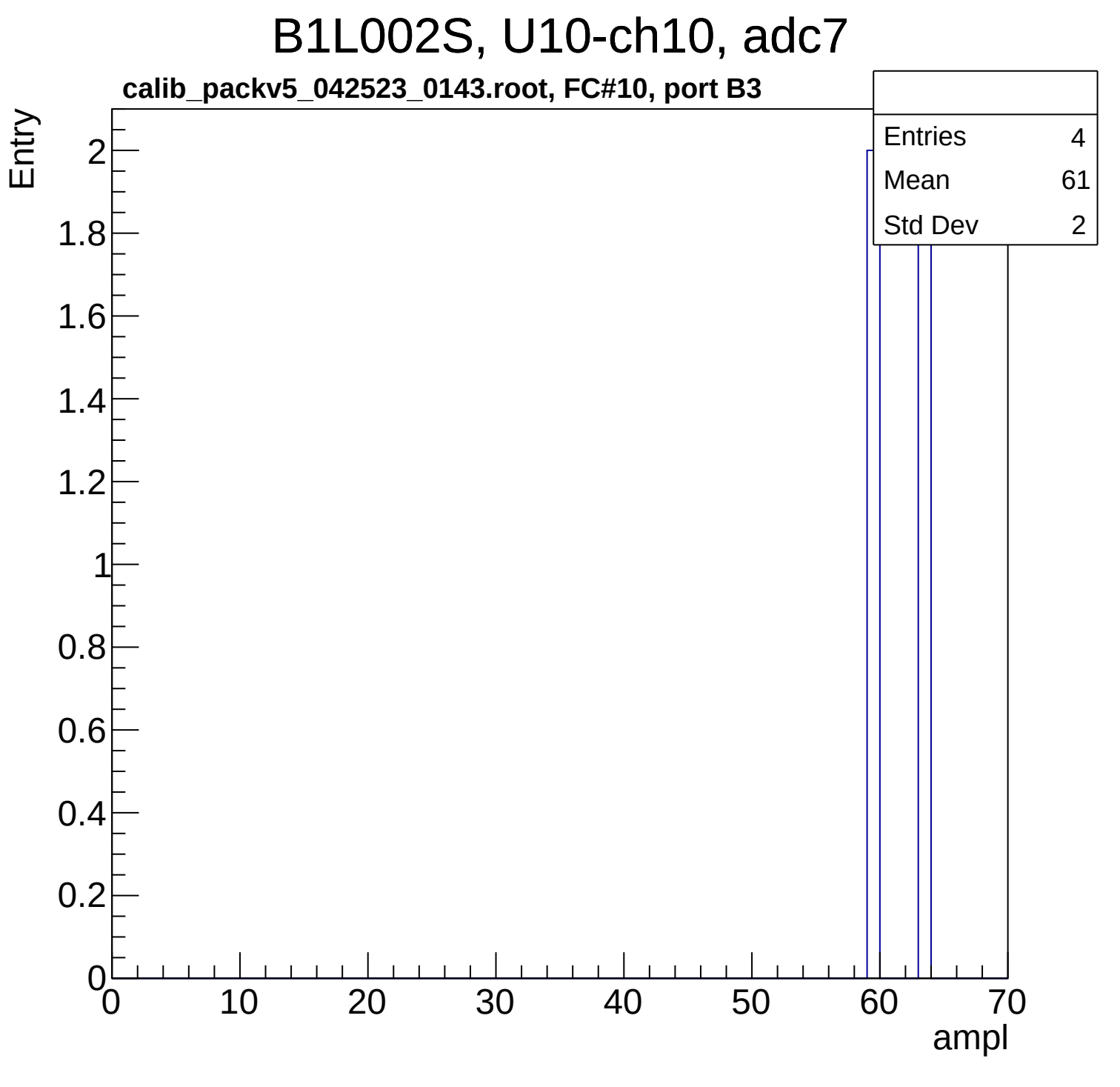
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61
Std Dev 2
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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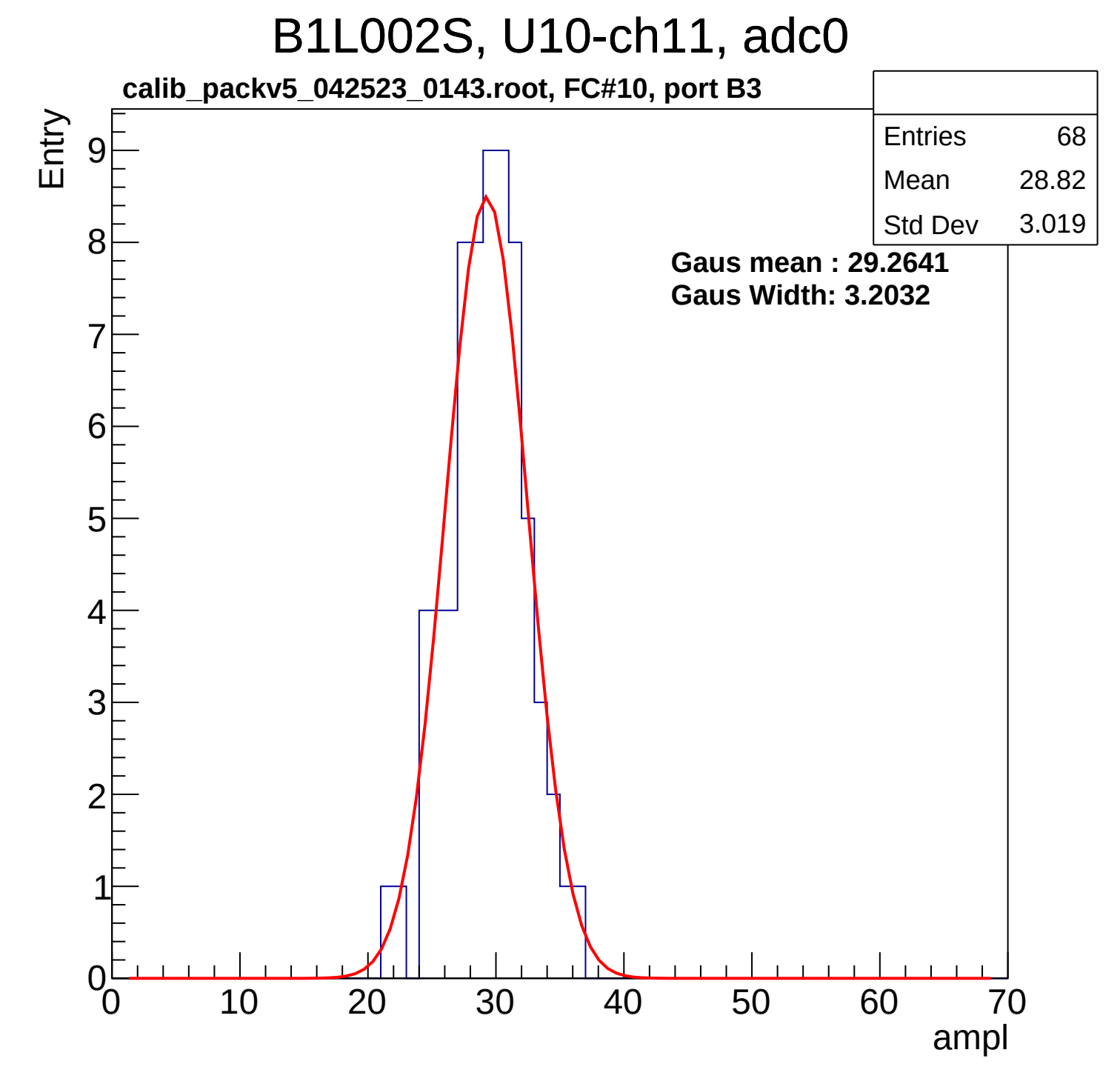
B1L002S, U10-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.82
Std Dev 3.019
Gaus mean : 29.2641
Gaus Width: 3.2032
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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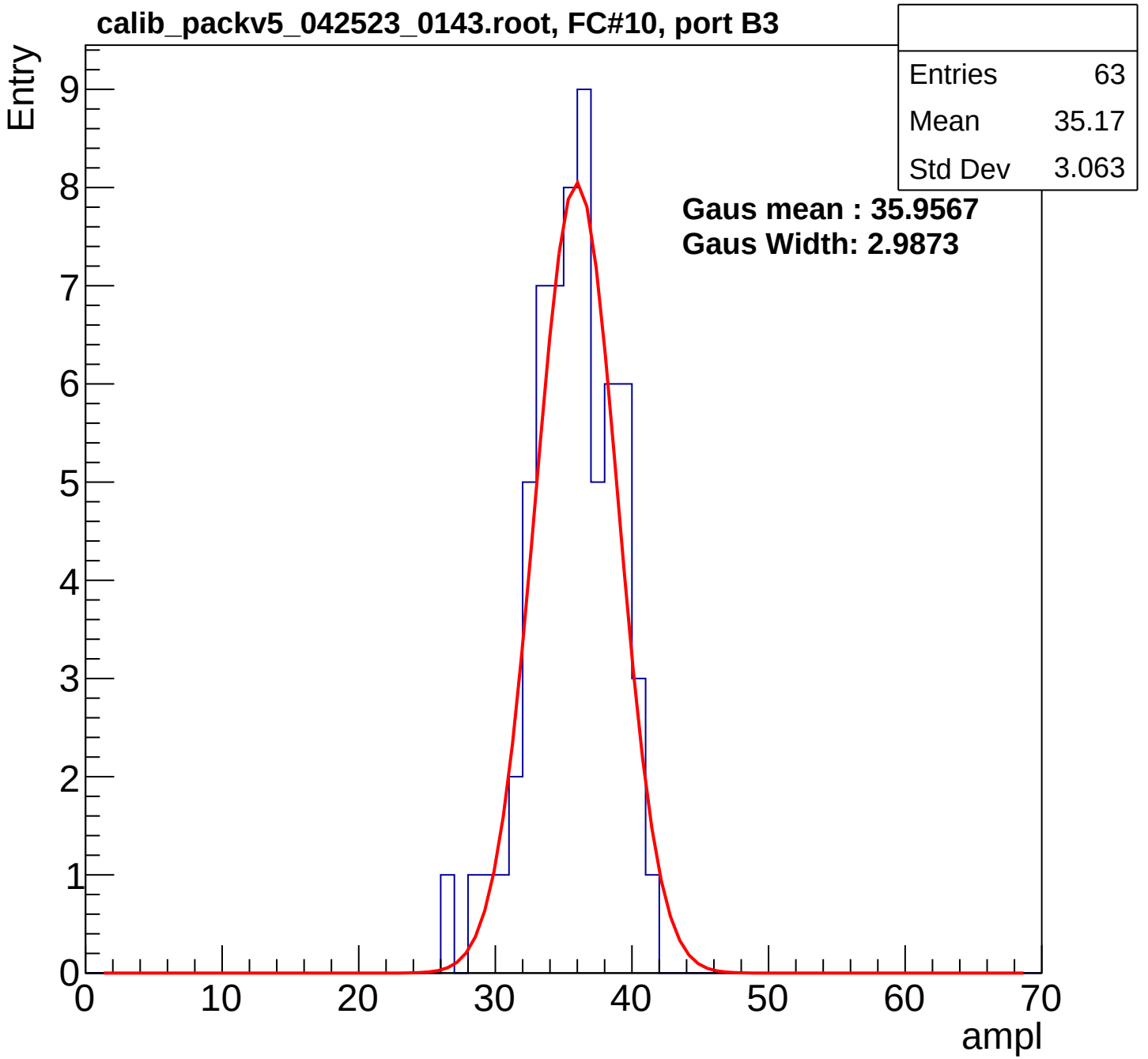
B1L002S, U10-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.17
Std Dev 3.063
Gaus mean : 35.9567
Gaus Width: 2.9873
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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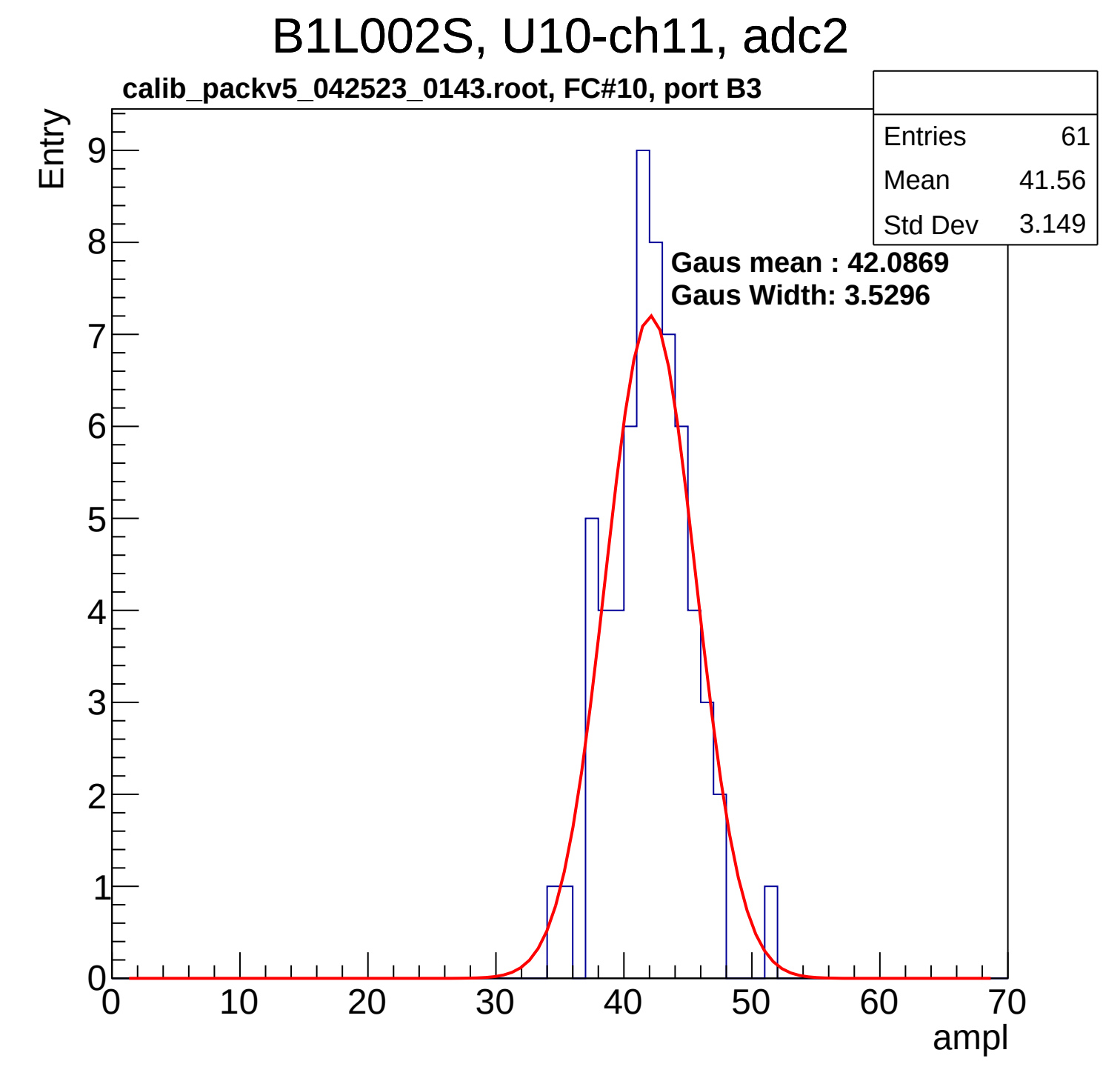
B1L002S, U10-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.56
Std Dev 3.149
Gaus mean : 42.0869
Gaus Width: 3.5296
Entry
ampl
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3
4
5
6
7
8
9
0
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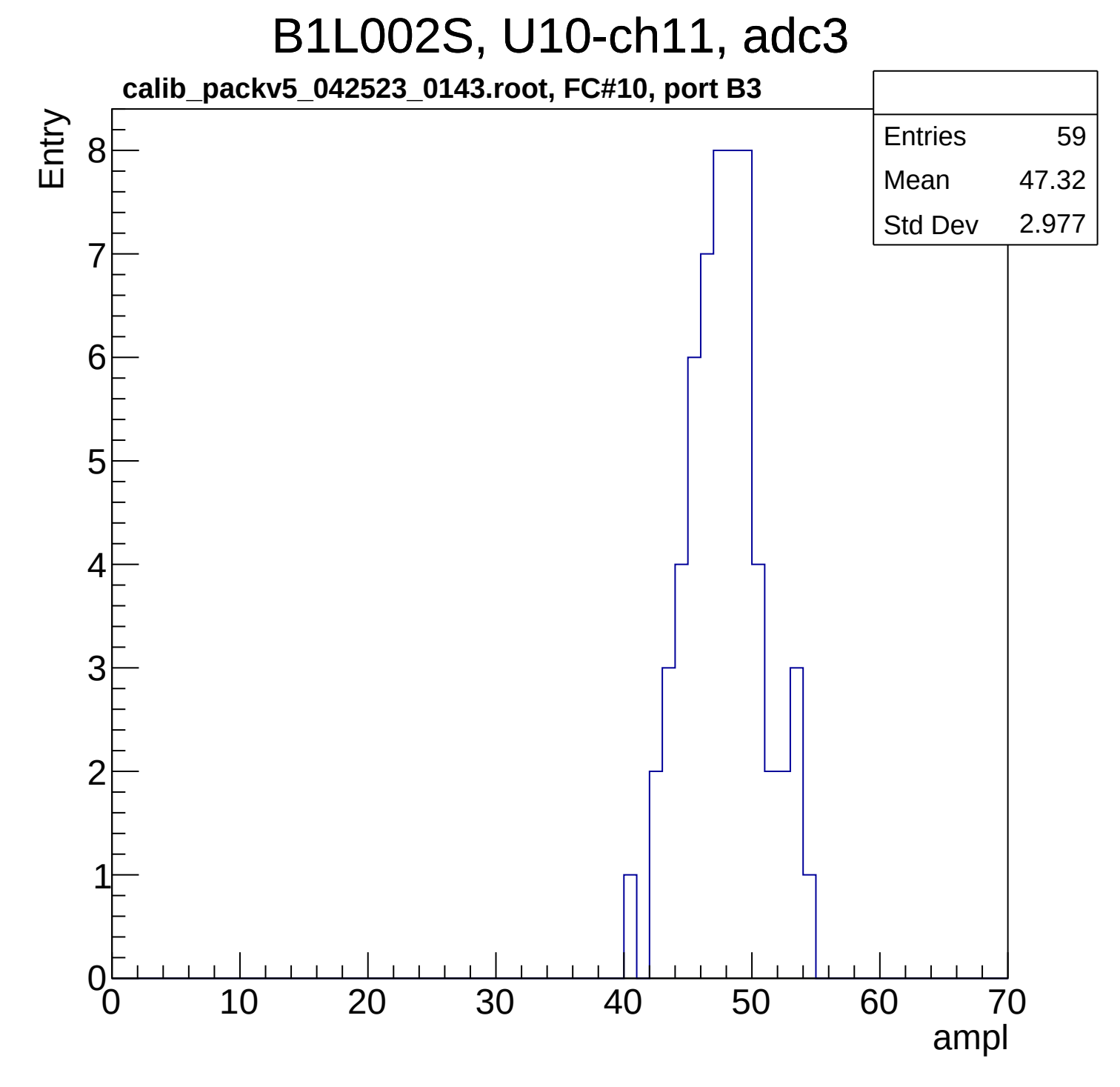
B1L002S, U10-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.32
Std Dev 2.977
Entry
ampl
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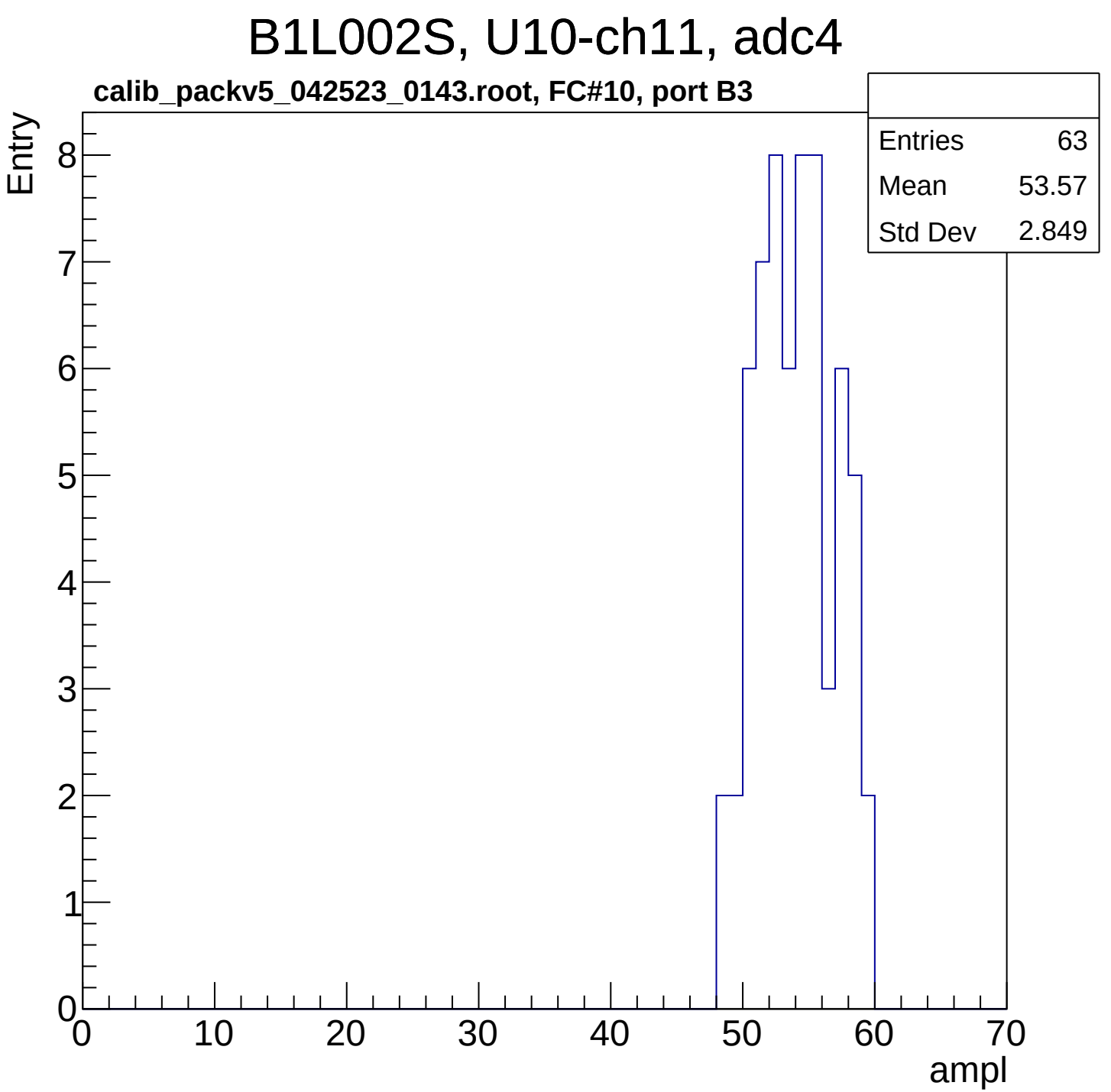
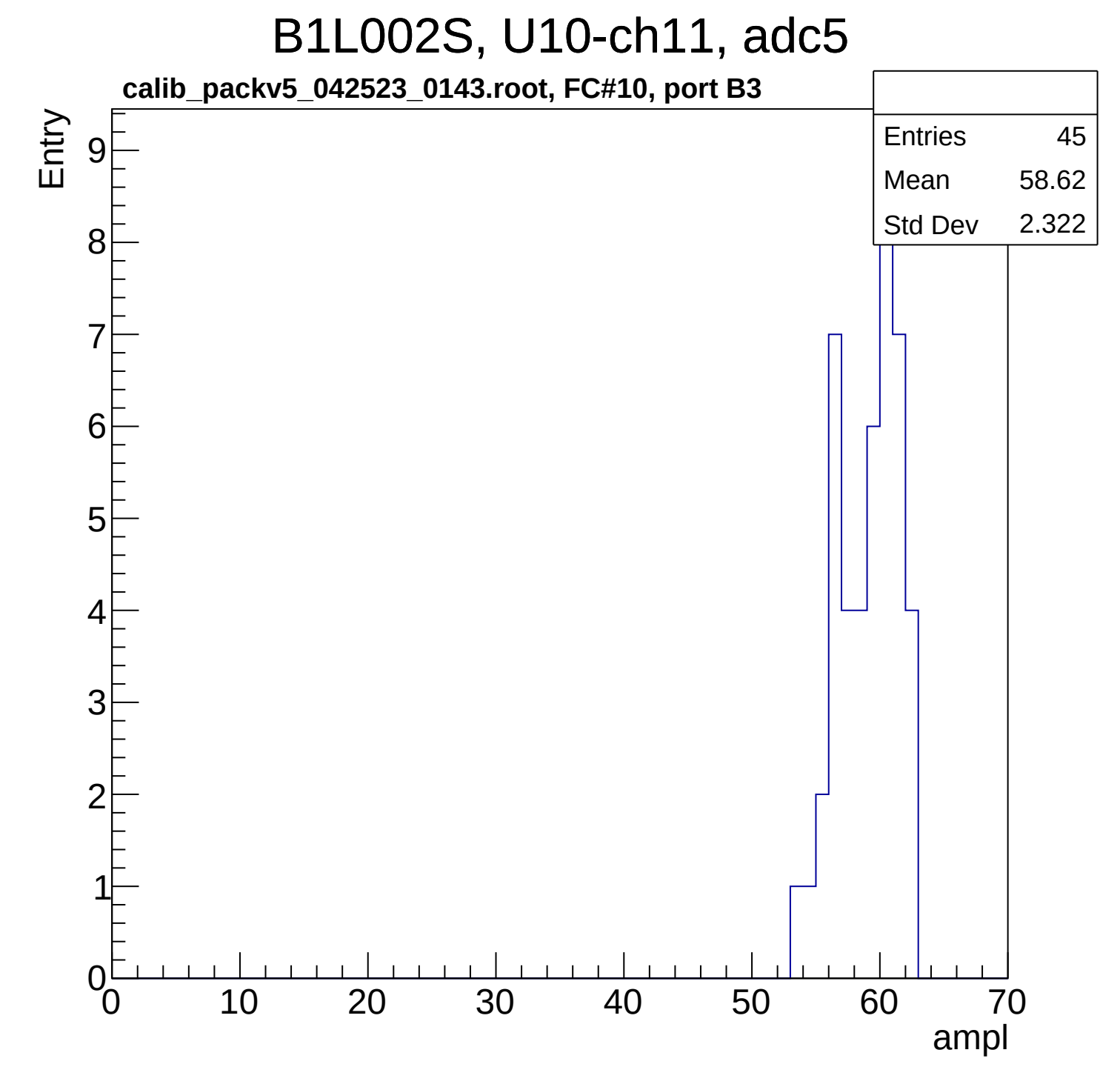

B1L002S, U10-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.57
Std Dev 2.849
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.62
Std Dev 2.322
Entry
ampl
0
1
2
3
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5
6
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0
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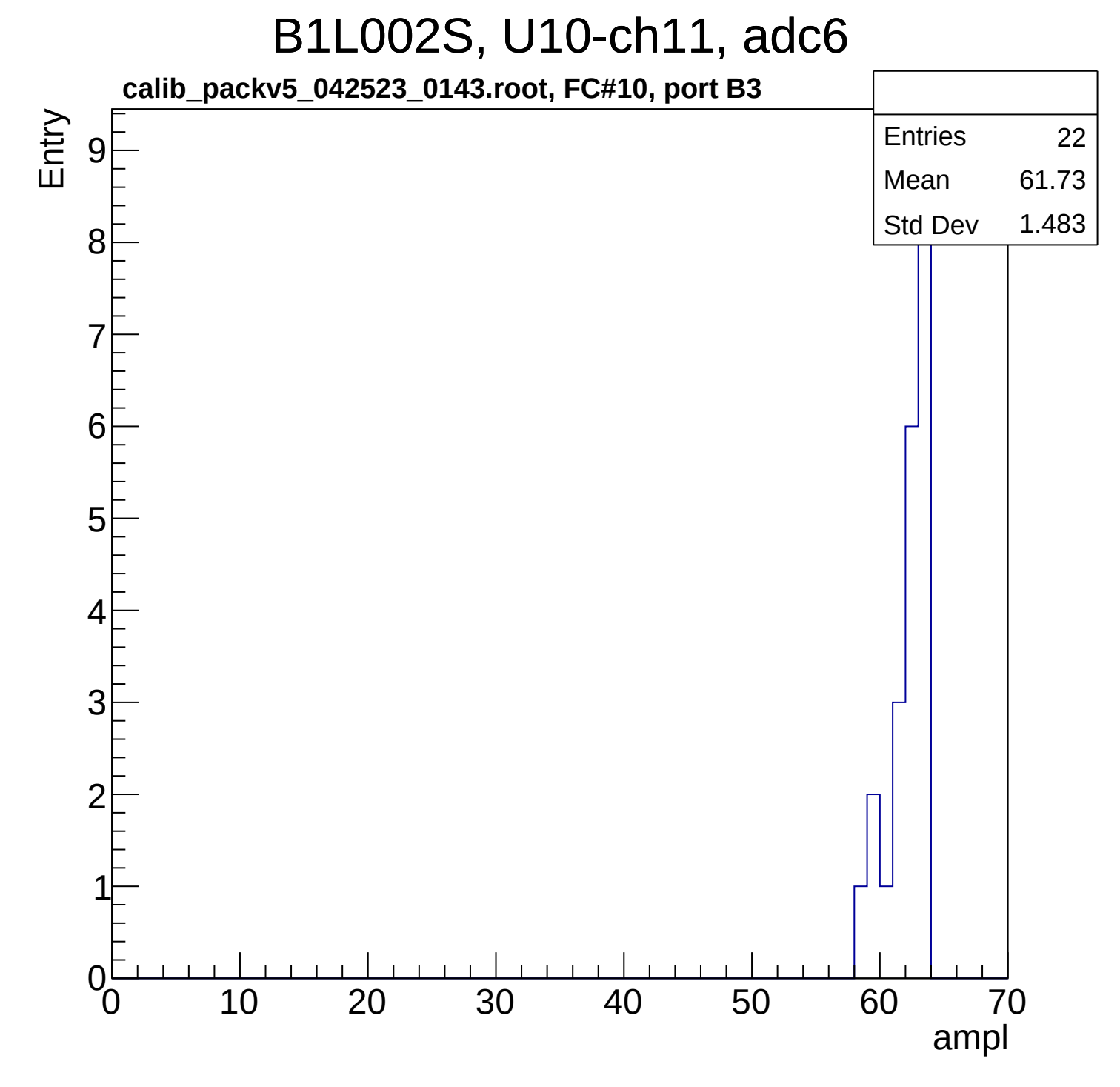
B1L002S, U10-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.73
Std Dev 1.483
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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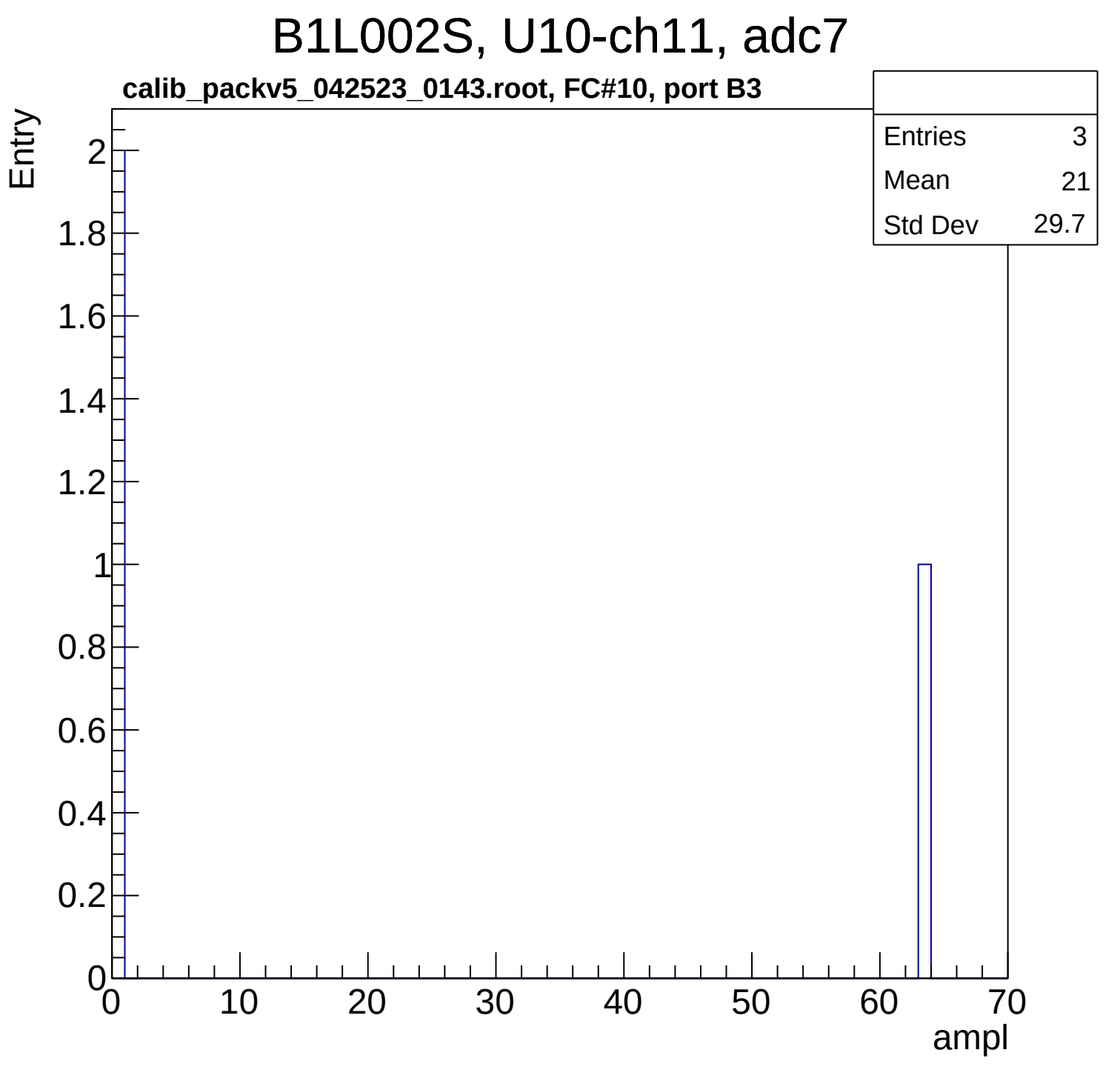
B1L002S, U10-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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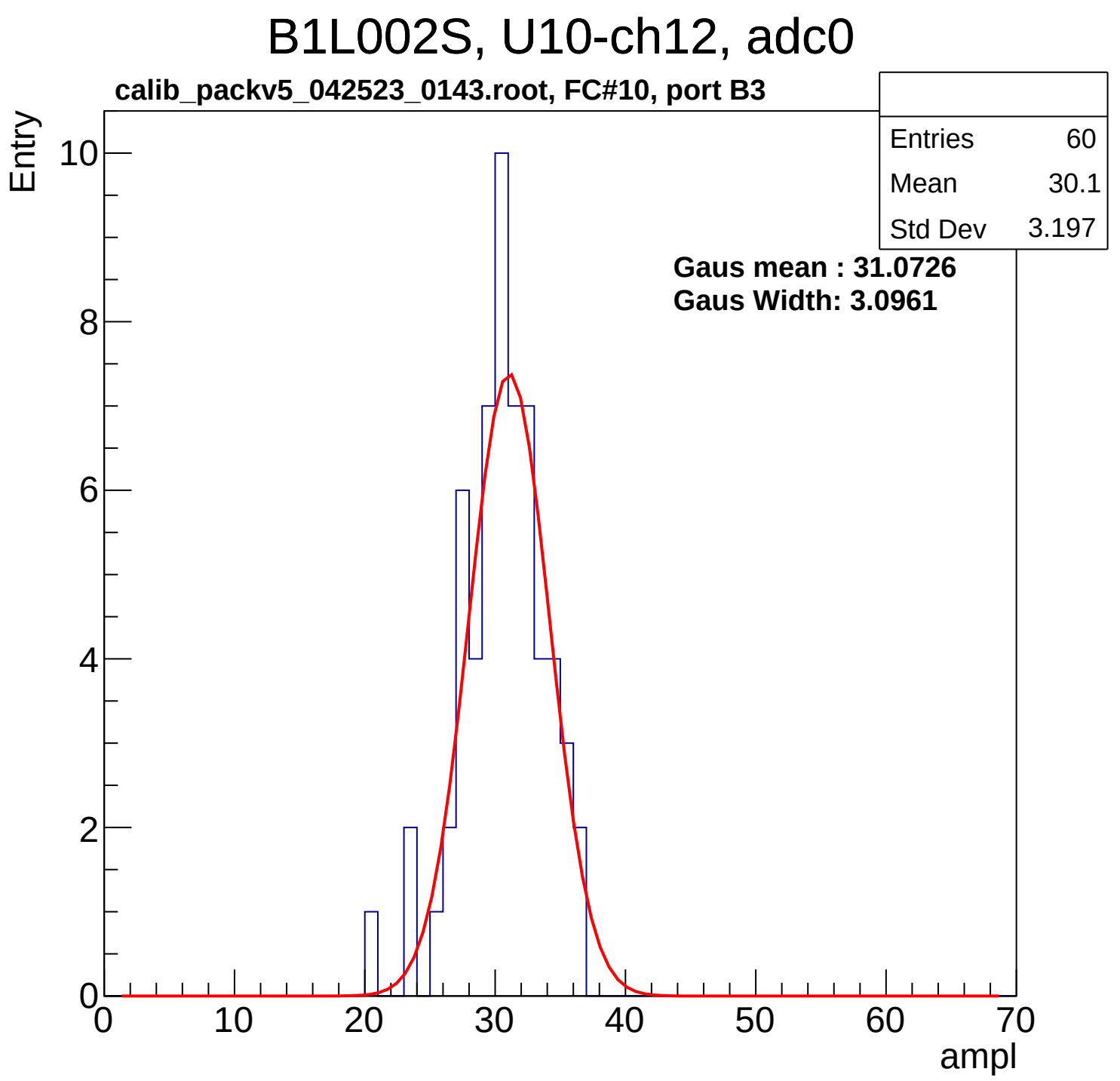
B1L002S, U10-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 30.1
Std Dev 3.197
Gaus mean : 31.0726
Gaus Width: 3.0961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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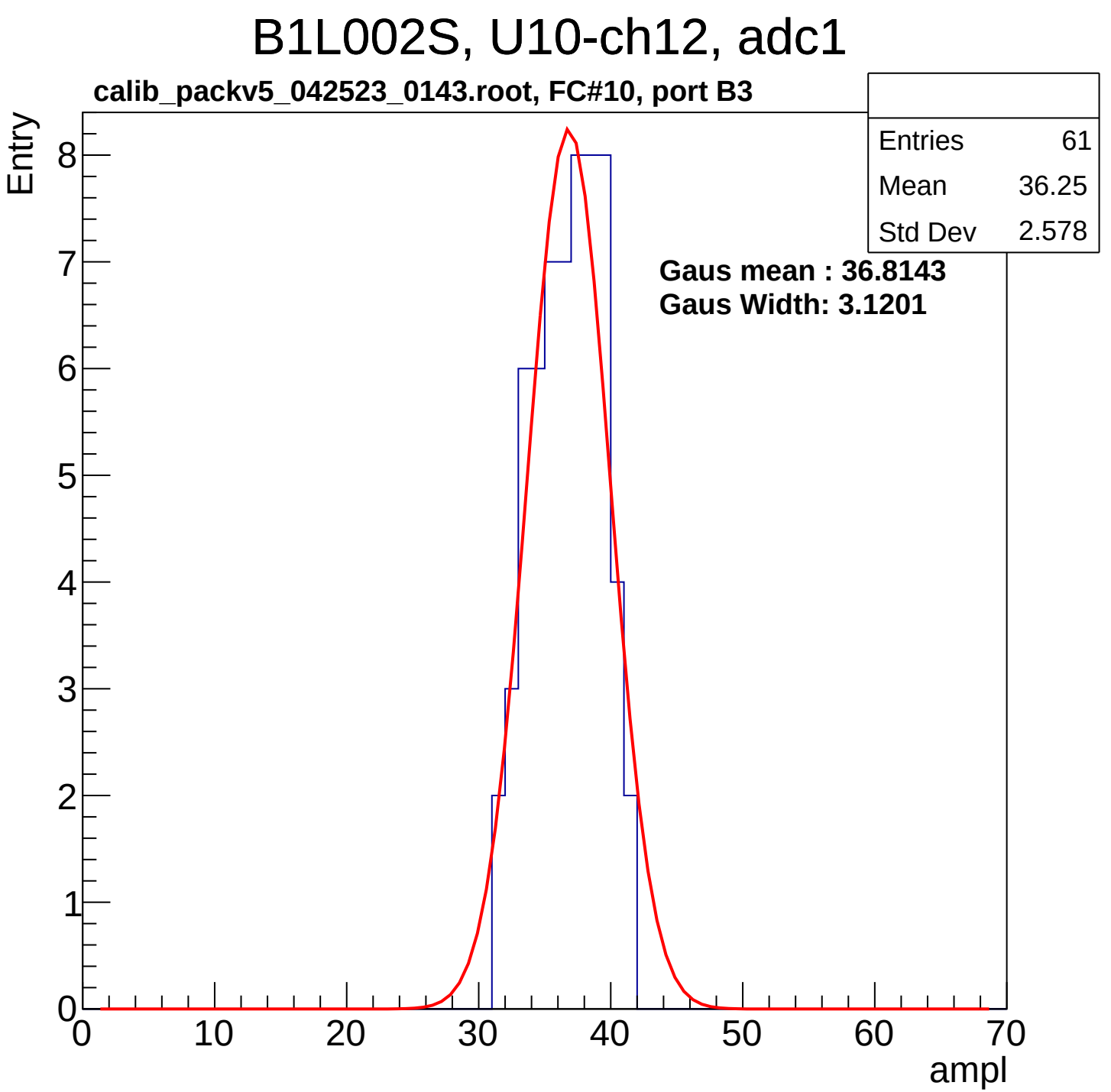
B1L002S, U10-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 36.25
Std Dev 2.578
Gaus mean : 36.8143
Gaus Width: 3.1201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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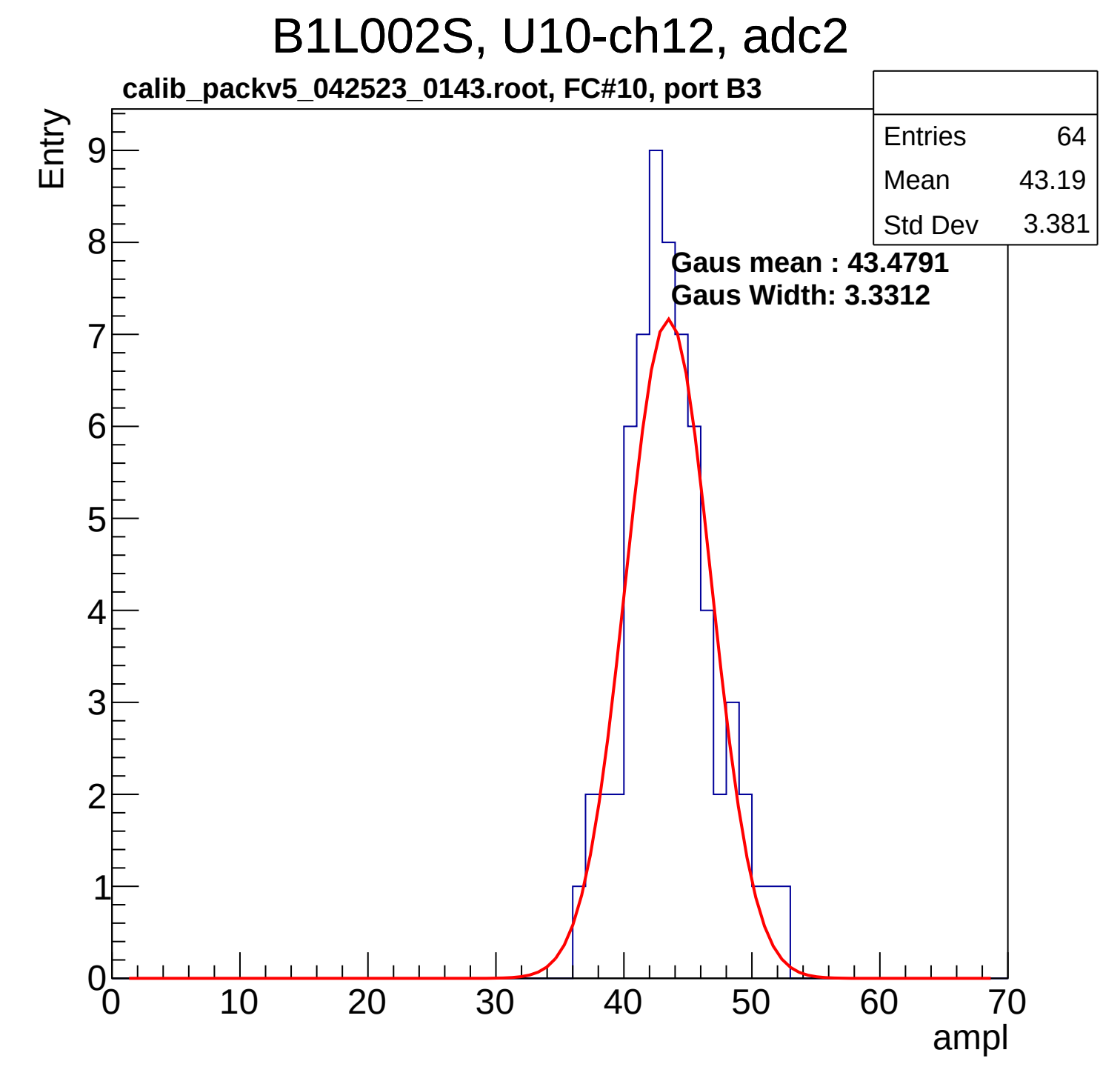
B1L002S, U10-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 43.19
Std Dev 3.381
Gaus mean : 43.4791
Gaus Width: 3.3312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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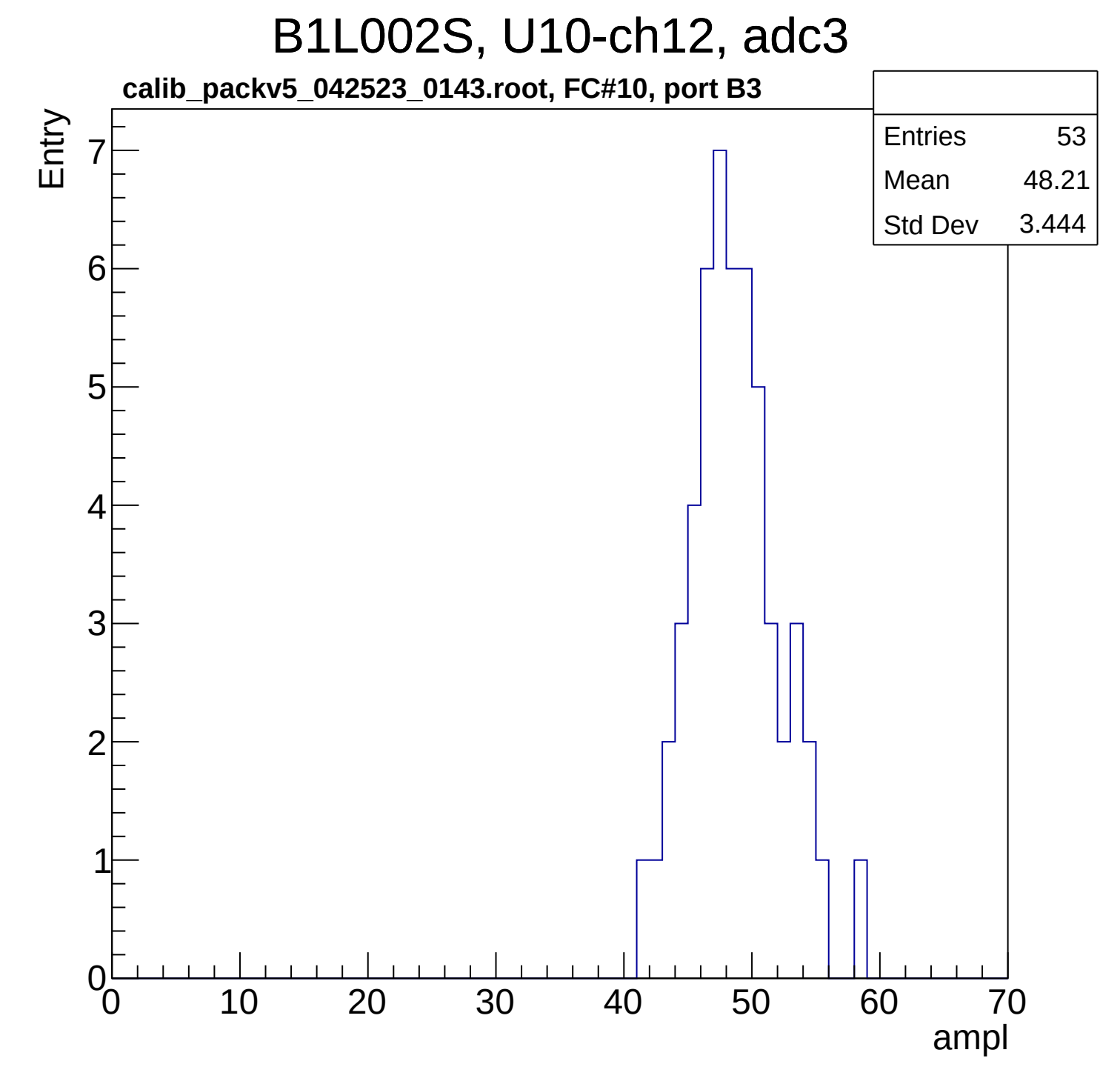
B1L002S, U10-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.21
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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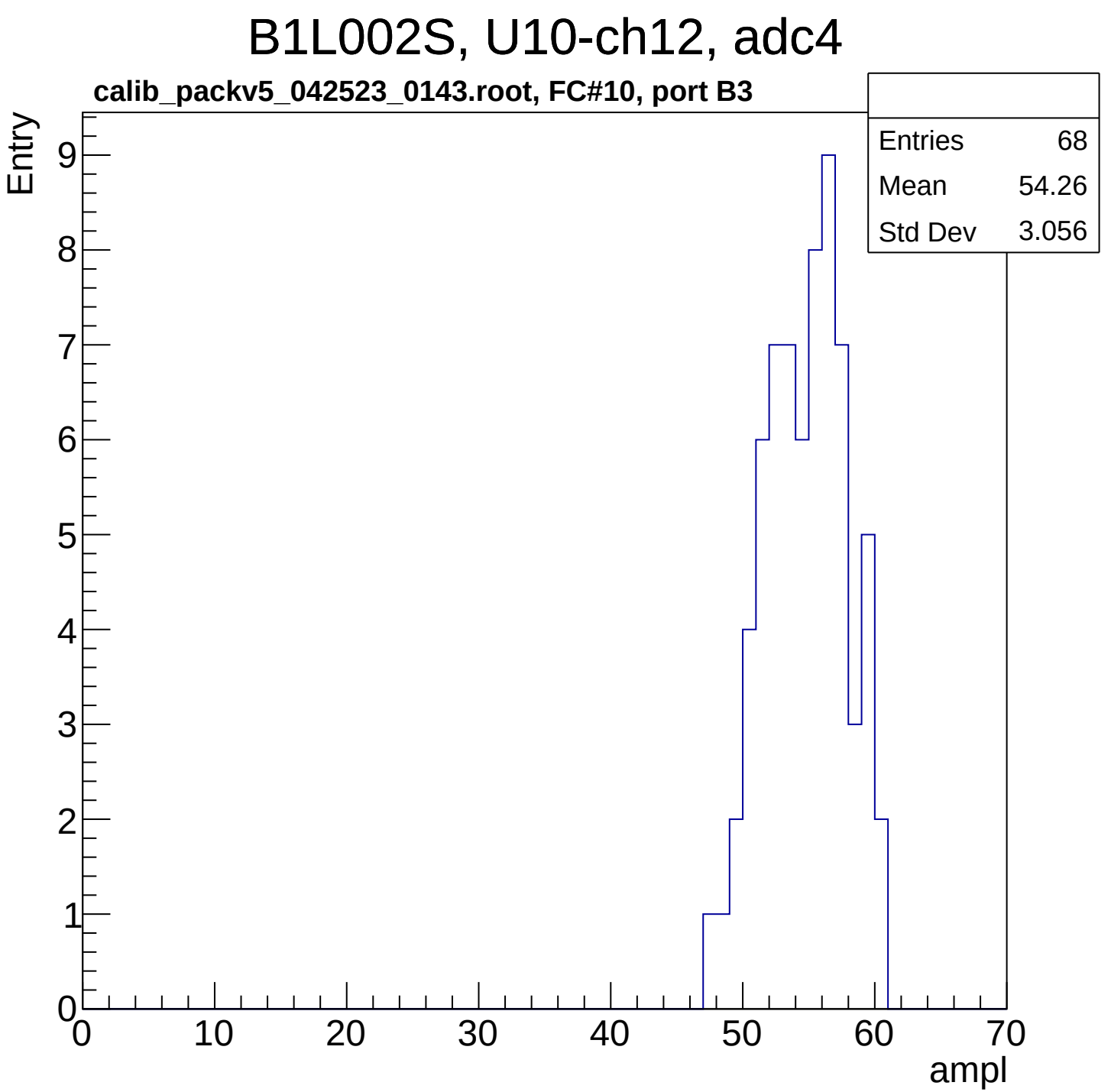
B1L002S, U10-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 54.26
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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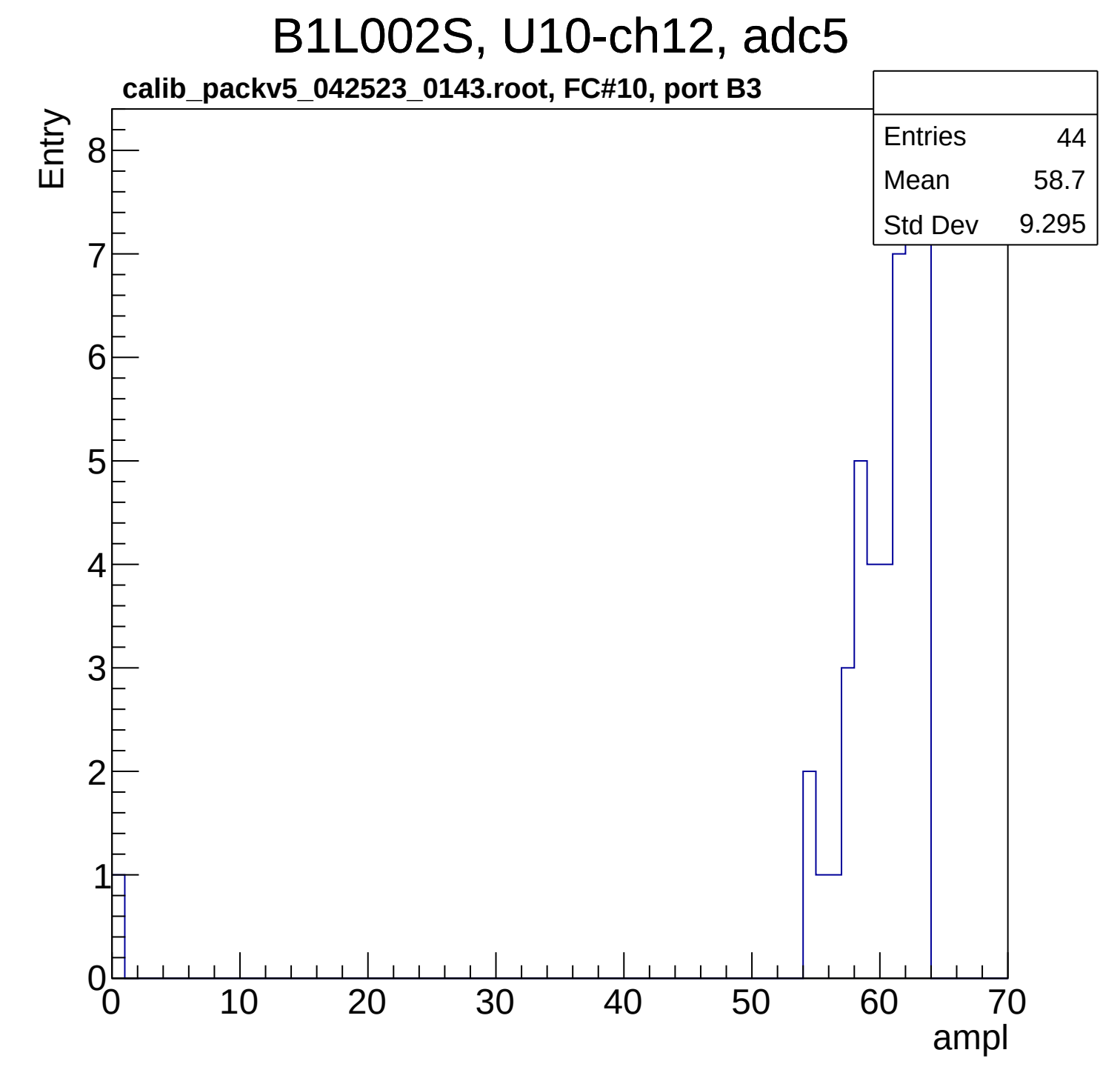
B1L002S, U10-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.7
Std Dev 9.295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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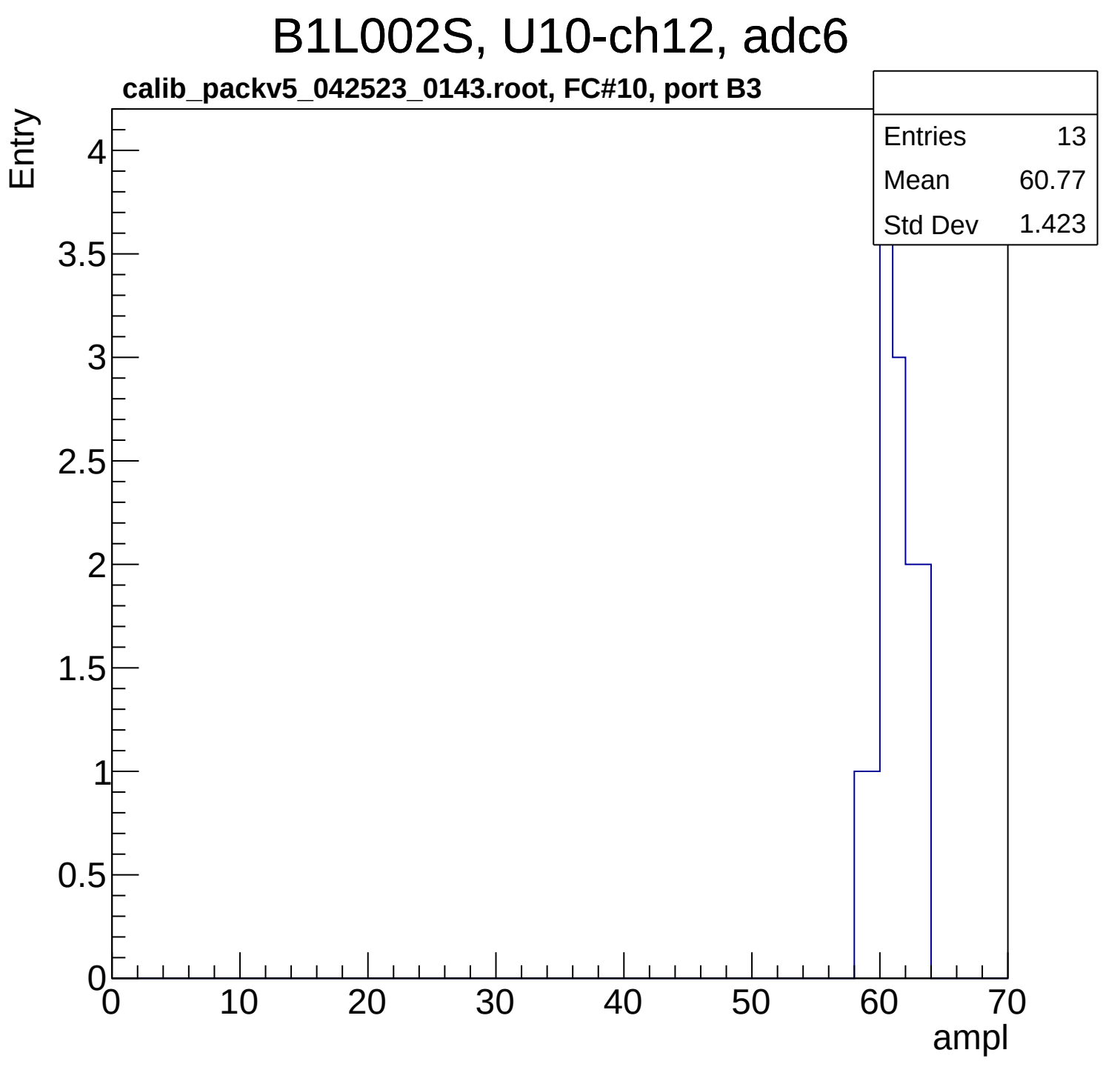
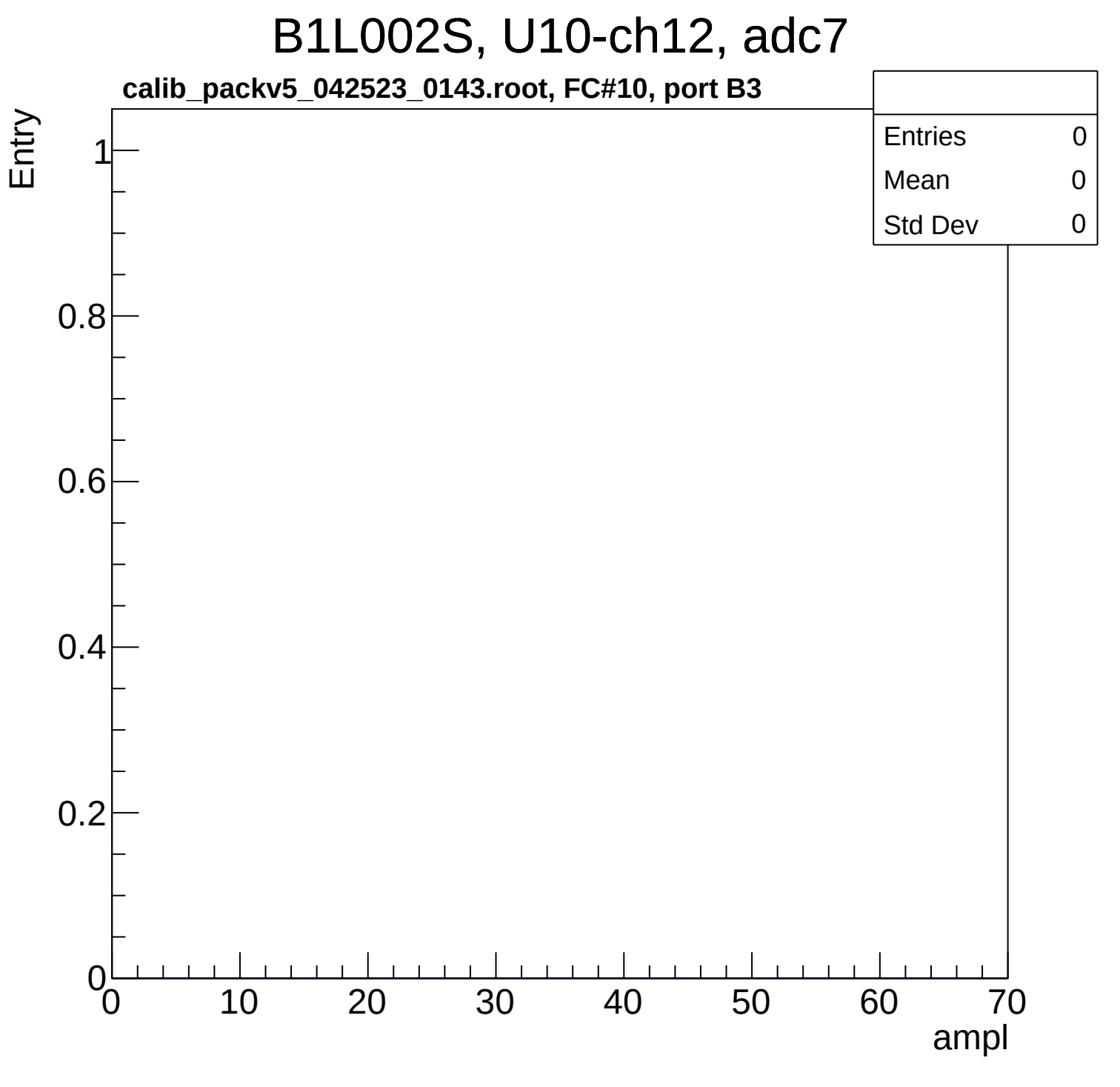
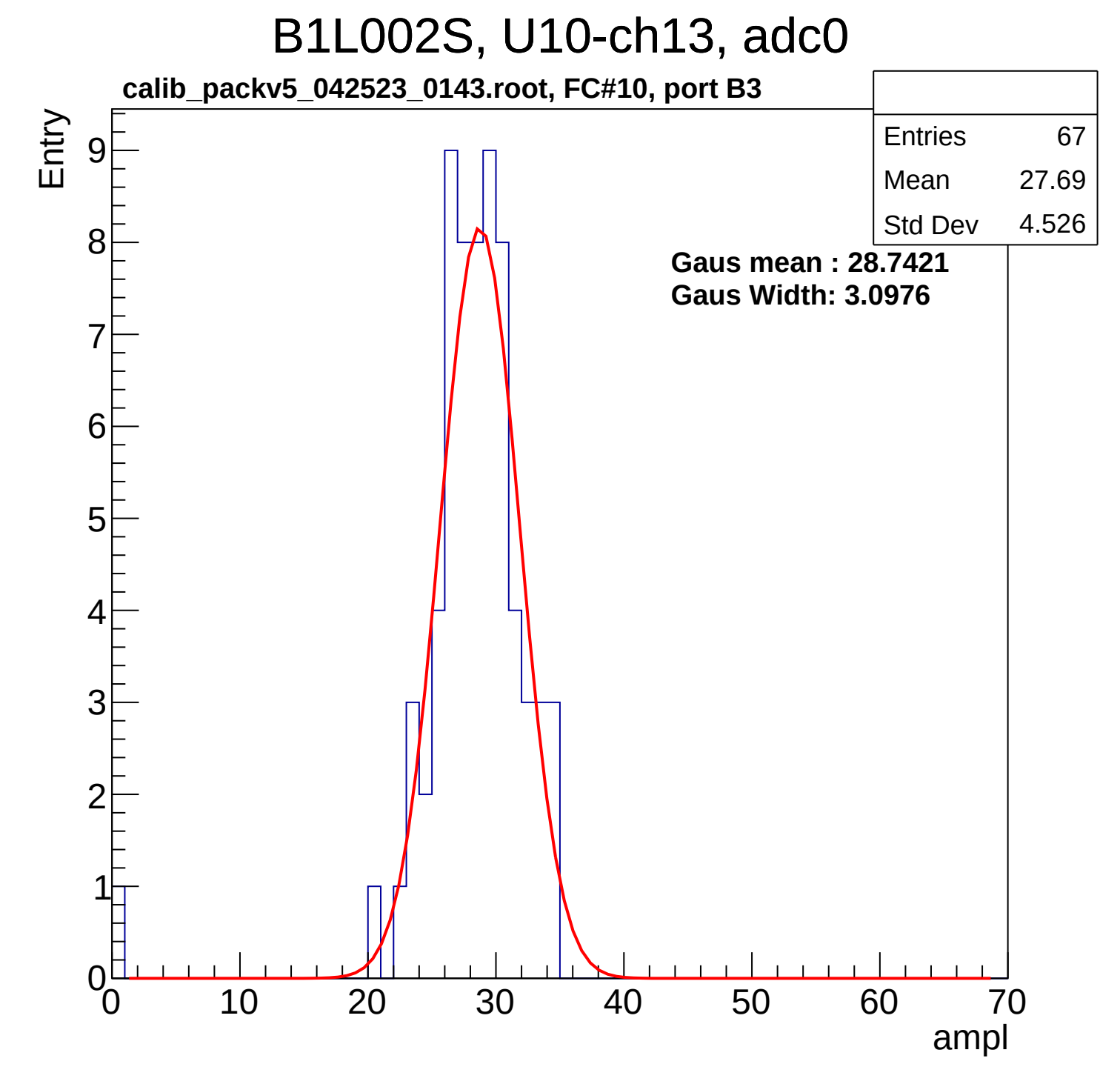

B1L002S, U10-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 60.77
Std Dev 1.423
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.69
Std Dev 4.526
Gaus mean : 28.7421
Gaus Width: 3.0976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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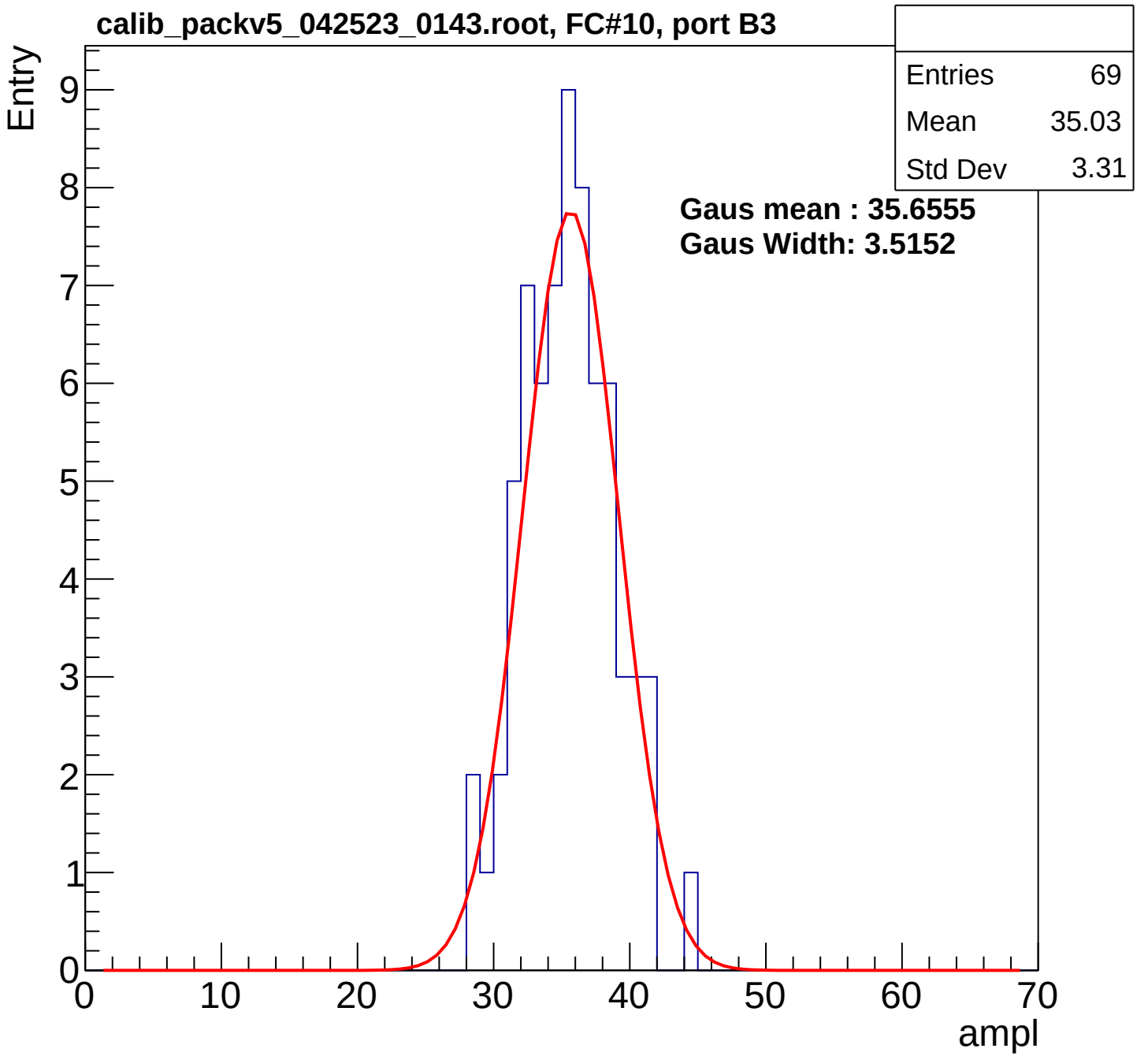
B1L002S, U10-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.03
Std Dev 3.31
Gaus mean : 35.6555
Gaus Width: 3.5152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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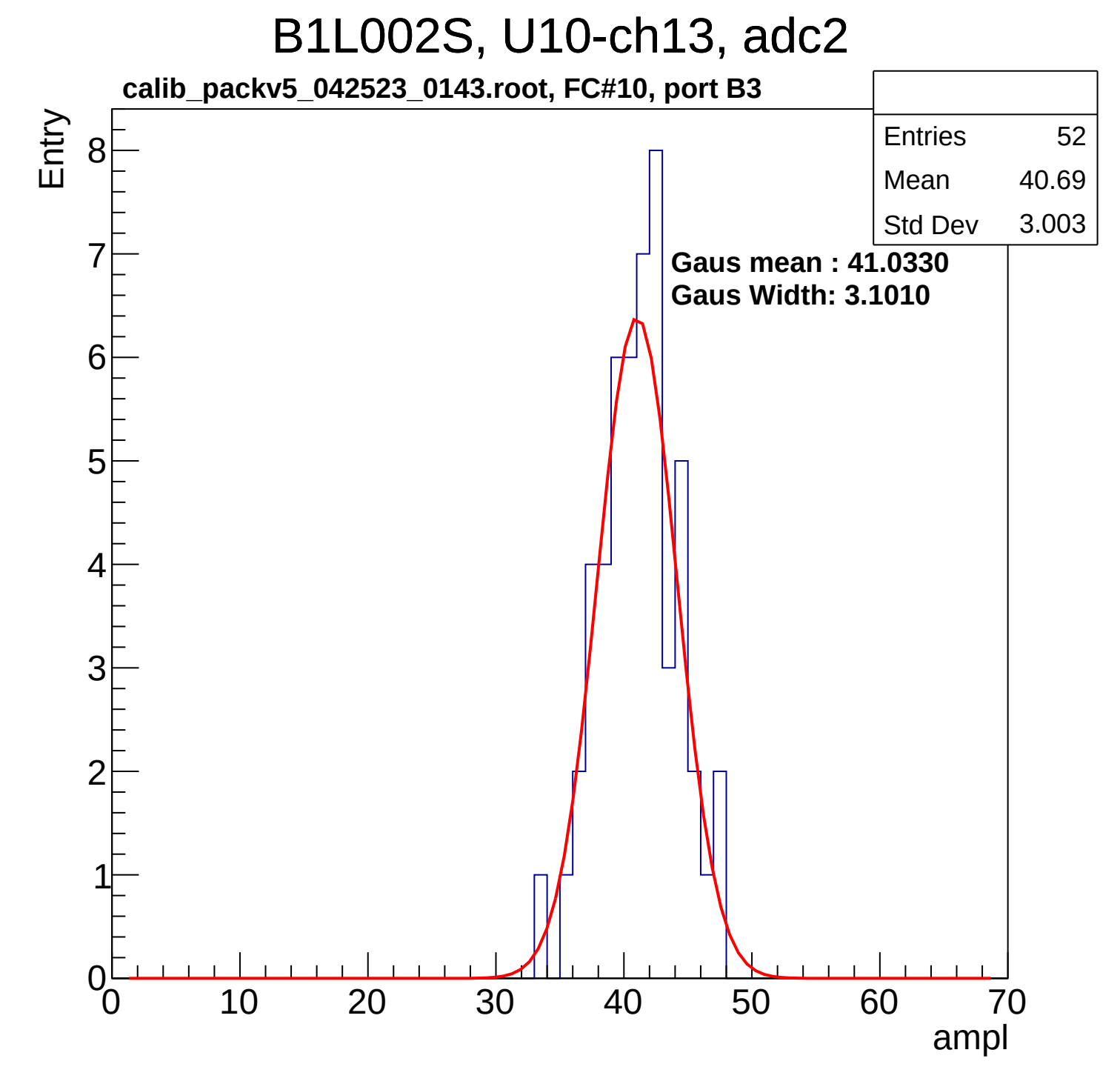
B1L002S, U10-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.69
Std Dev 3.003
Gaus mean : 41.0330
Gaus Width: 3.1010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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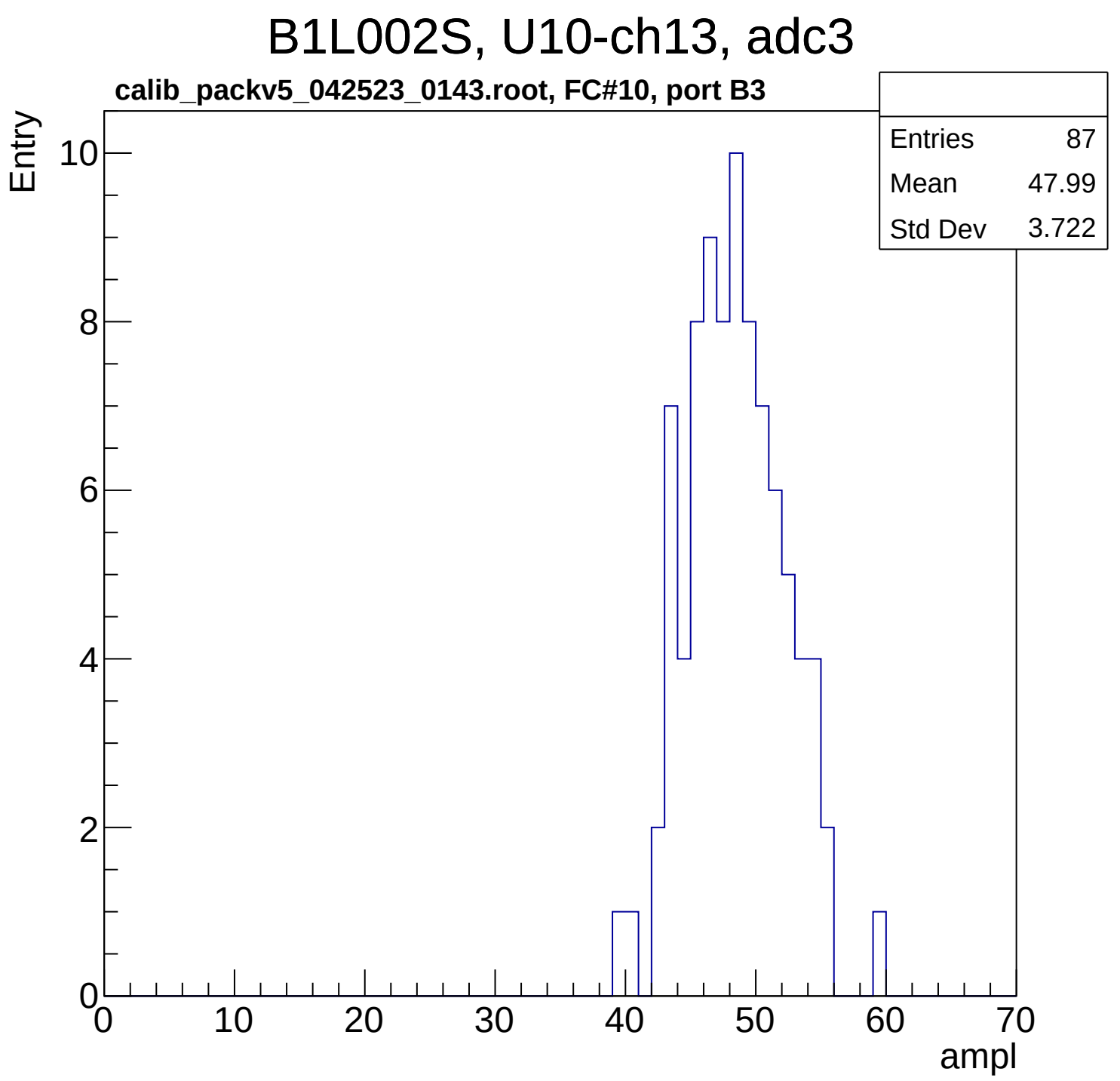
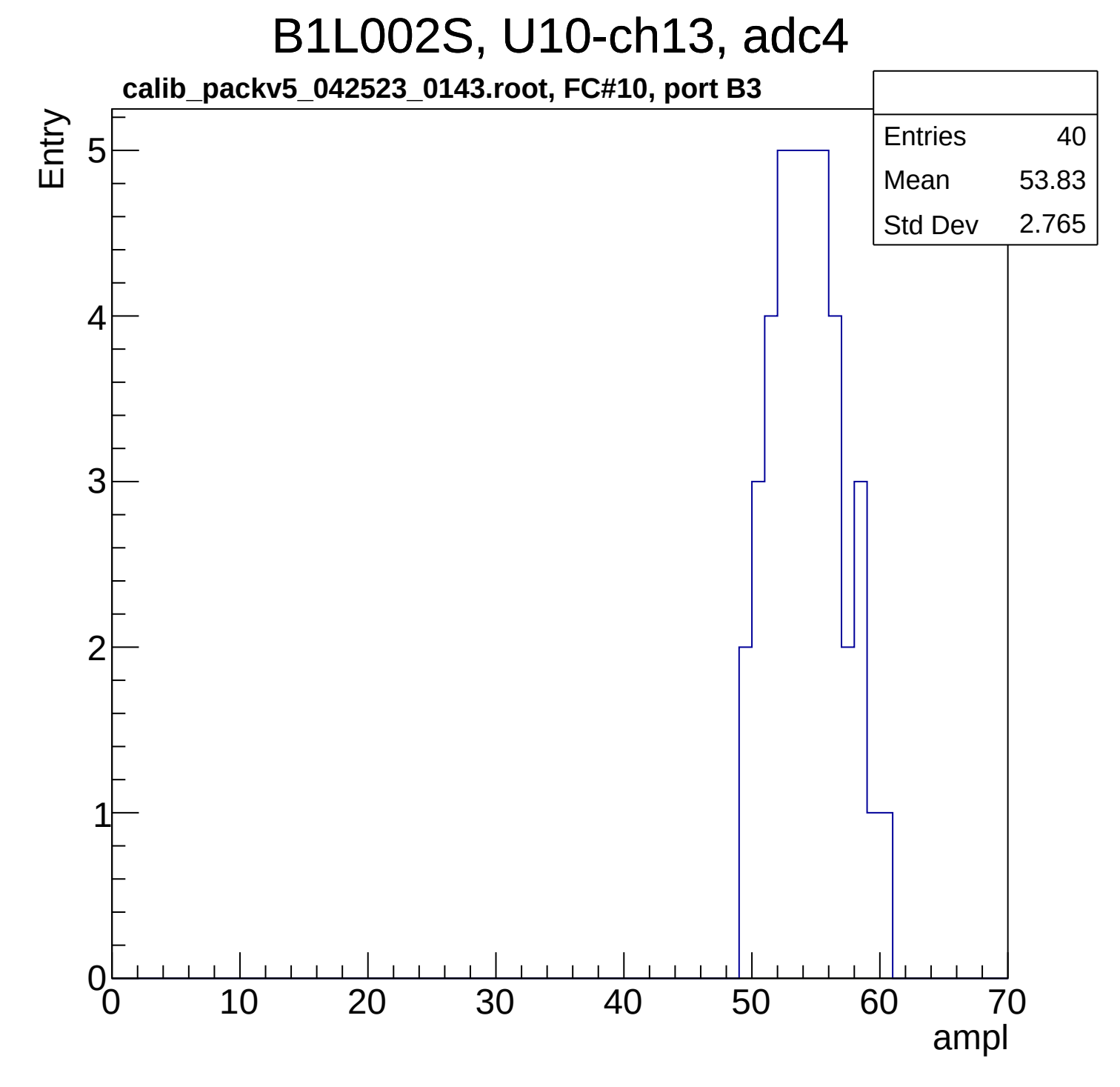
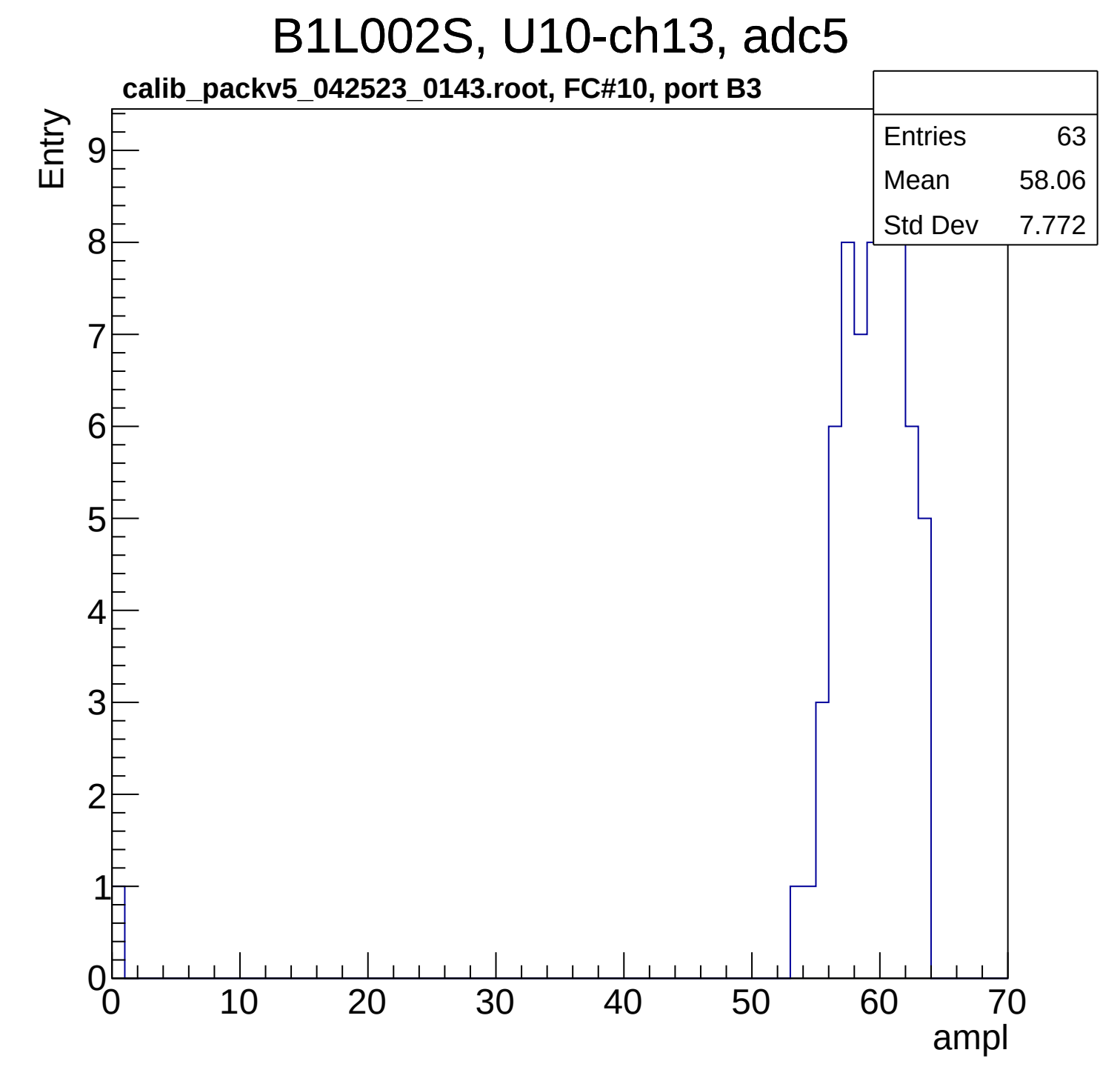
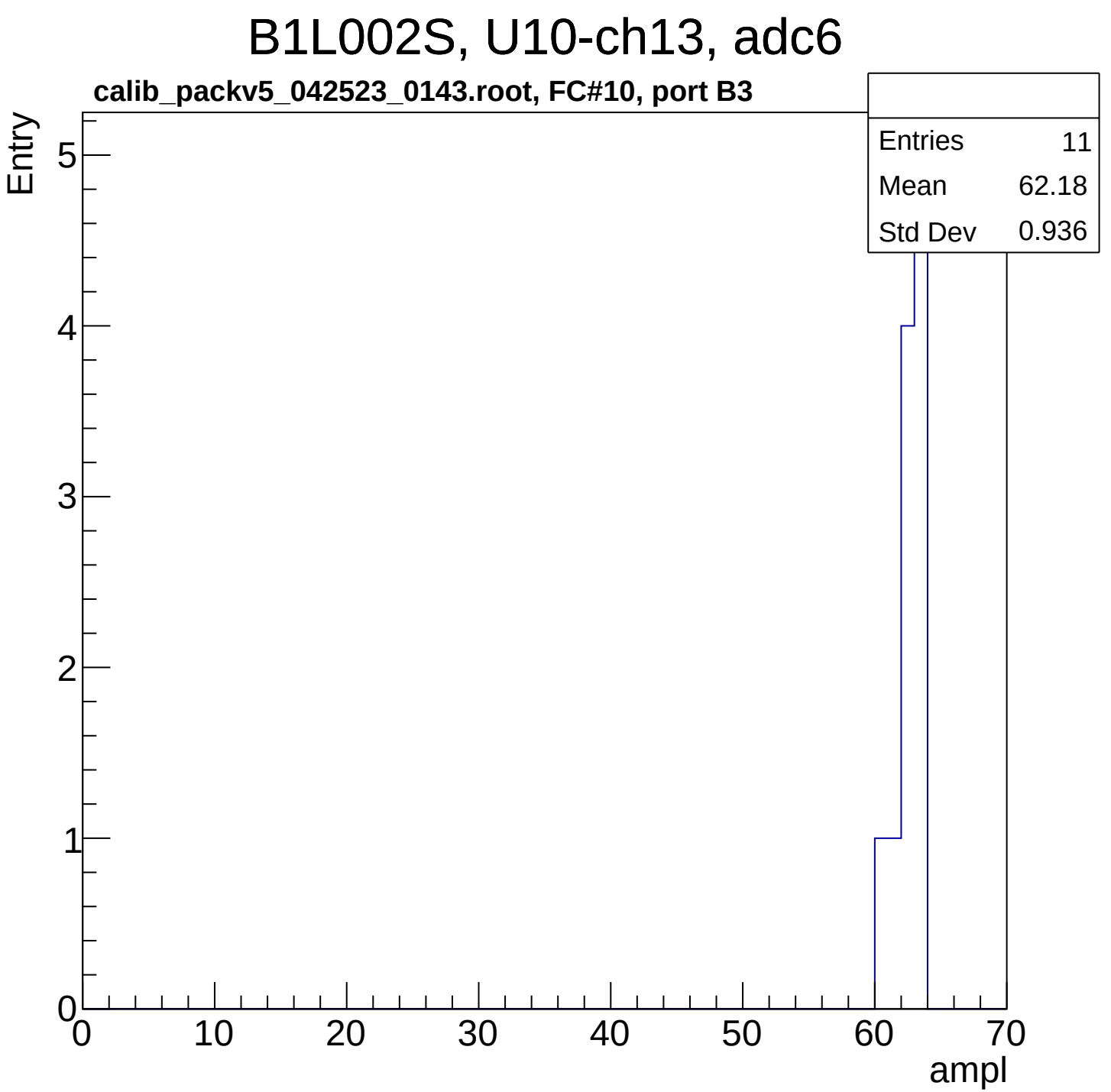
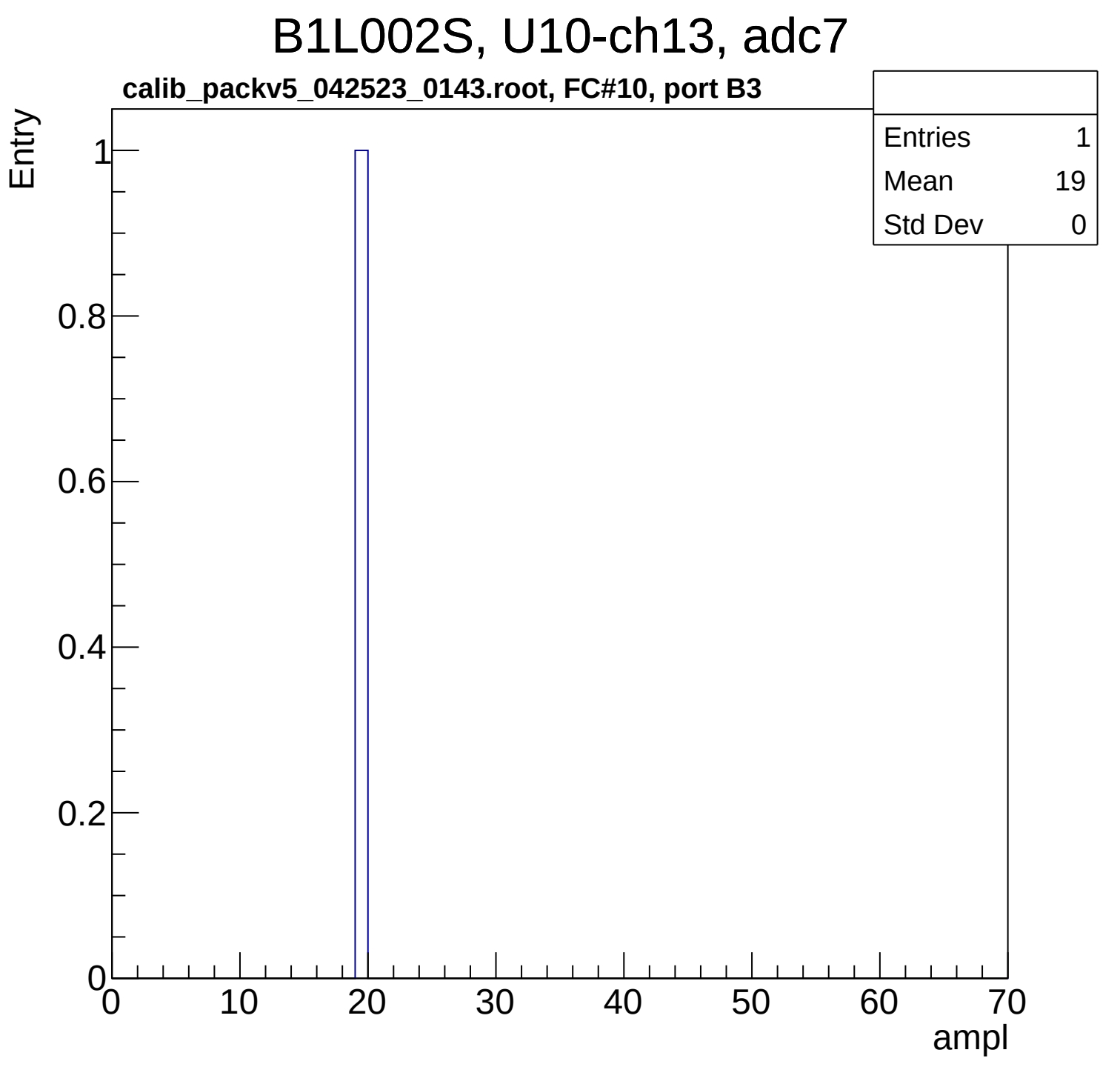

B1L002S, U10-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 47.99
Std Dev 3.722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 53.83
Std Dev 2.765
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.06
Std Dev 7.772
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.18
Std Dev 0.936
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
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0.6
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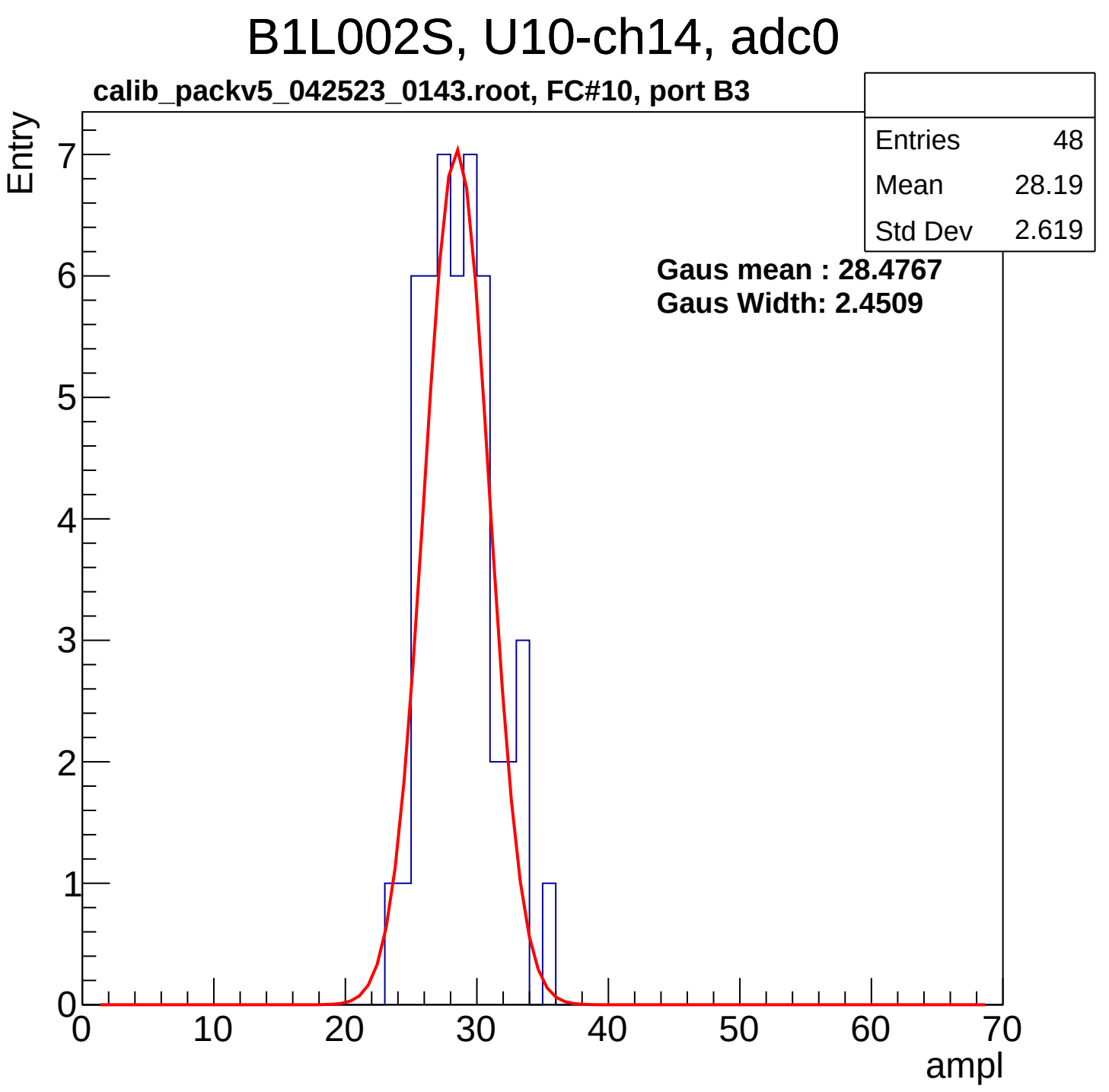
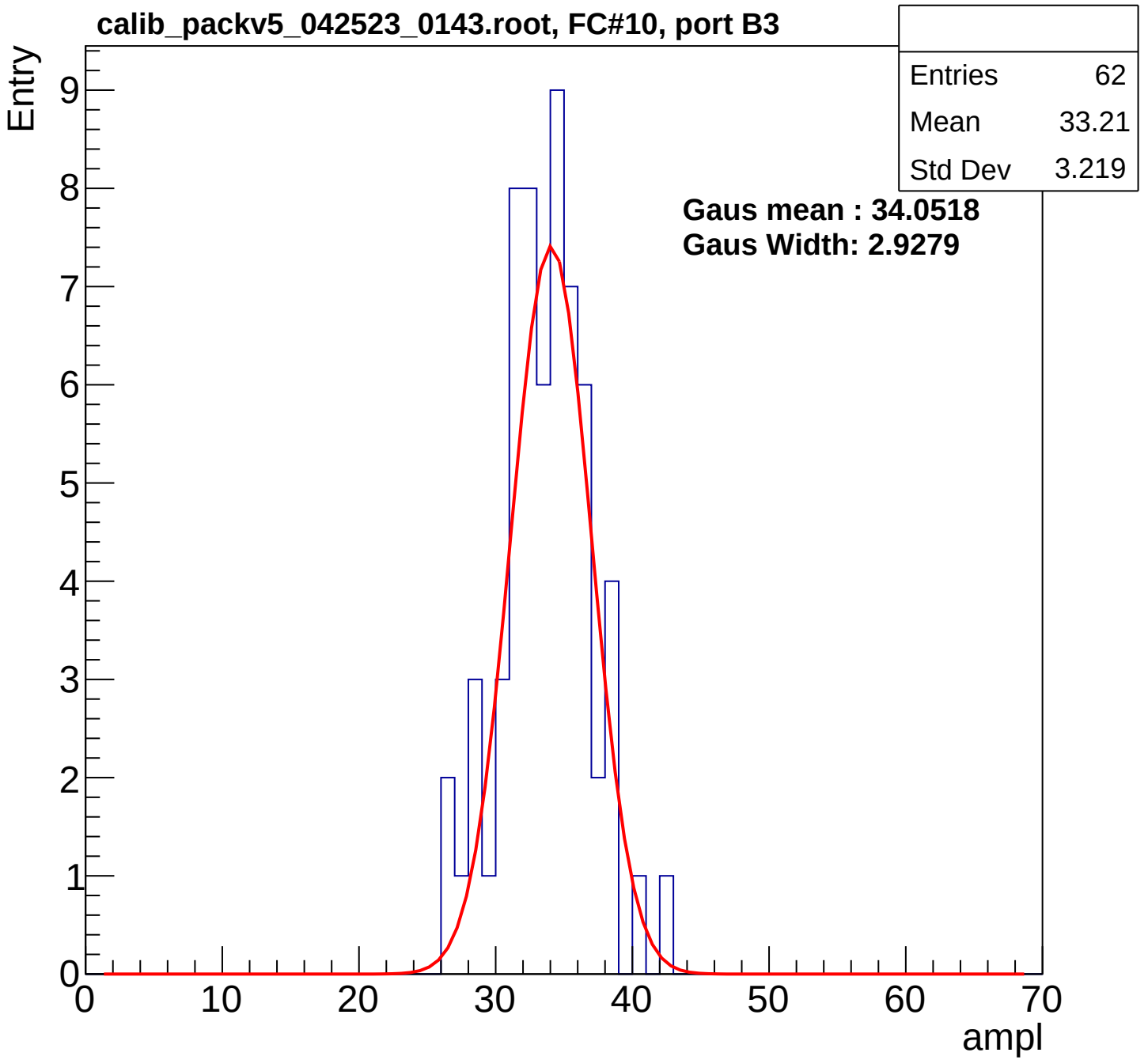
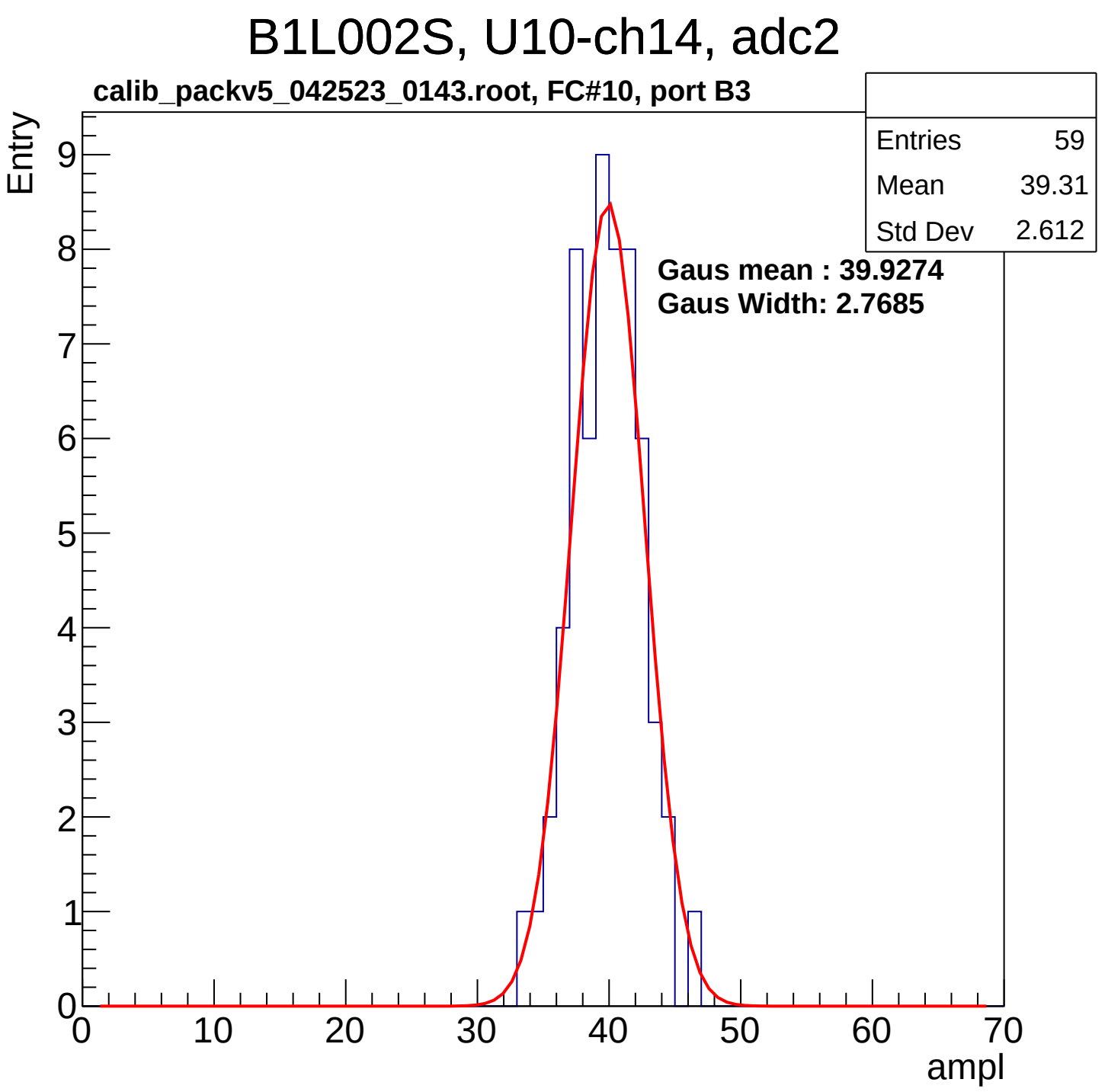
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.19
Std Dev 2.619
Gaus mean : 28.4767
Gaus Width: 2.4509
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.21
Std Dev 3.219
Gaus mean : 34.0518
Gaus Width: 2.9279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.31
Std Dev 2.612
Gaus mean : 39.9274
Gaus Width: 2.7685
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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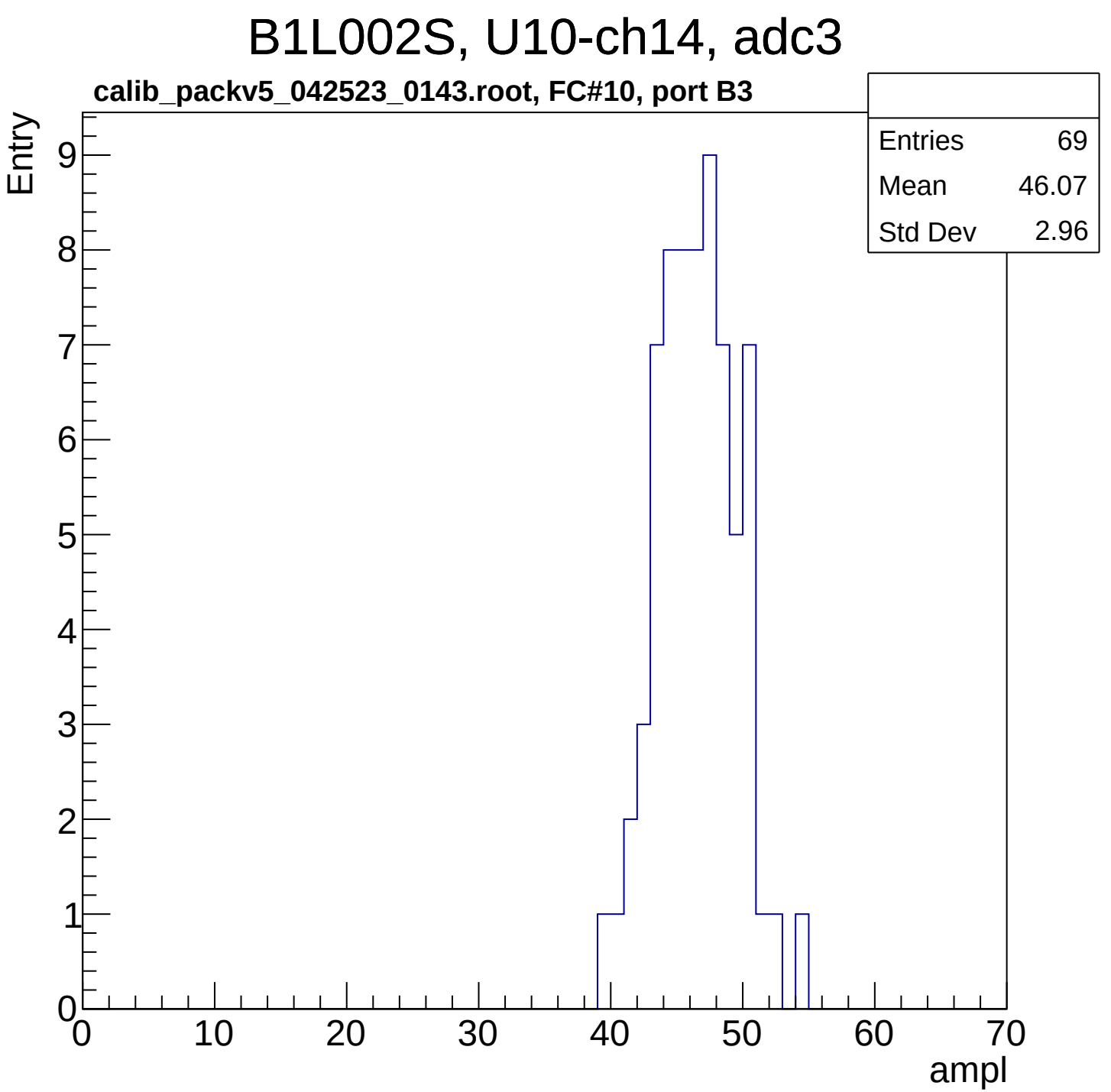
B1L002S, U10-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.07
Std Dev 2.96
Entry
ampl
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3
4
5
6
7
8
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0
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20
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60
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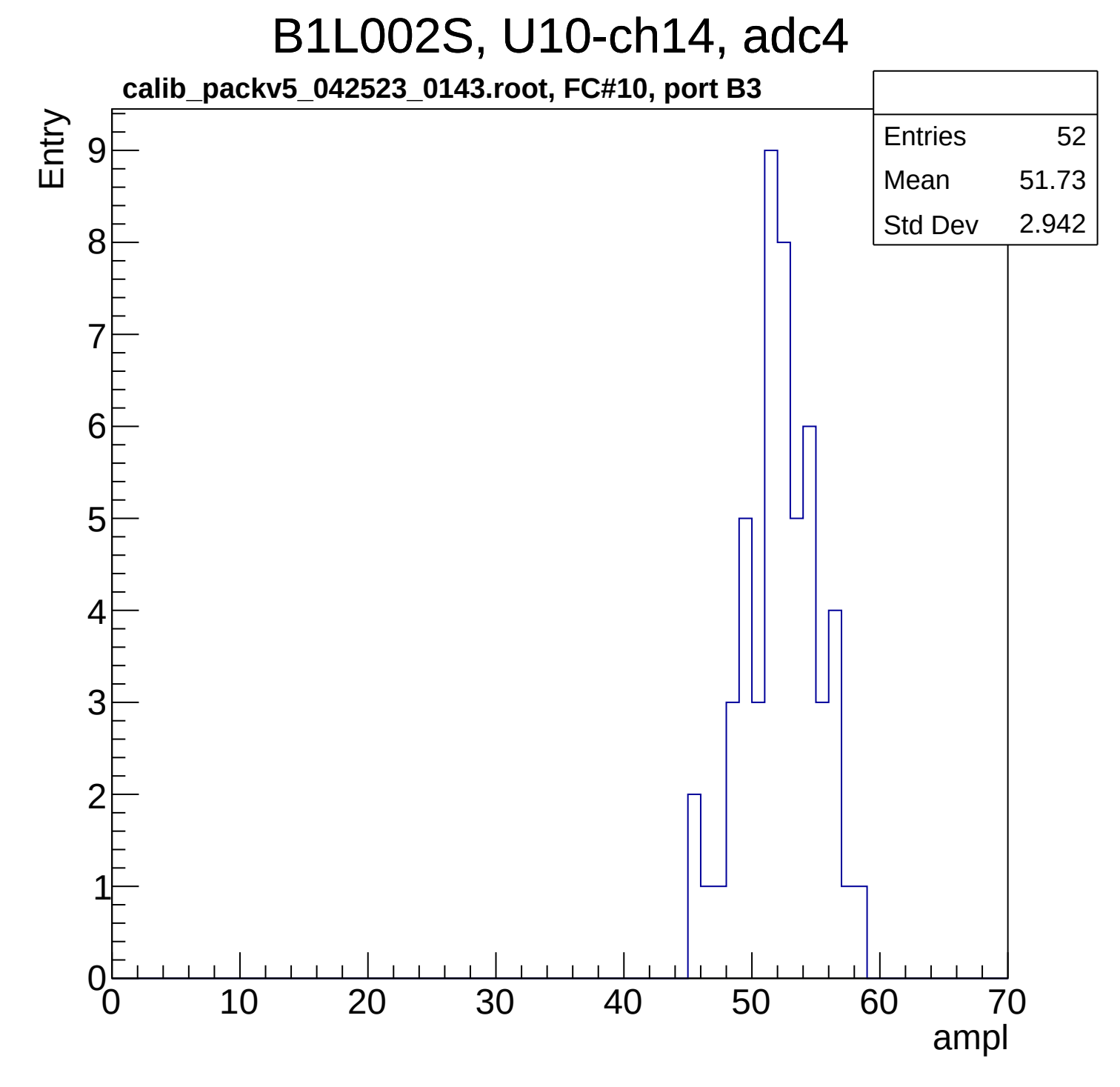
B1L002S, U10-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.73
Std Dev 2.942
Entry
ampl
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3
4
5
6
7
8
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0
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60
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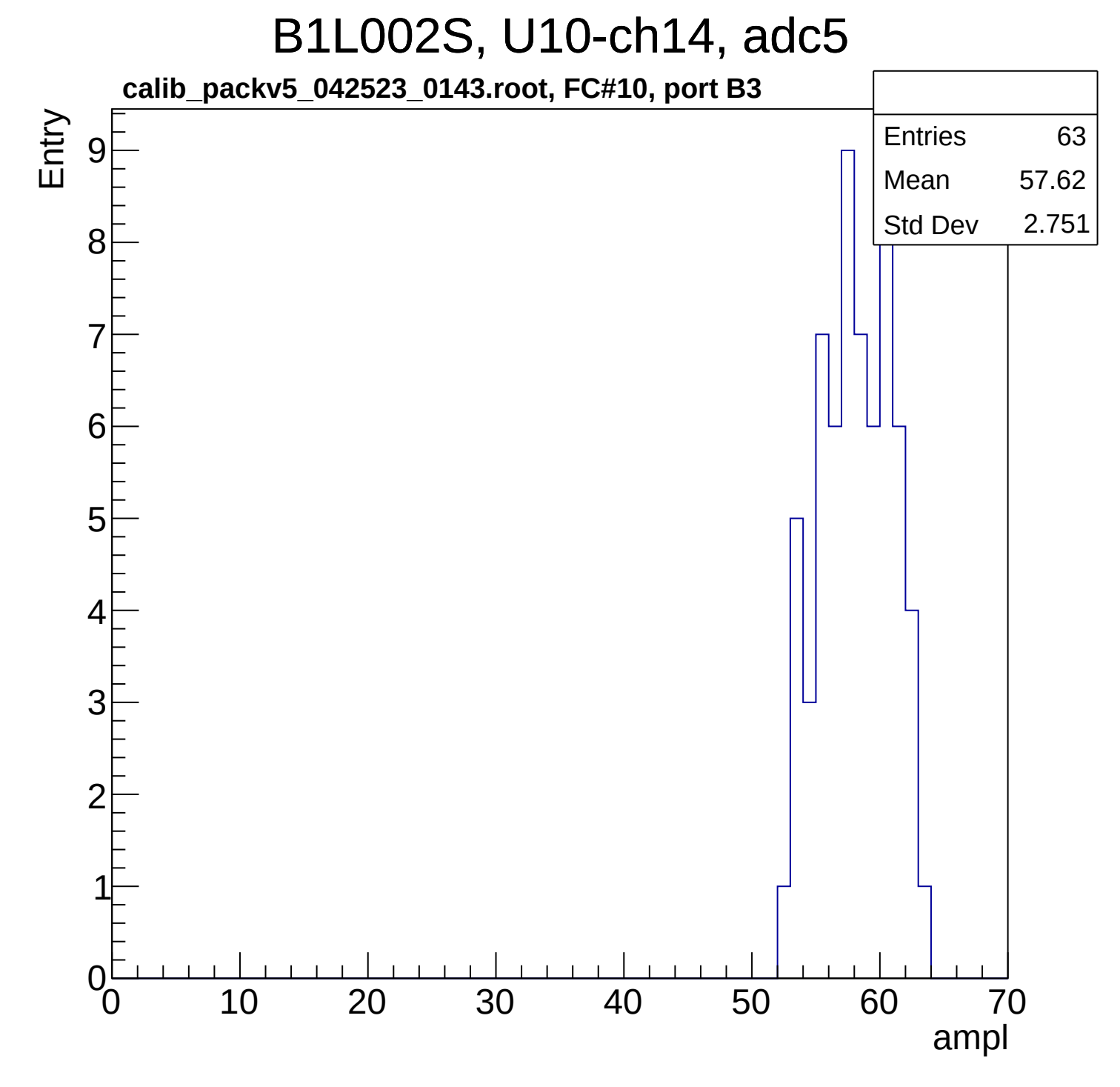
B1L002S, U10-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.62
Std Dev 2.751
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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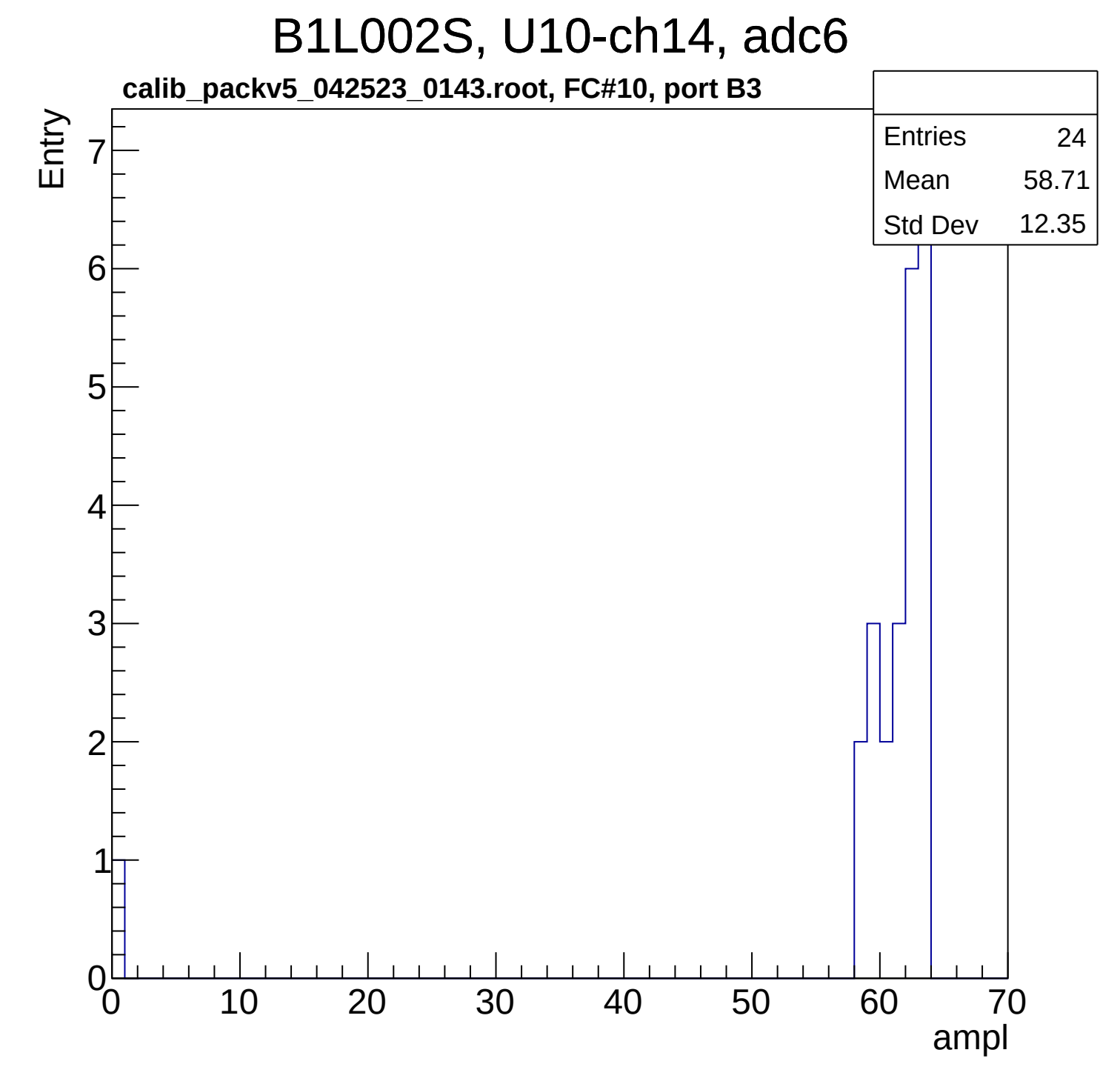
B1L002S, U10-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.71
Std Dev 12.35
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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50
60
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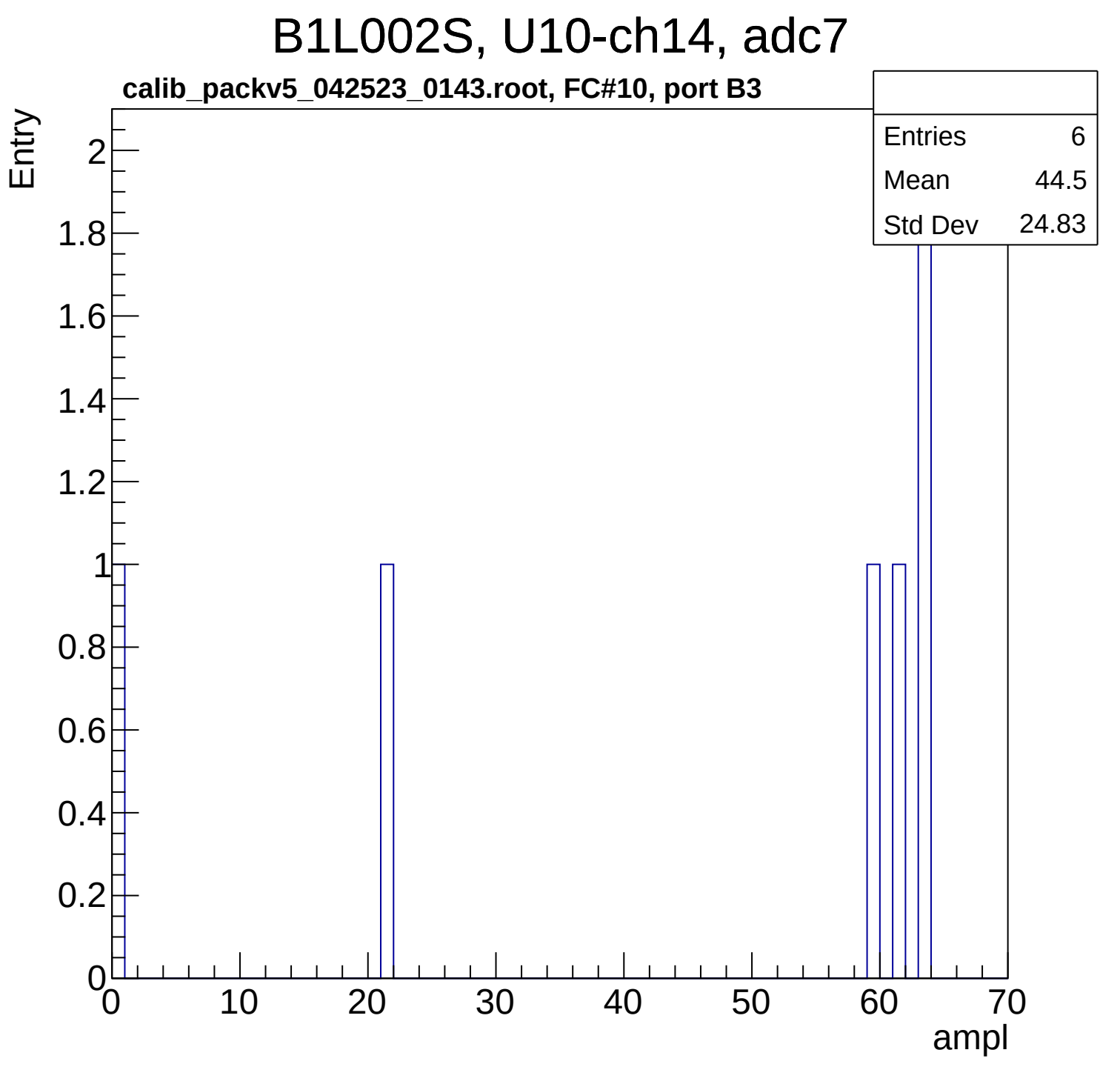
B1L002S, U10-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.5
Std Dev 24.83
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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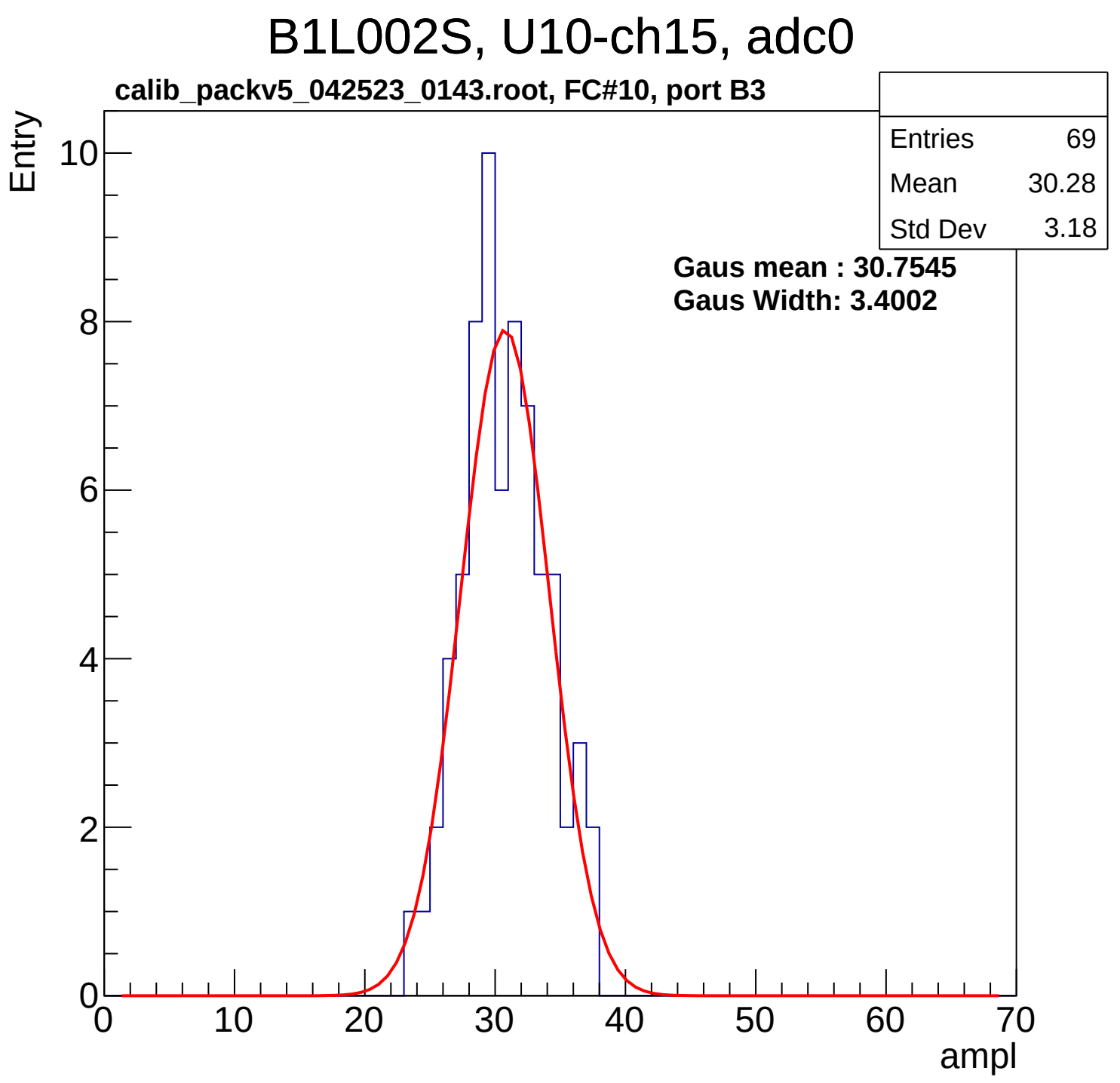
B1L002S, U10-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 30.28
Std Dev 3.18
Gaus mean : 30.7545
Gaus Width: 3.4002
Entry
ampl
0
2
4
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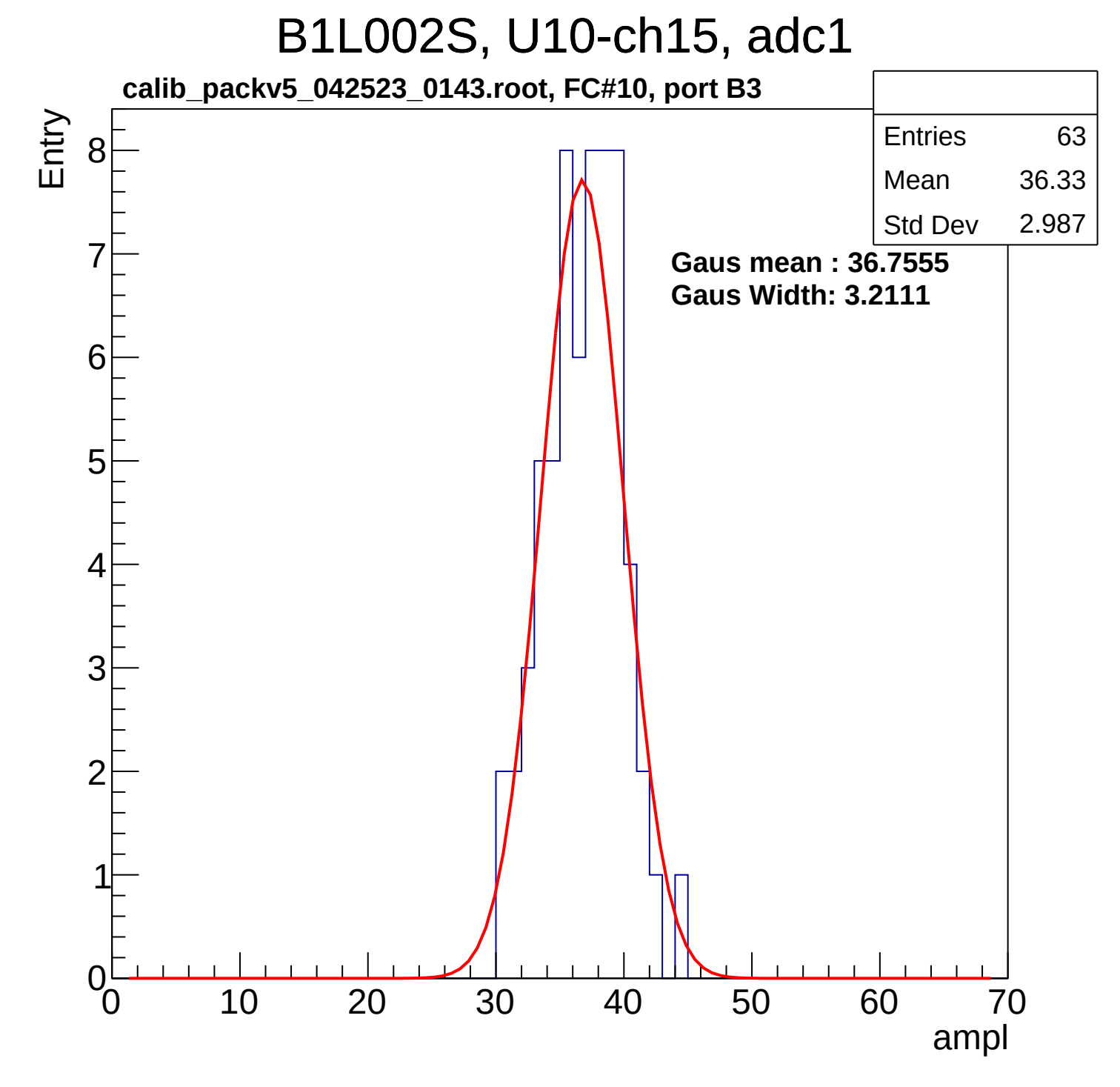
B1L002S, U10-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.33
Std Dev 2.987
Gaus mean : 36.7555
Gaus Width: 3.2111
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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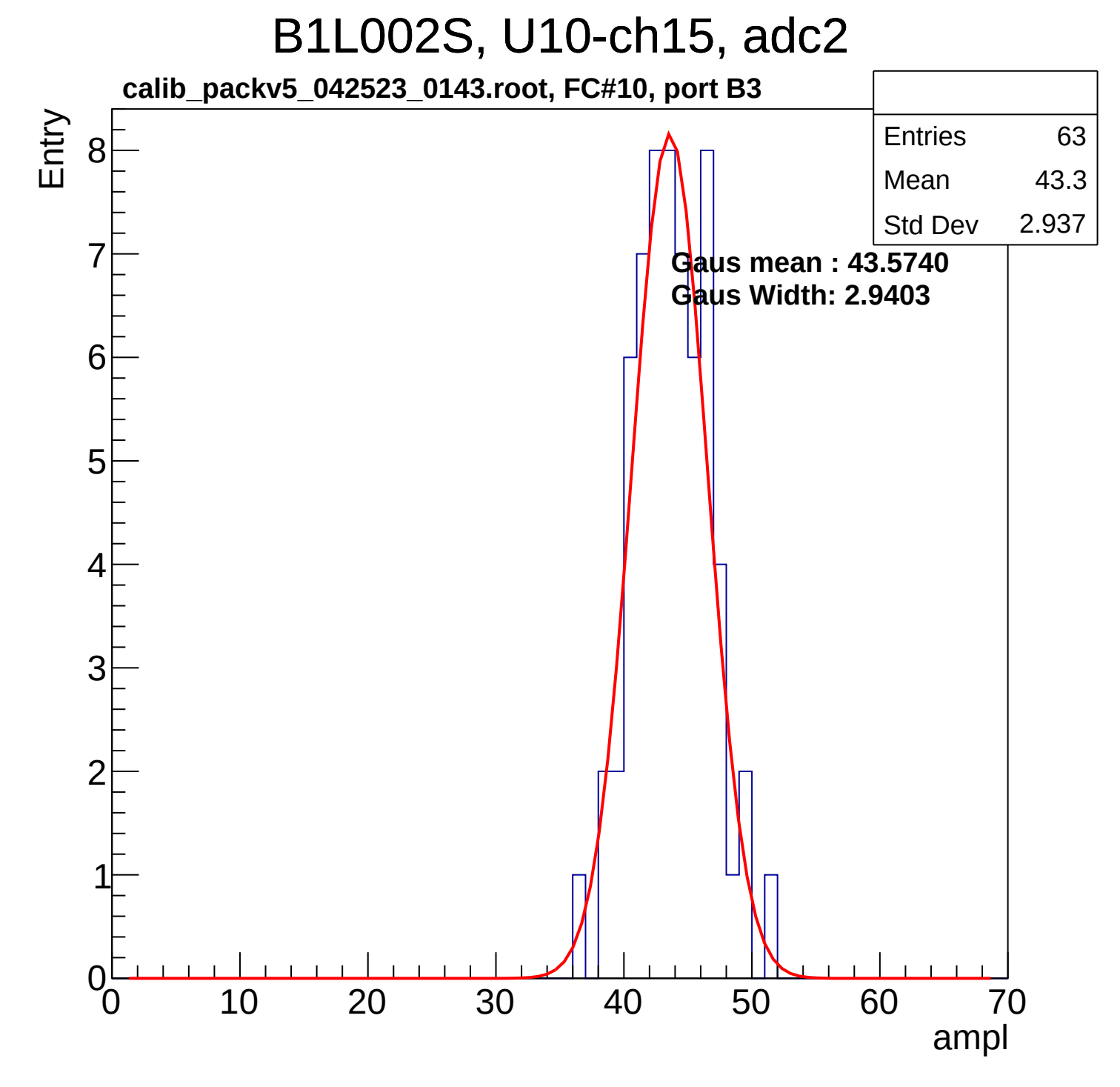
B1L002S, U10-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 43.3
Std Dev 2.937
Gaus mean : 43.5740
Gaus Width: 2.9403
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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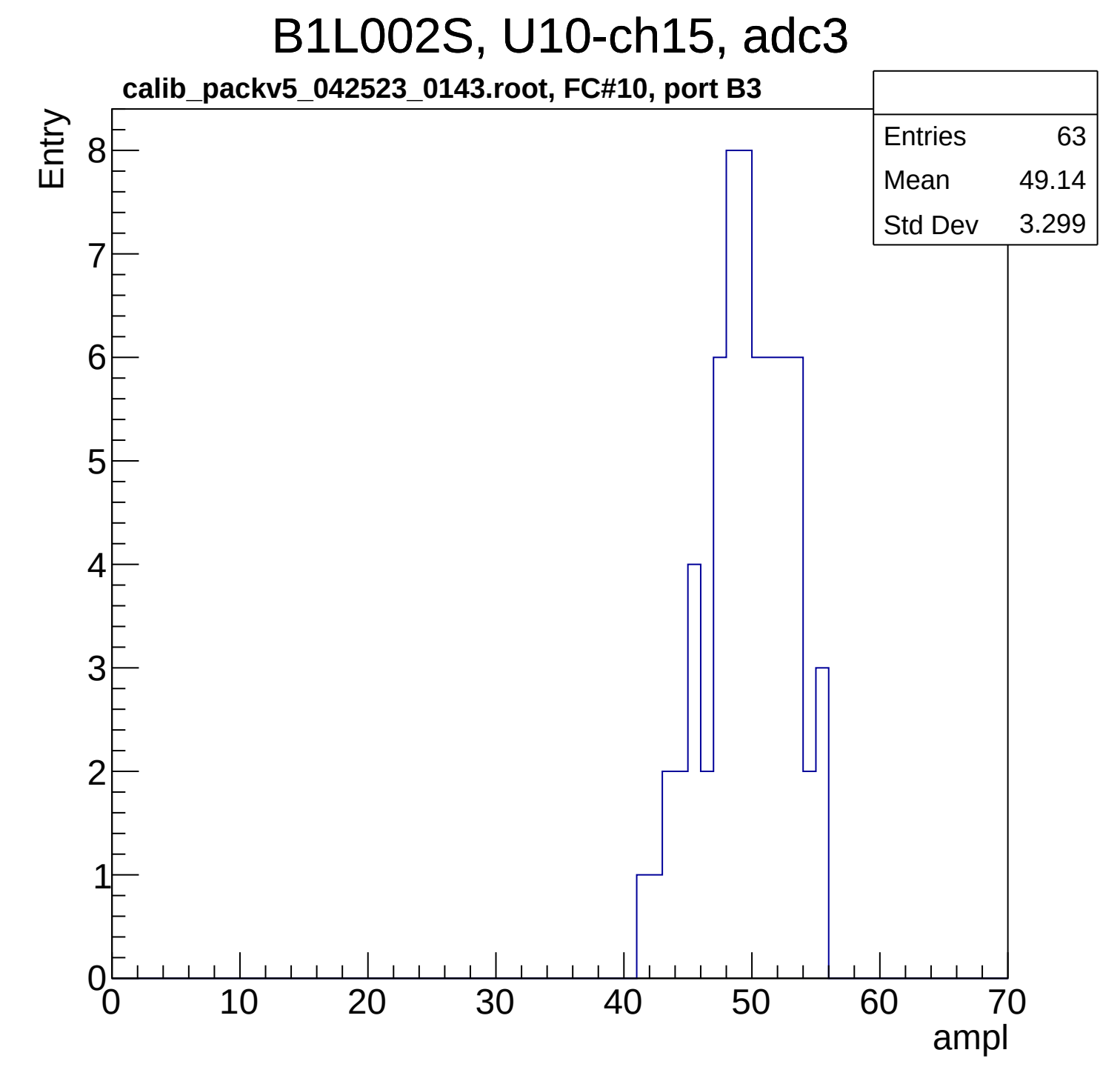
B1L002S, U10-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 49.14
Std Dev 3.299
Entry
ampl
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1
2
3
4
5
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0
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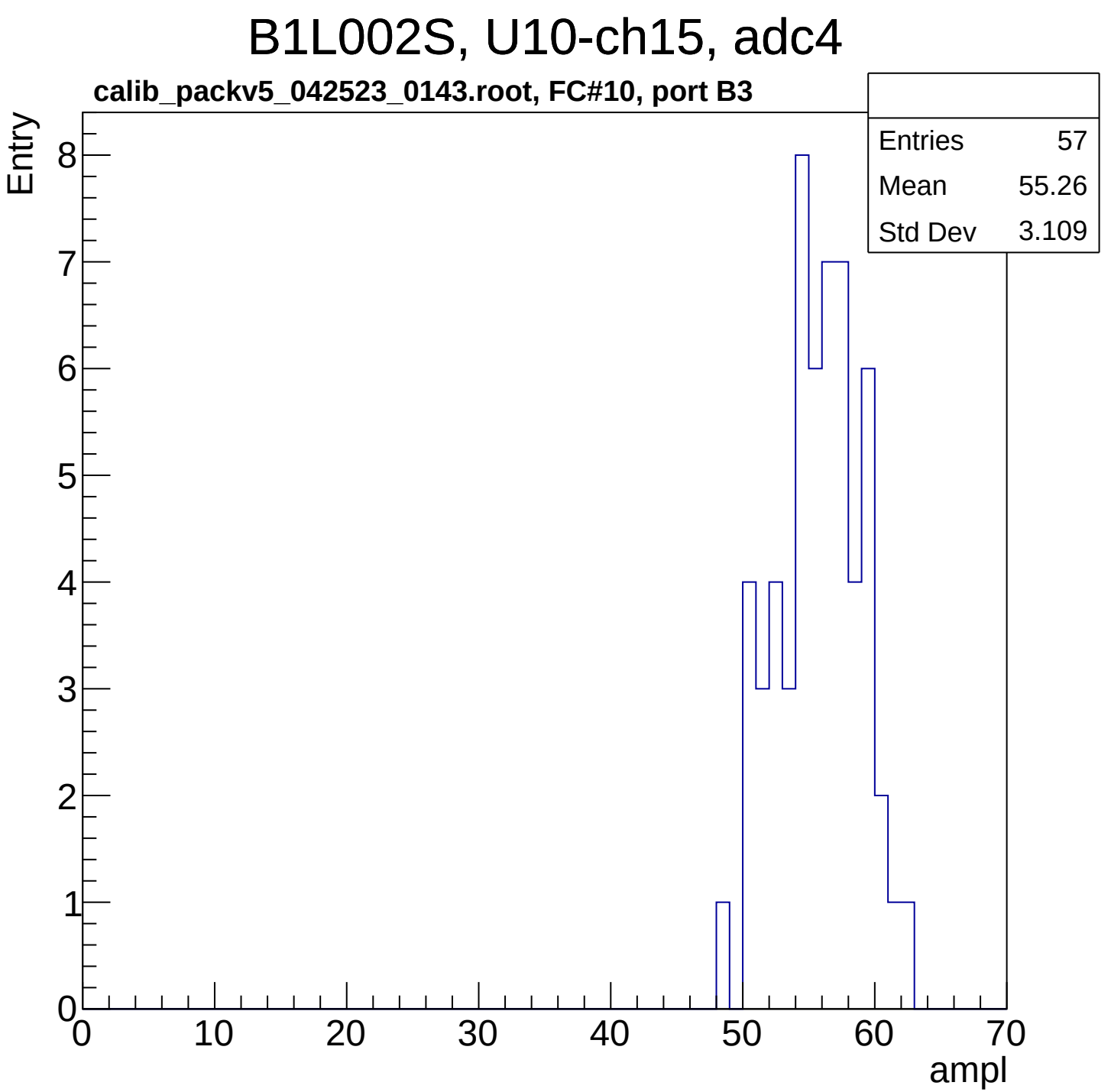
B1L002S, U10-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 55.26
Std Dev 3.109
Entry
ampl
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2
3
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5
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7
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0
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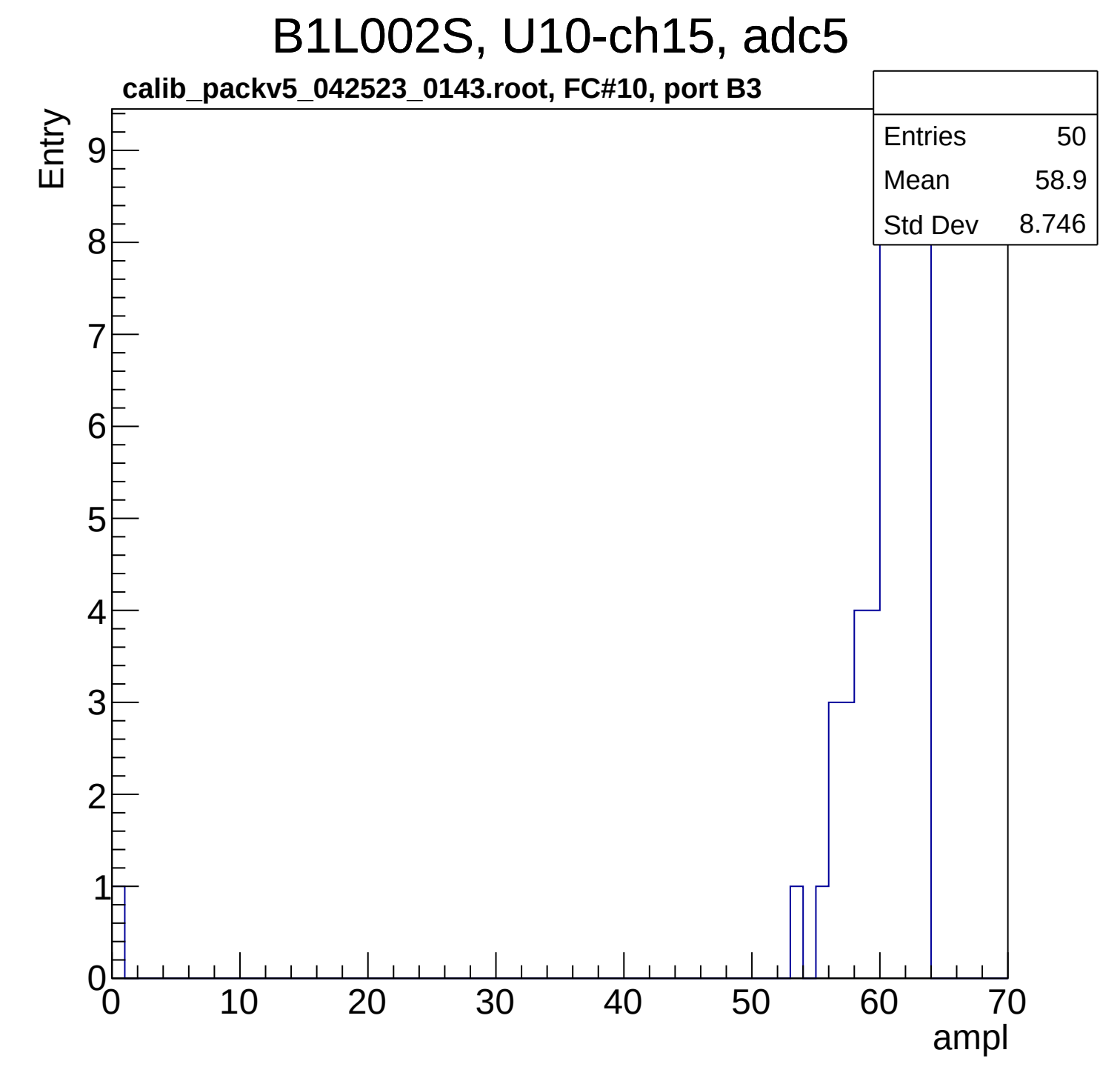
B1L002S, U10-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.9
Std Dev 8.746
Entry
ampl
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2
3
4
5
6
7
8
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0
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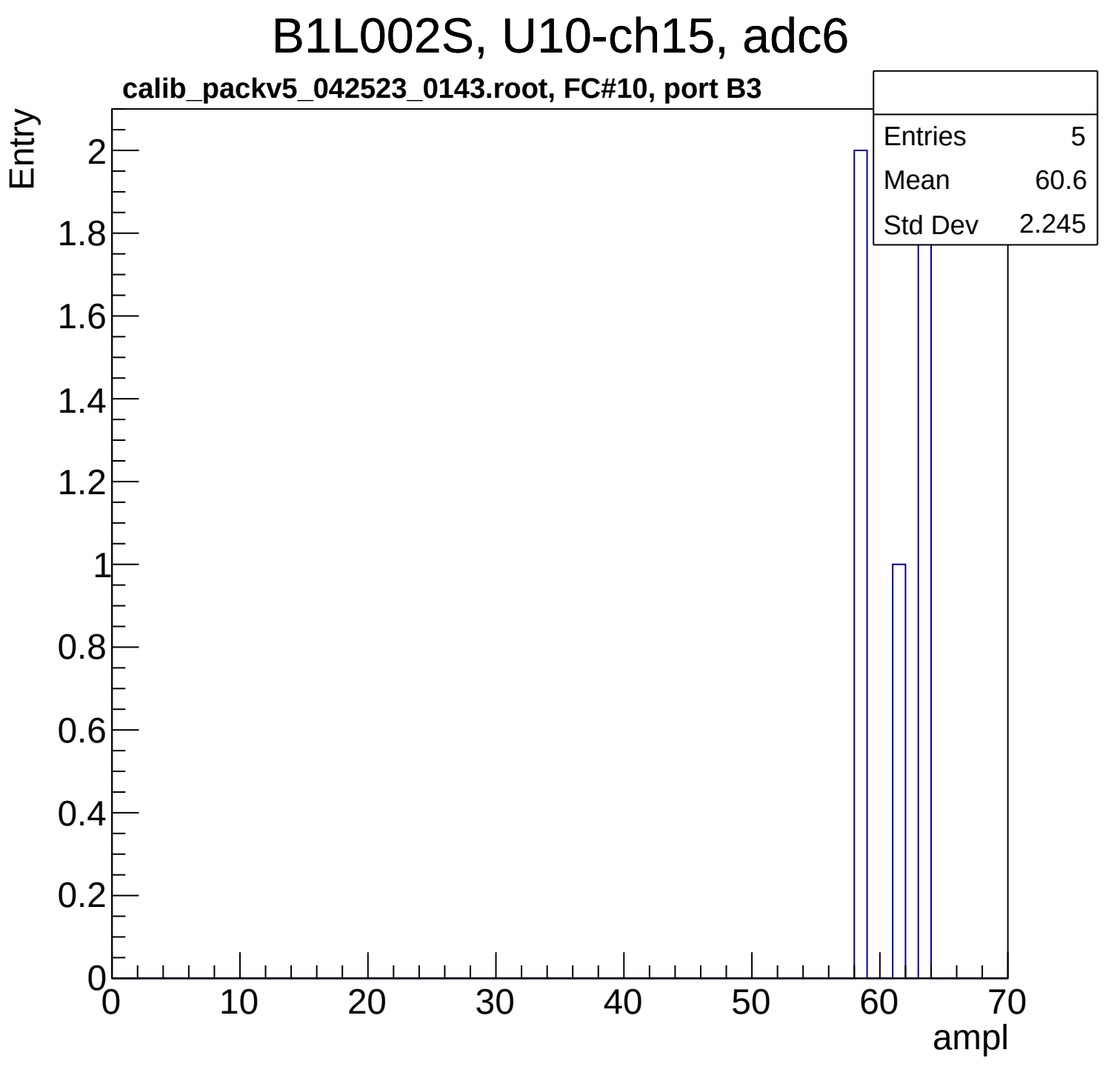
B1L002S, U10-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 60.6
Std Dev 2.245
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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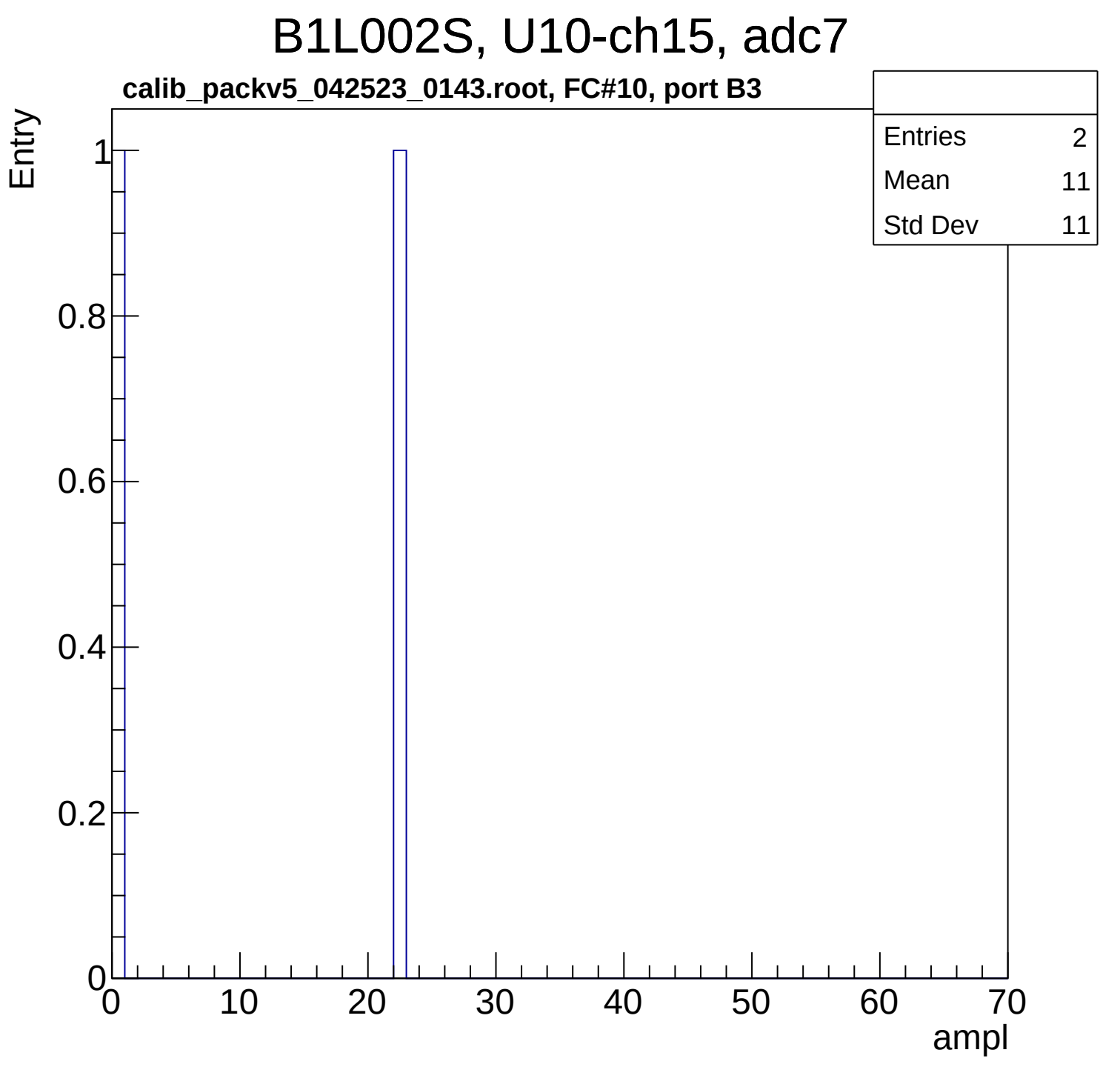
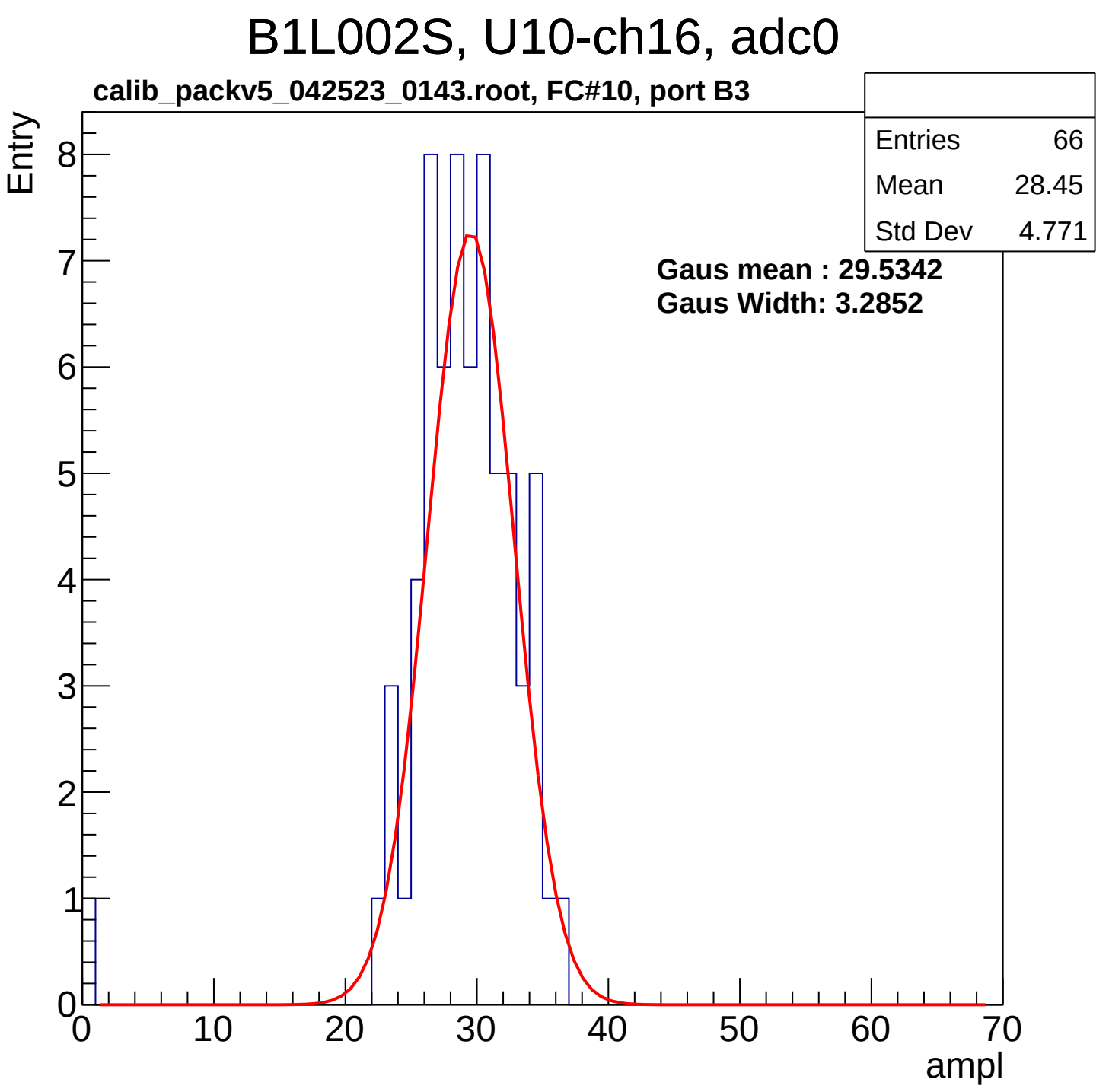

B1L002S, U10-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.45
Std Dev 4.771
Gaus mean : 29.5342
Gaus Width: 3.2852
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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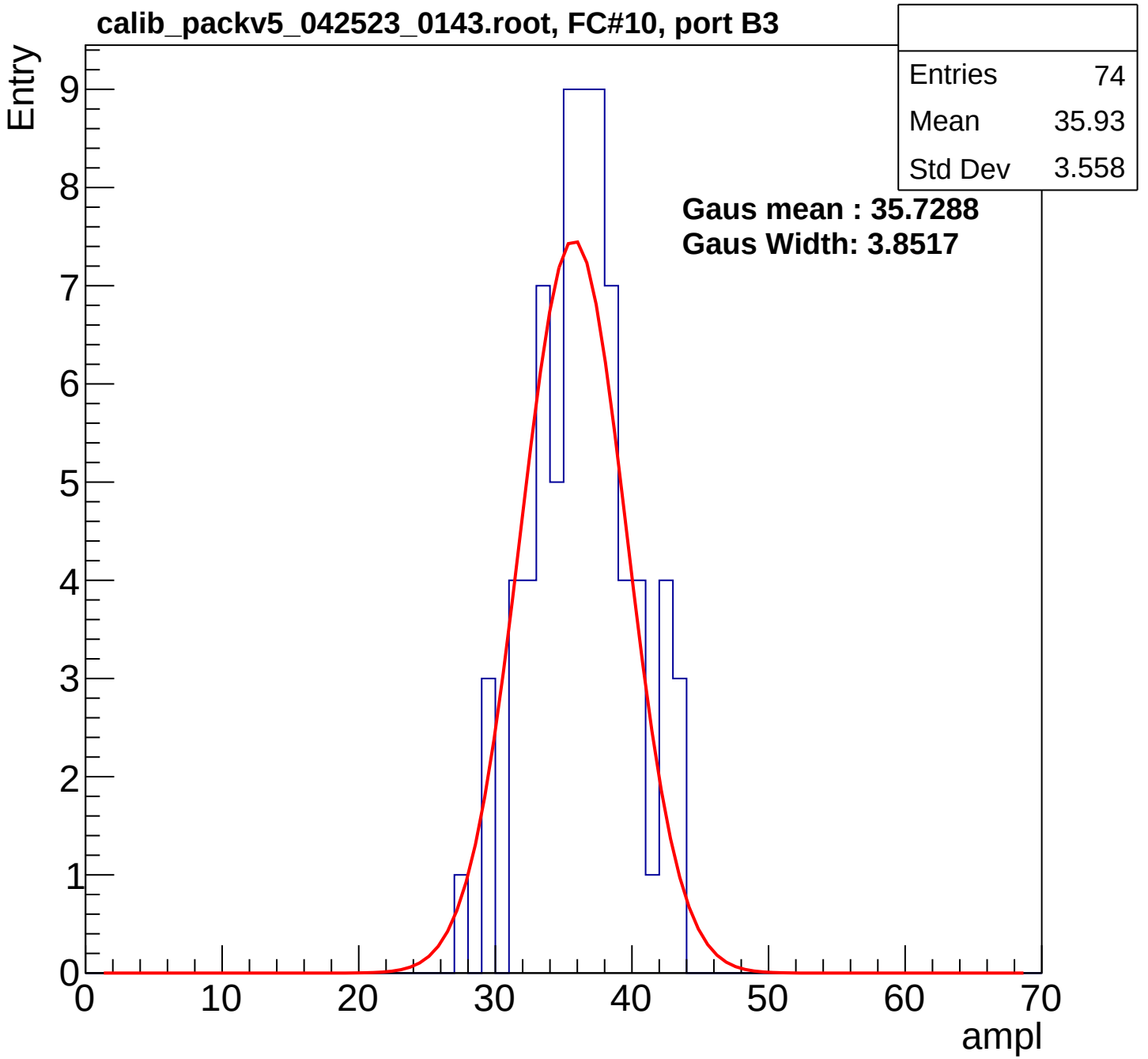
B1L002S, U10-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.93
Std Dev 3.558
Gaus mean : 35.7288
Gaus Width: 3.8517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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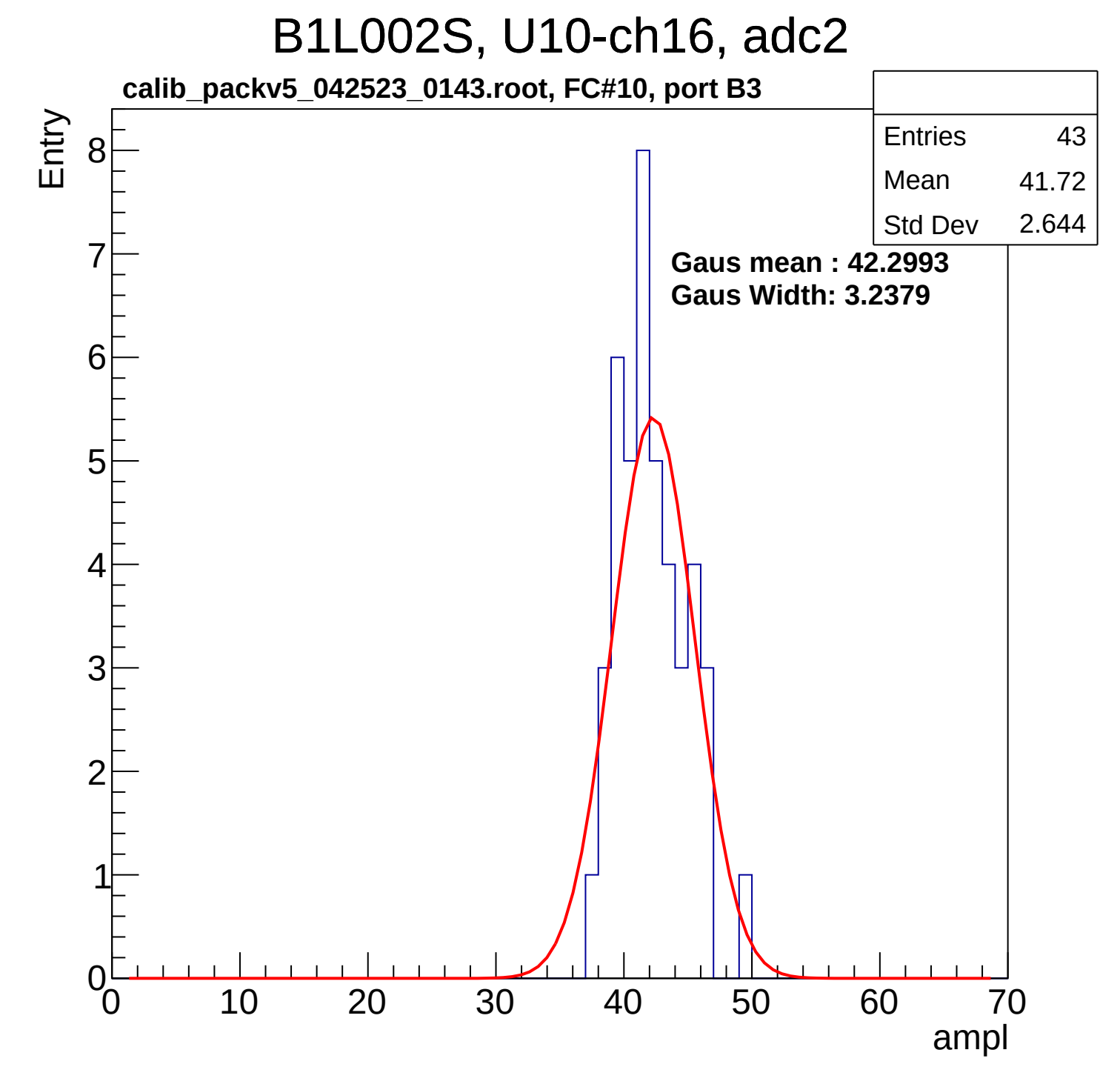
B1L002S, U10-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 41.72
Std Dev 2.644
Gaus mean : 42.2993
Gaus Width: 3.2379
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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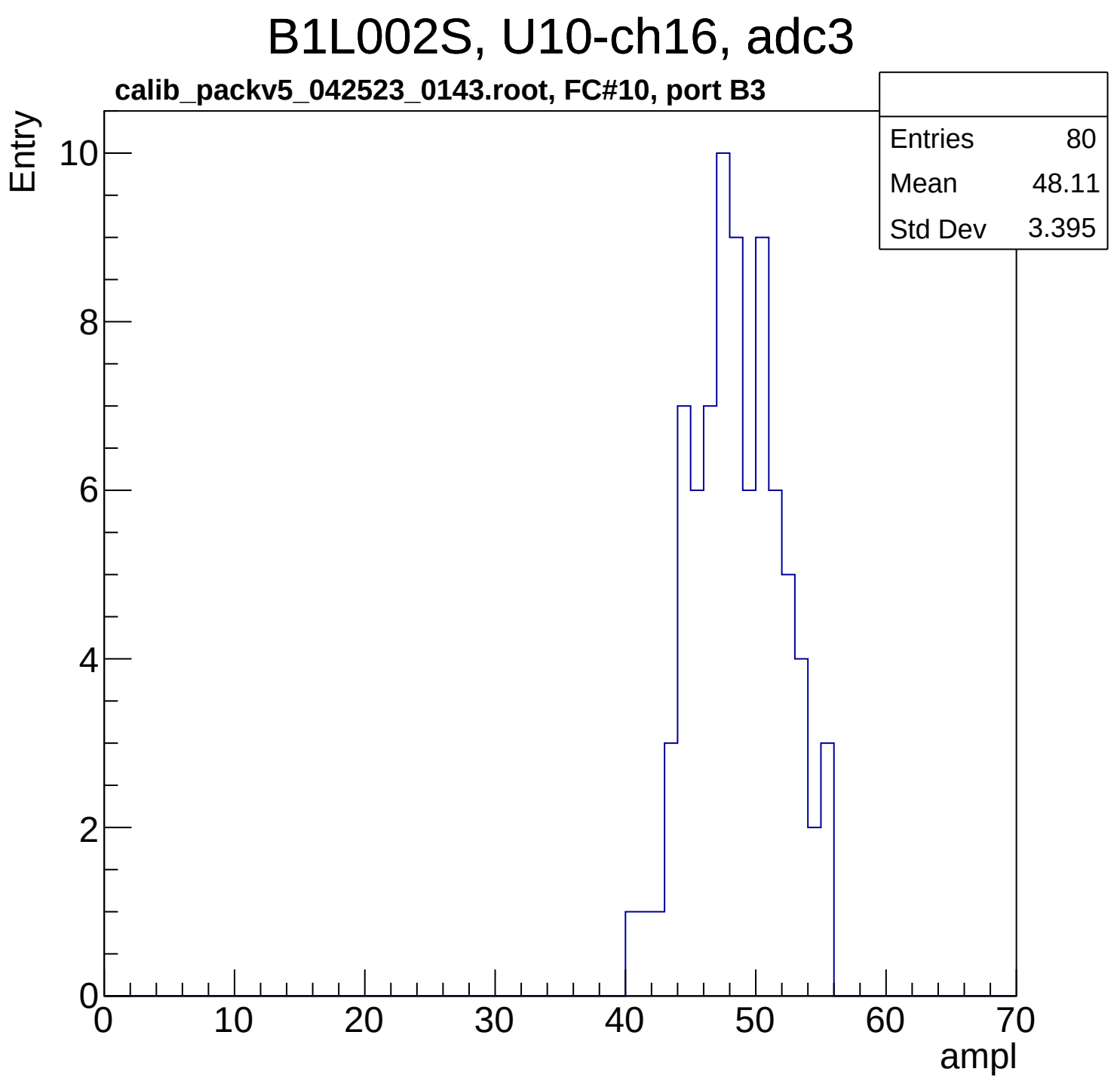
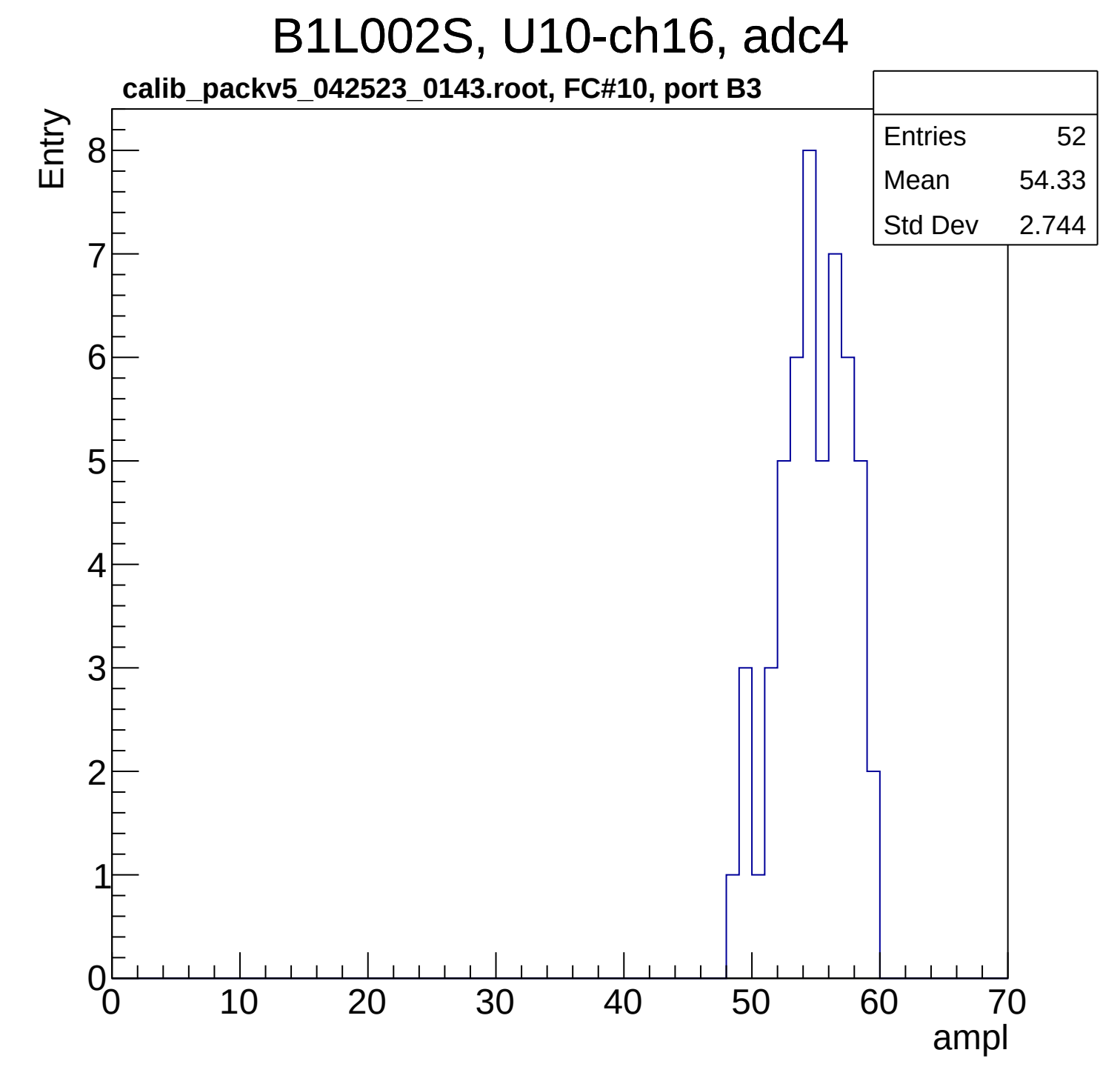

B1L002S, U10-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 48.11
Std Dev 3.395
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 54.33
Std Dev 2.744
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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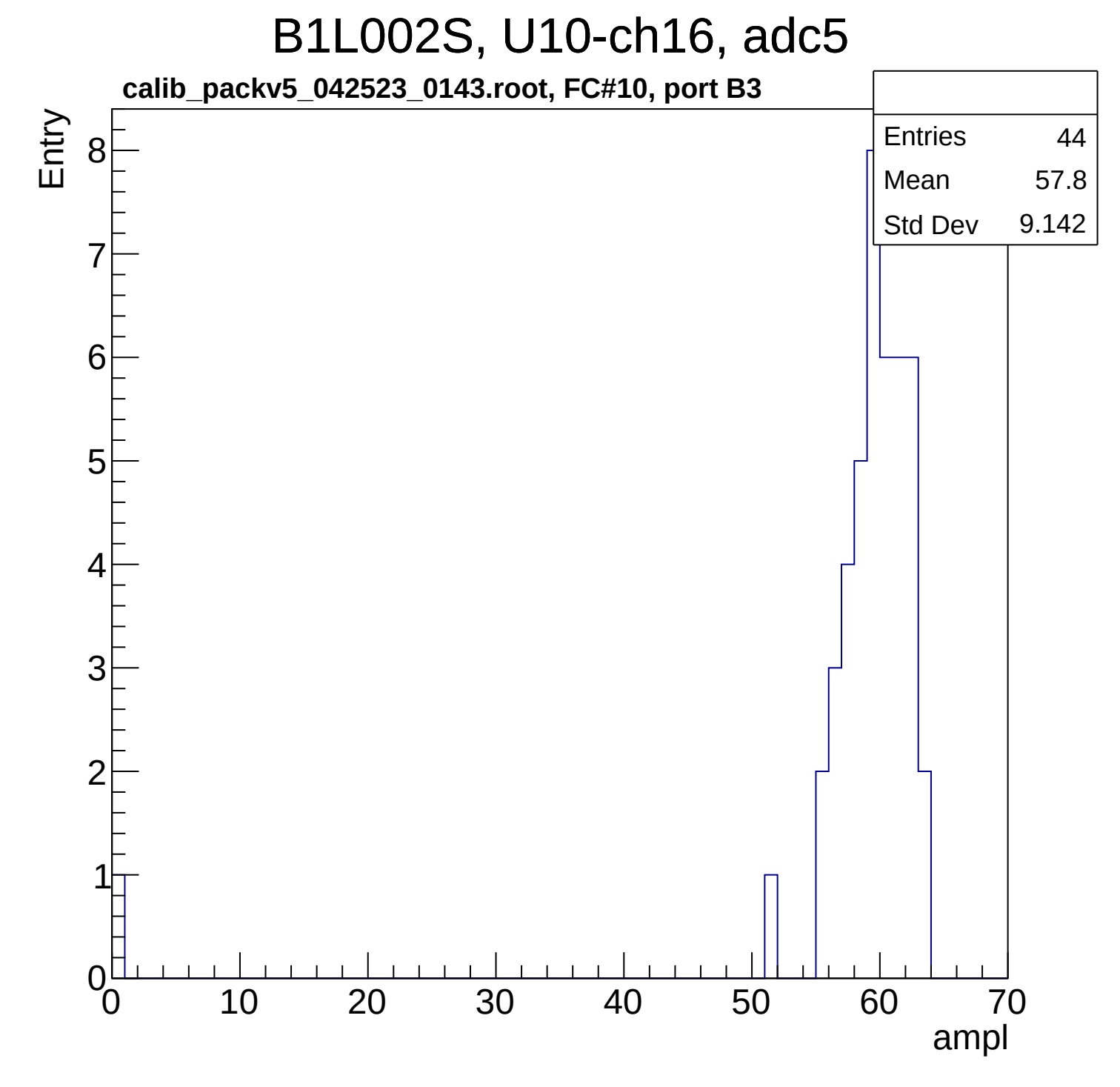
B1L002S, U10-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.8
Std Dev 9.142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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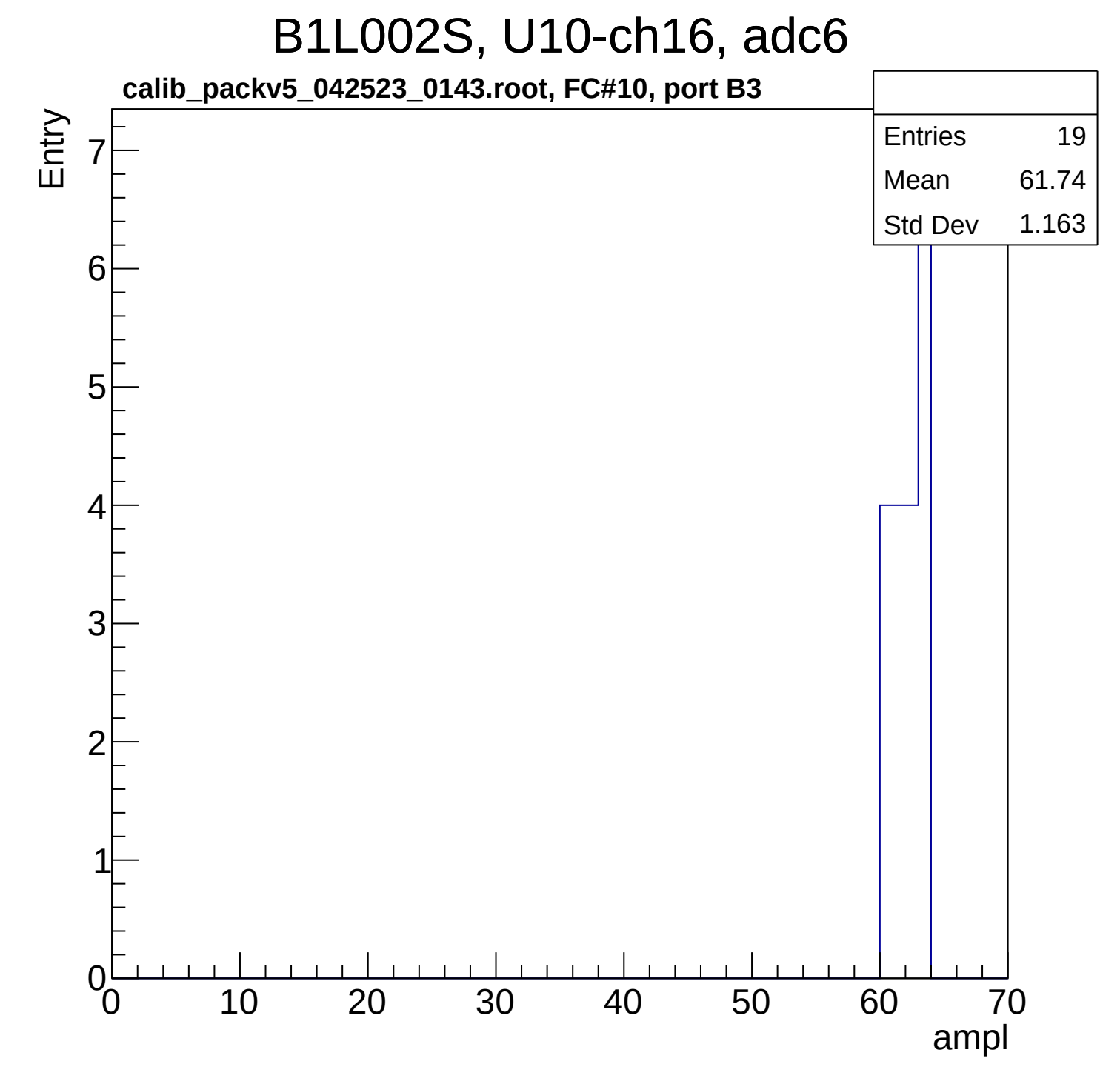
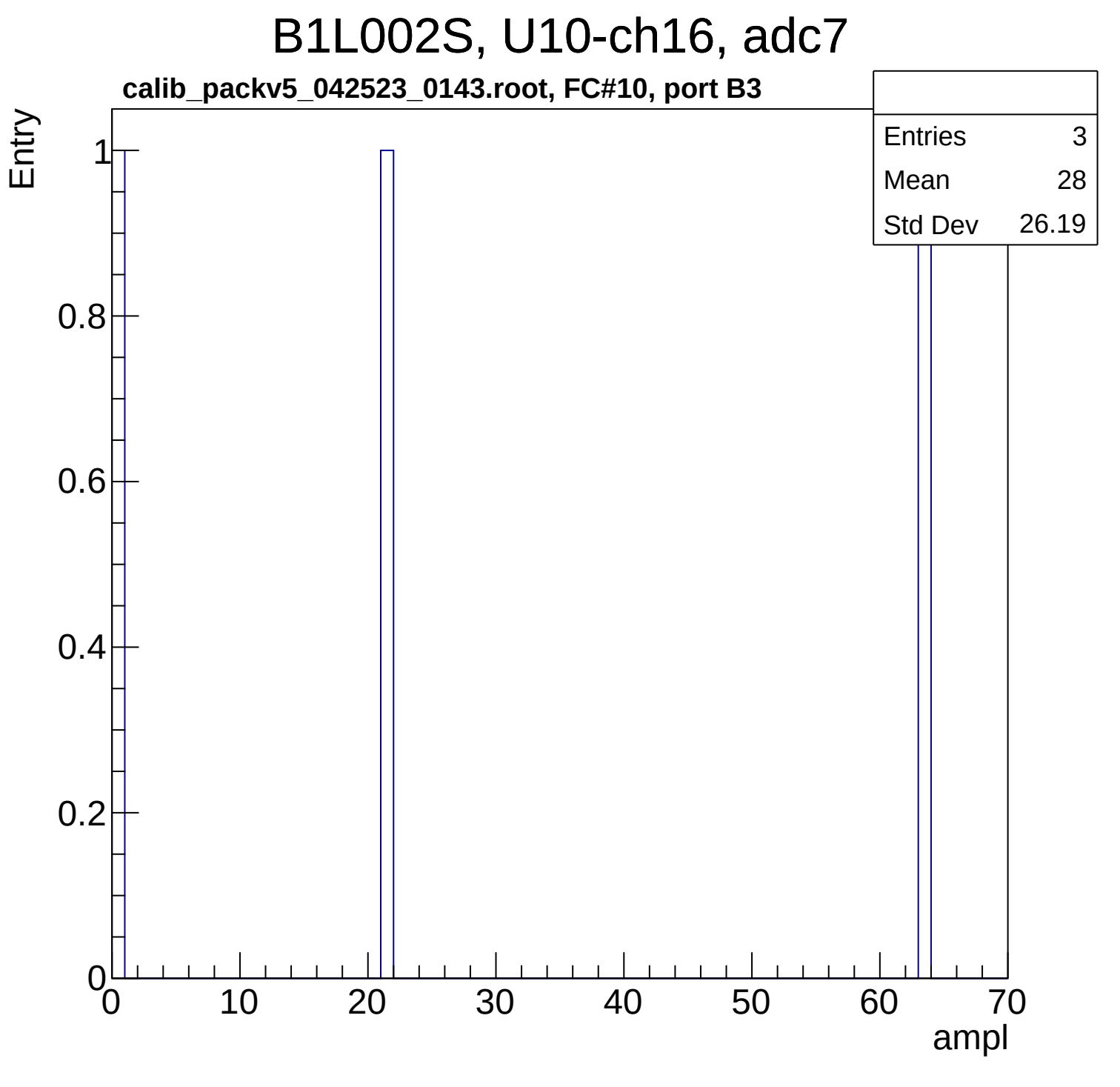

B1L002S, U10-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.74
Std Dev 1.163
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
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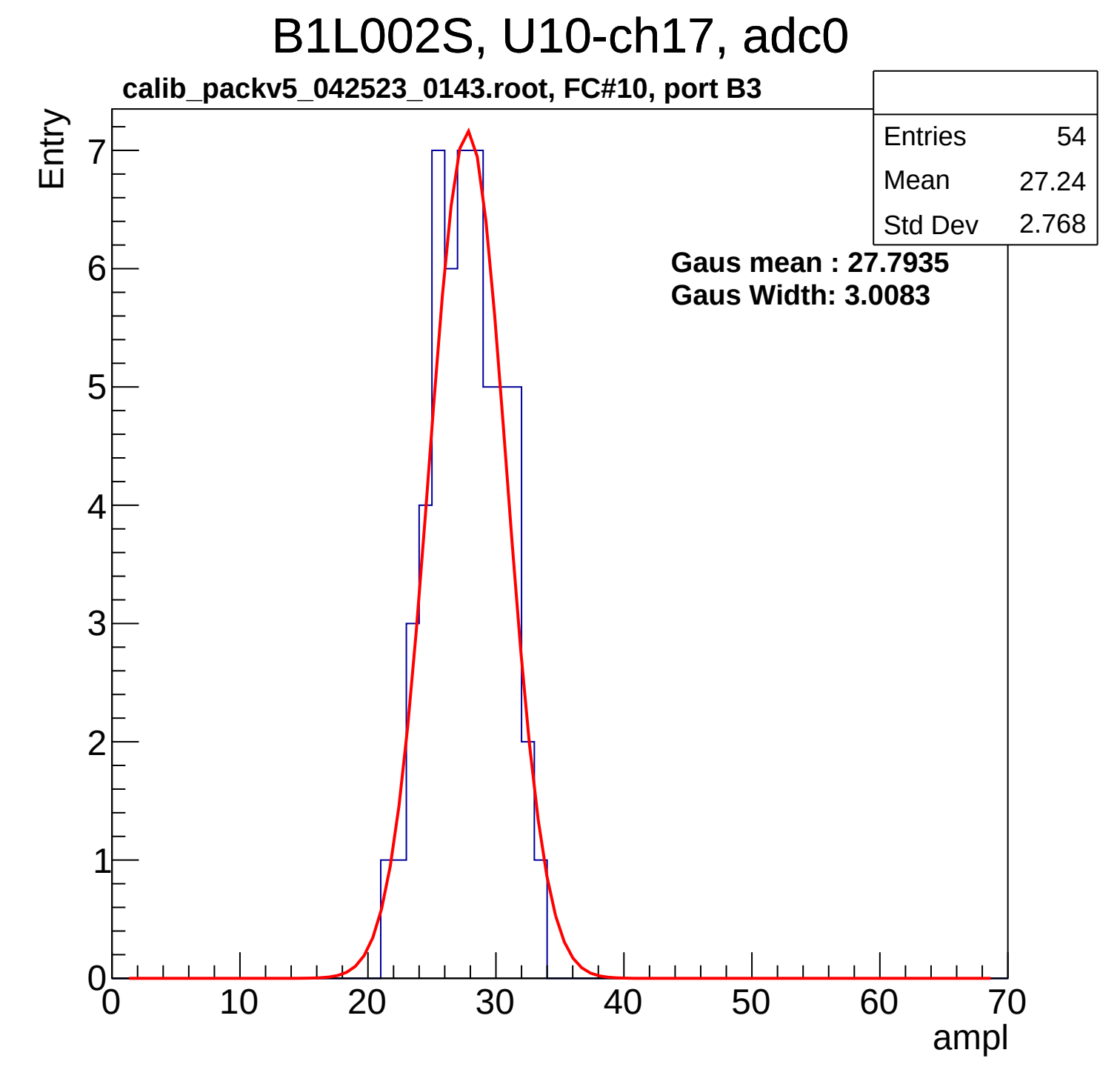
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U10-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.24
Std Dev 2.768
Gaus mean : 27.7935
Gaus Width: 3.0083
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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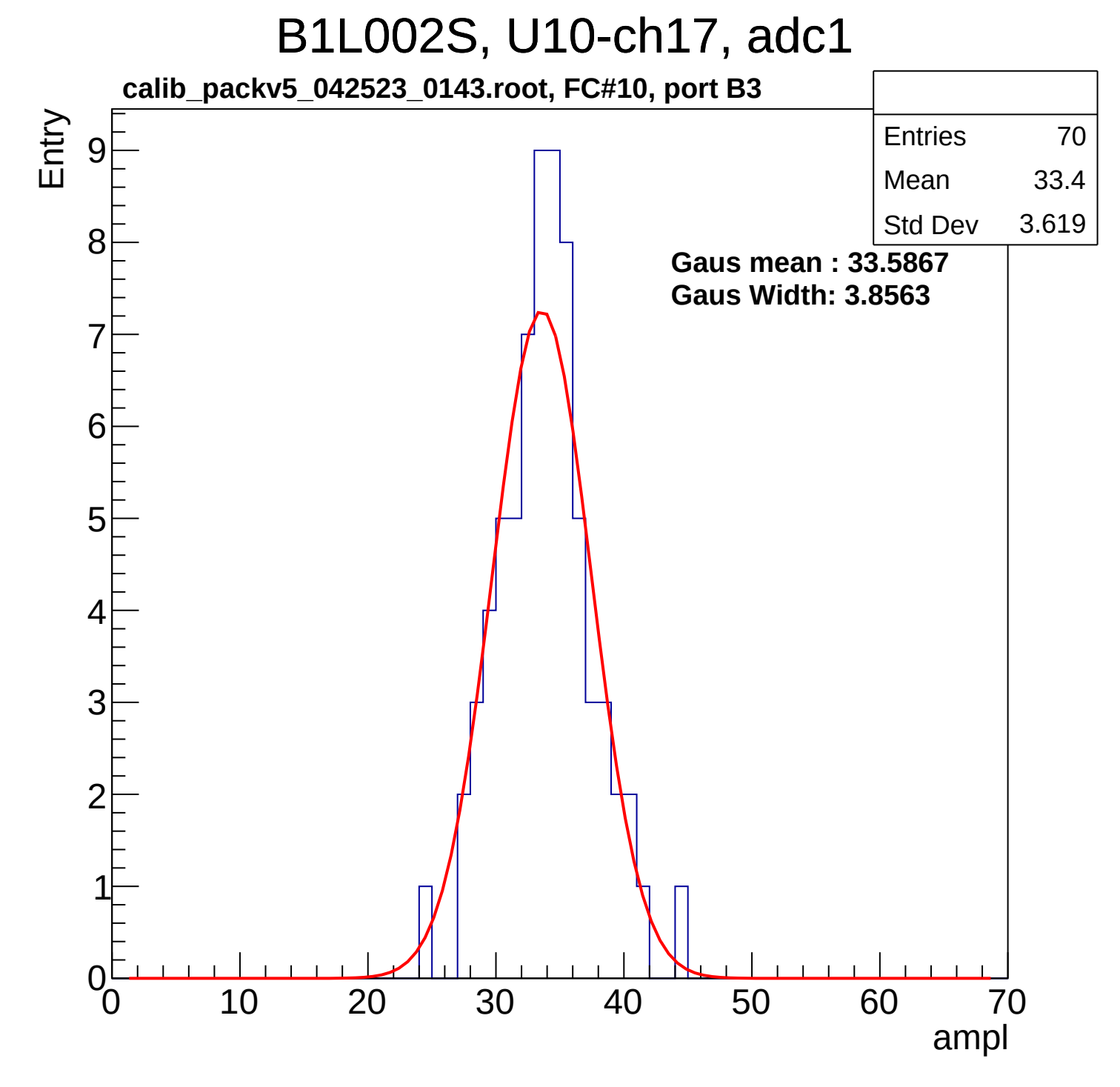
B1L002S, U10-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.4
Std Dev 3.619
Gaus mean : 33.5867
Gaus Width: 3.8563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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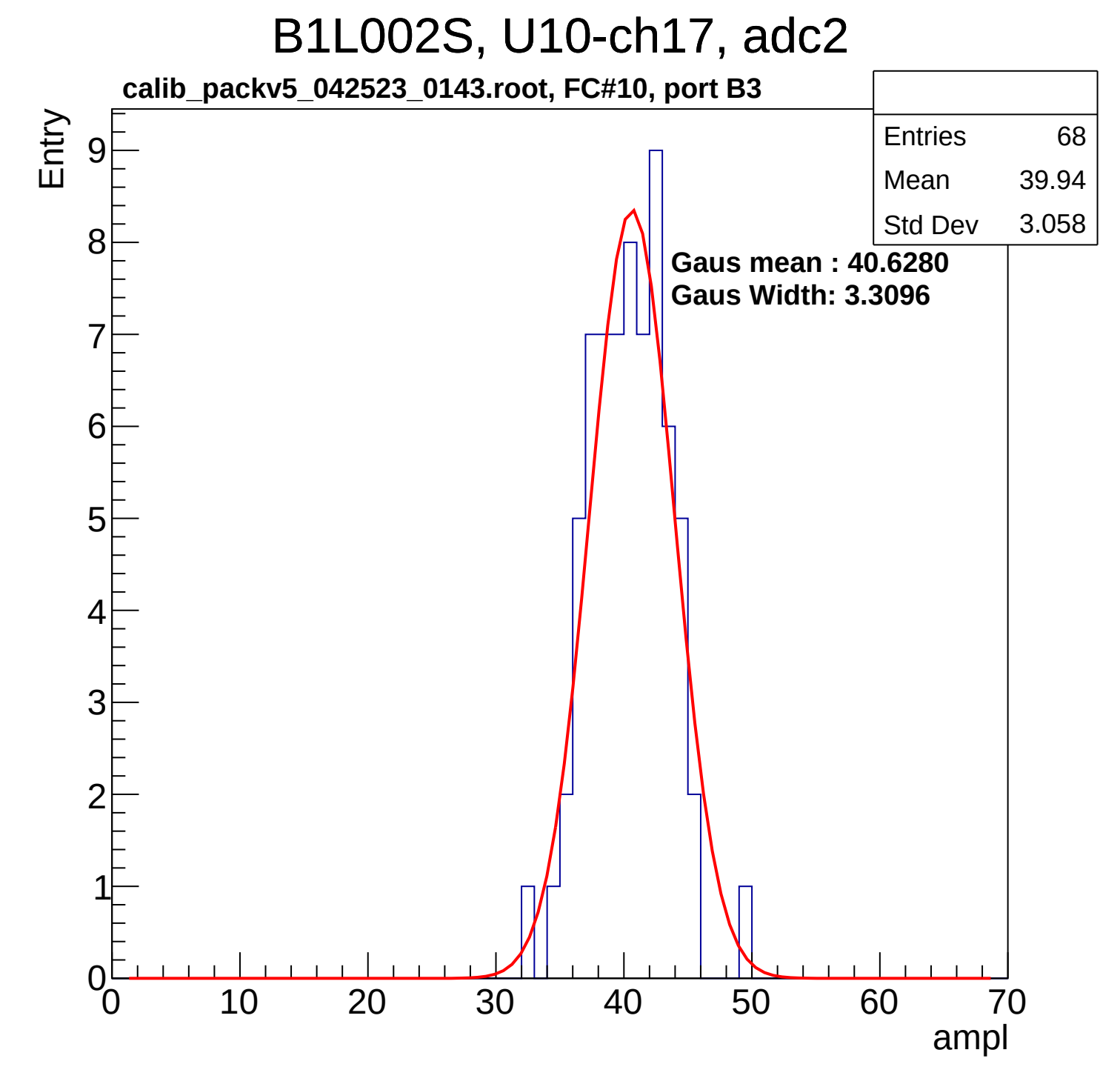
B1L002S, U10-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 39.94
Std Dev 3.058
Gaus mean : 40.6280
Gaus Width: 3.3096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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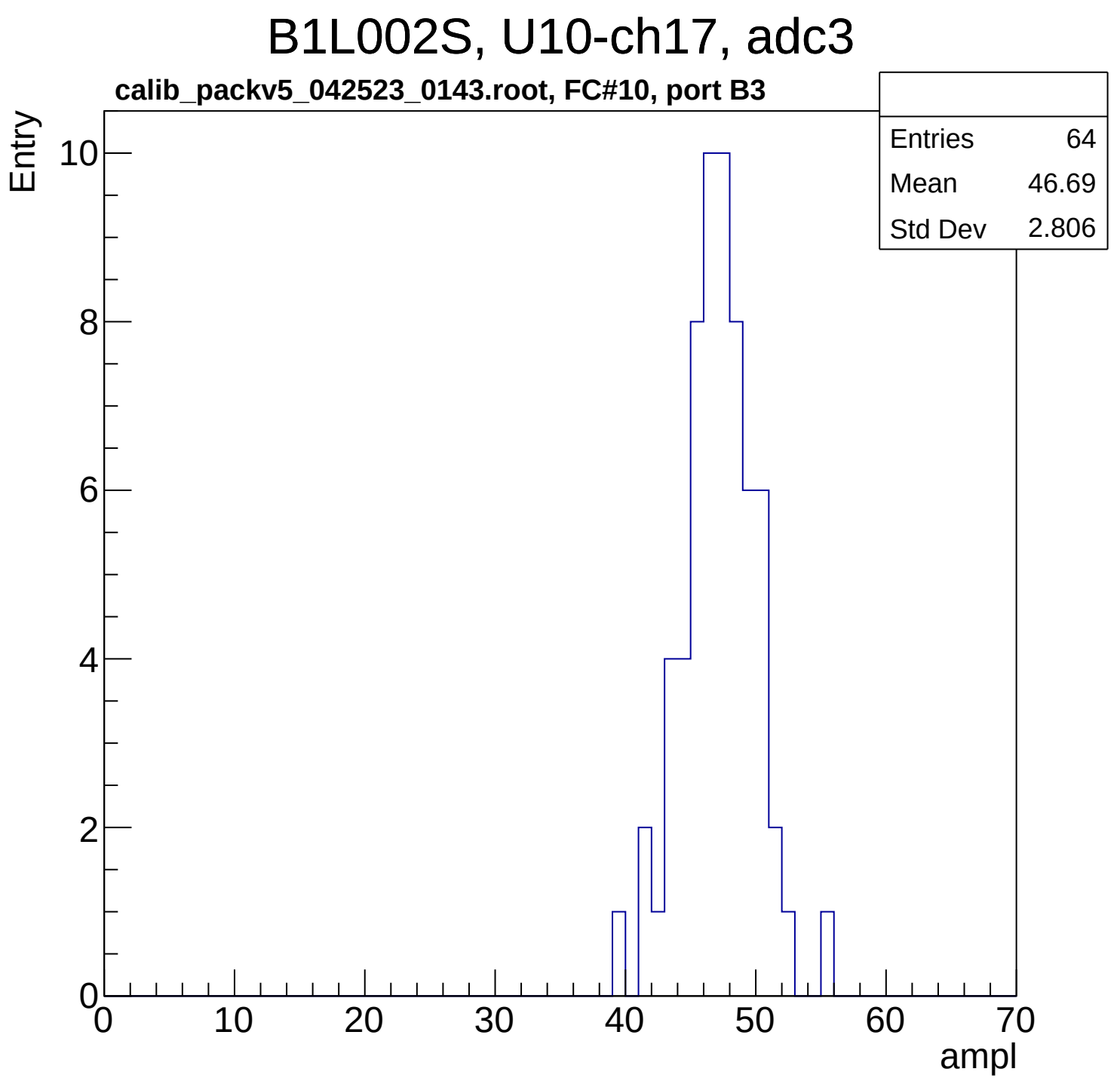
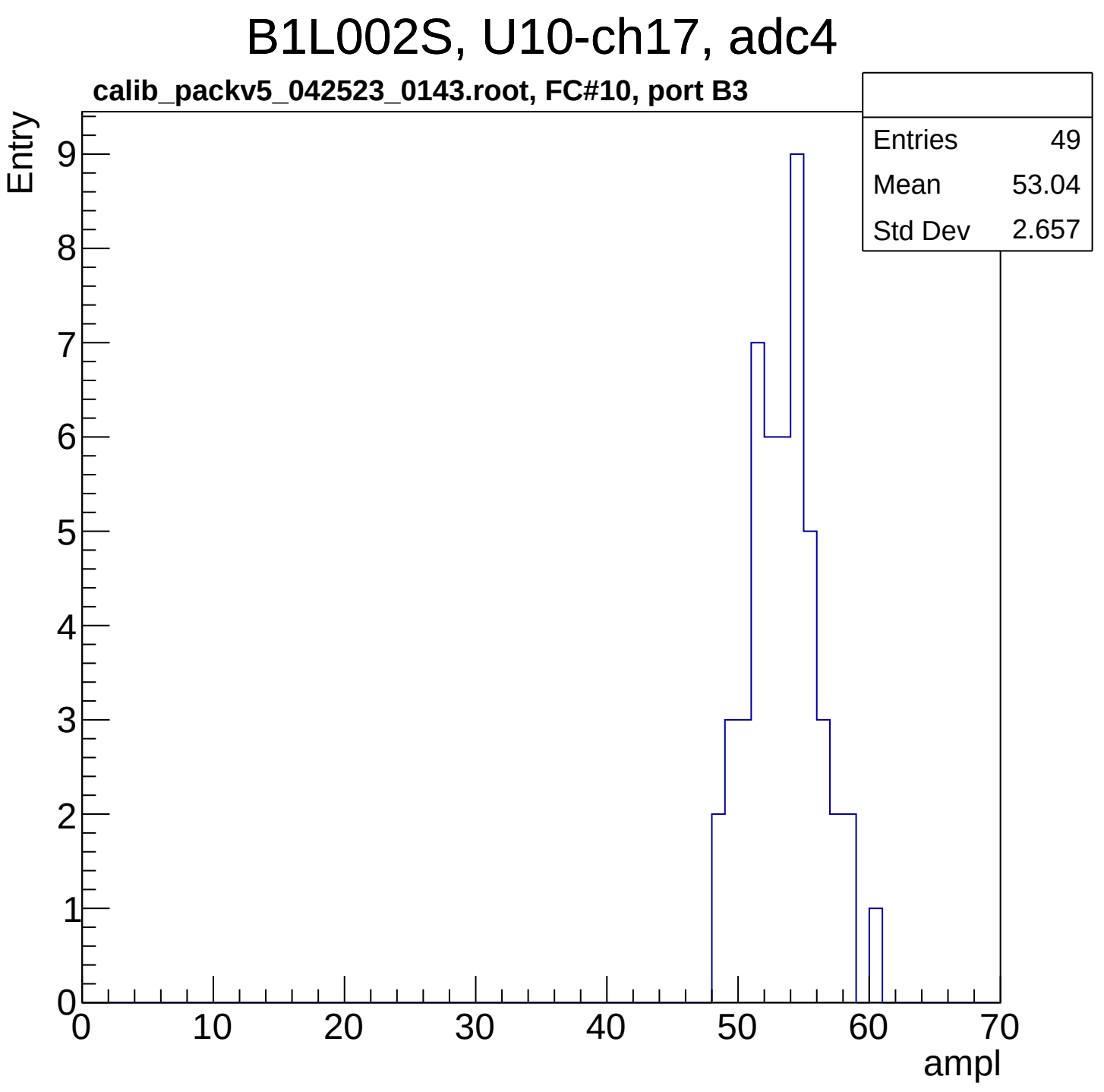

B1L002S, U10-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.69
Std Dev 2.806
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.04
Std Dev 2.657
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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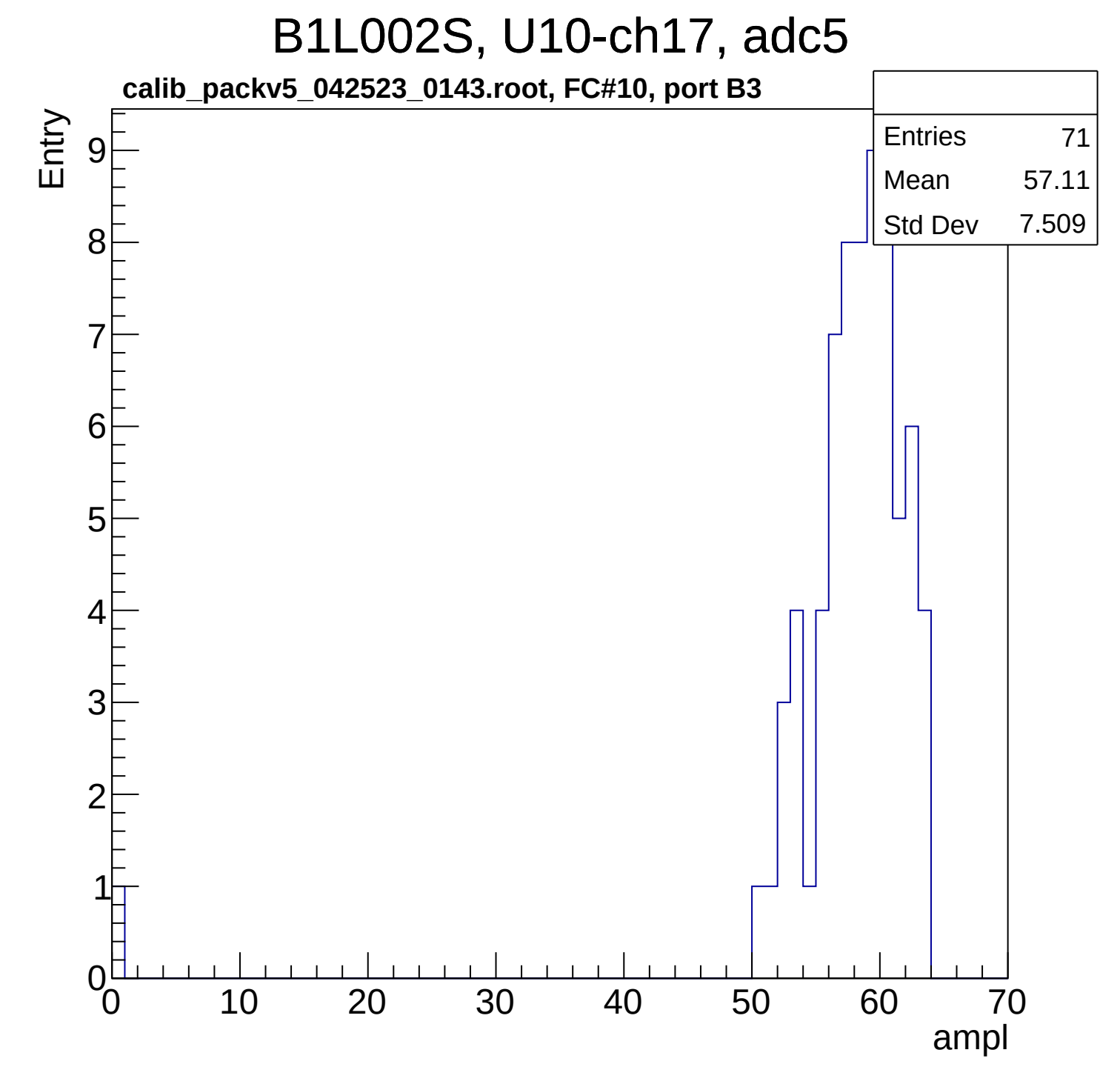
B1L002S, U10-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.11
Std Dev 7.509
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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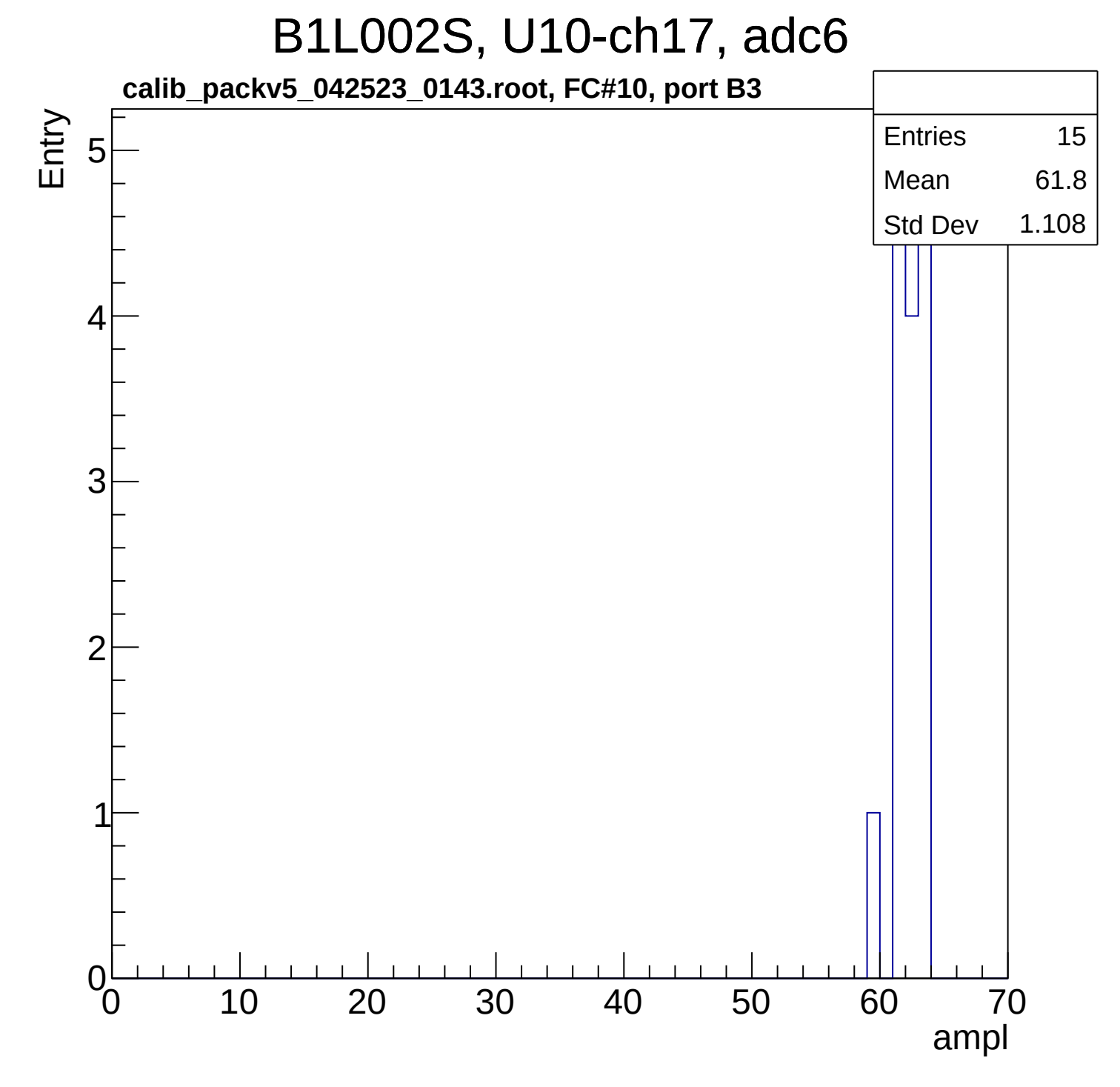
B1L002S, U10-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.8
Std Dev 1.108
Entry
0
1
2
3
4
5
0
10
20
30
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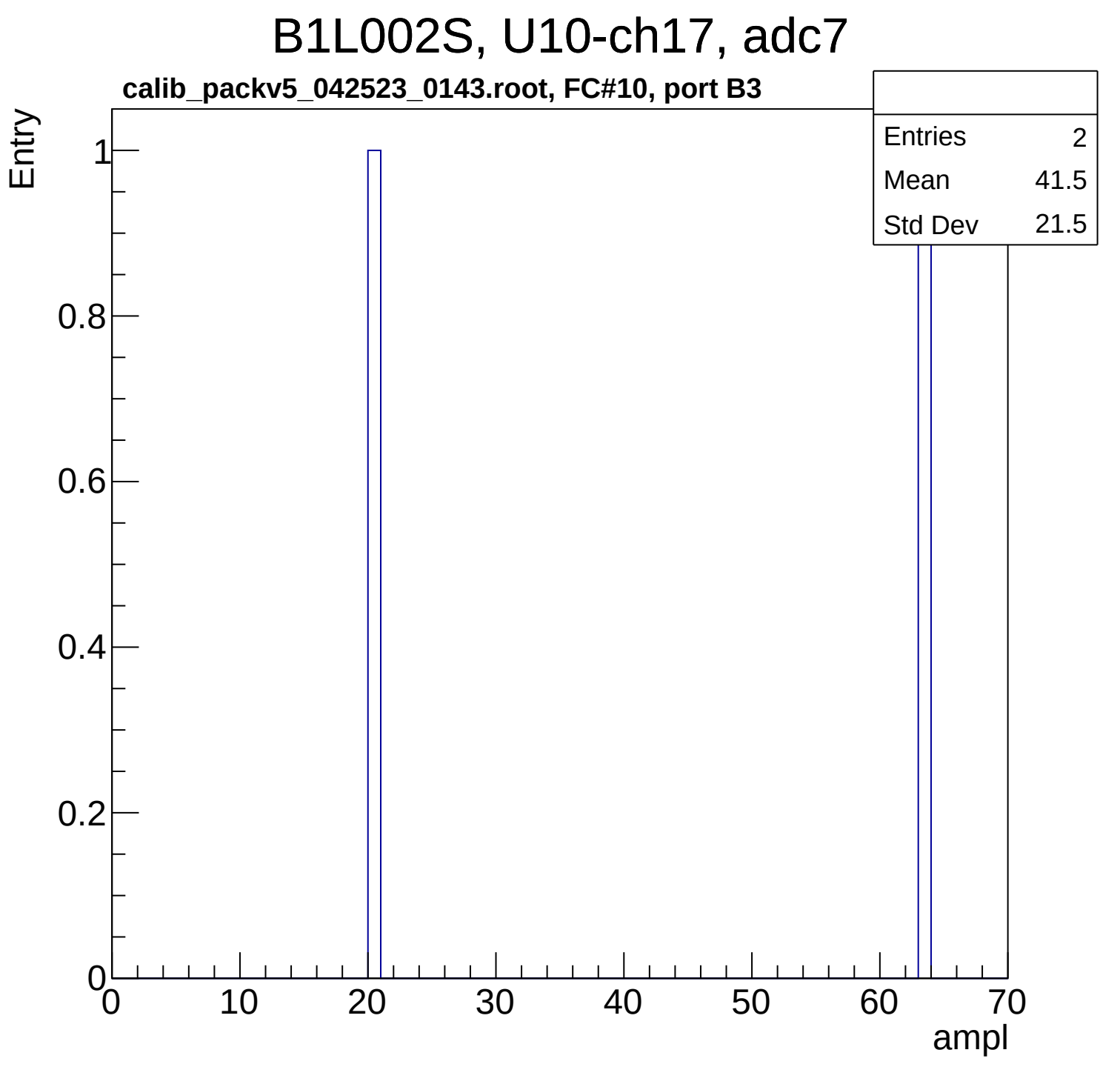
50
60
70
ampl


B1L002S, U10-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
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0.6
0.4
0.2
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0
10
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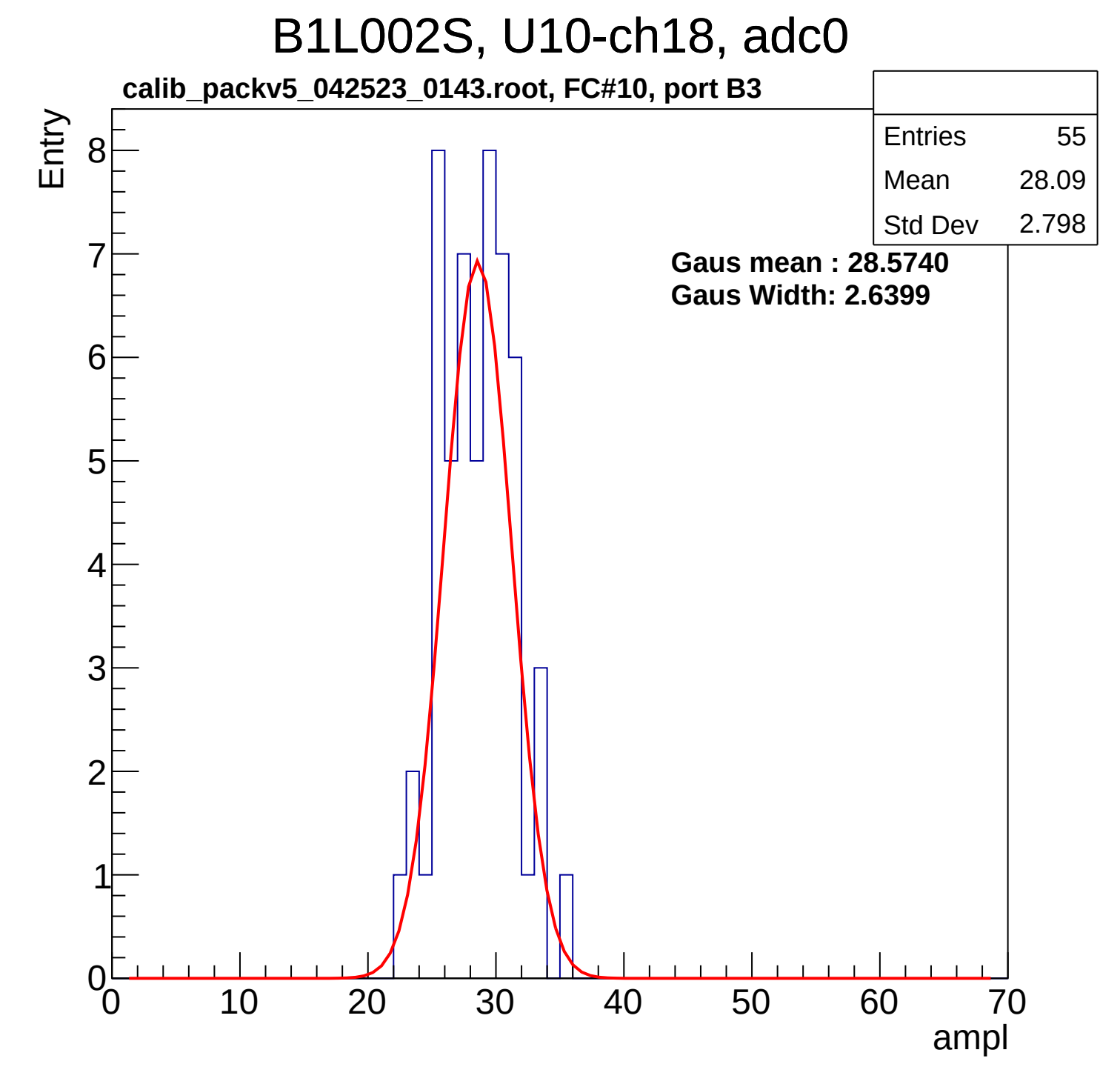
30
40
50
60
70
ampl


B1L002S, U10-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.09
Std Dev 2.798
Gaus mean : 28.5740
Gaus Width: 2.6399
Entry
ampl
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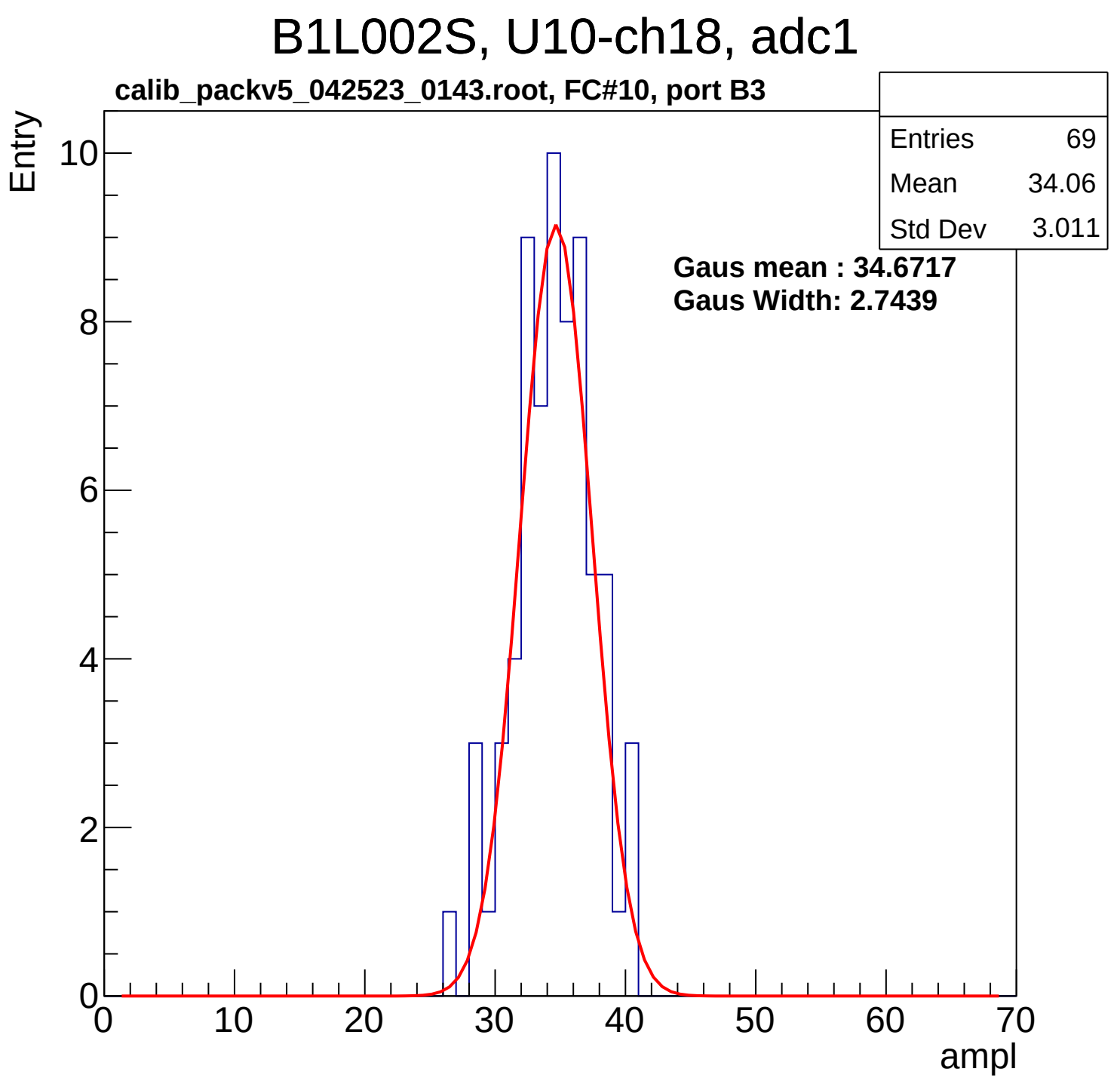
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.06
Std Dev 3.011
Gaus mean : 34.6717
Gaus Width: 2.7439
Entry
ampl
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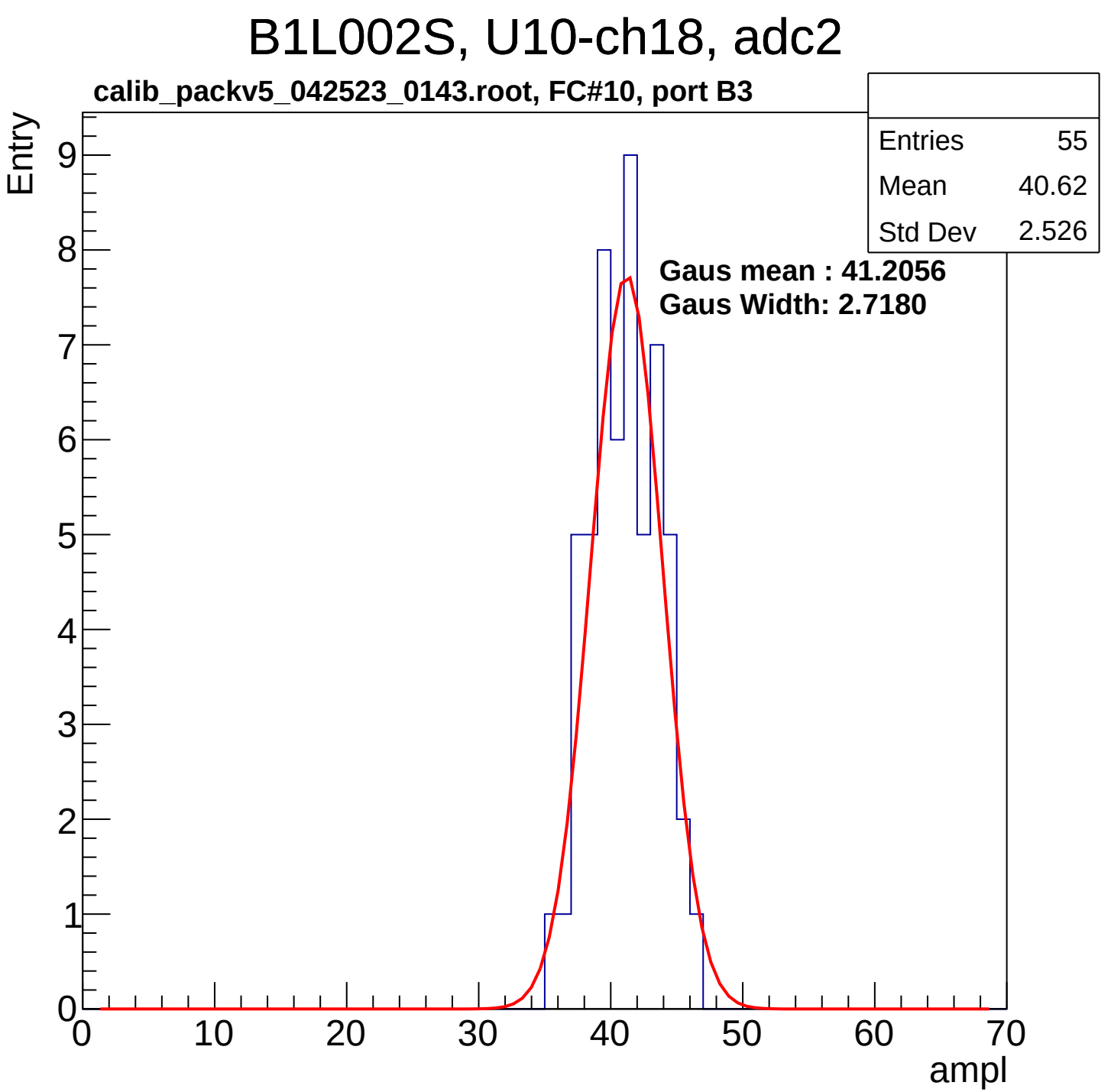
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.62
Std Dev 2.526
Gaus mean : 41.2056
Gaus Width: 2.7180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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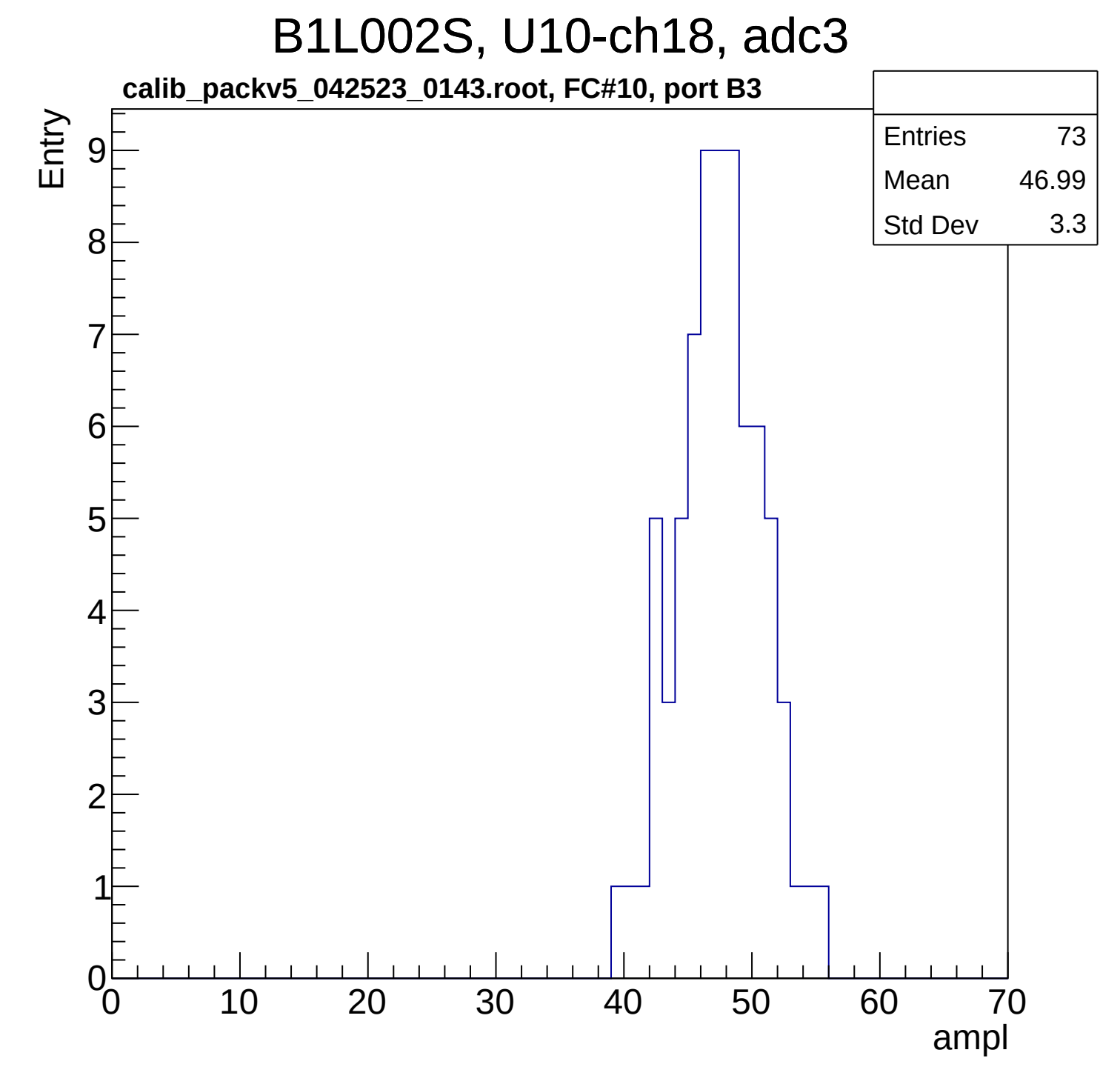
B1L002S, U10-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 46.99
Std Dev 3.3
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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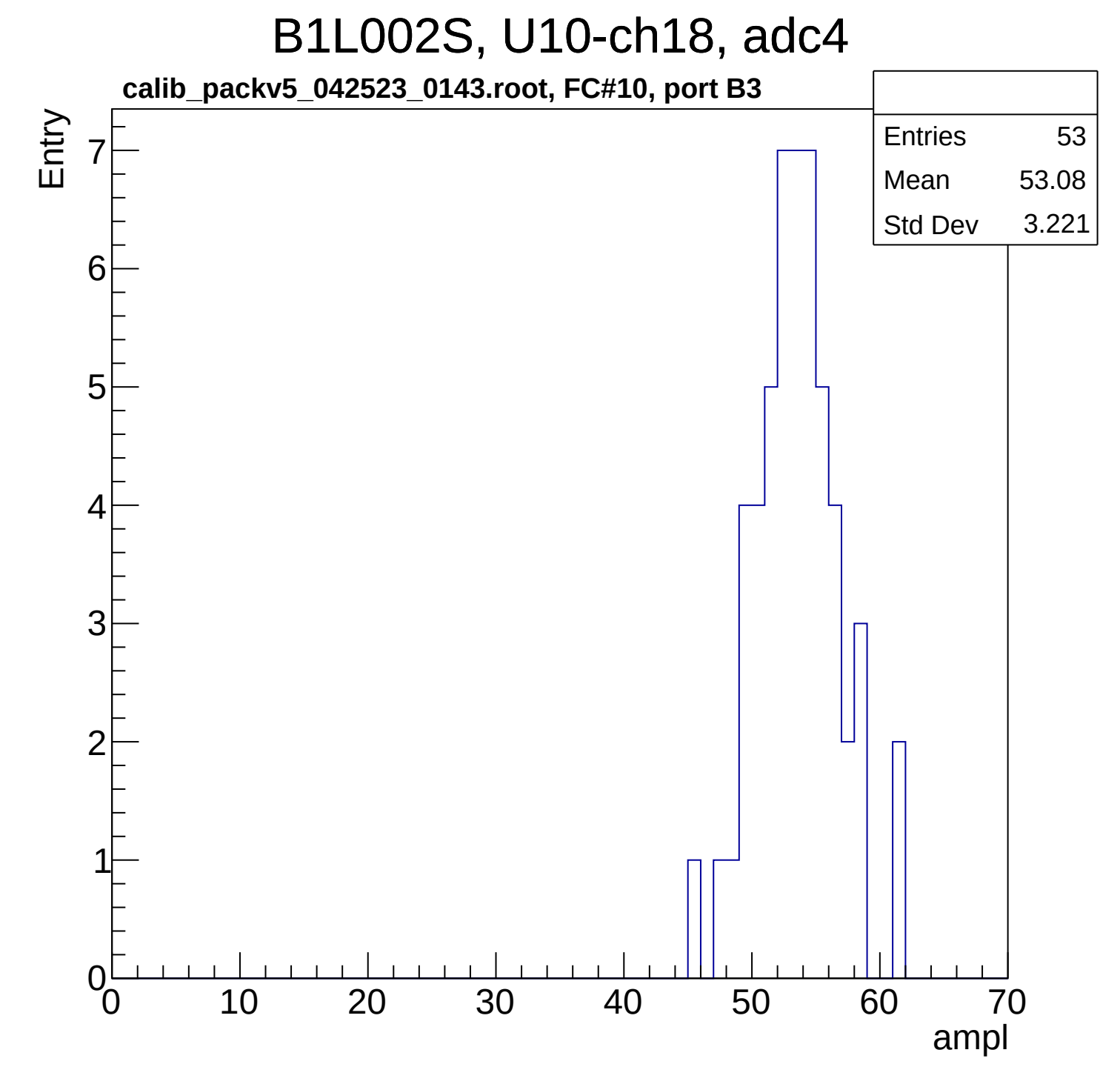
B1L002S, U10-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.08
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
0
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40
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60
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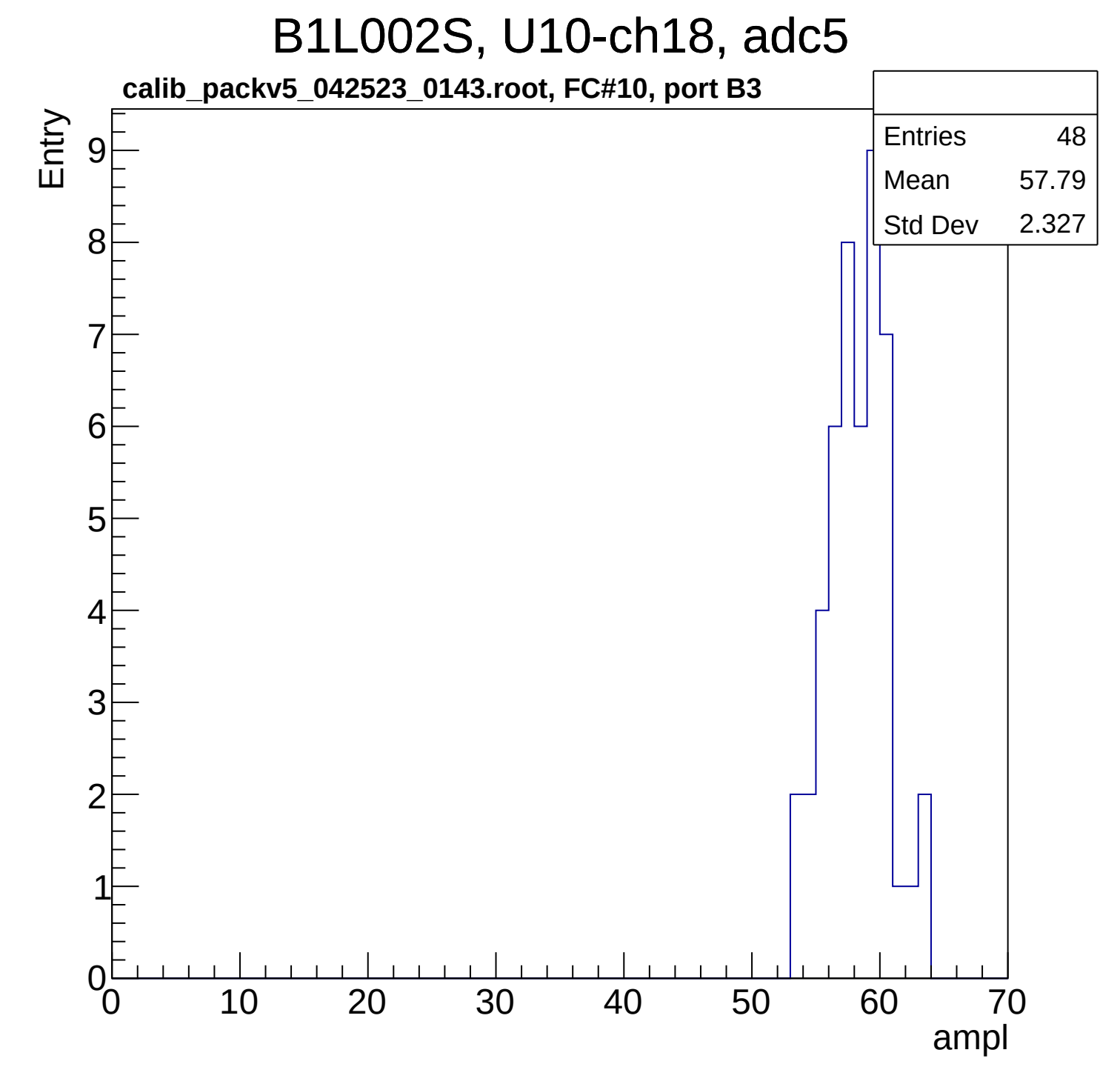
B1L002S, U10-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.79
Std Dev 2.327
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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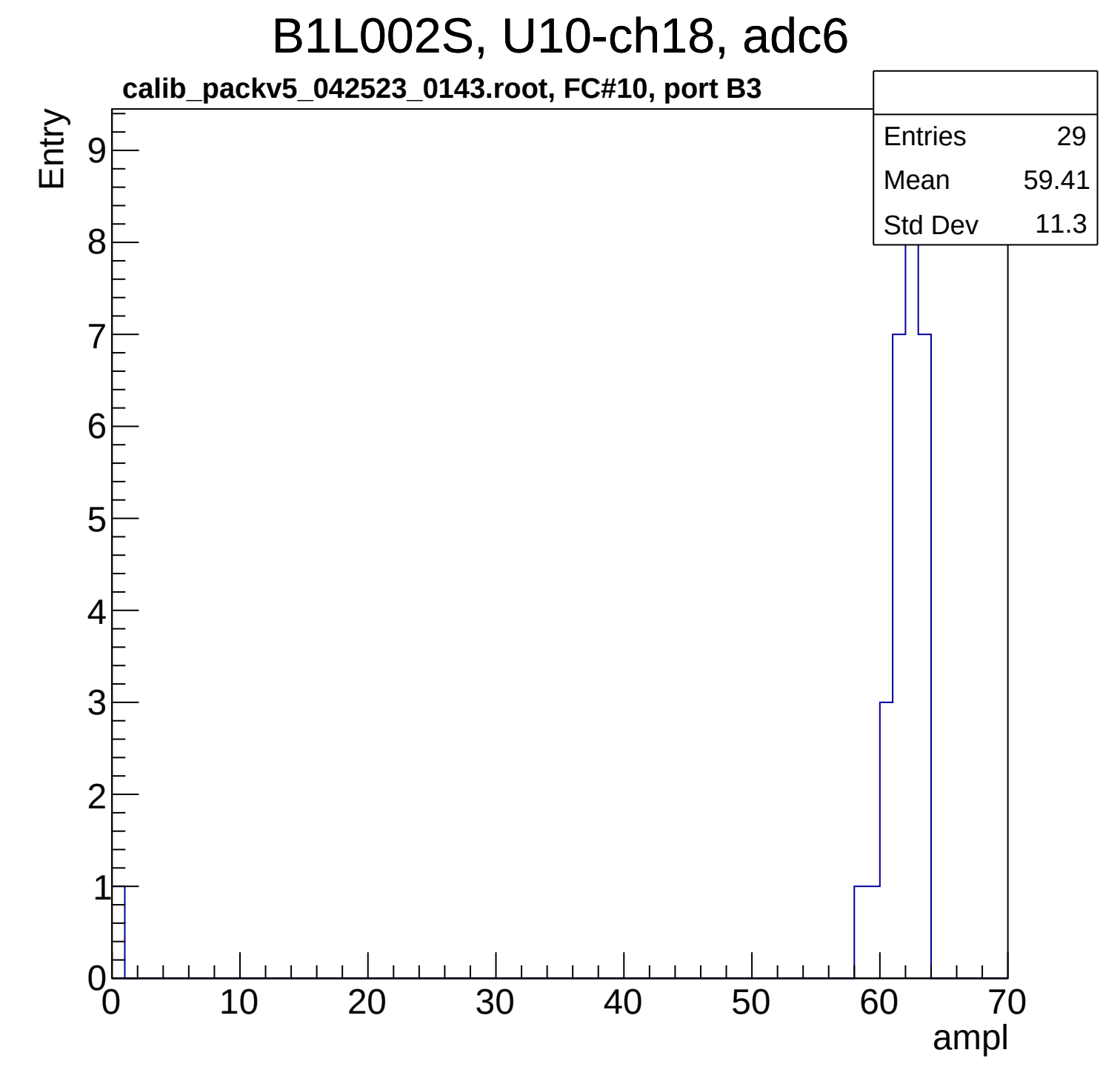
B1L002S, U10-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.41
Std Dev 11.3
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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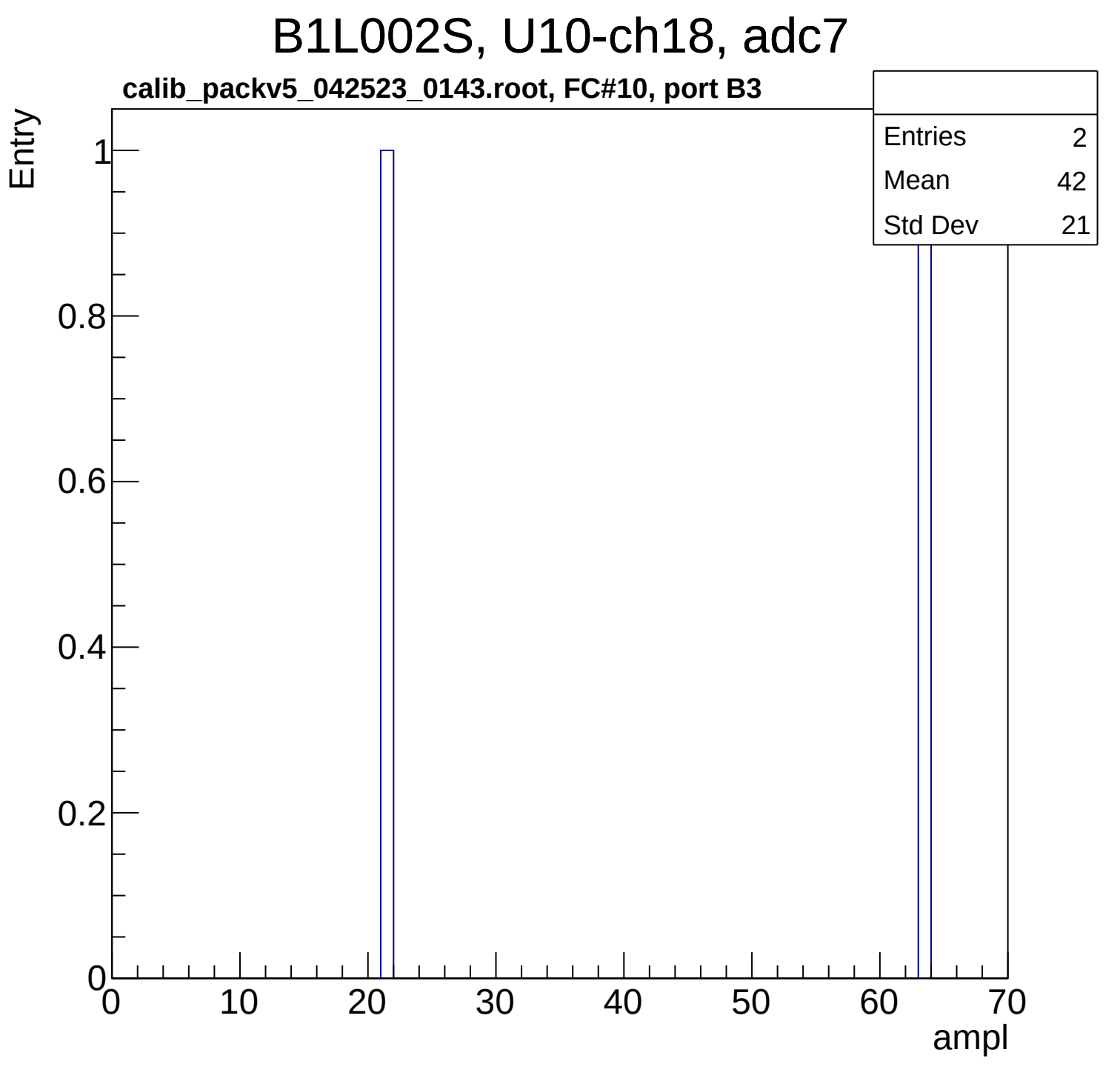
B1L002S, U10-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
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0.6
0.4
0.2
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10
20
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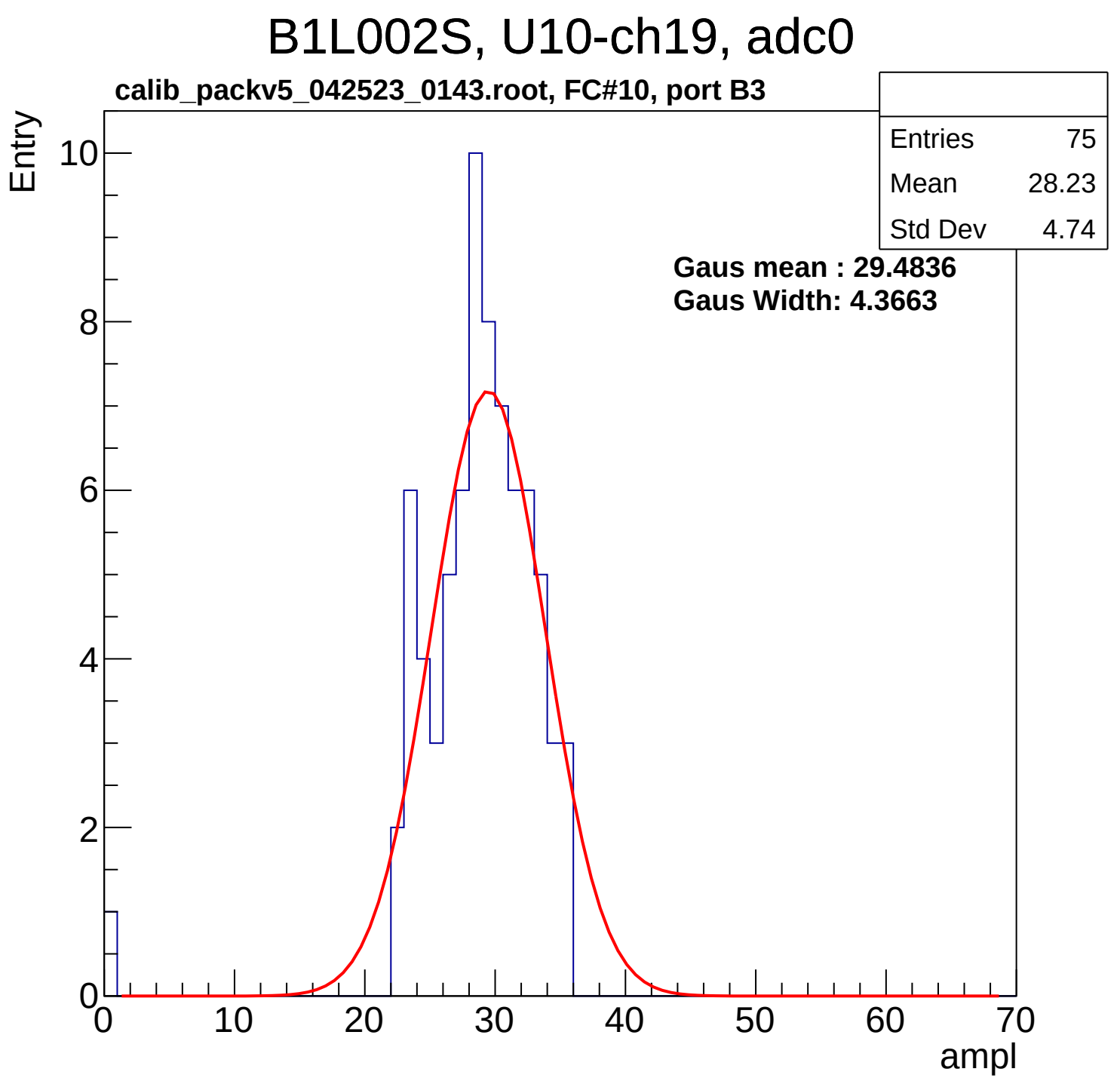
50
60
70
ampl


B1L002S, U10-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.23
Std Dev 4.74
Gaus mean : 29.4836
Gaus Width: 4.3663
Entry
ampl
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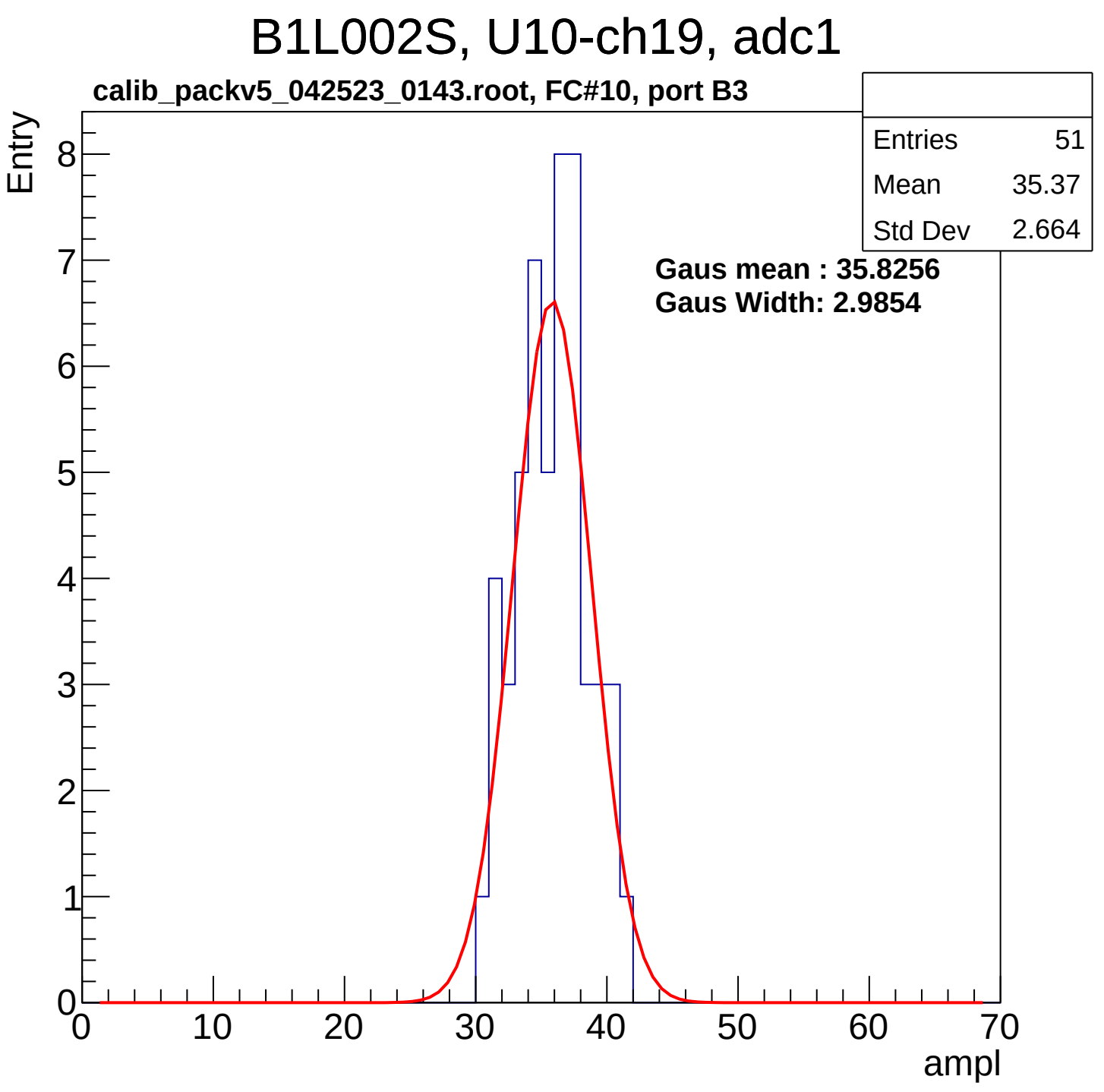
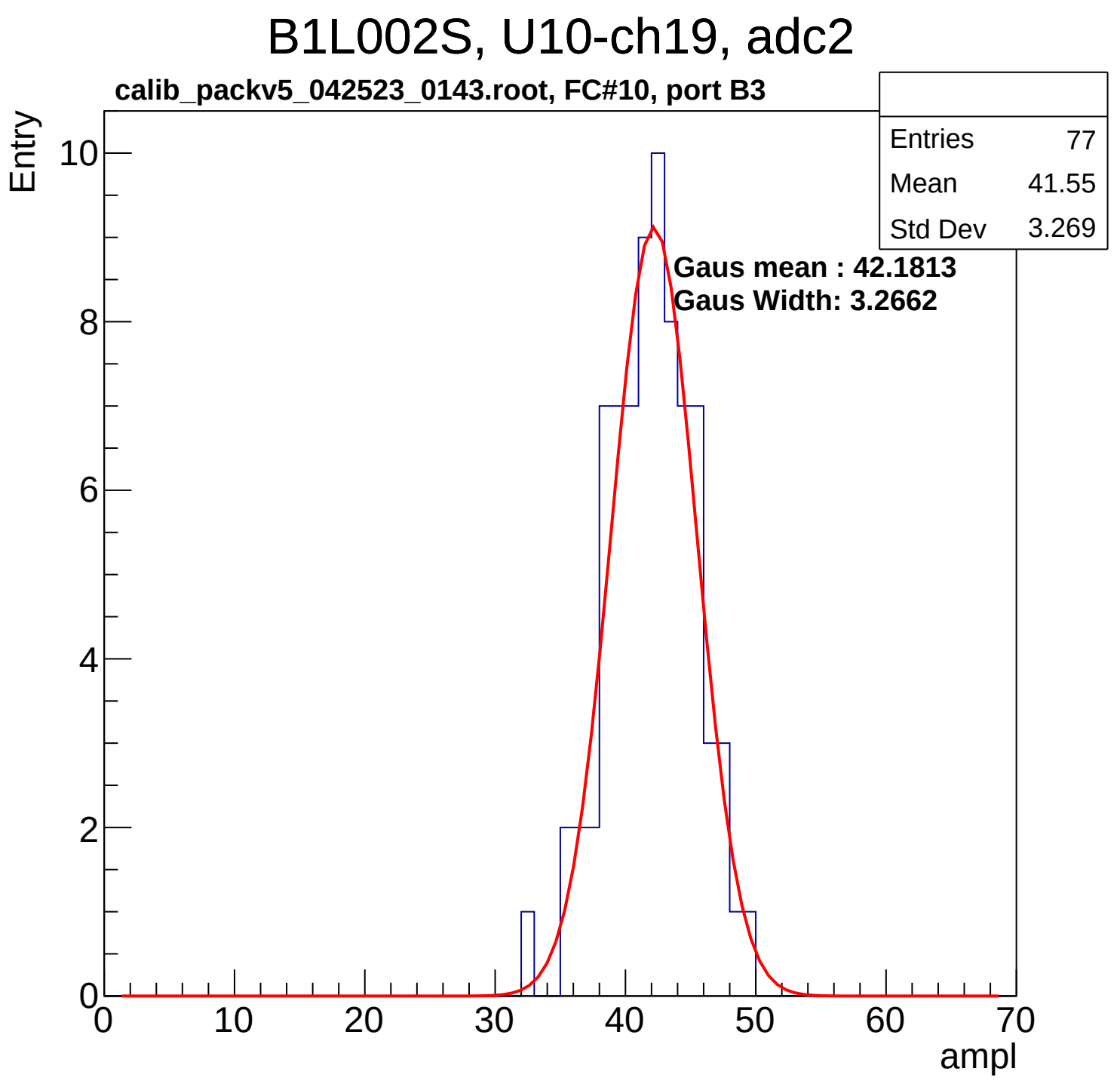
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 35.37
Std Dev 2.664
Gaus mean : 35.8256
Gaus Width: 2.9854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41.55
Std Dev 3.269
Gaus mean : 42.1813
Gaus Width: 3.2662
Entry
ampl
0
2
4
6
8
10
0
10
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60
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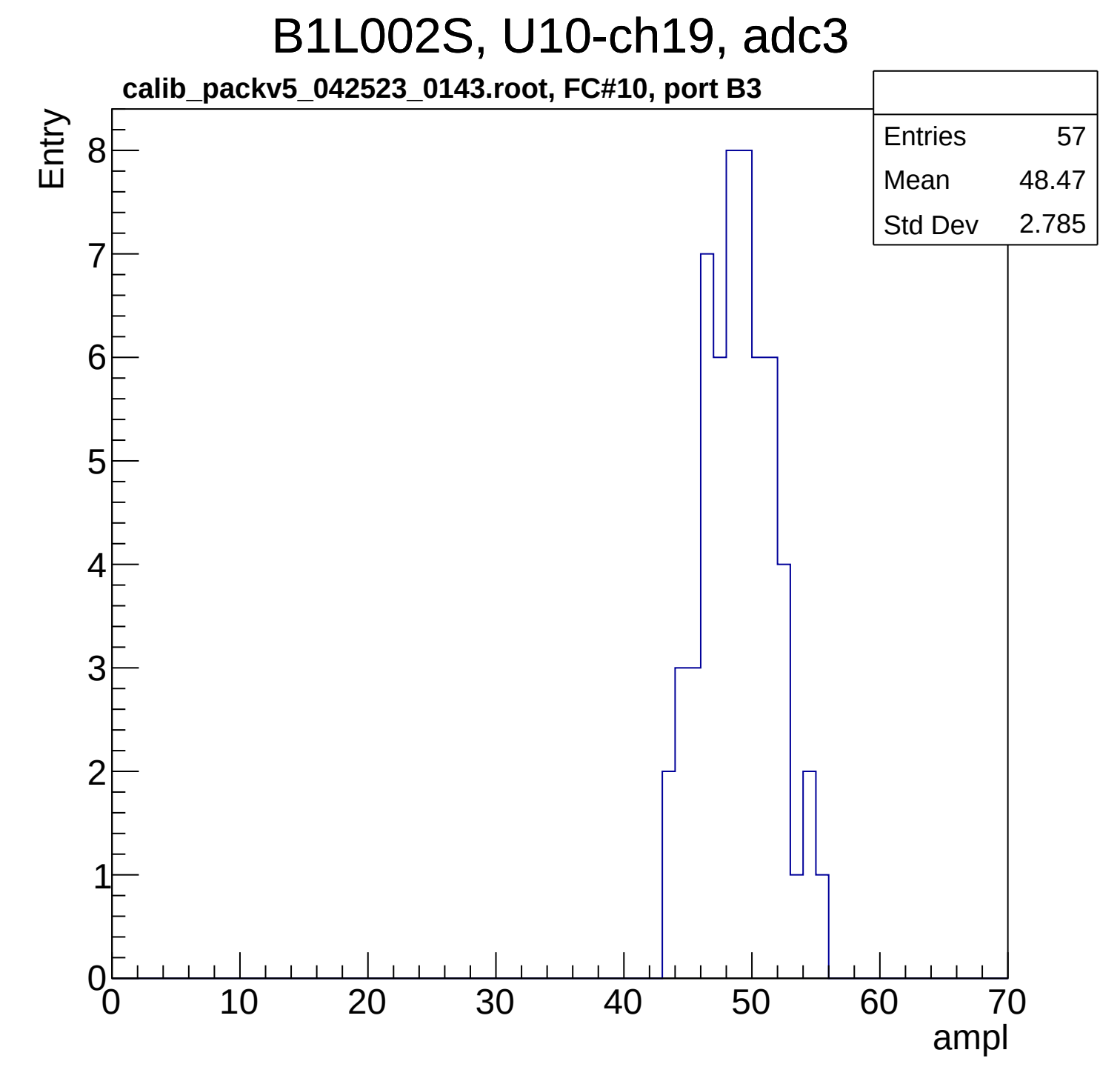
B1L002S, U10-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.47
Std Dev 2.785
Entry
ampl
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1
2
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4
5
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7
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0
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40
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60
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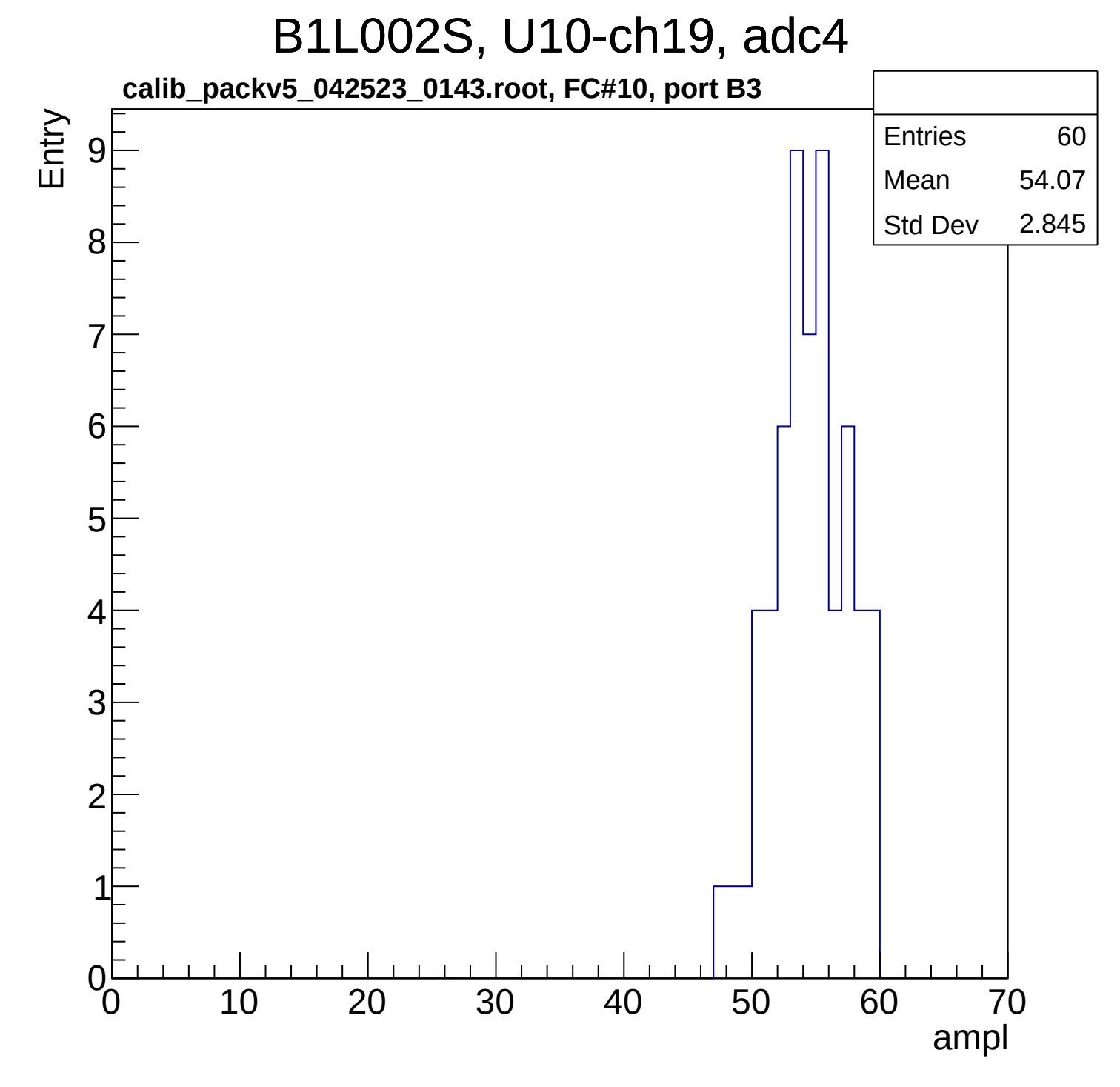
B1L002S, U10-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.07
Std Dev 2.845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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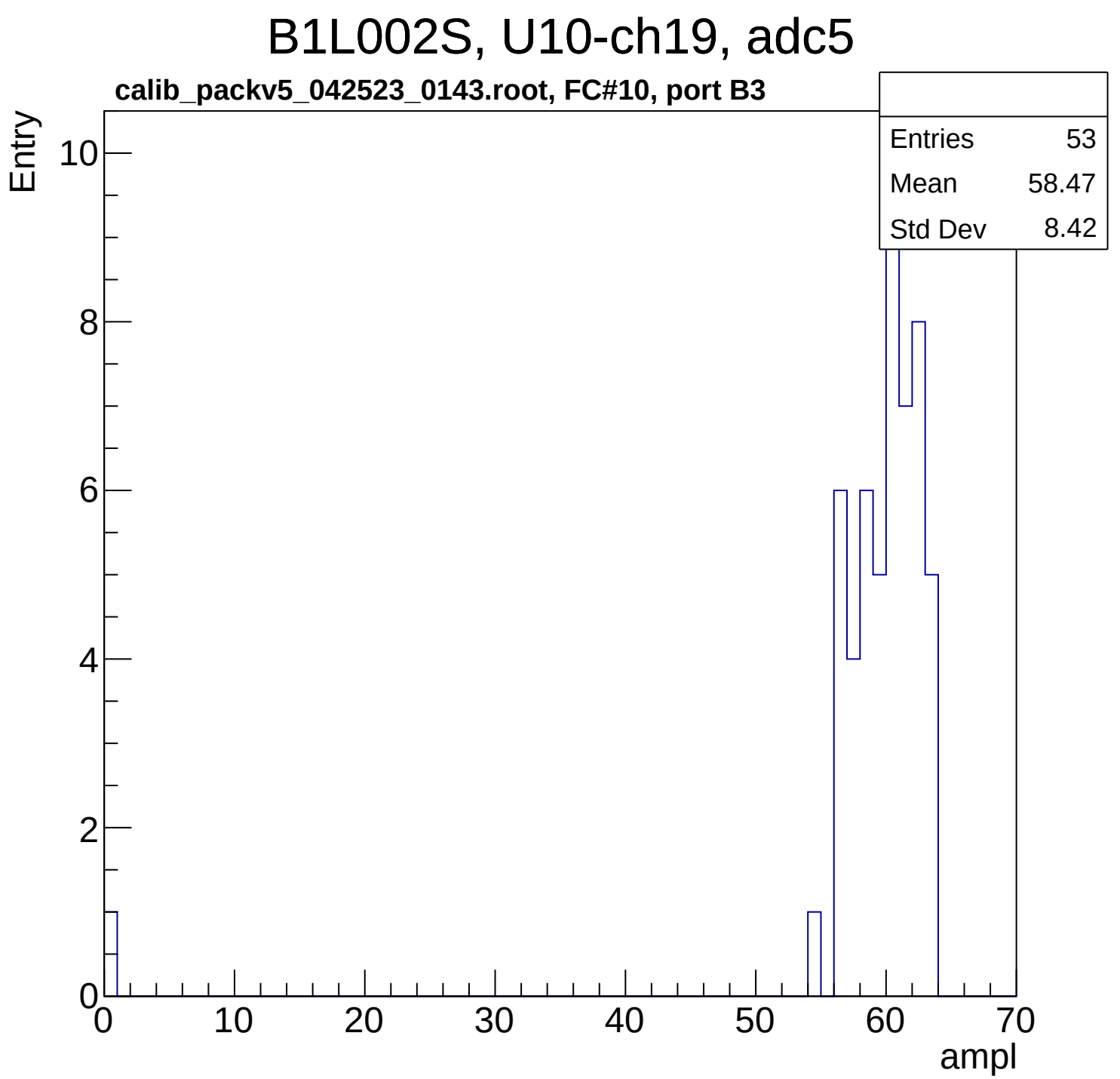
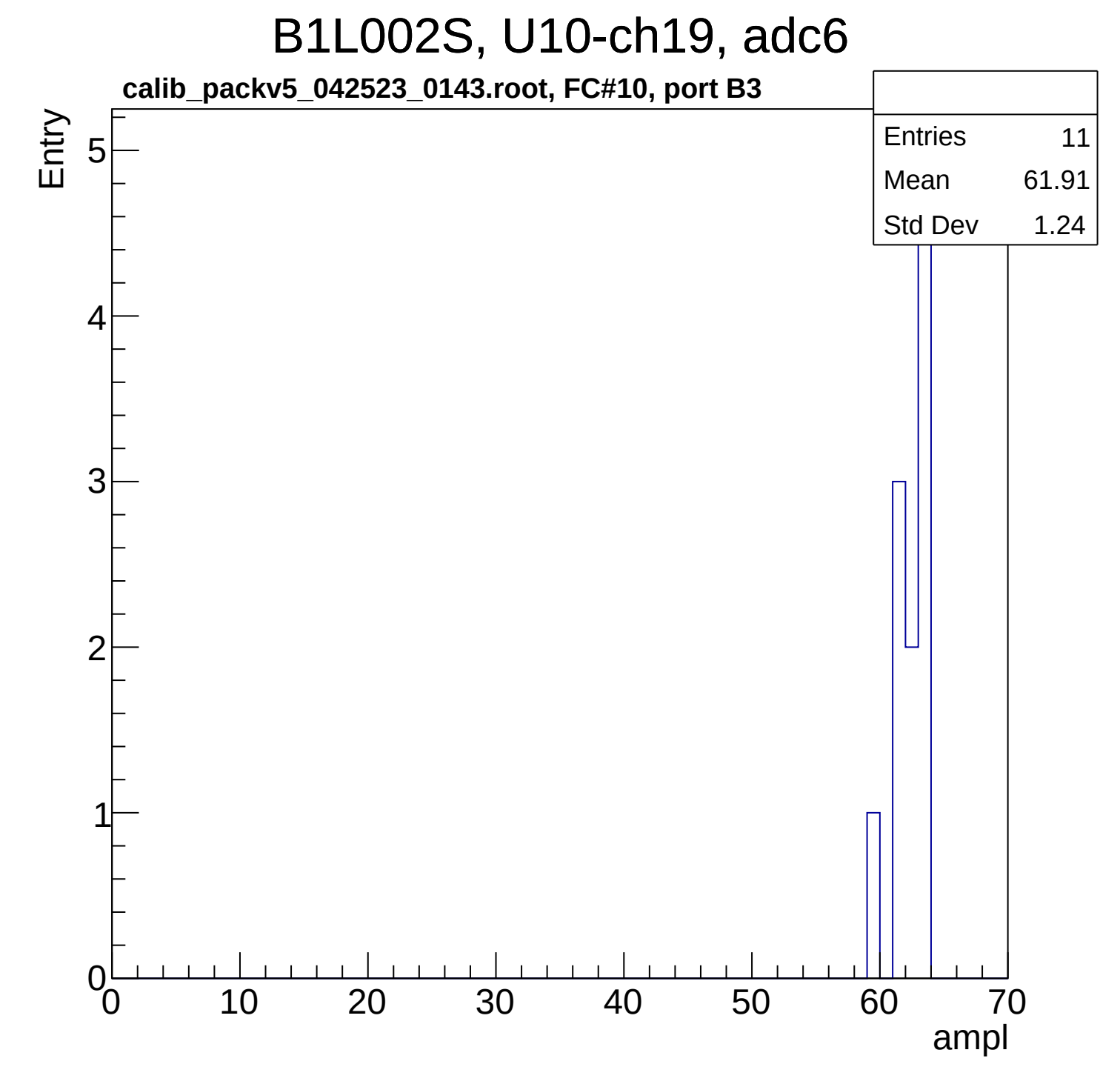
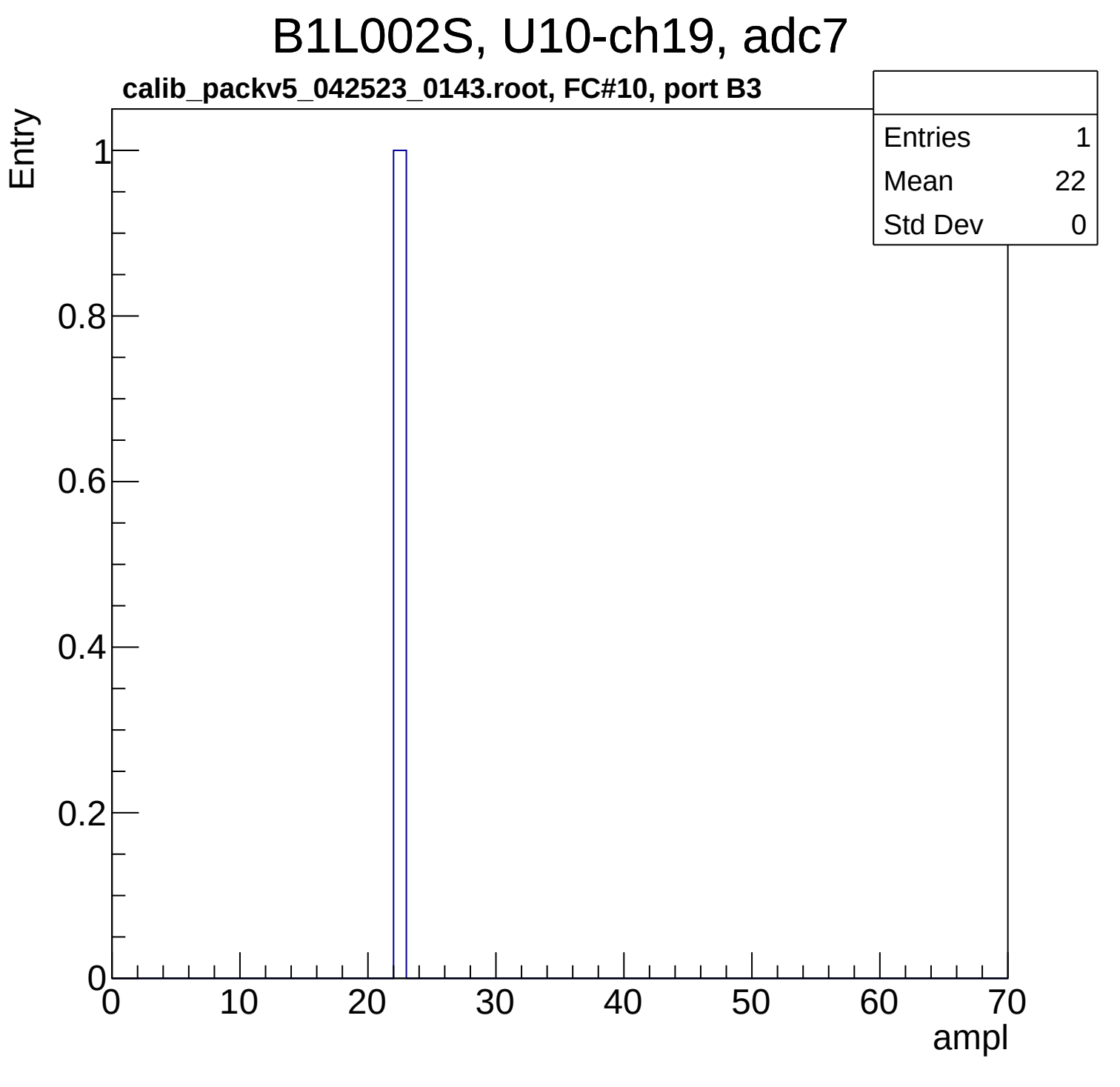
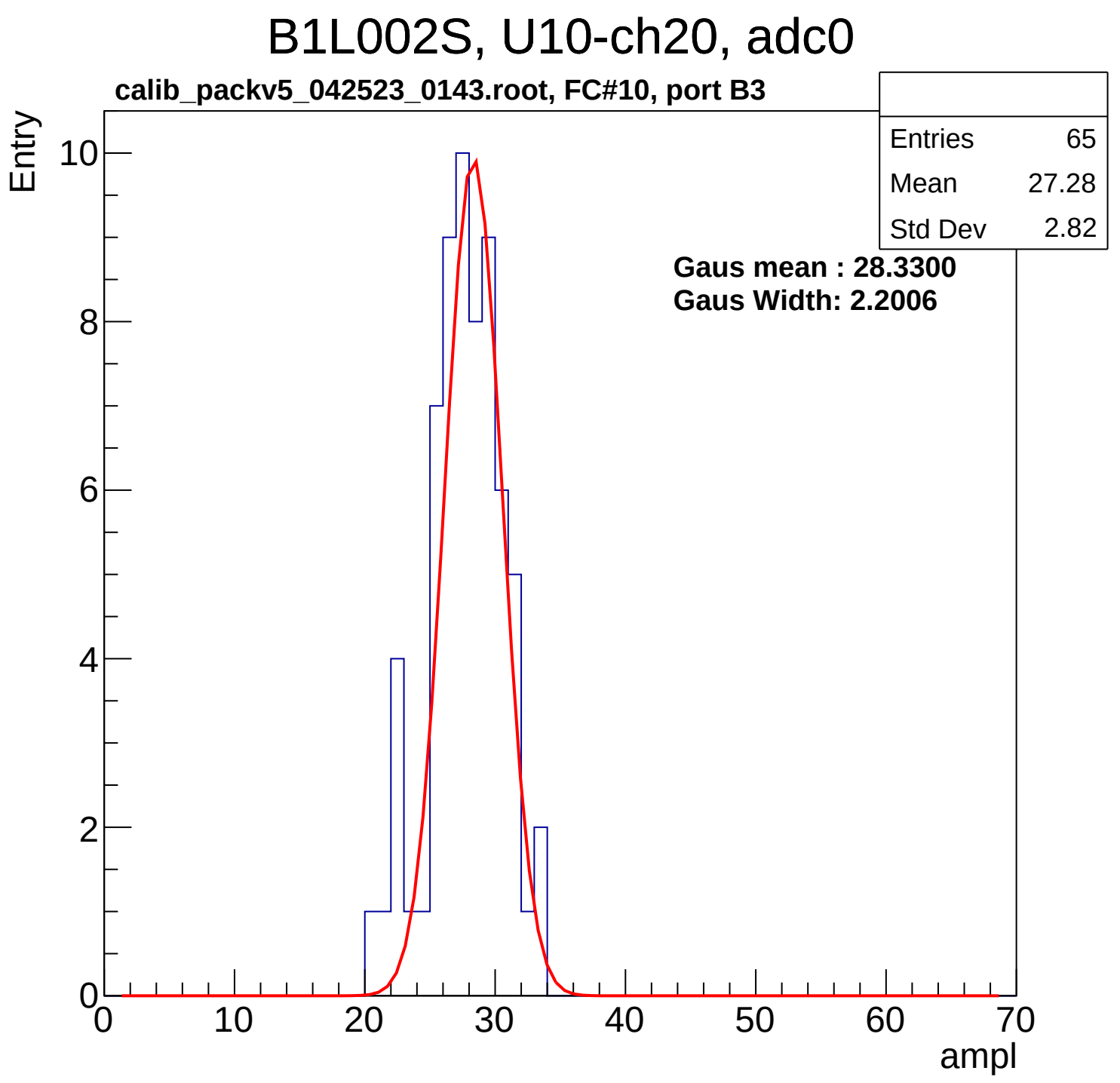
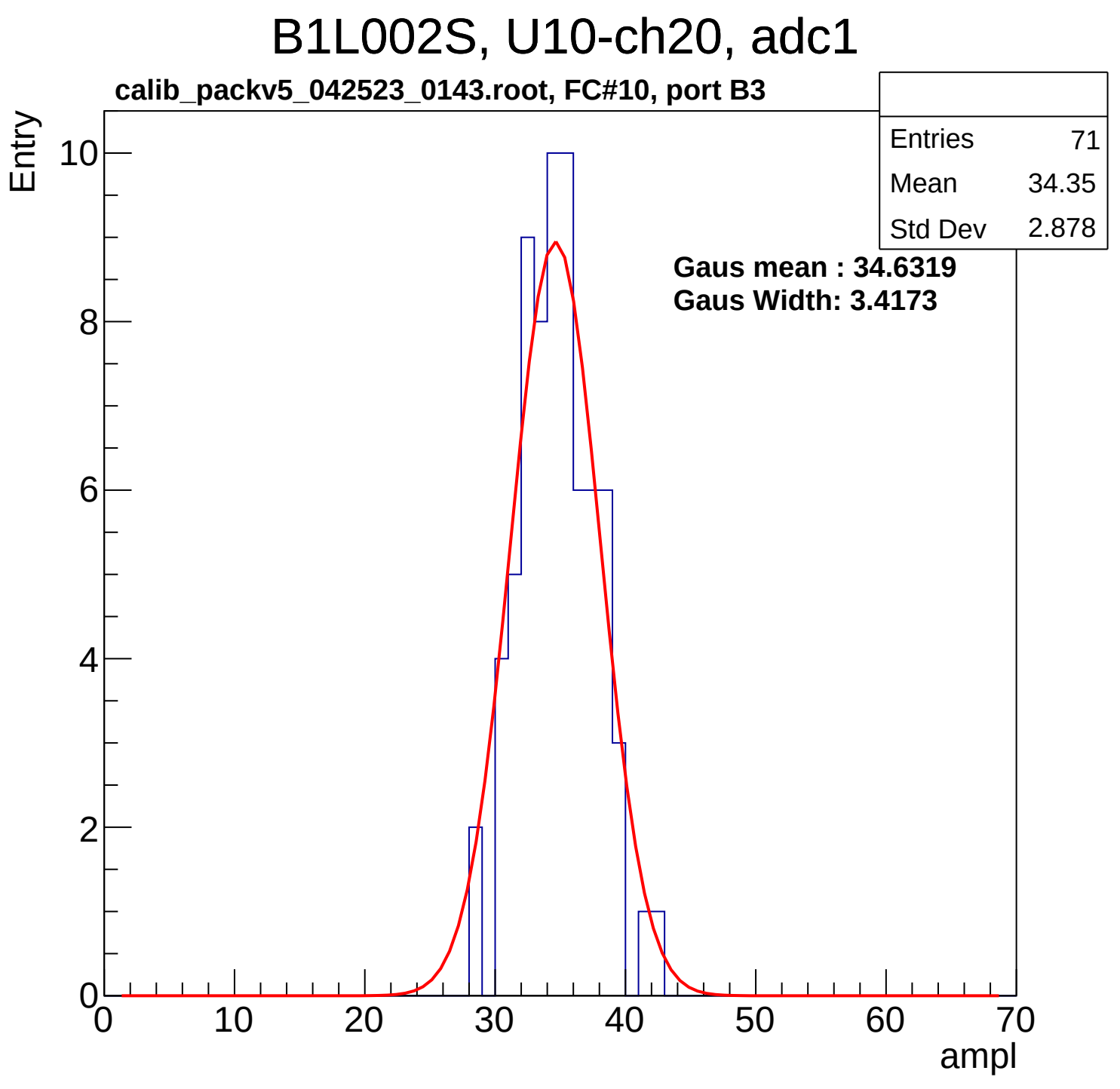

B1L002S, U10-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.47
Std Dev 8.42
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.91
Std Dev 1.24
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.28
Std Dev 2.82
Gaus mean : 28.3300
Gaus Width: 2.2006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.35
Std Dev 2.878
Gaus mean : 34.6319
Gaus Width: 3.4173
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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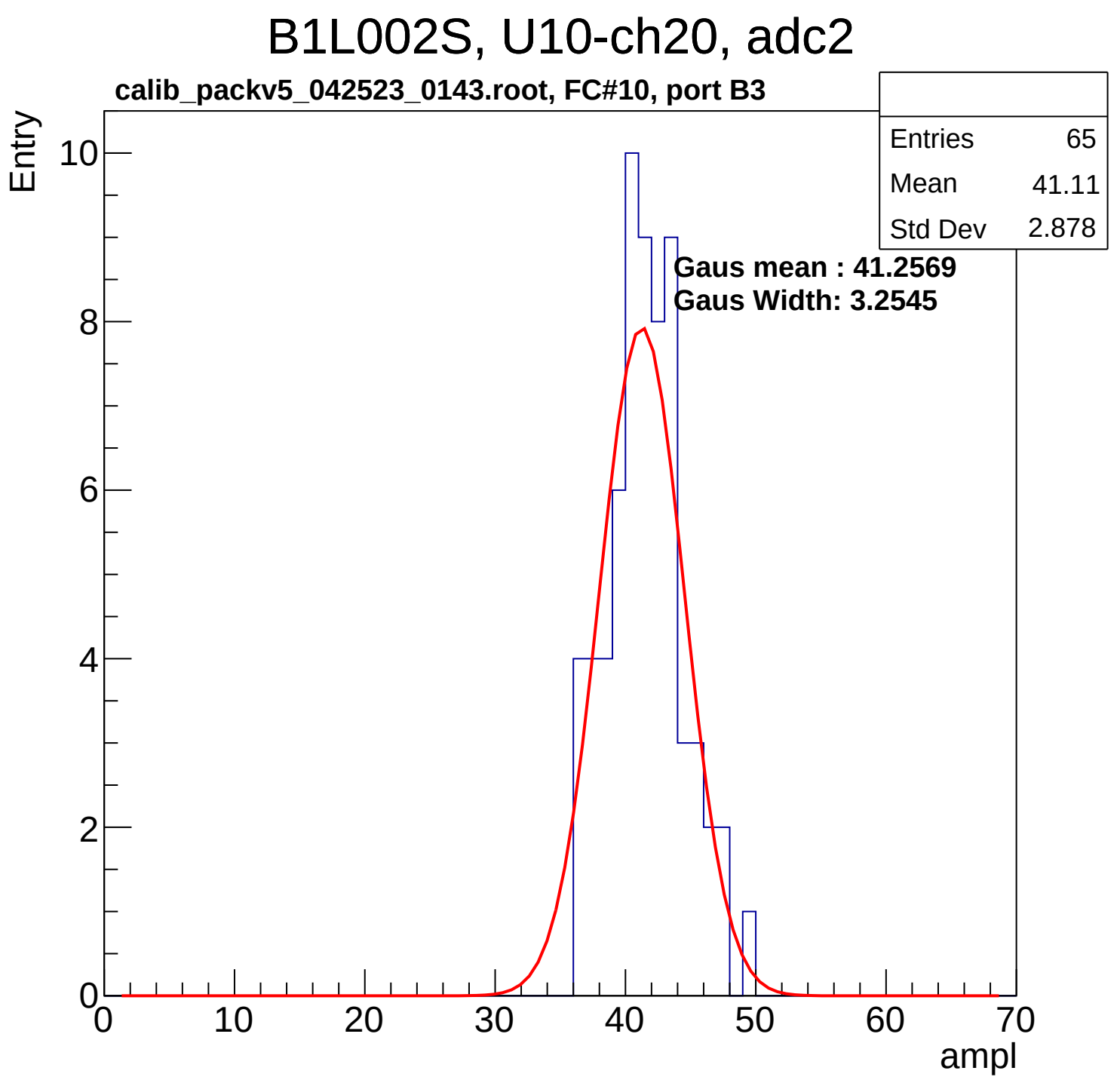
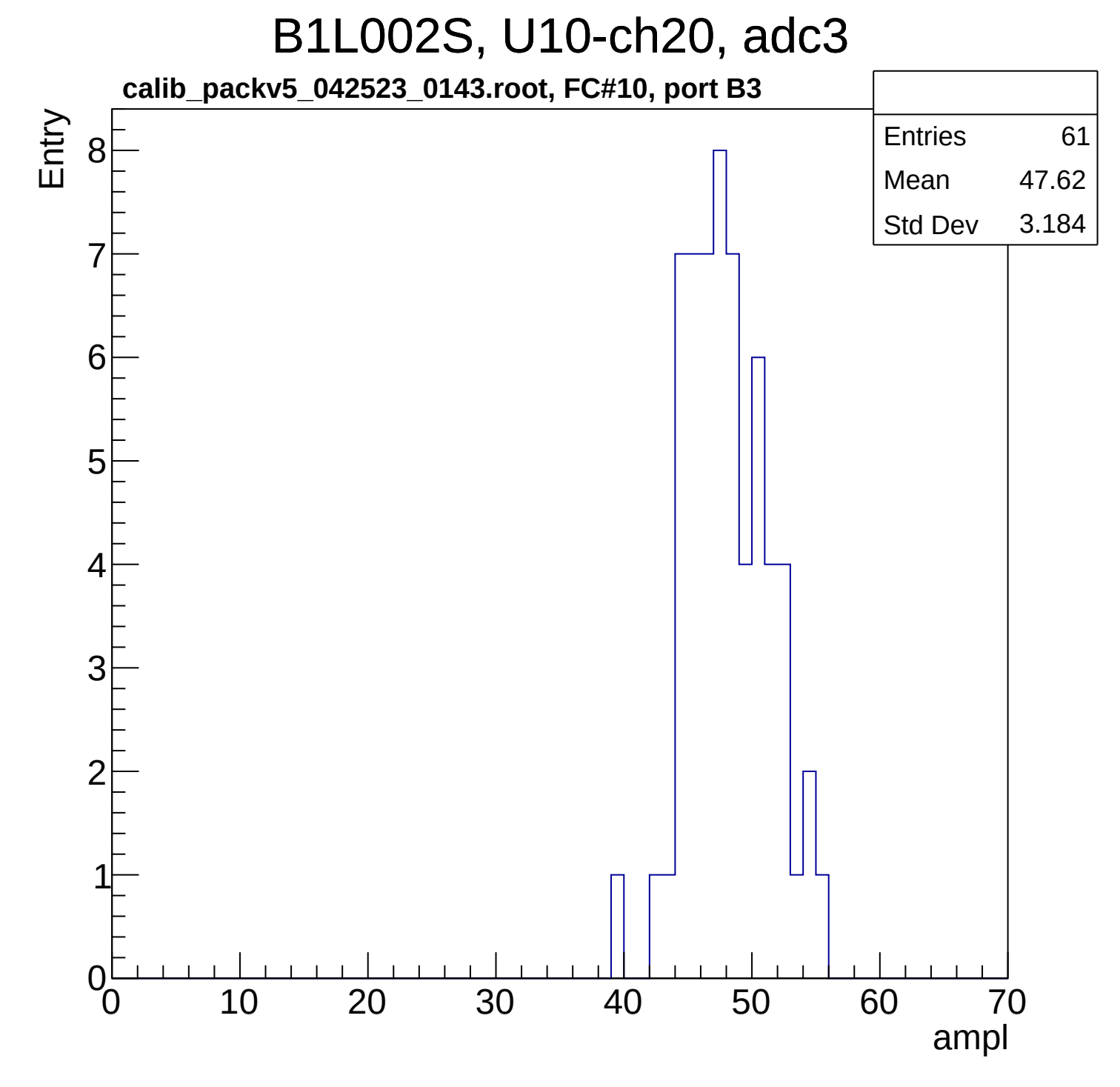
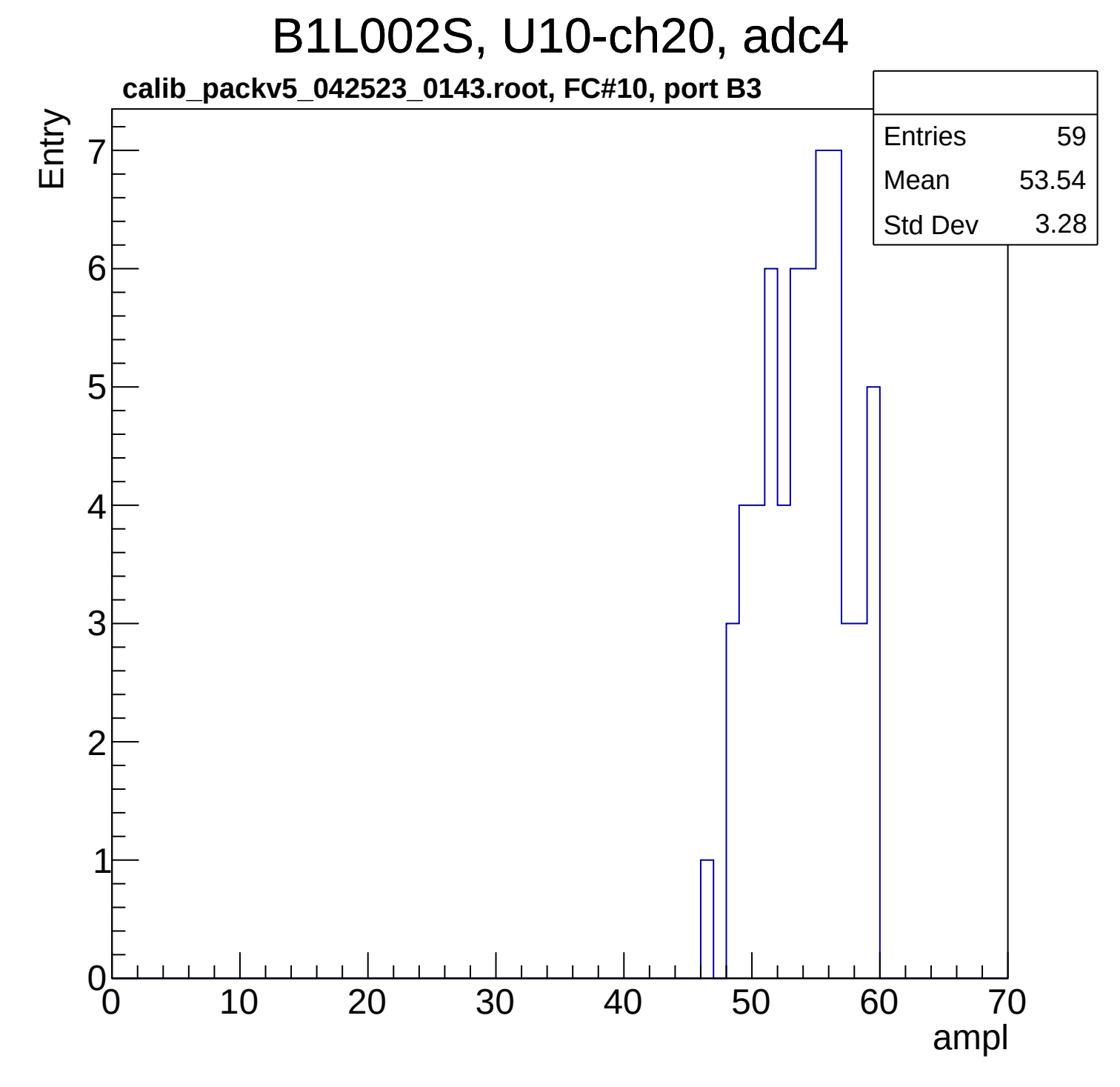
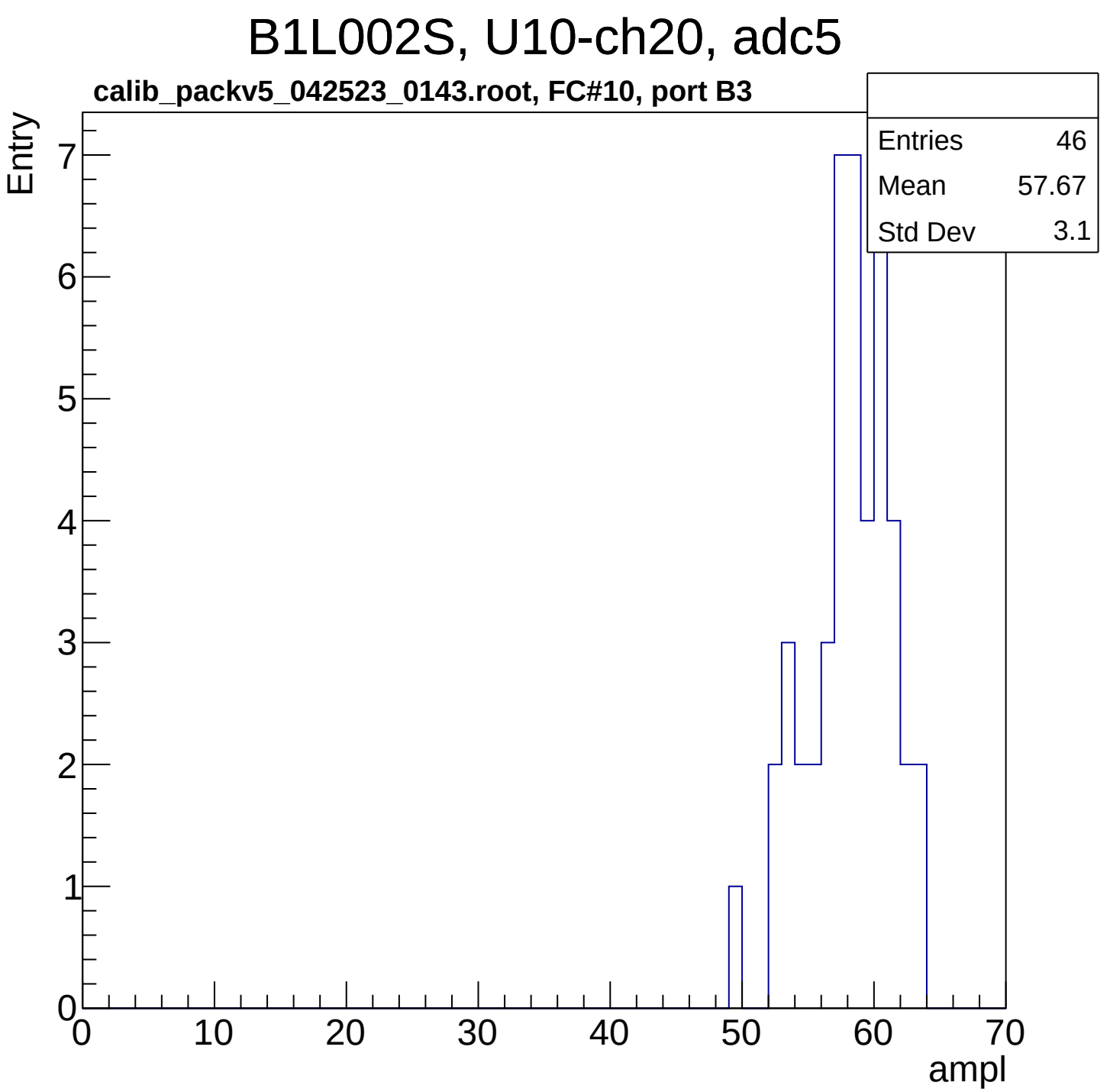
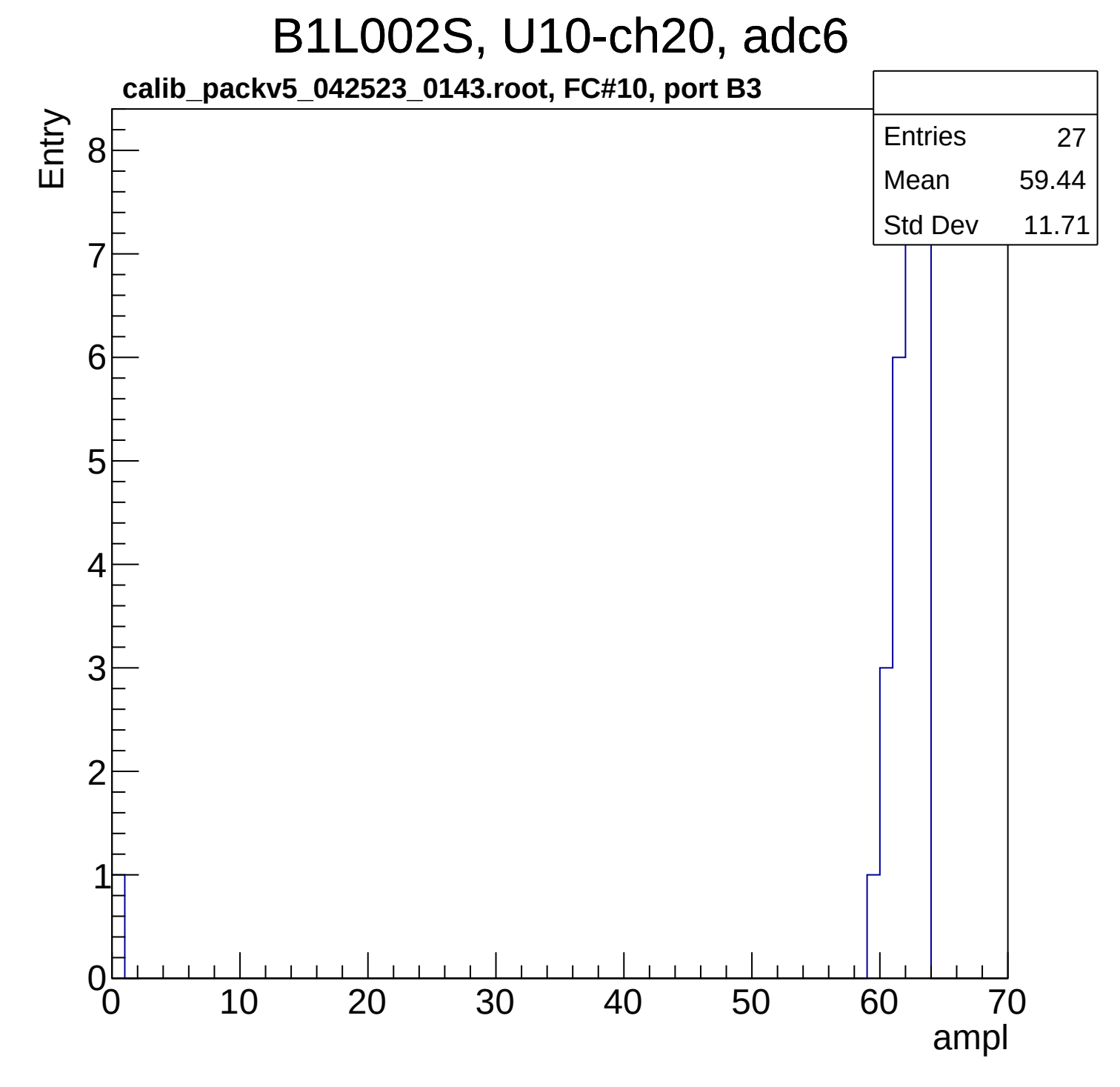
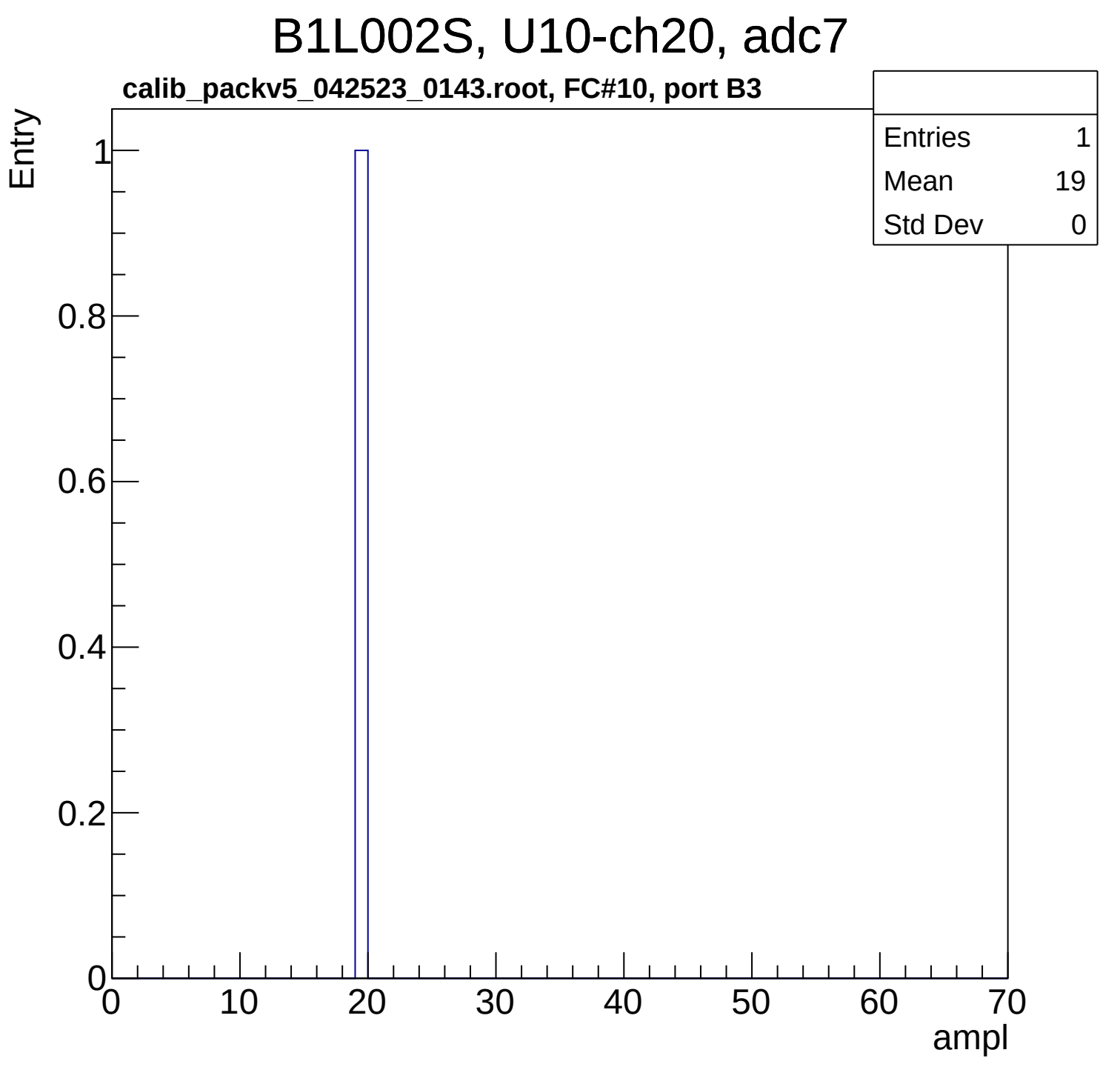

B1L002S, U10-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.11
Std Dev 2.878
Gaus mean : 41.2569
Gaus Width: 3.2545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

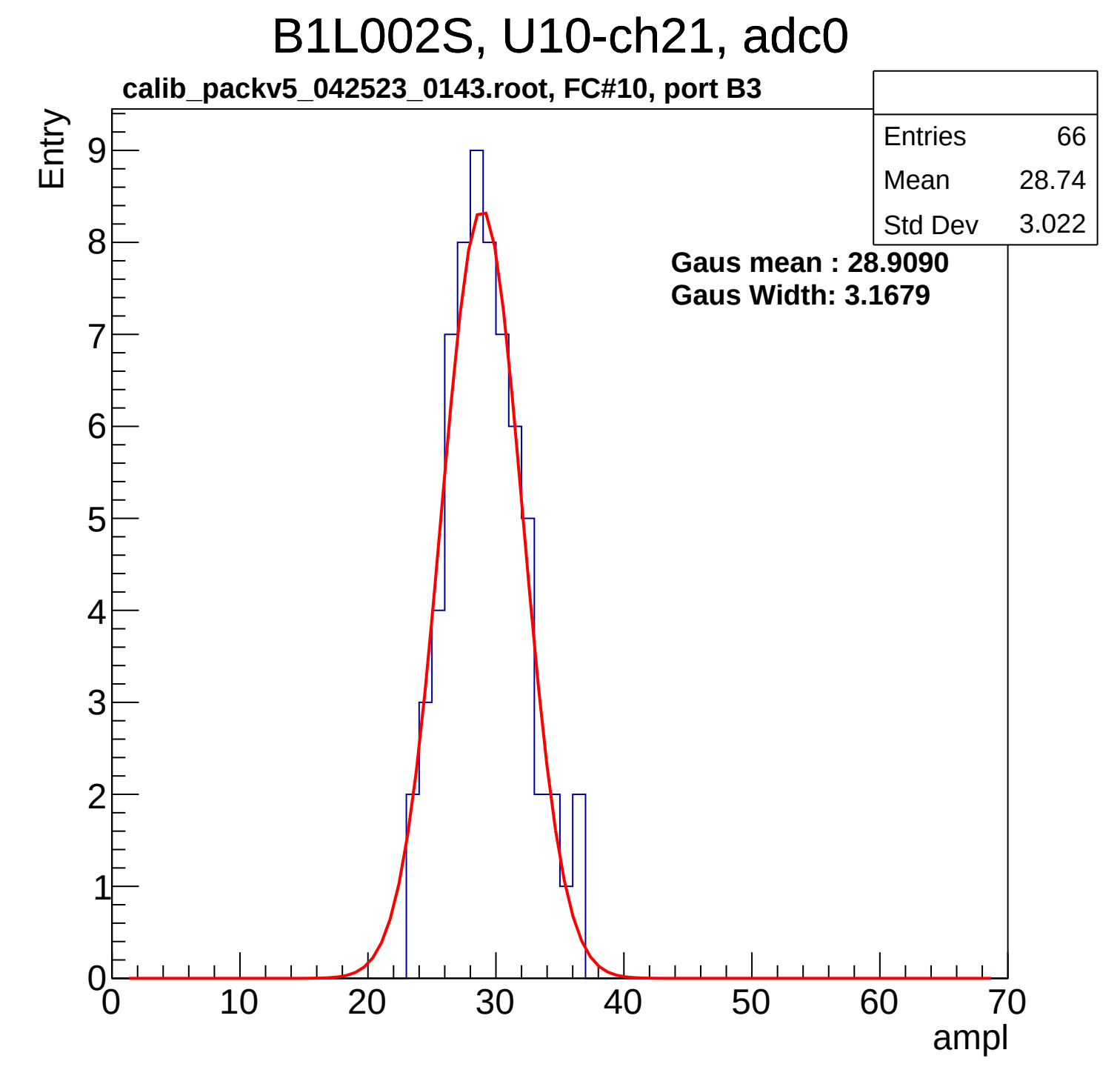

B1L002S, U10-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.62
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.54
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.67
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.44
Std Dev 11.71
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.74
Std Dev 3.022
Gaus mean : 28.9090
Gaus Width: 3.1679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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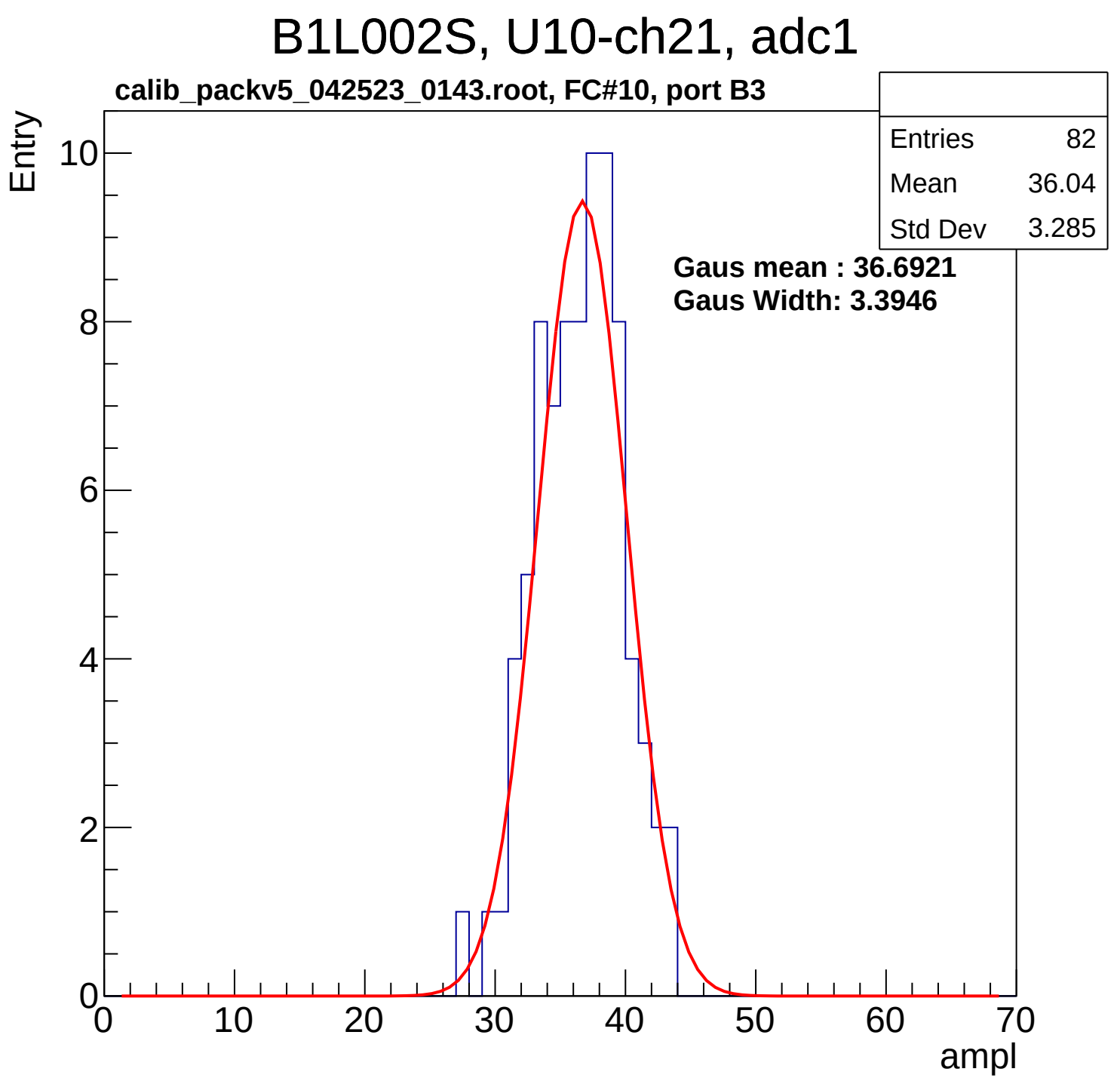
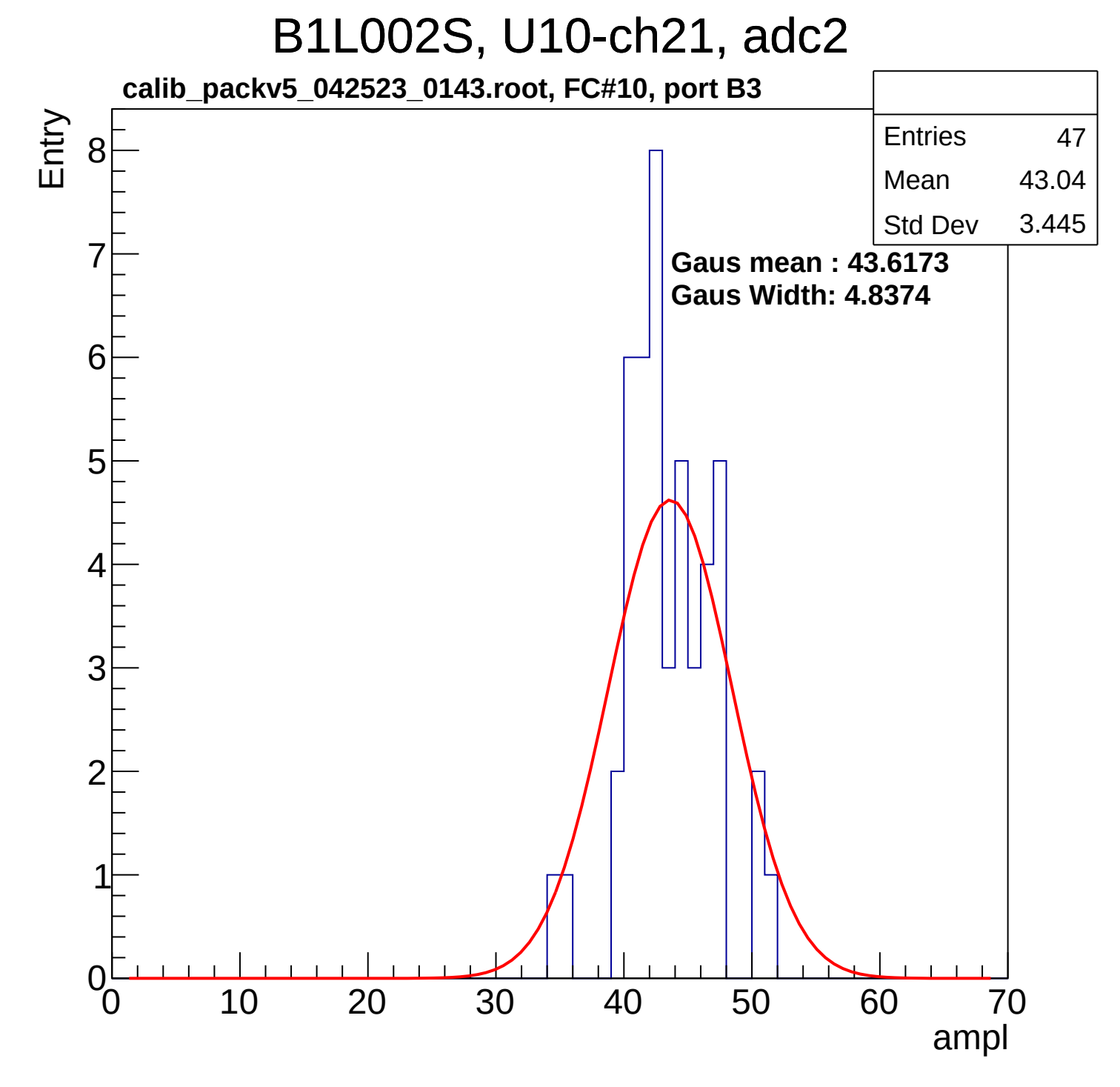
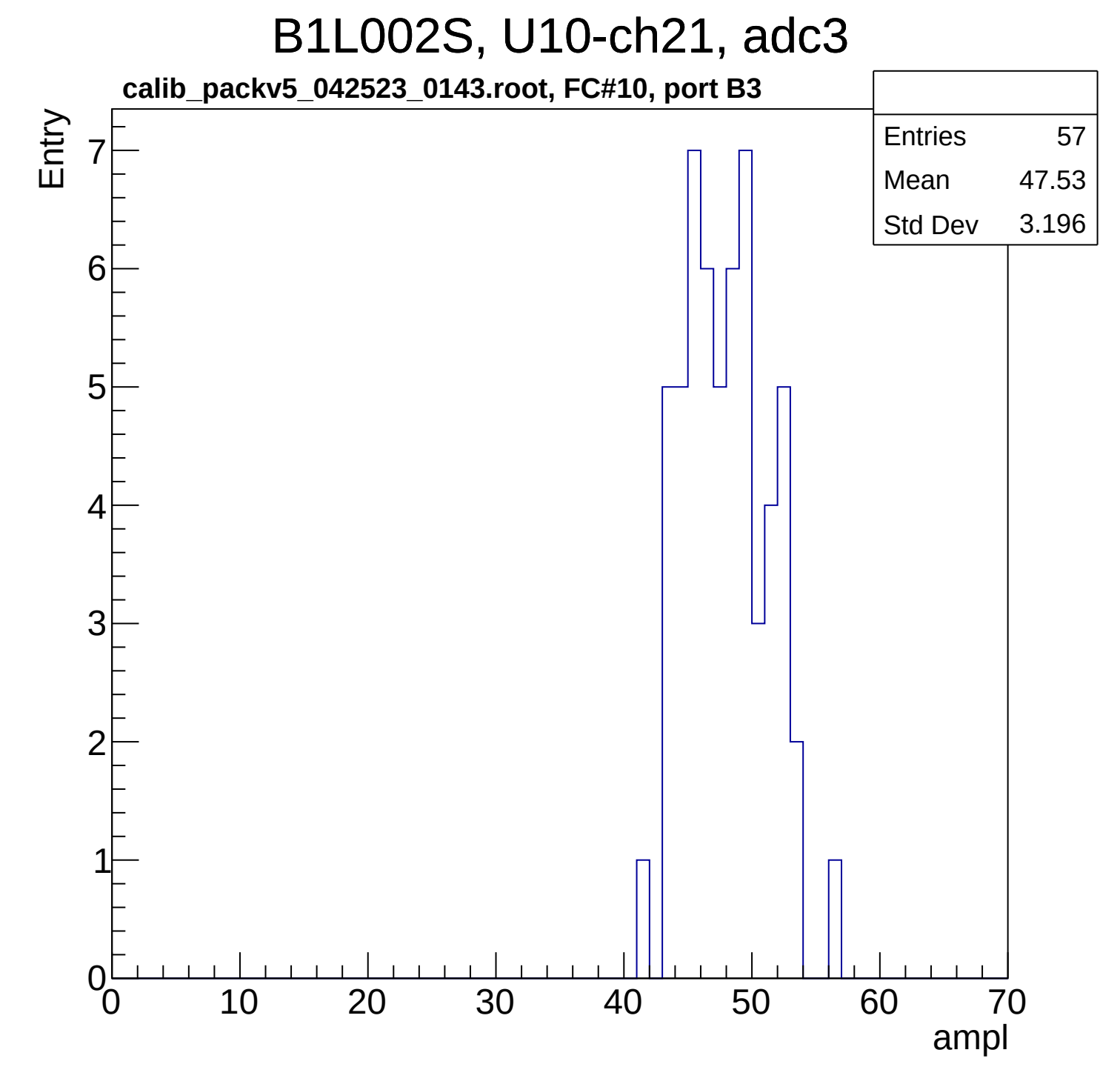
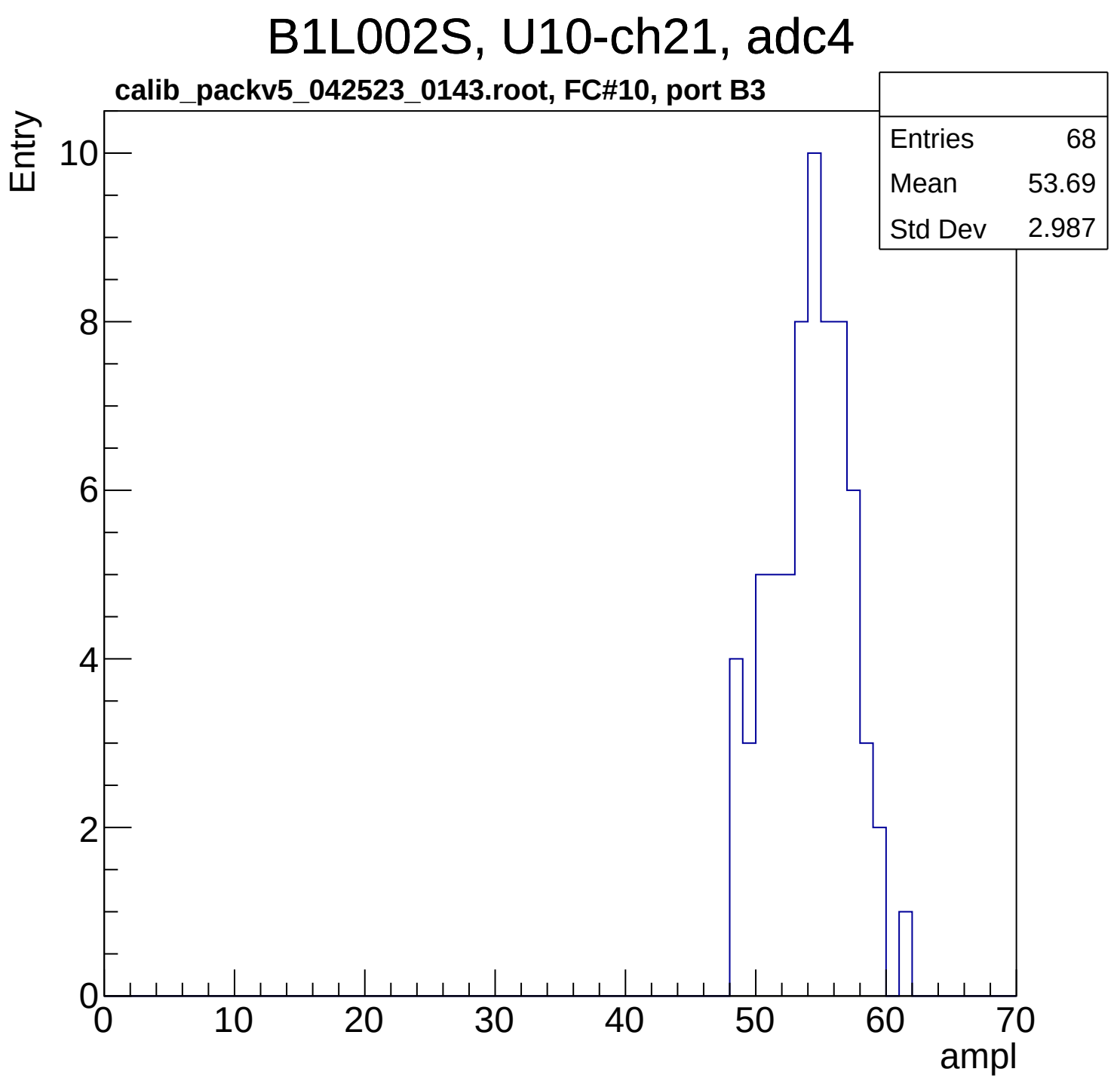
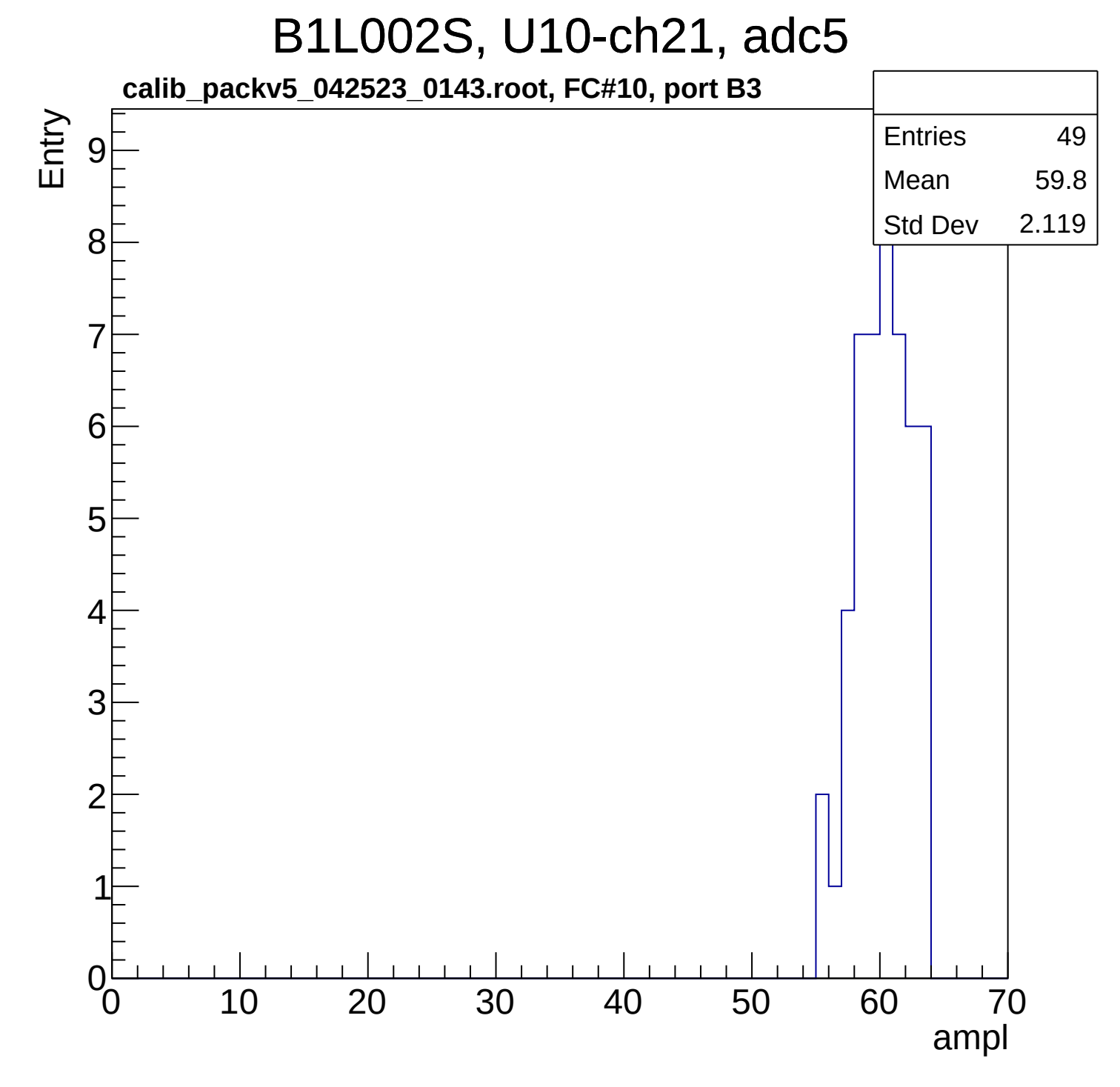

B1L002S, U10-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 36.04
Std Dev 3.285
Gaus mean : 36.6921
Gaus Width: 3.3946
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 43.04
Std Dev 3.445
Gaus mean : 43.6173
Gaus Width: 4.8374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.53
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.69
Std Dev 2.987
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.8
Std Dev 2.119
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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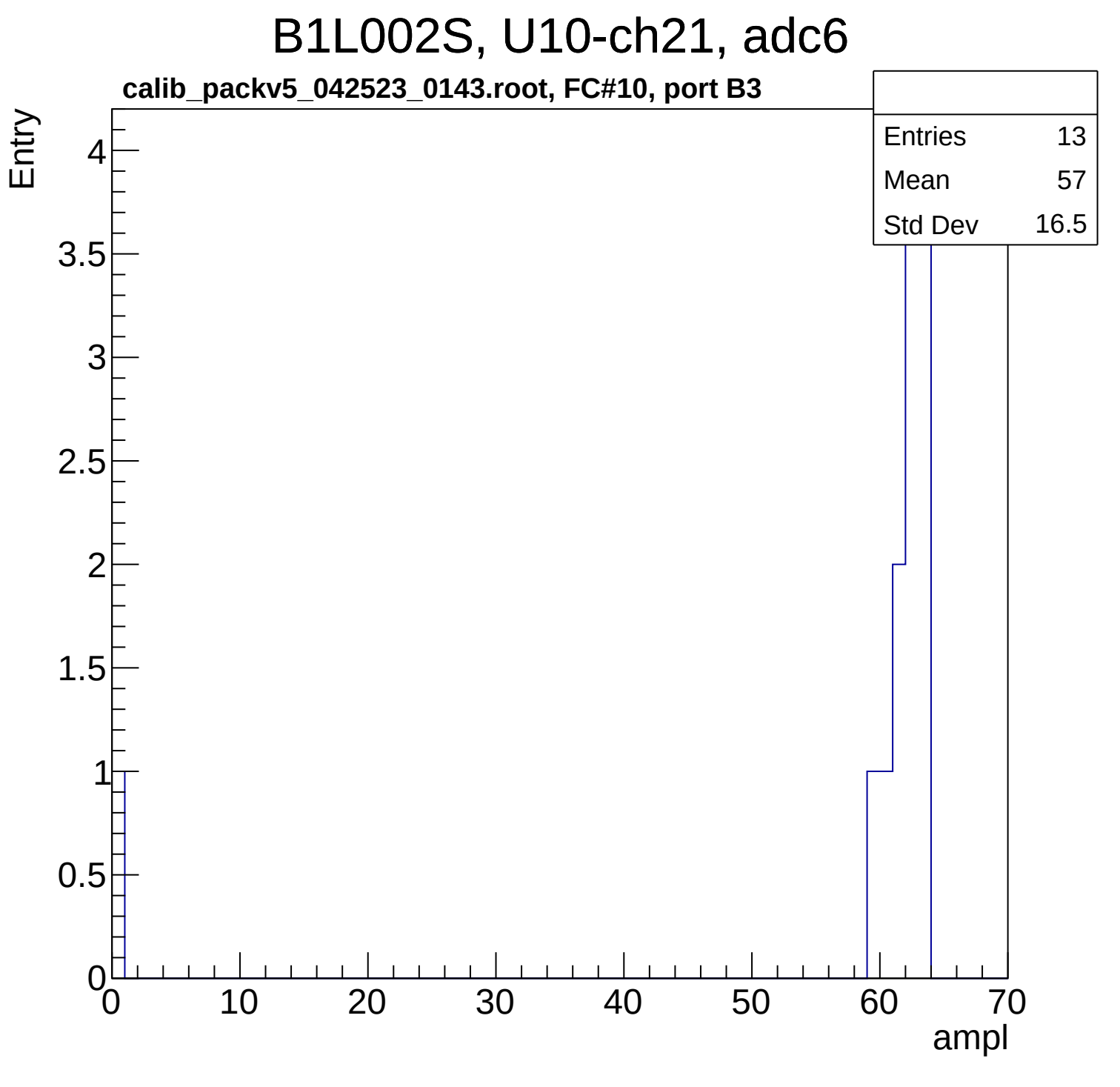
B1L002S, U10-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57
Std Dev 16.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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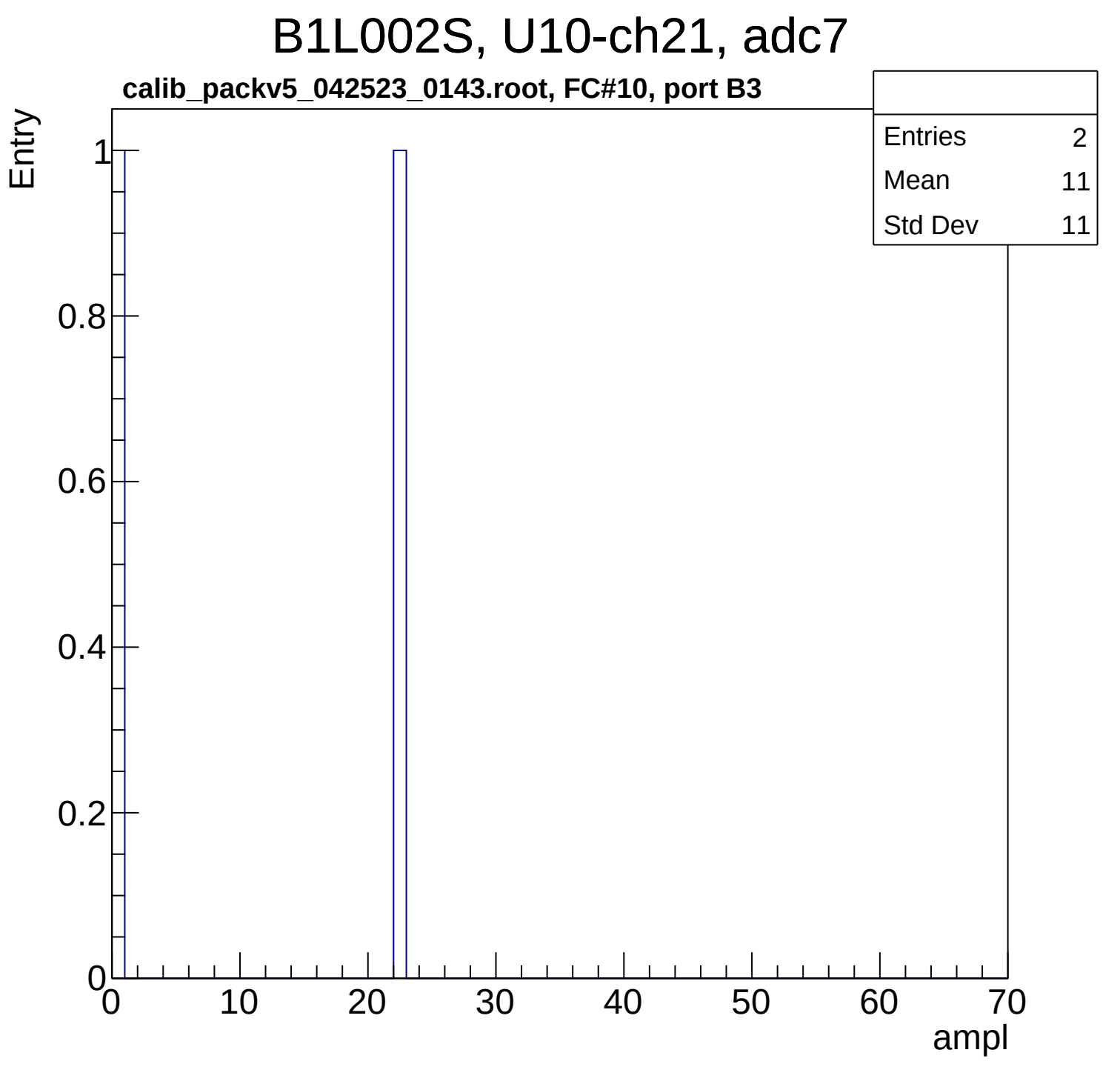
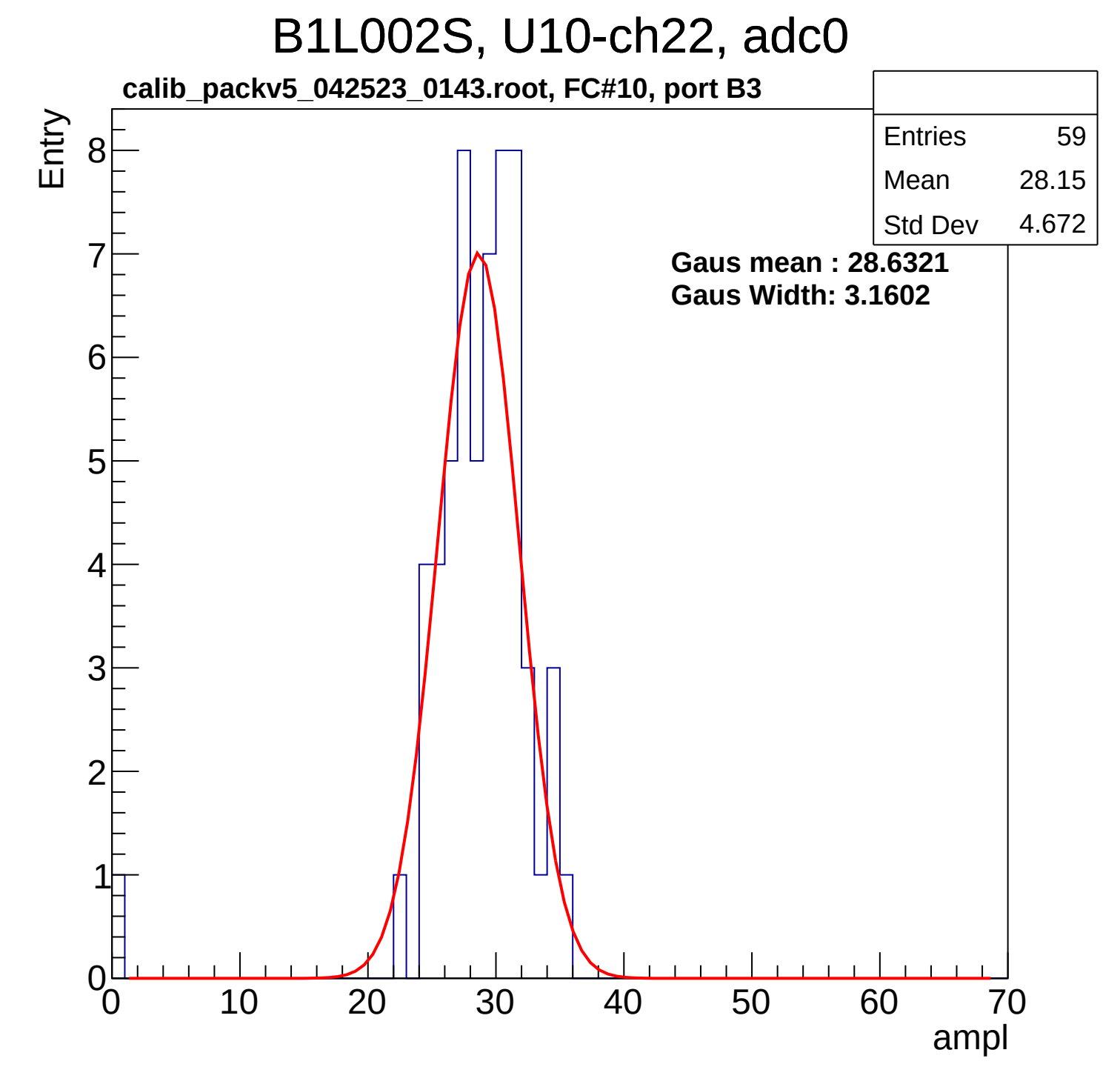
40
50
60
70


B1L002S, U10-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.15
Std Dev 4.672
Gaus mean : 28.6321
Gaus Width: 3.1602
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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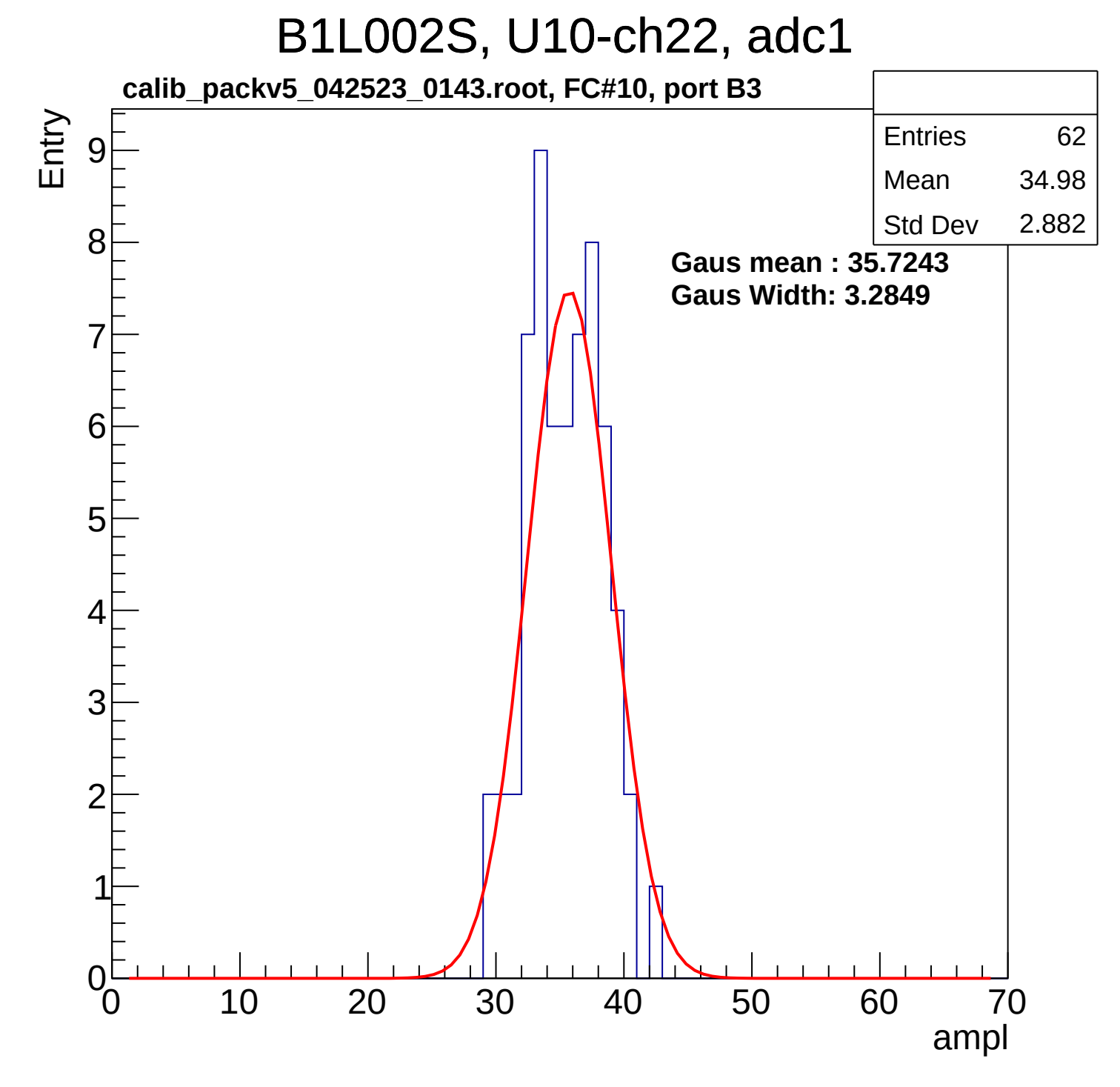
B1L002S, U10-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.98
Std Dev 2.882
Gaus mean : 35.7243
Gaus Width: 3.2849
Entry
ampl
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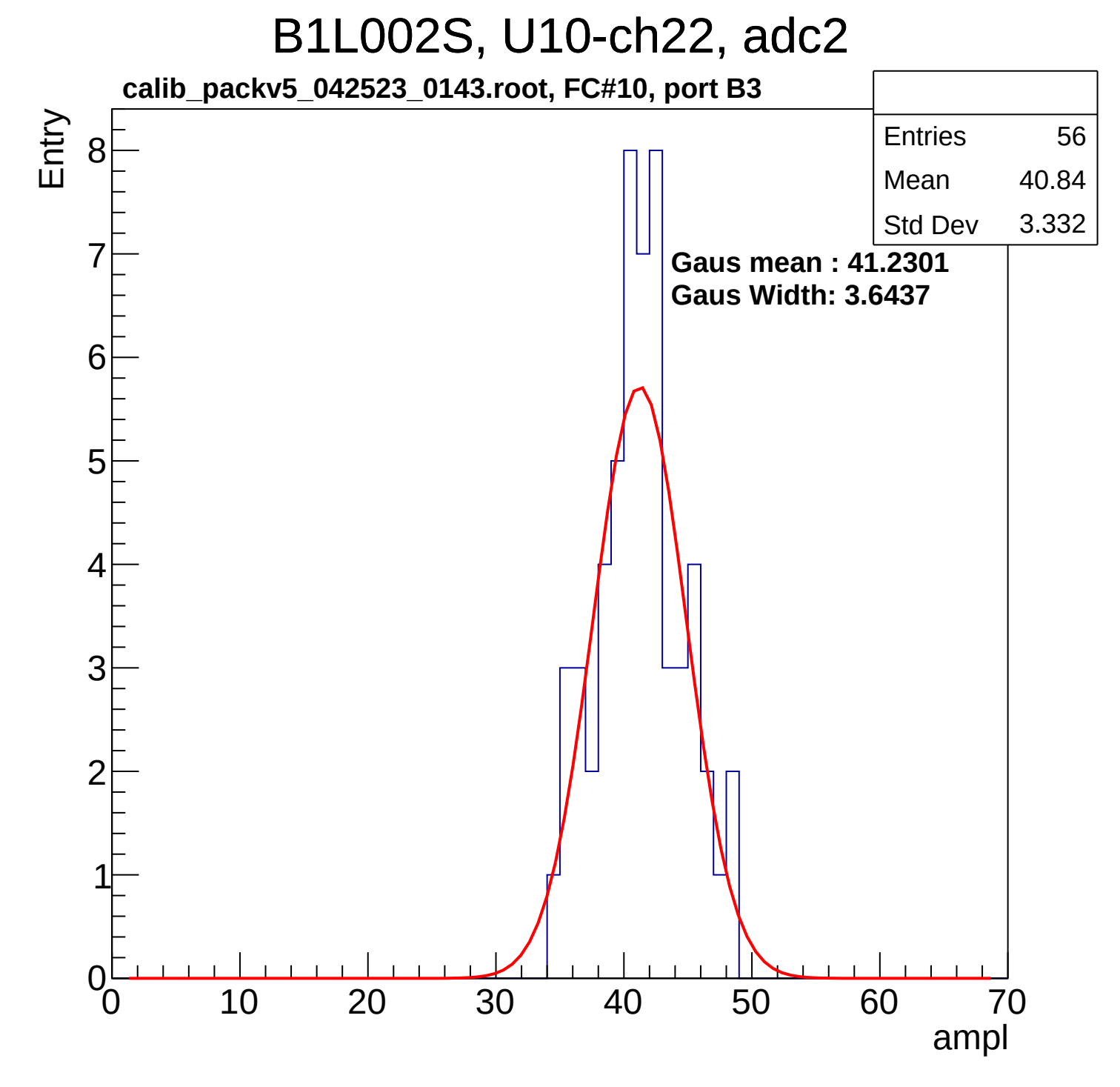
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.84
Std Dev 3.332
Gaus mean : 41.2301
Gaus Width: 3.6437
Entry
ampl
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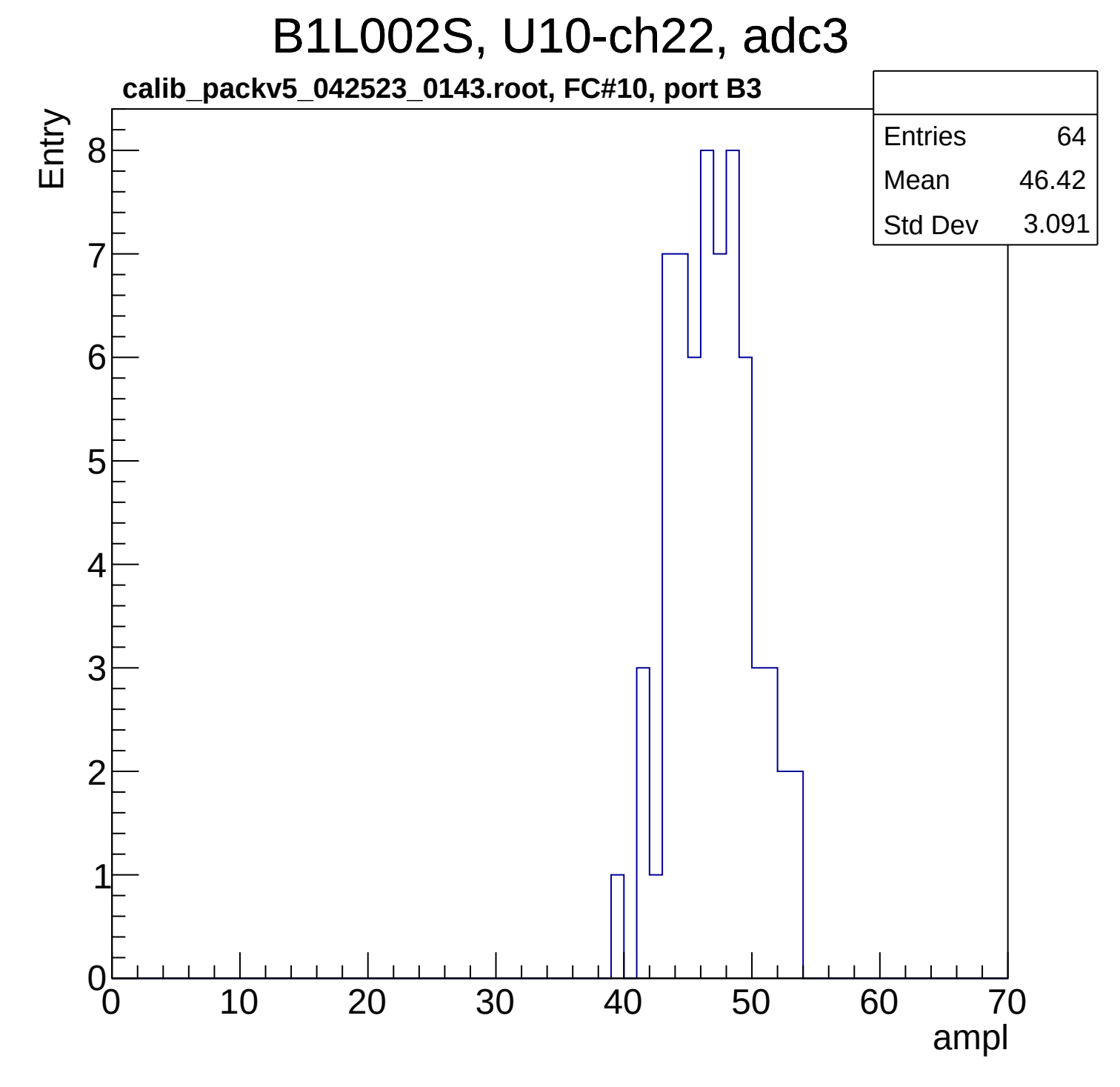
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.42
Std Dev 3.091
Entry
ampl
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1
2
3
4
5
6
7
8
0
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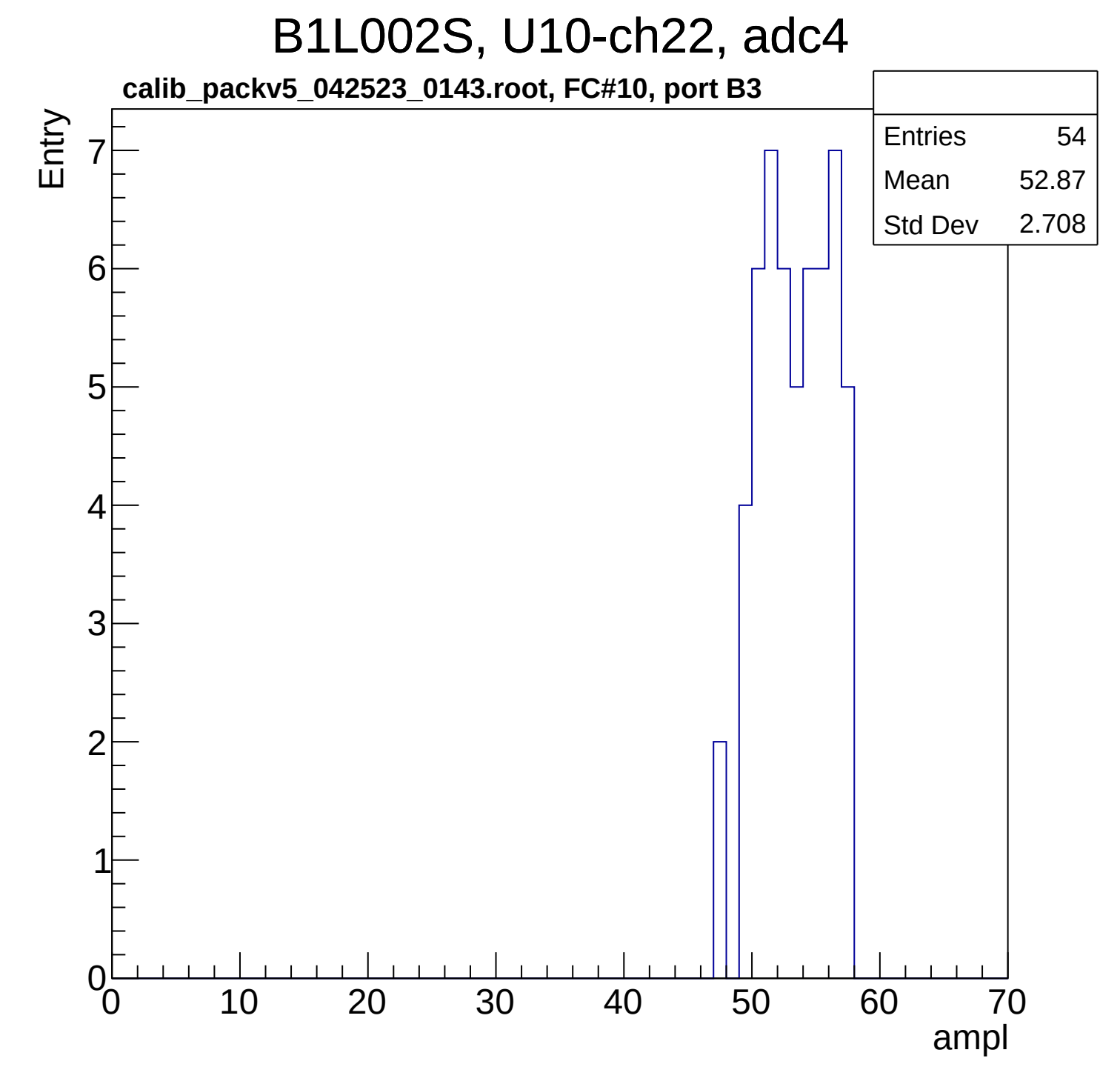
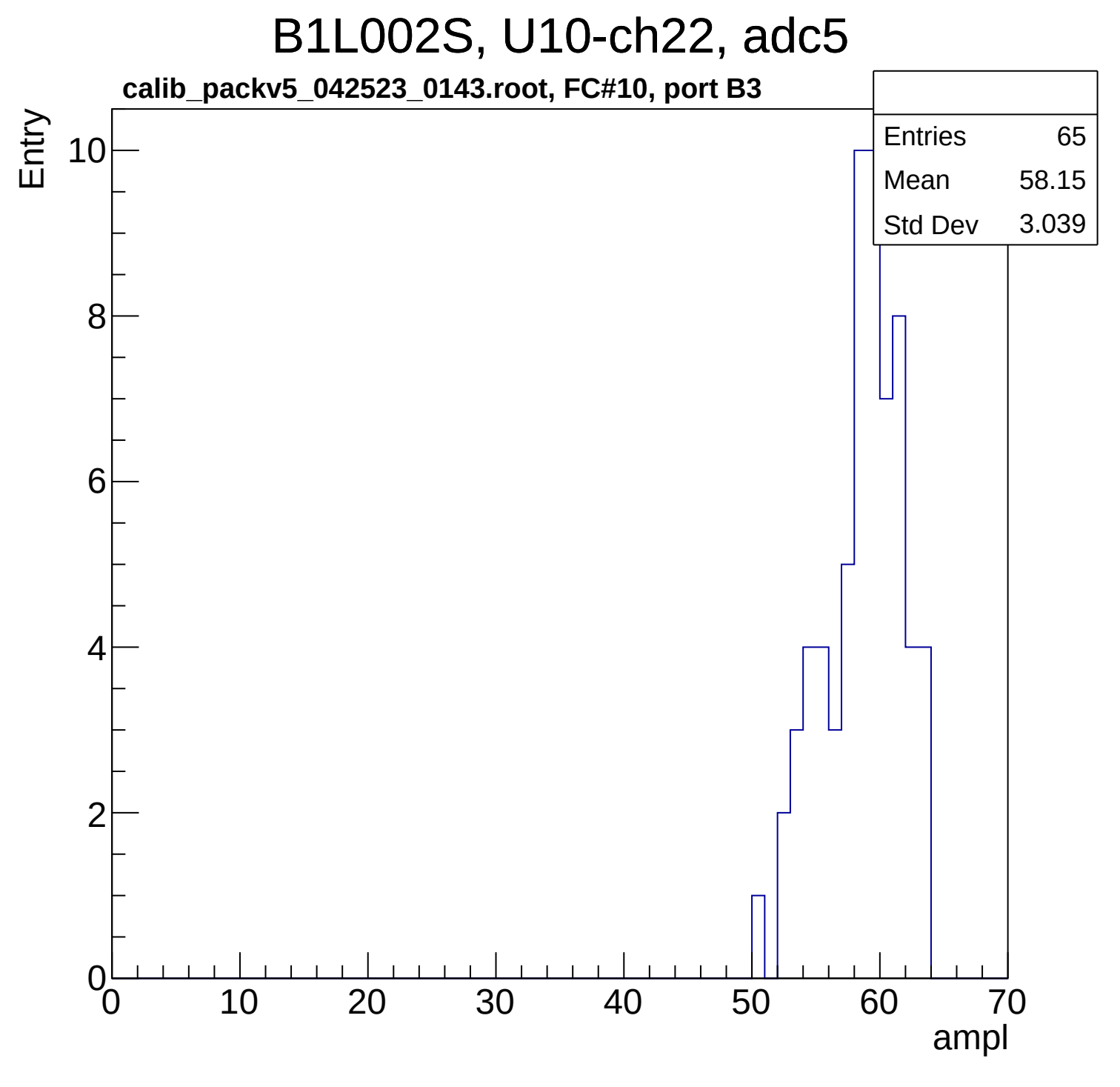
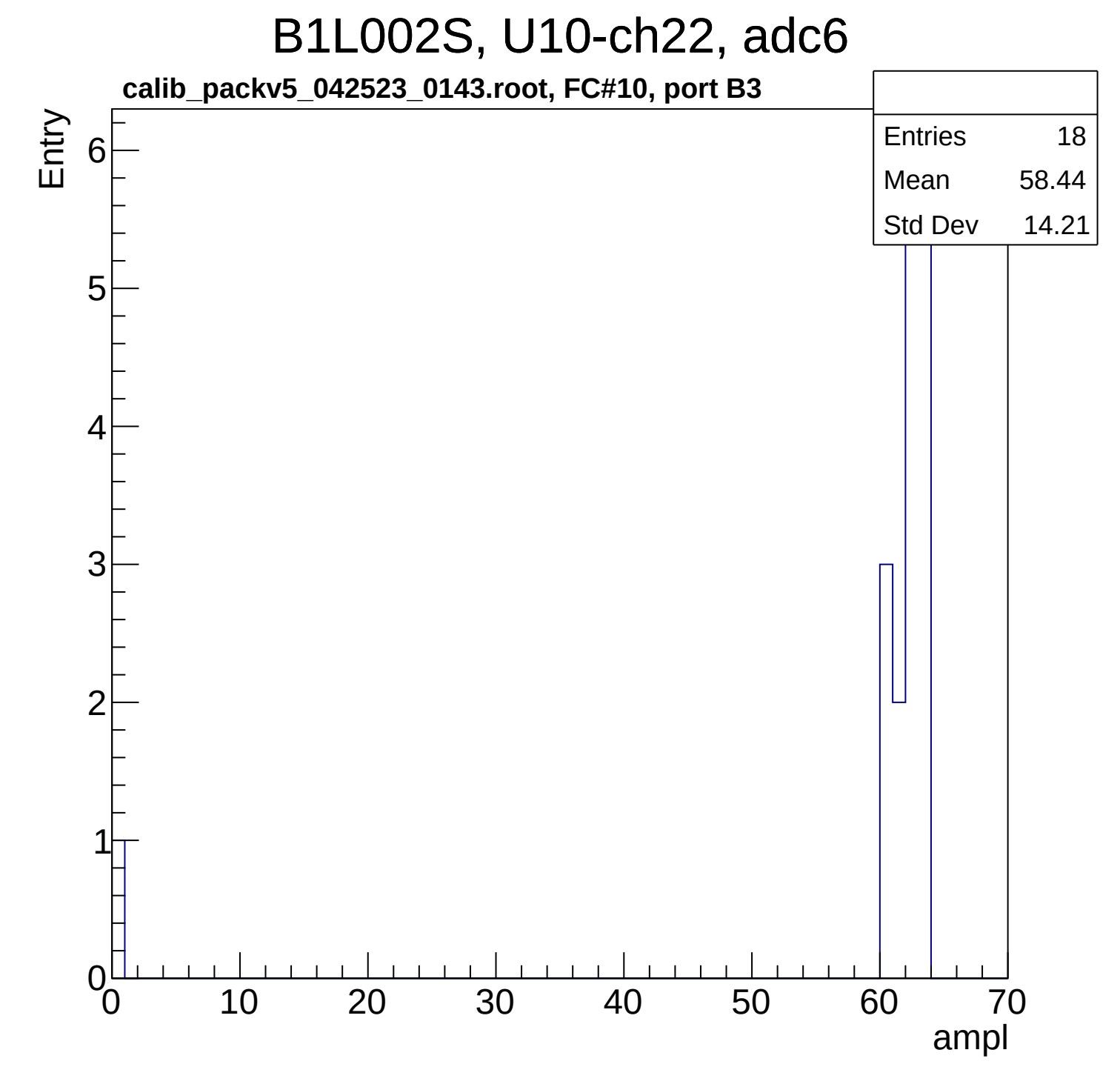
20
30
40
50
60
70


B1L002S, U10-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.87
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.15
Std Dev 3.039
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.44
Std Dev 14.21
Entry
0
1
2
3
4
5
6
0
10
20
30
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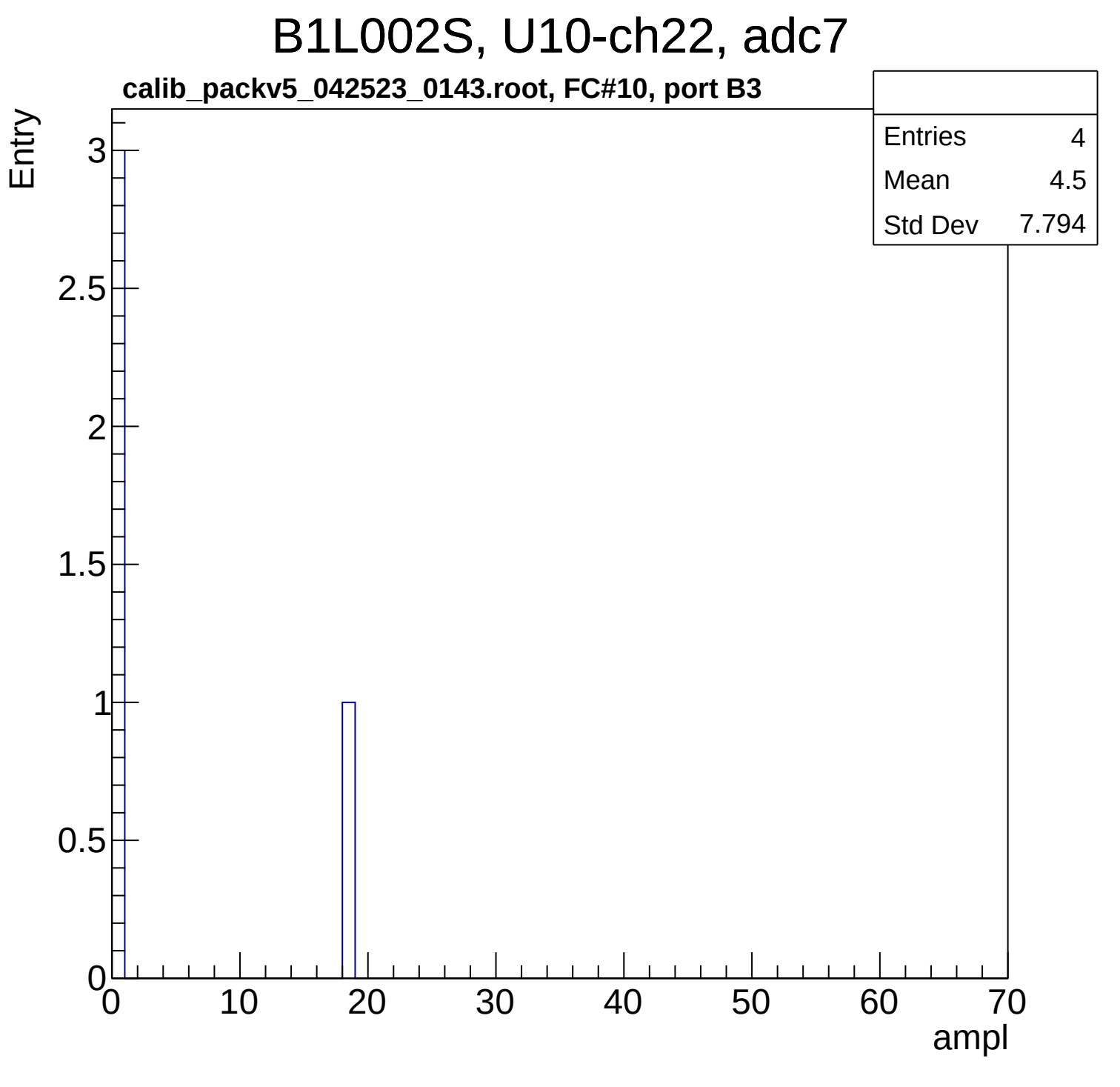
50
60
70
ampl


B1L002S, U10-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 4.5
Std Dev 7.794
Entry
ampl
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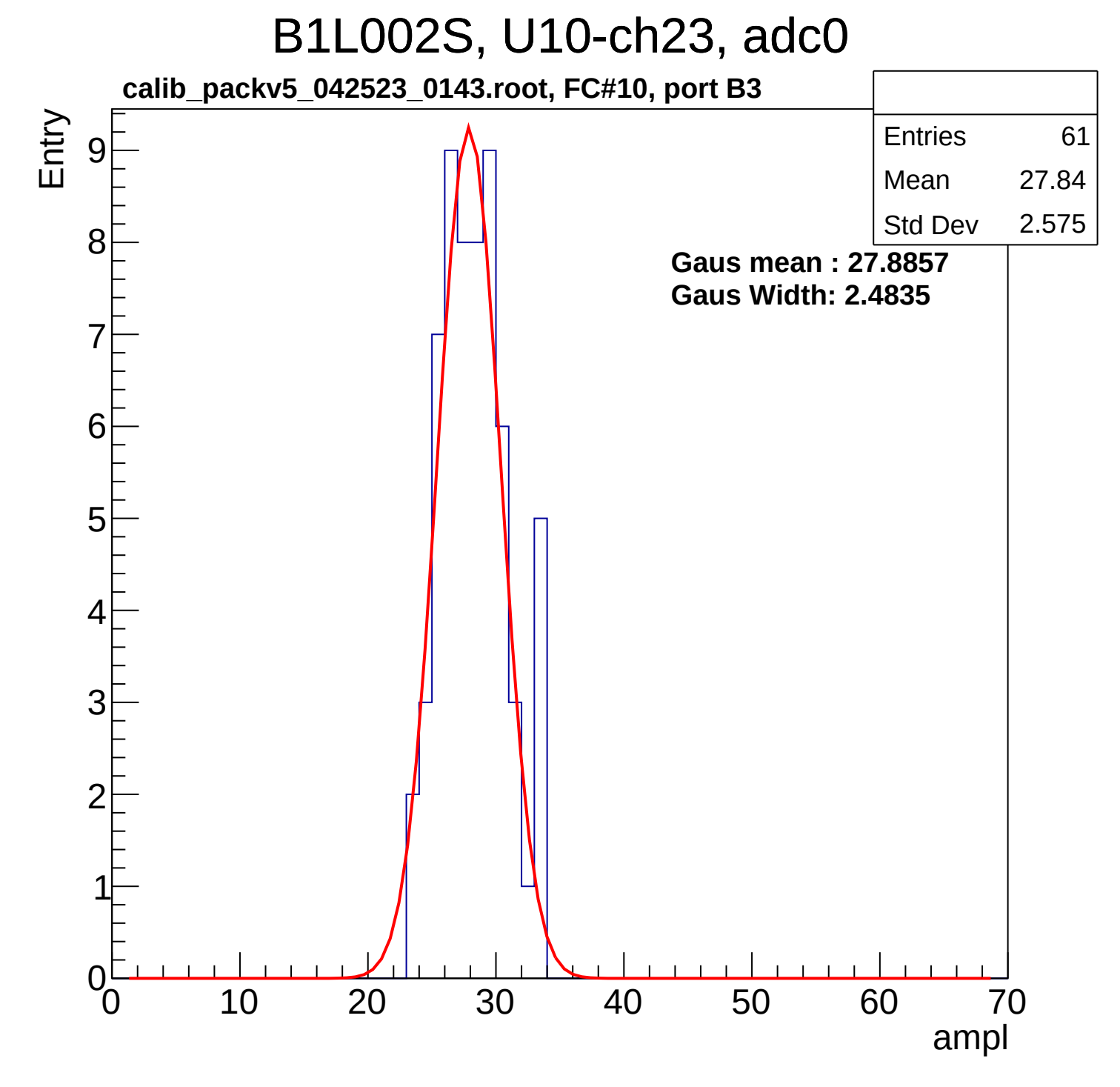
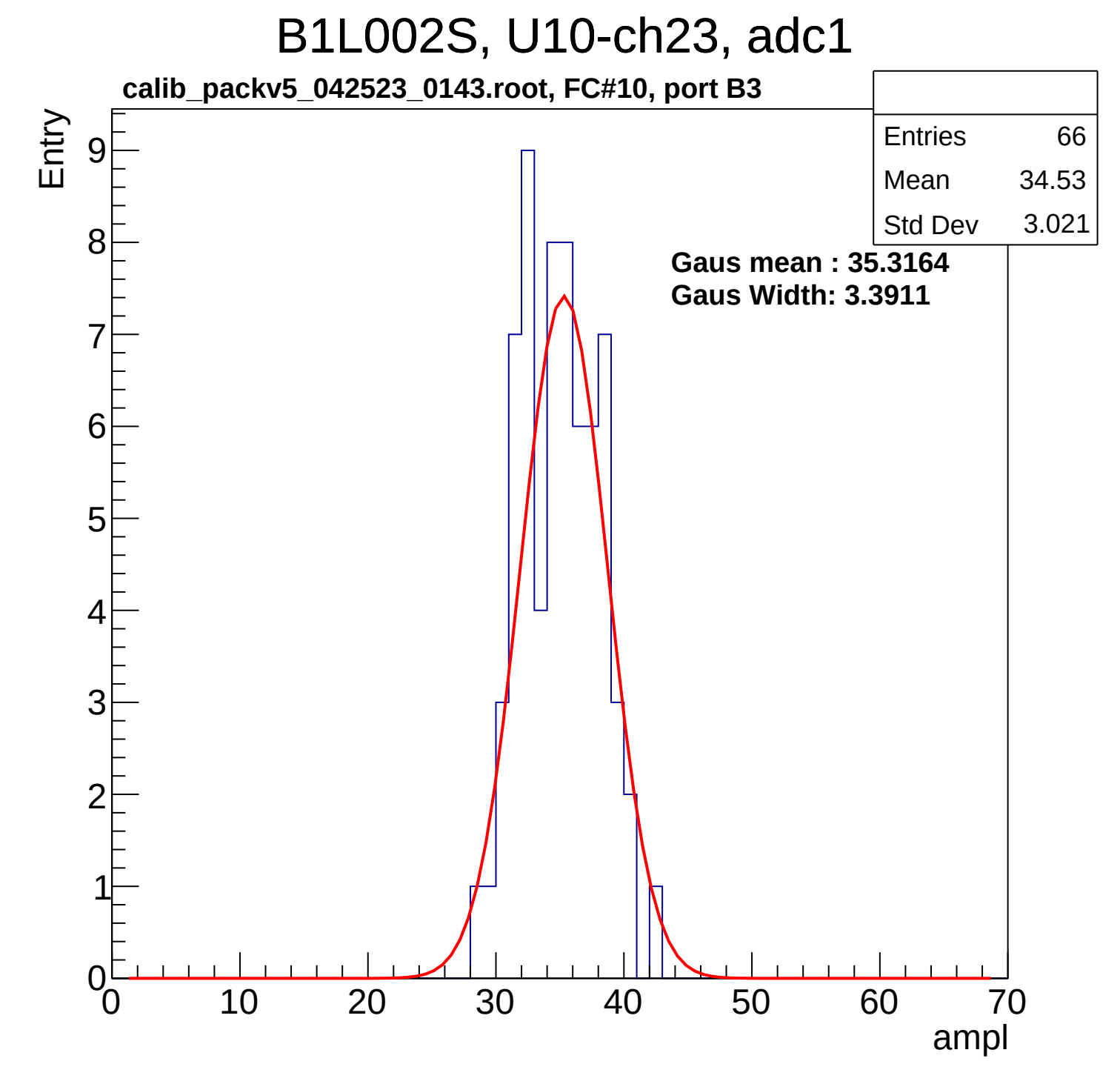
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.84
Std Dev 2.575
Gaus mean : 27.8857
Gaus Width: 2.4835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.53
Std Dev 3.021
Gaus mean : 35.3164
Gaus Width: 3.3911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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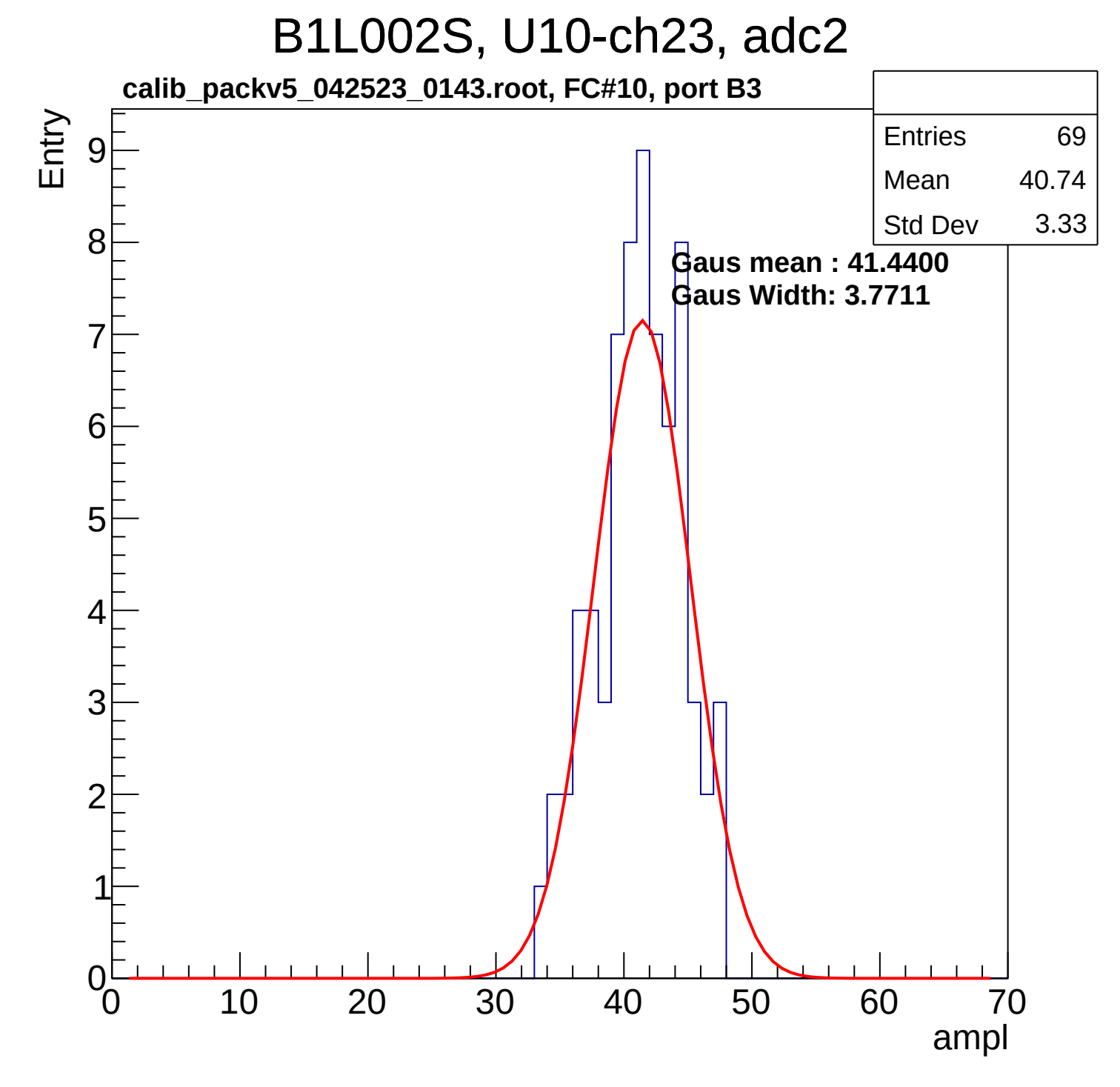
B1L002S, U10-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.74
Std Dev 3.33
Gaus mean : 41.4400
Gaus Width: 3.7711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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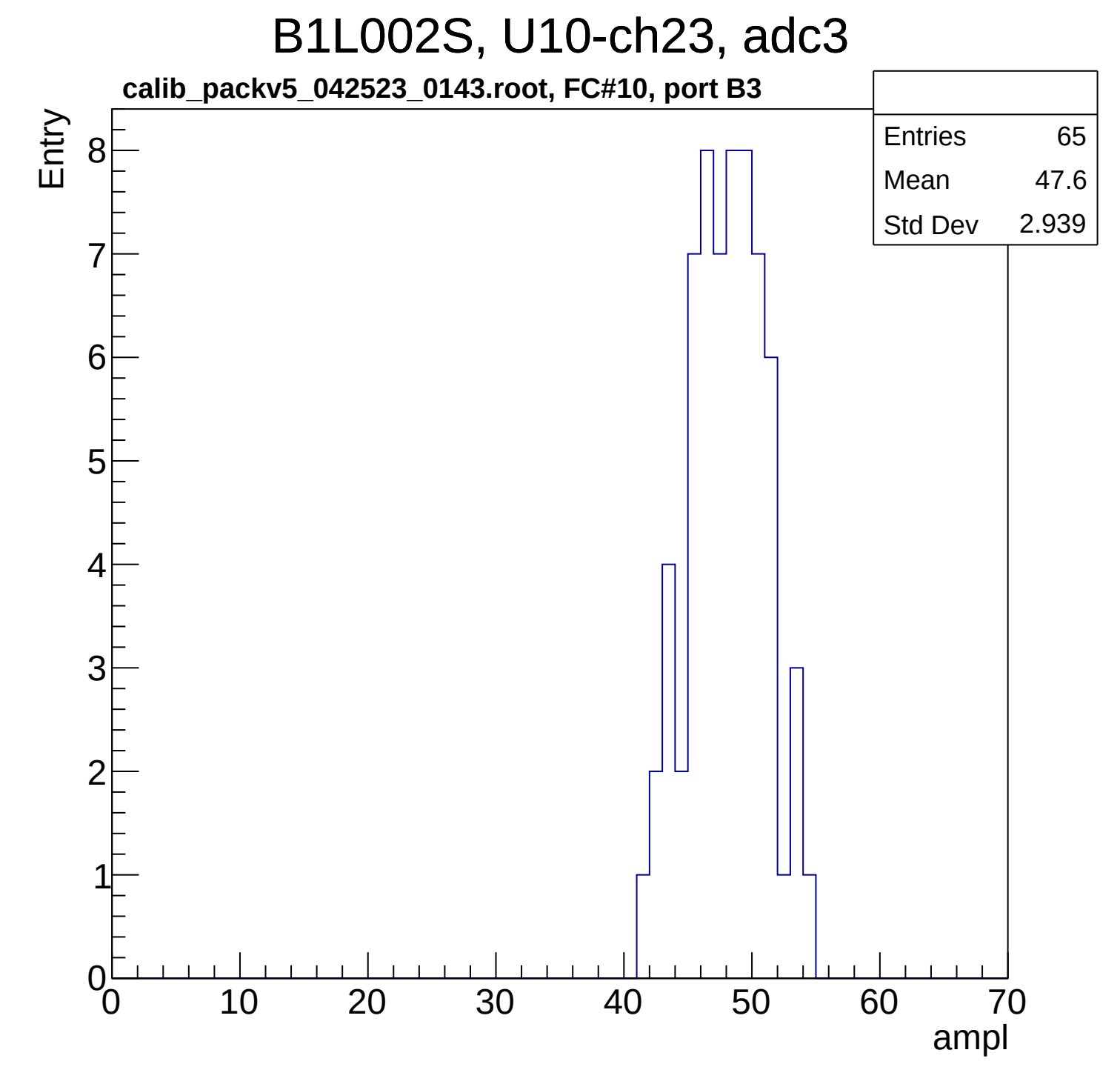
B1L002S, U10-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.6
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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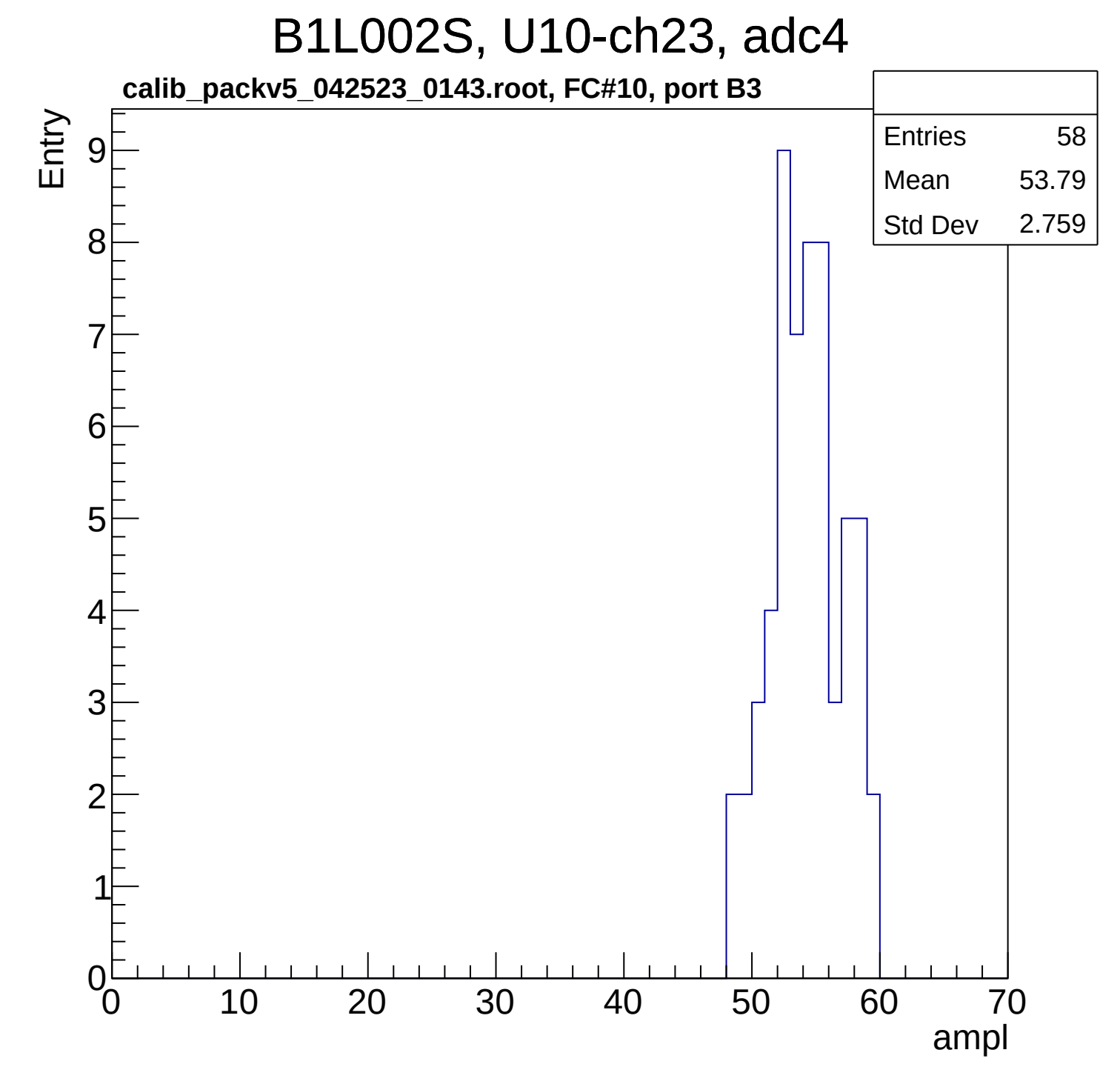
B1L002S, U10-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.79
Std Dev 2.759
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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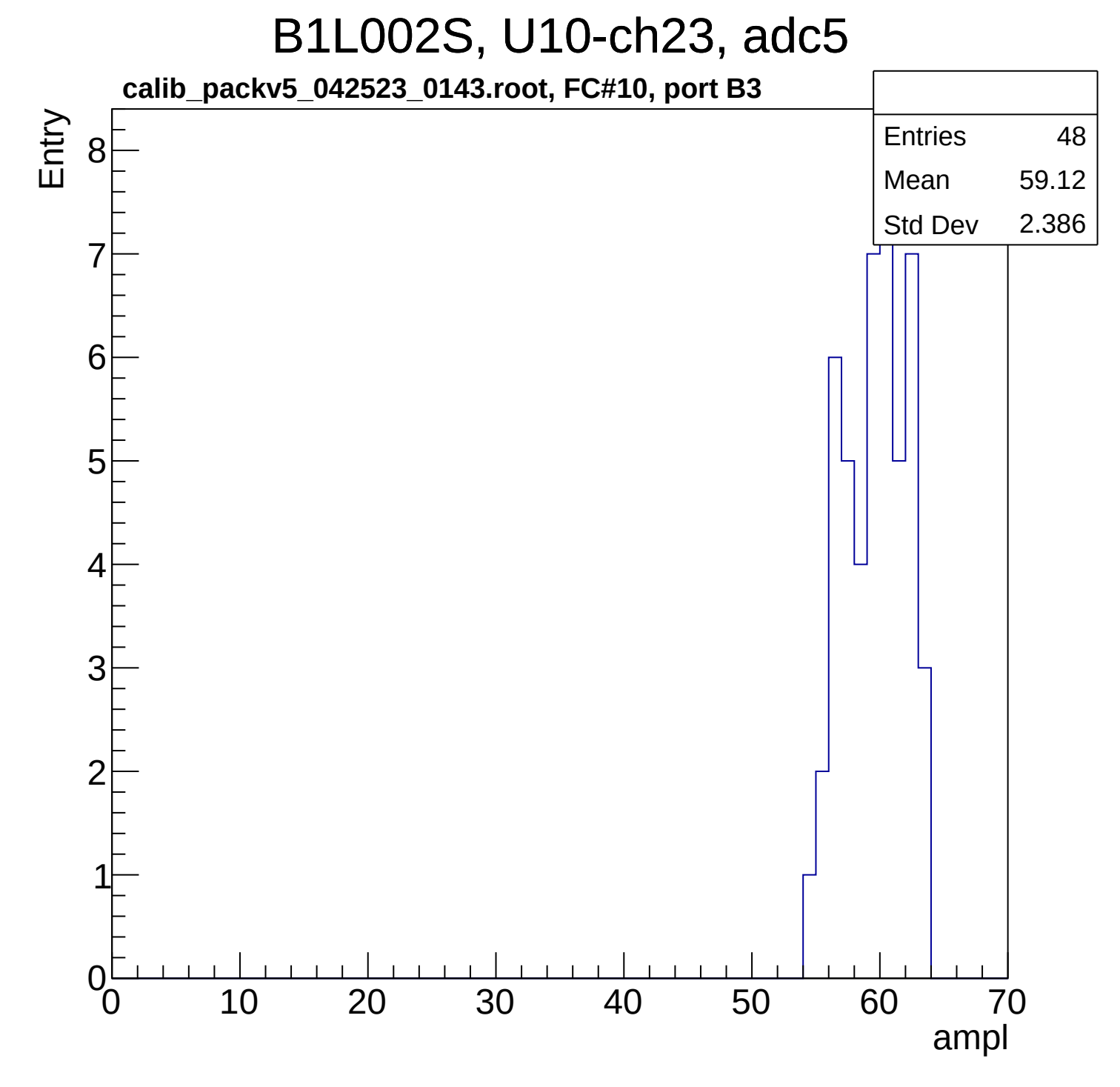
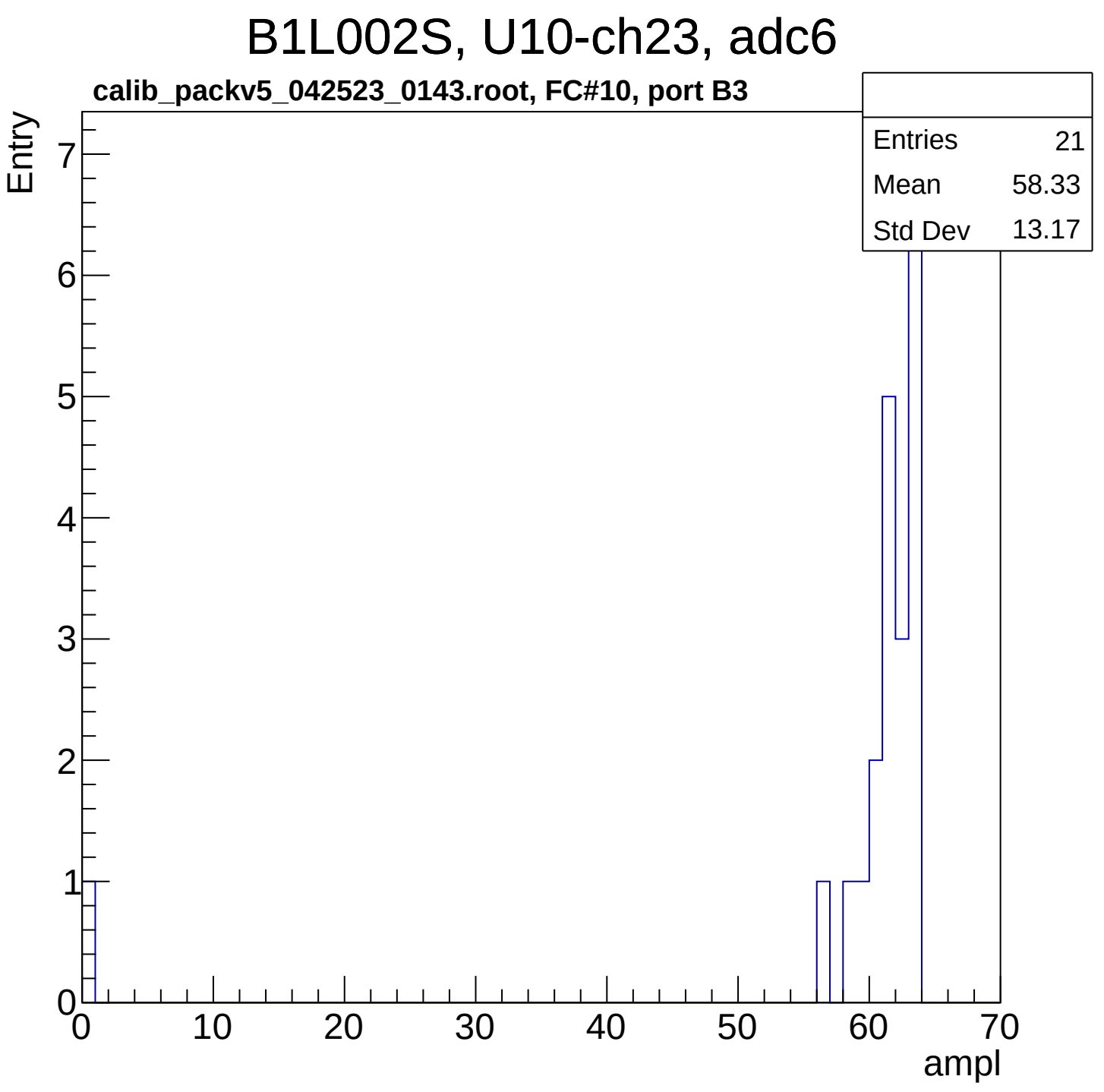

B1L002S, U10-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.12
Std Dev 2.386
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.33
Std Dev 13.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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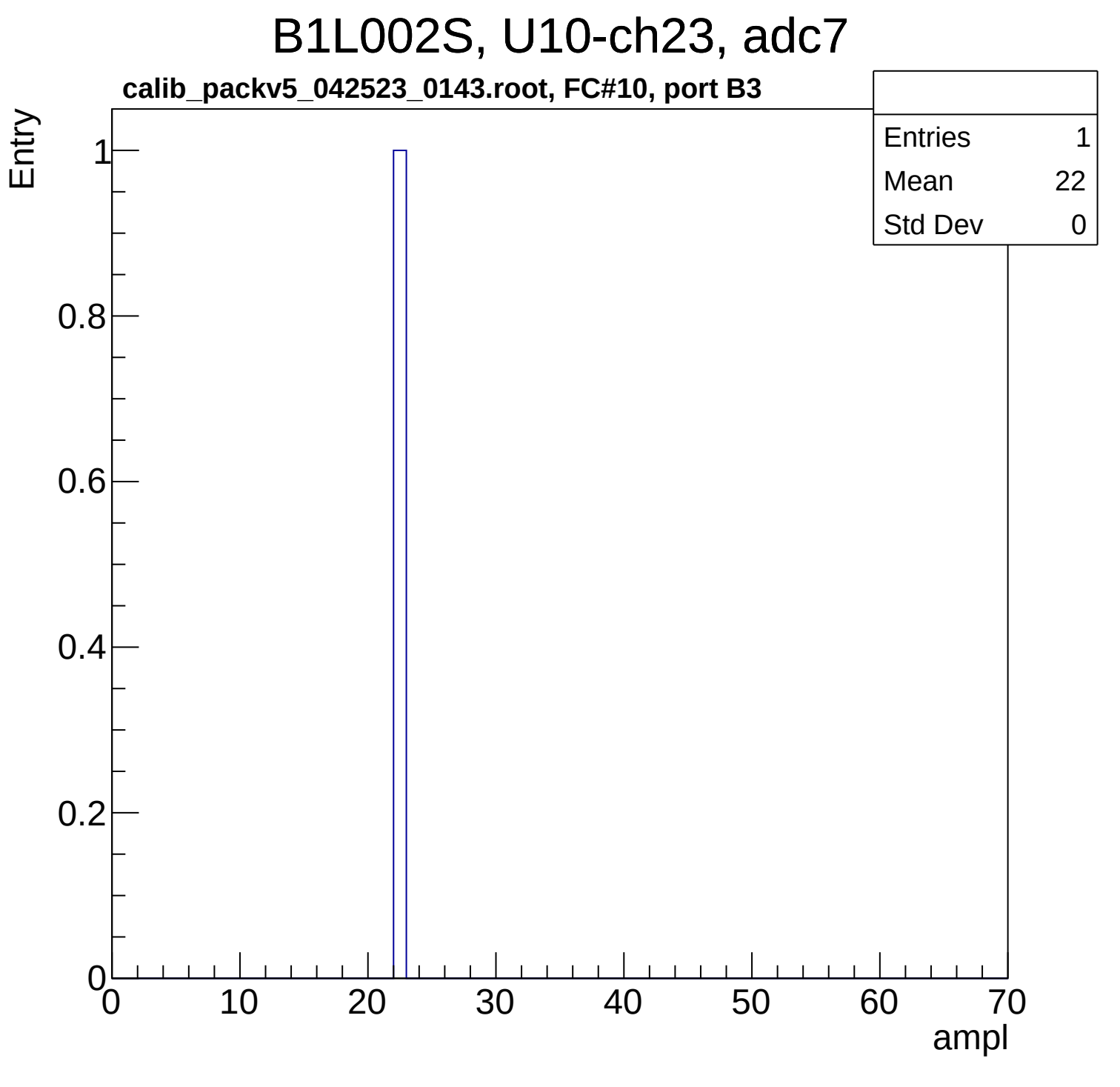
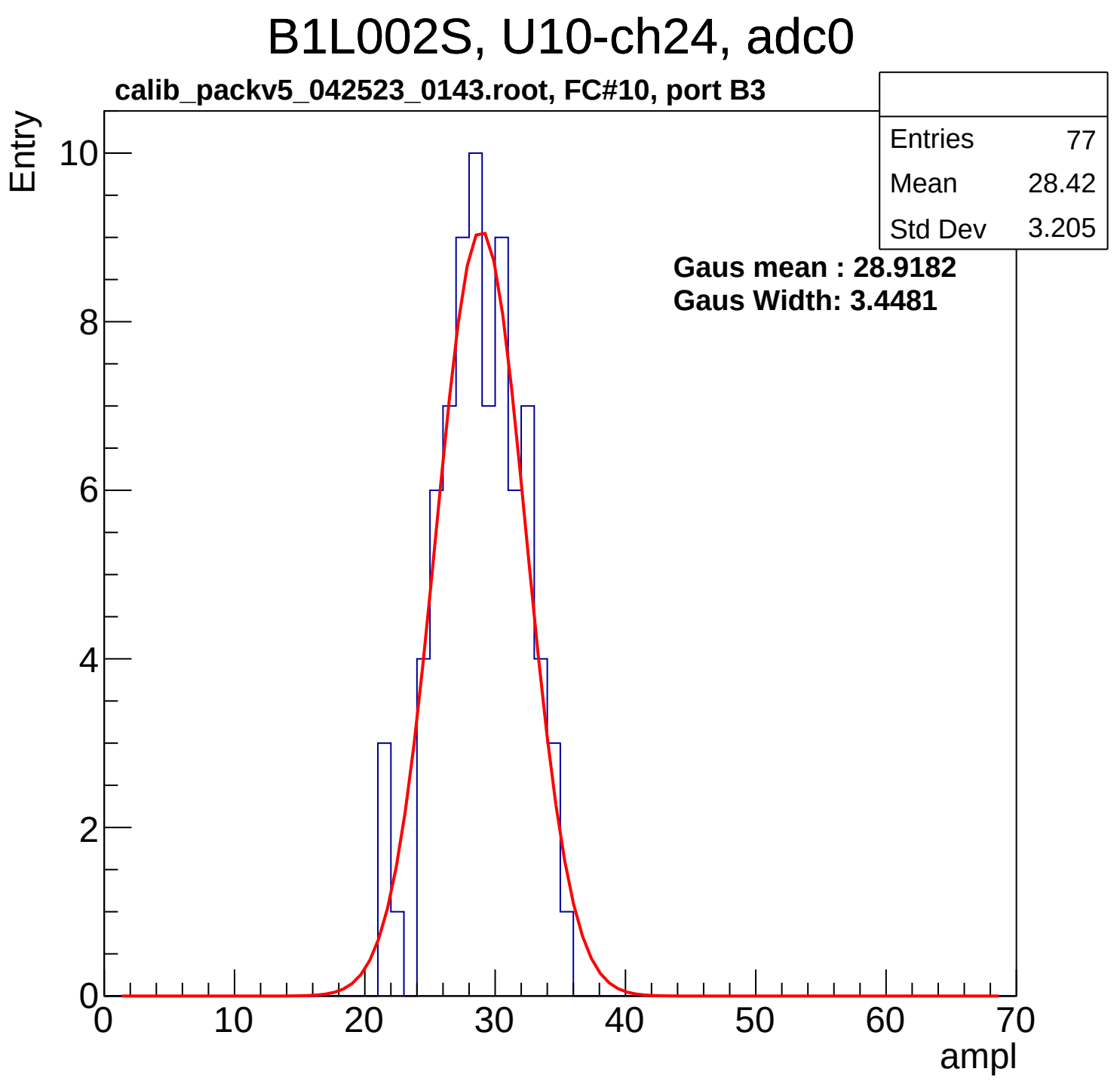

B1L002S, U10-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.42
Std Dev 3.205
Gaus mean : 28.9182
Gaus Width: 3.4481
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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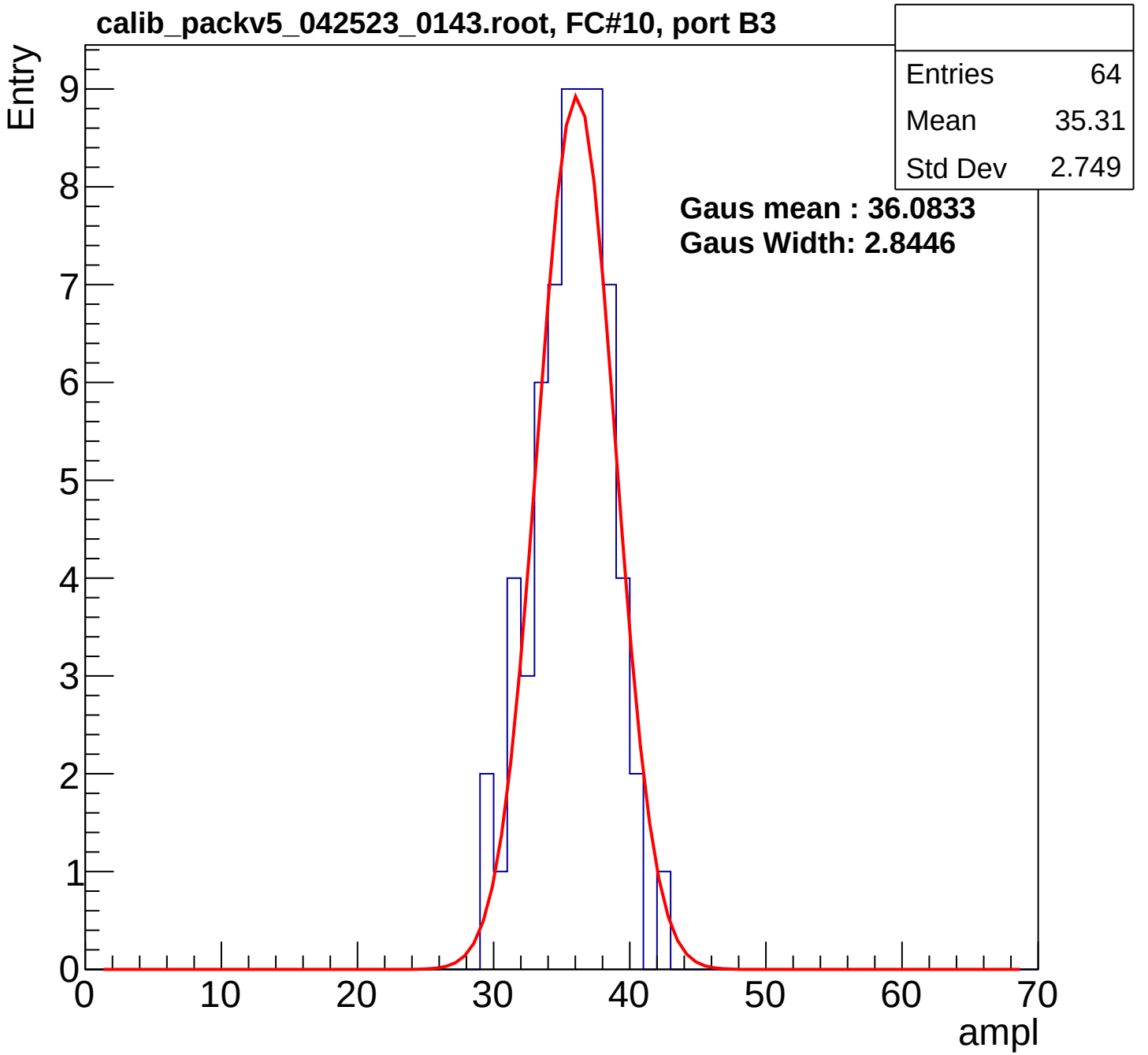
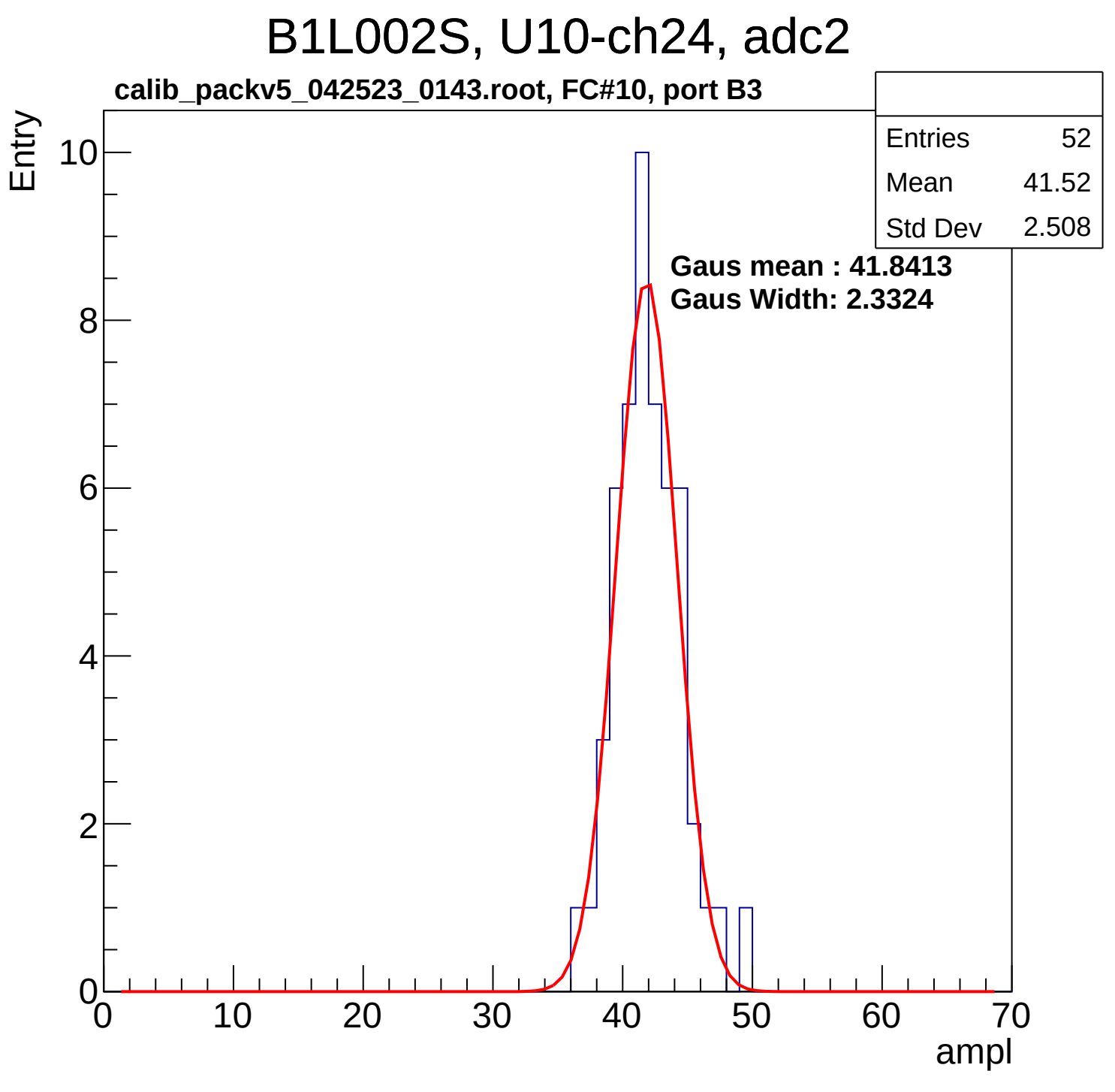
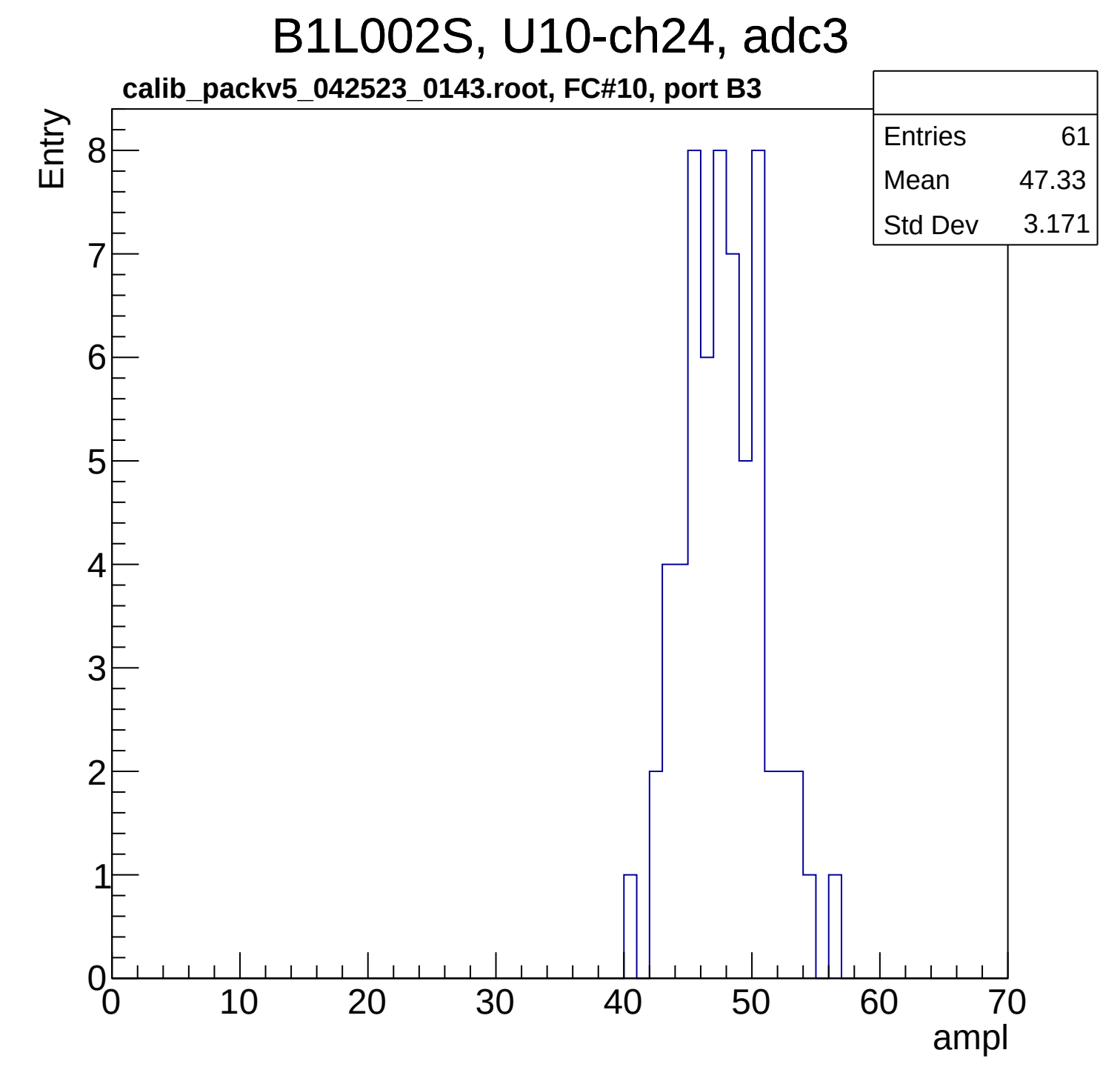
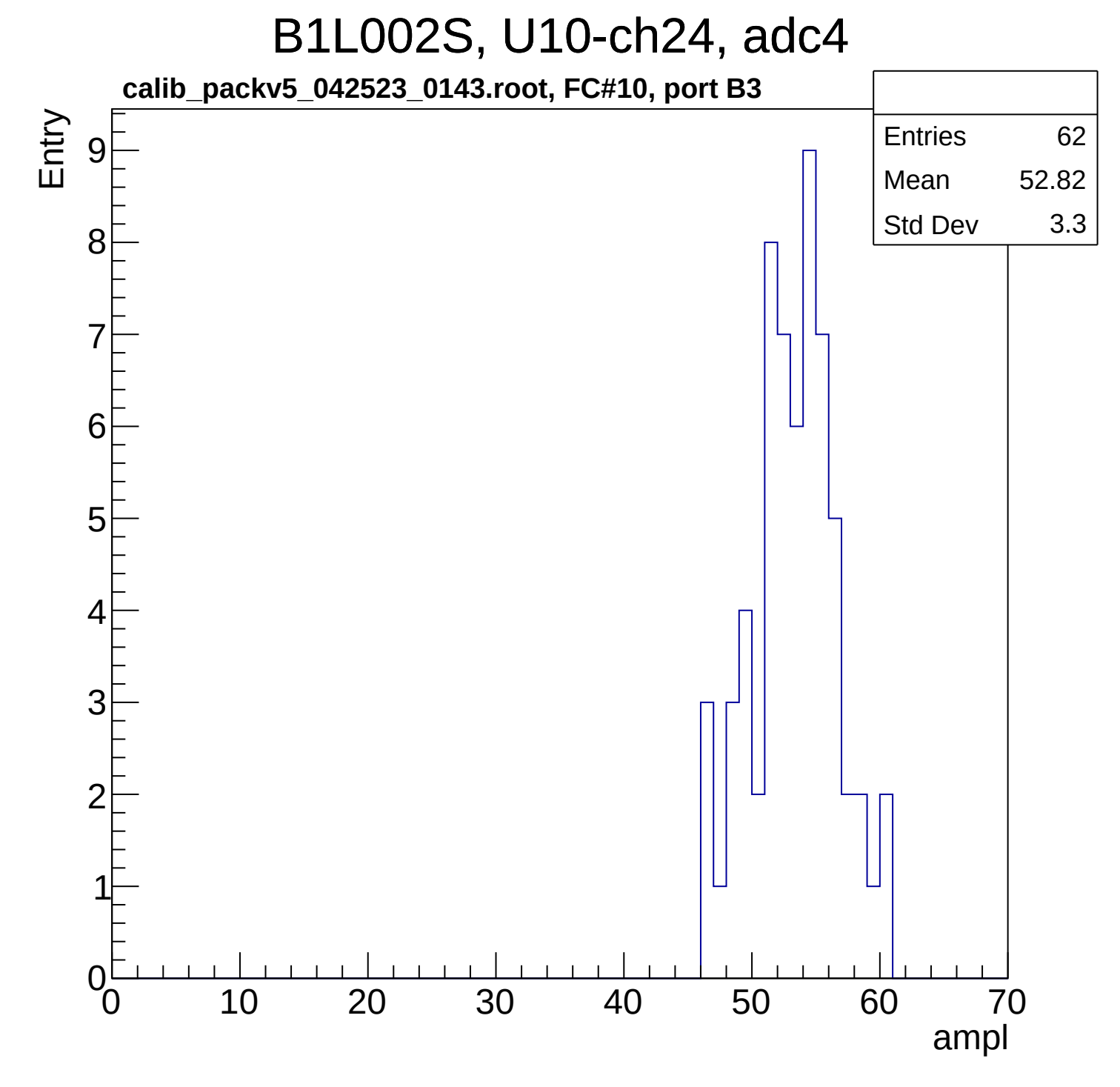
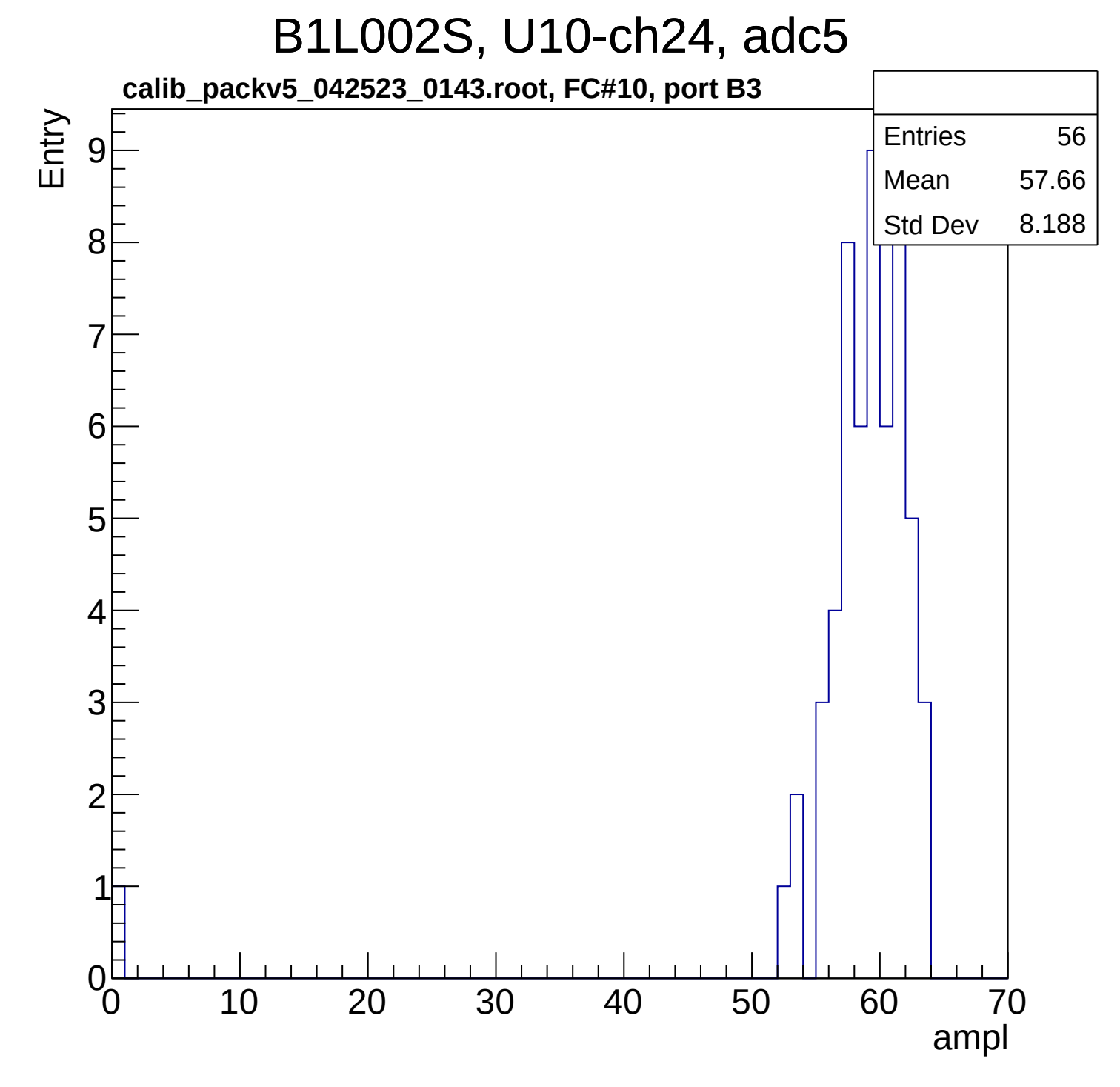
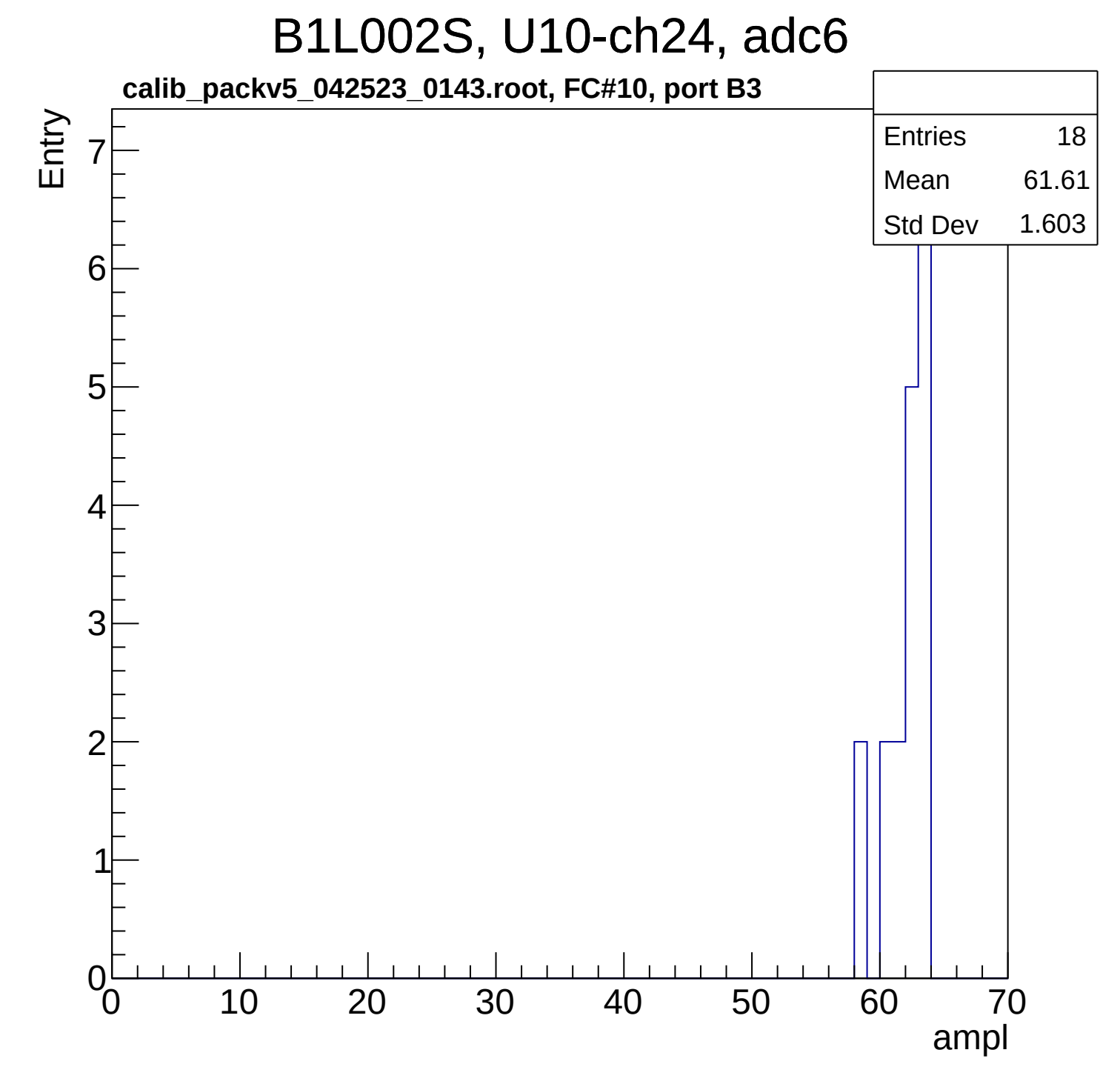

B1L002S, U10-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.31
Std Dev 2.749
Gaus mean : 36.0833
Gaus Width: 2.8446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

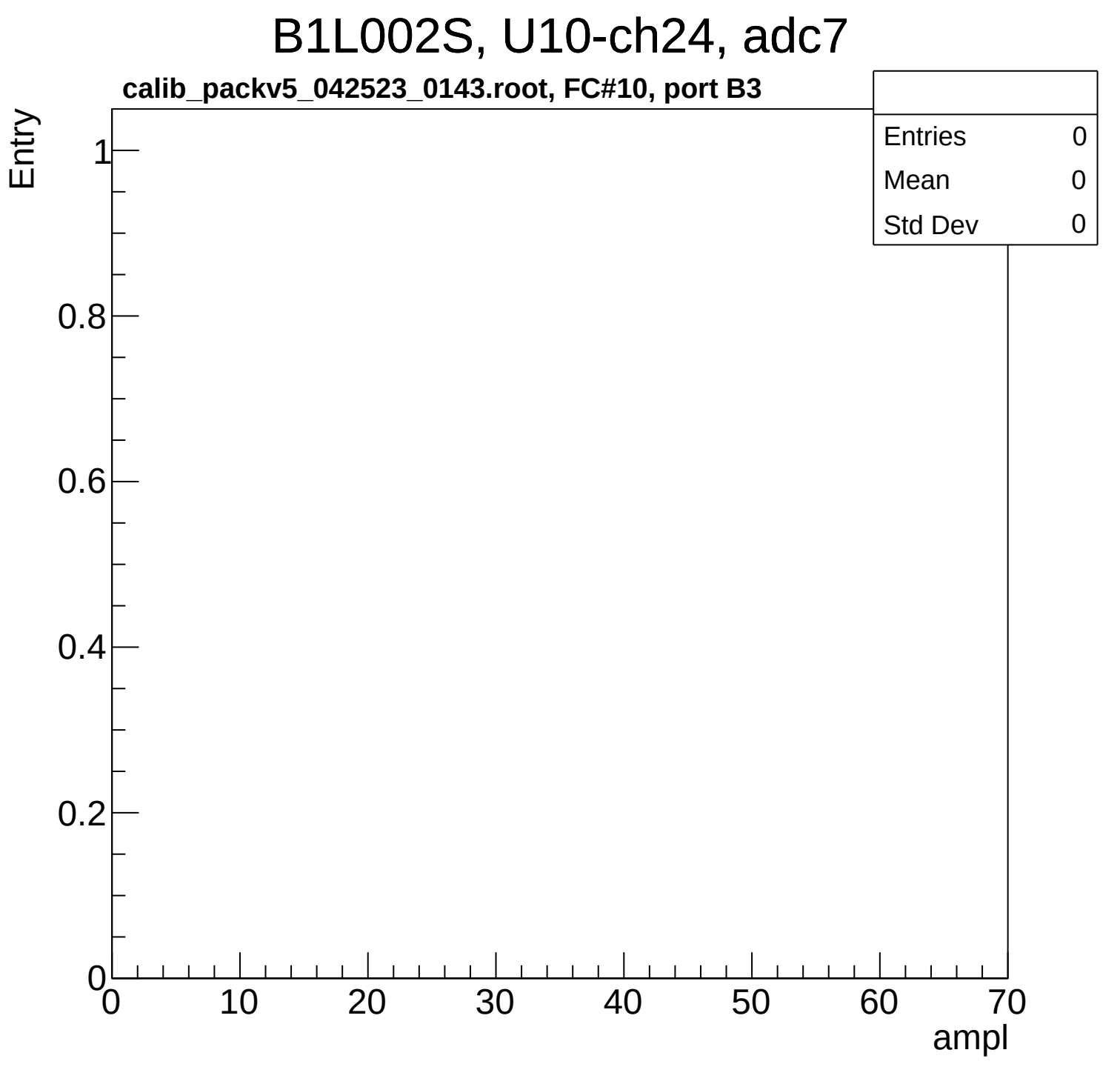

B1L002S, U10-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.52
Std Dev 2.508
Gaus mean : 41.8413
Gaus Width: 2.3324
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

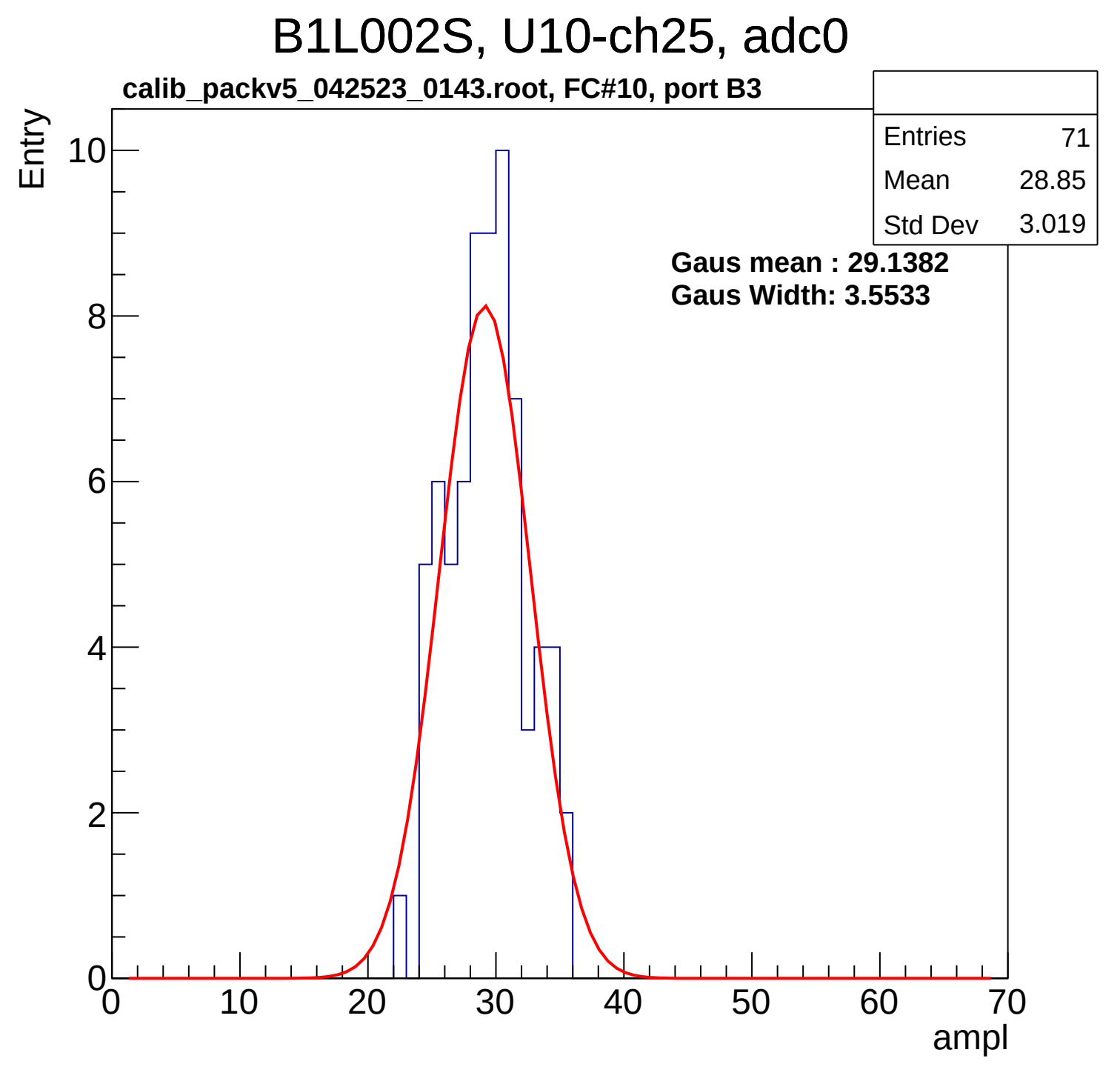

B1L002S, U10-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.33
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

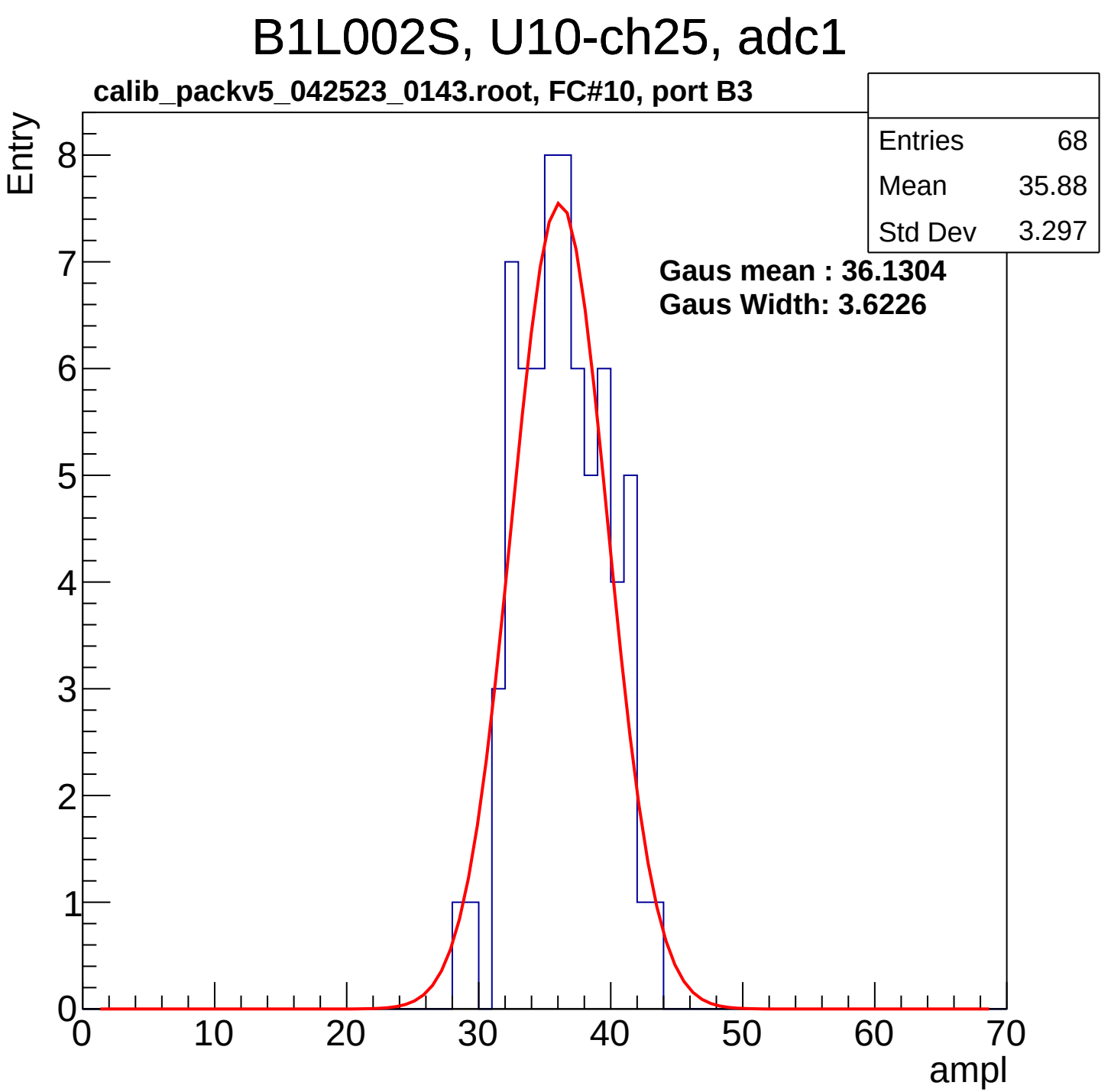

B1L002S, U10-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.82
Std Dev 3.3
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

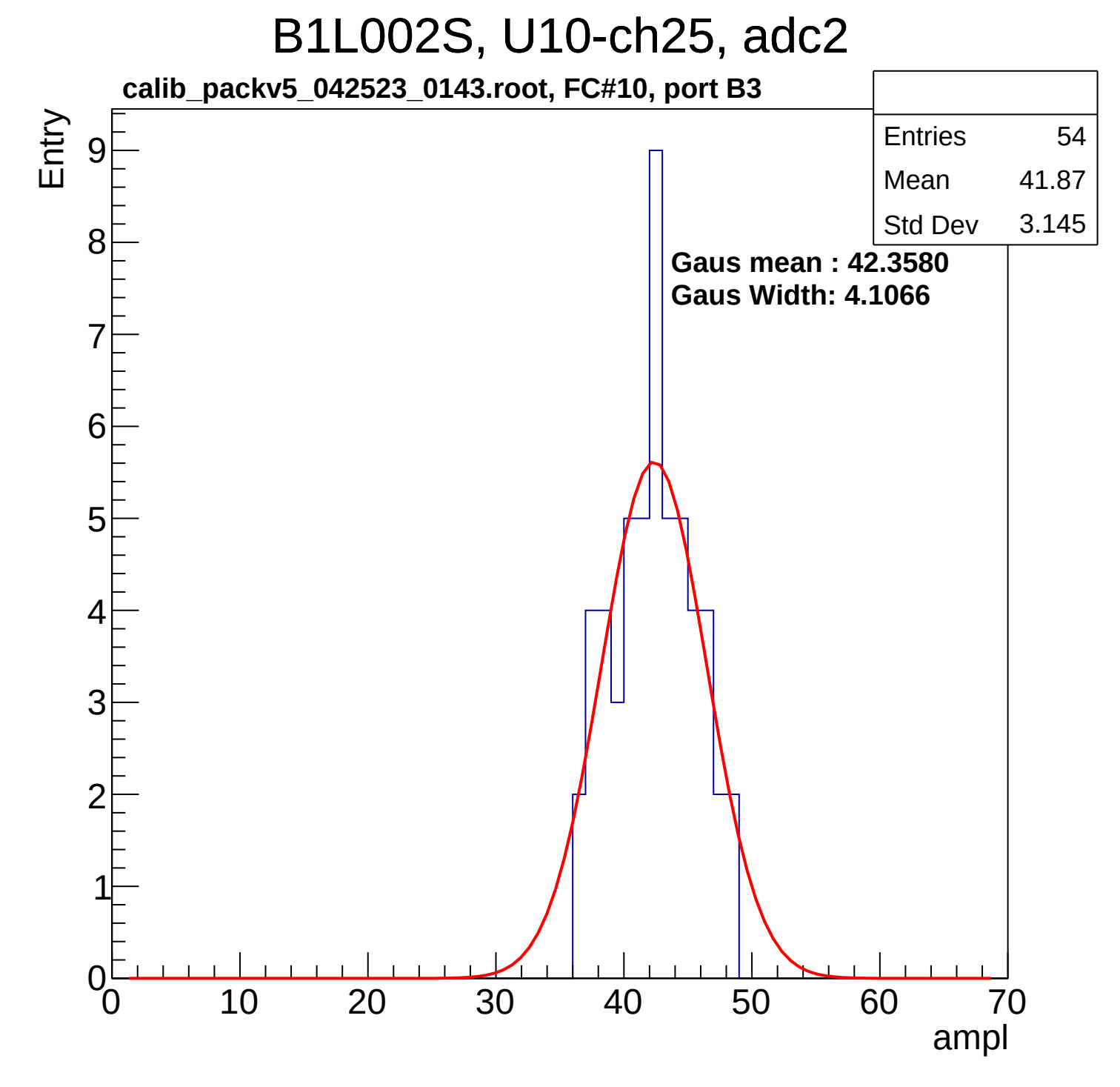

B1L002S, U10-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.66
Std Dev 8.188
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.61
Std Dev 1.603
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.85
Std Dev 3.019
Gaus mean : 29.1382
Gaus Width: 3.5533
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.88
Std Dev 3.297
Gaus mean : 36.1304
Gaus Width: 3.6226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 41.87
Std Dev 3.145
Gaus mean : 42.3580
Gaus Width: 4.1066
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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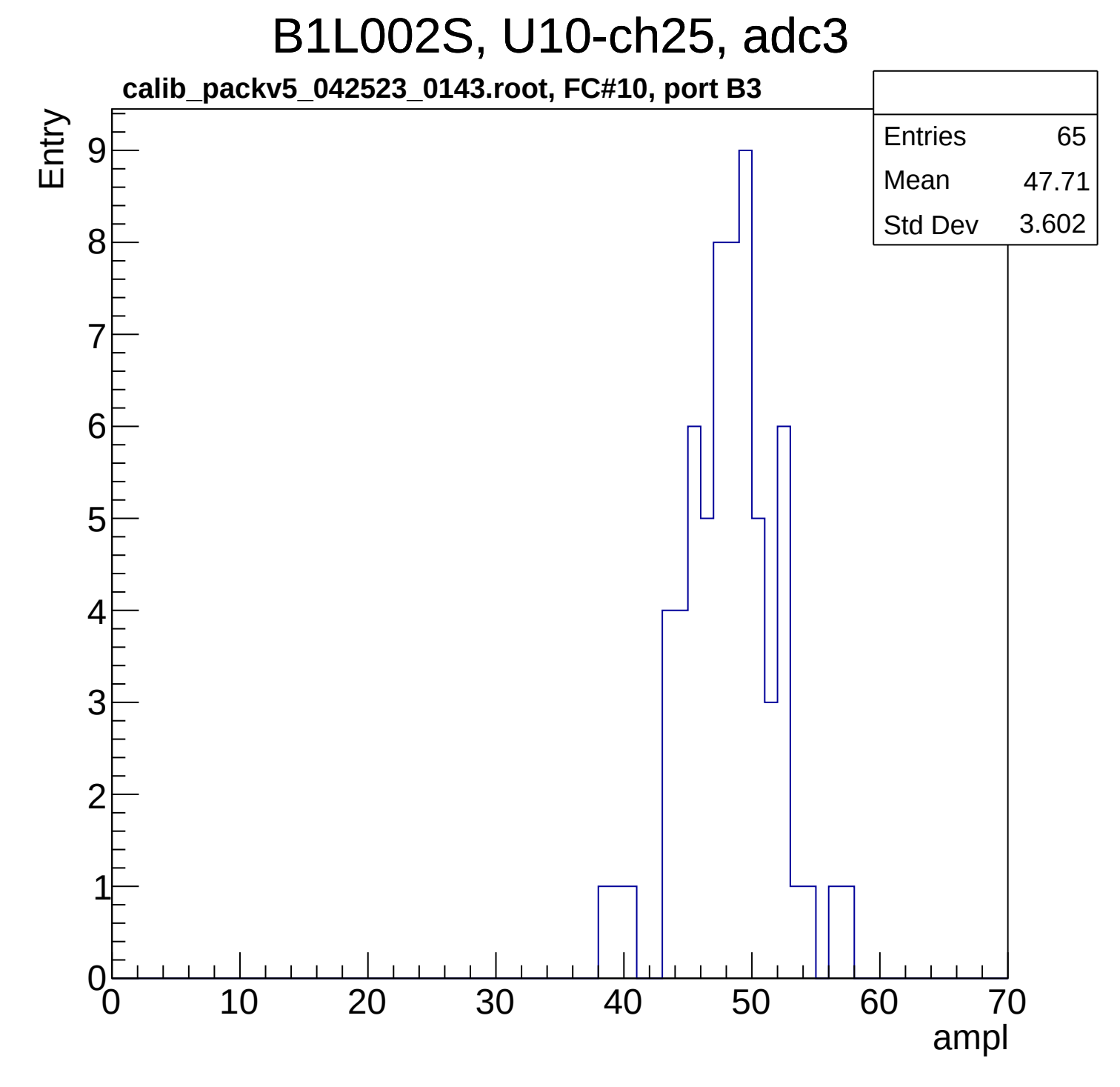
B1L002S, U10-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.71
Std Dev 3.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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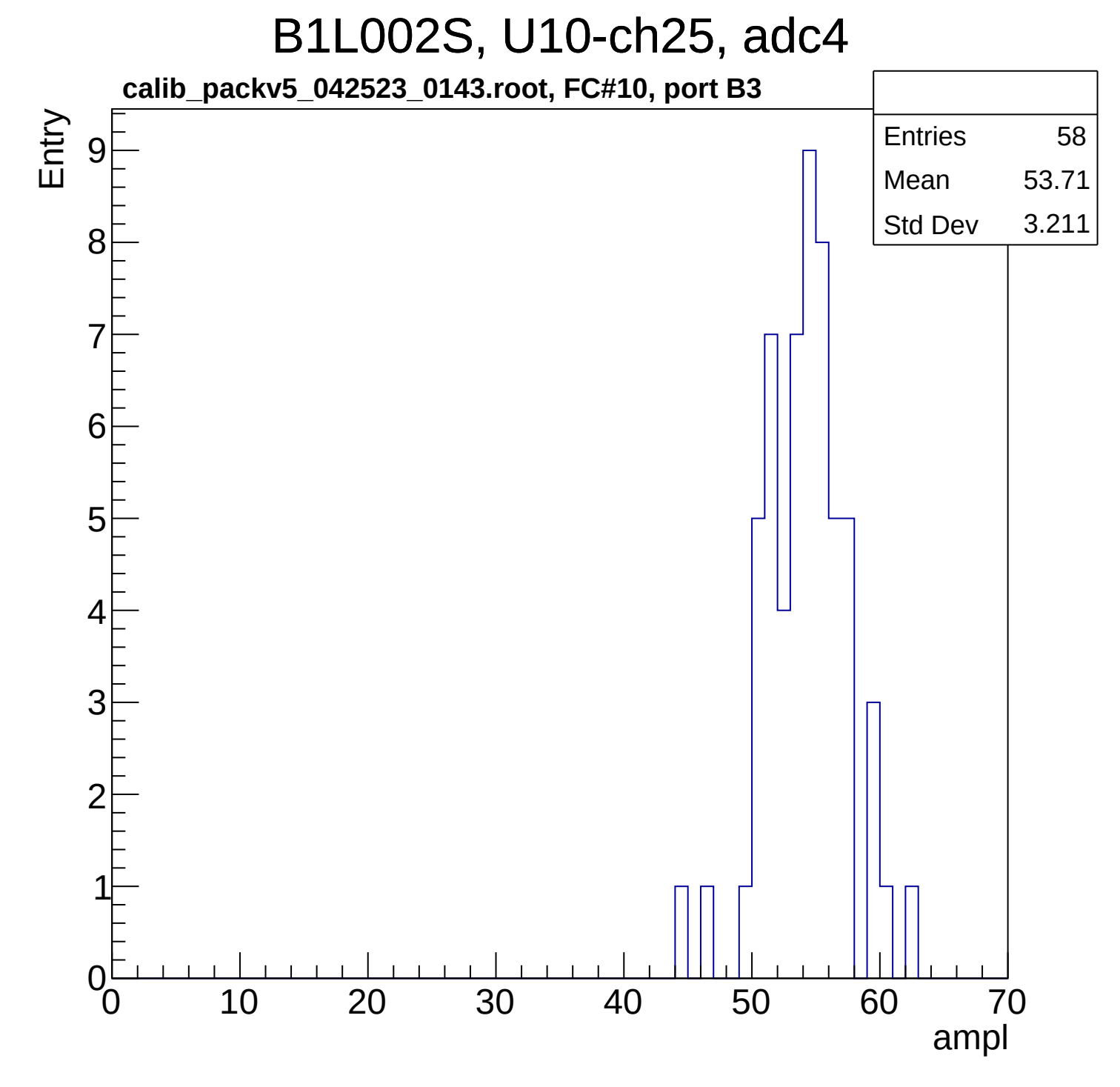
40
50
60
70


B1L002S, U10-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.71
Std Dev 3.211
Entry
ampl
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2
3
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5
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0
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50
60
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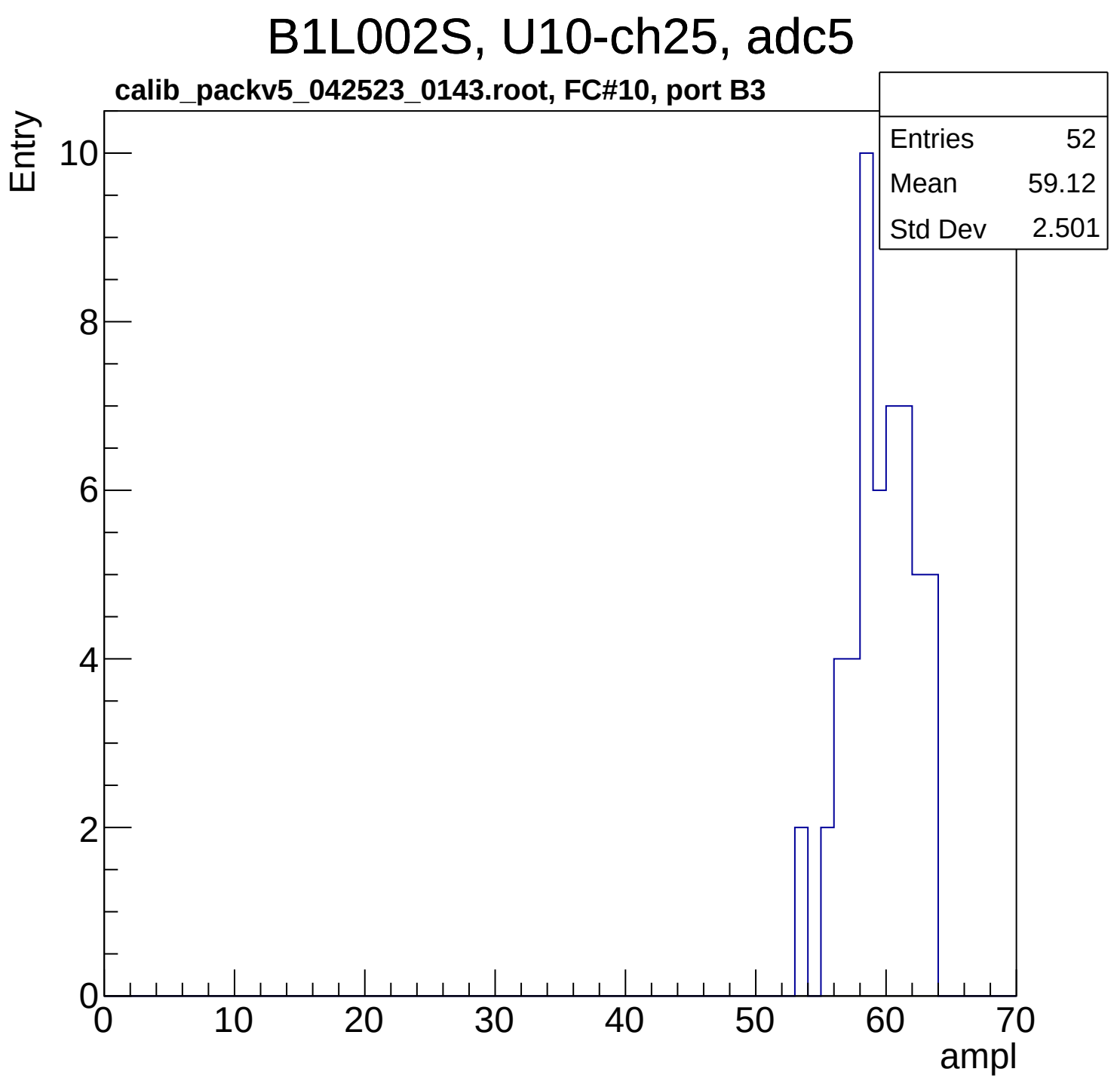
B1L002S, U10-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 59.12
Std Dev 2.501
Entry
ampl
0
2
4
6
8
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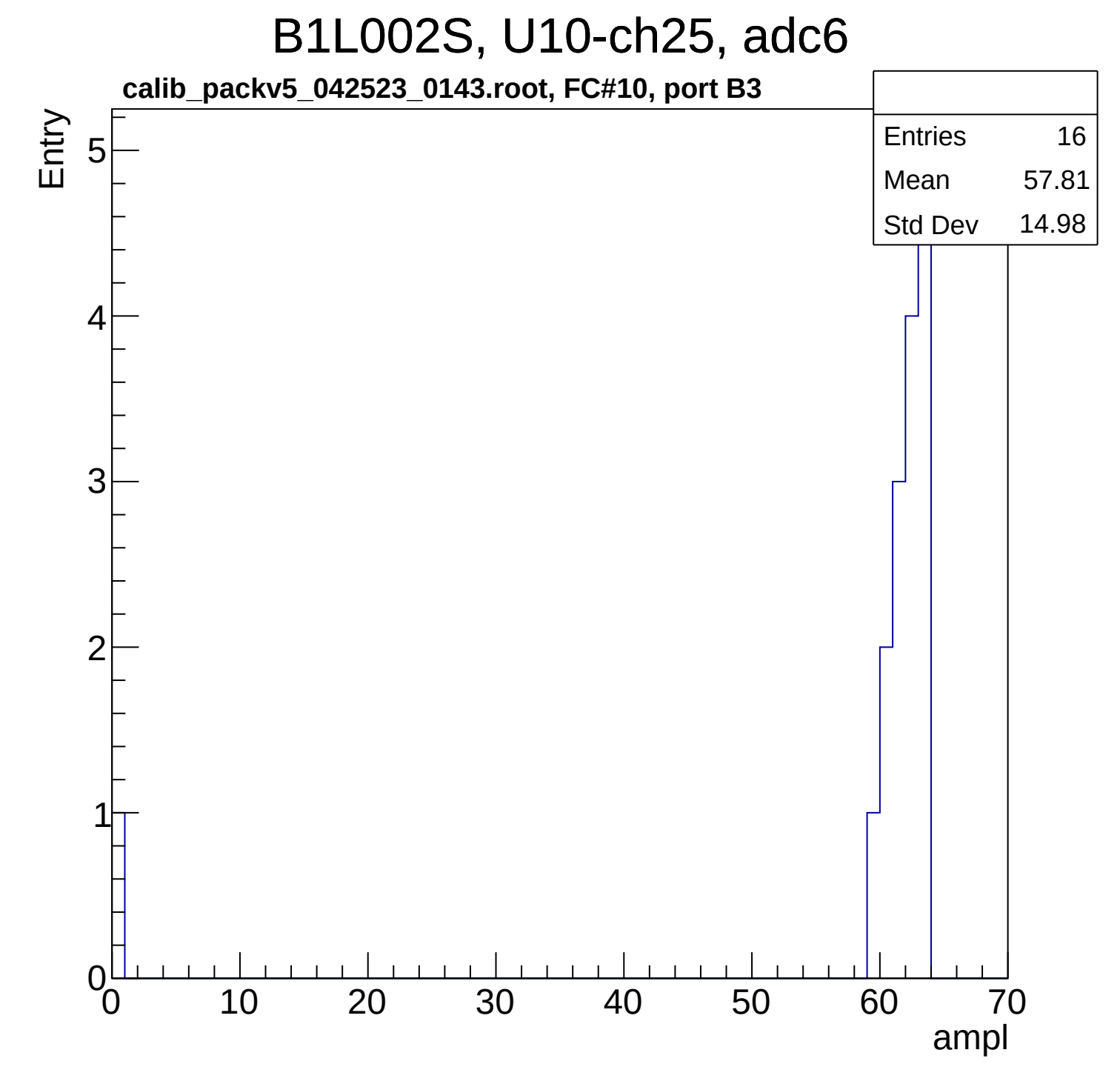
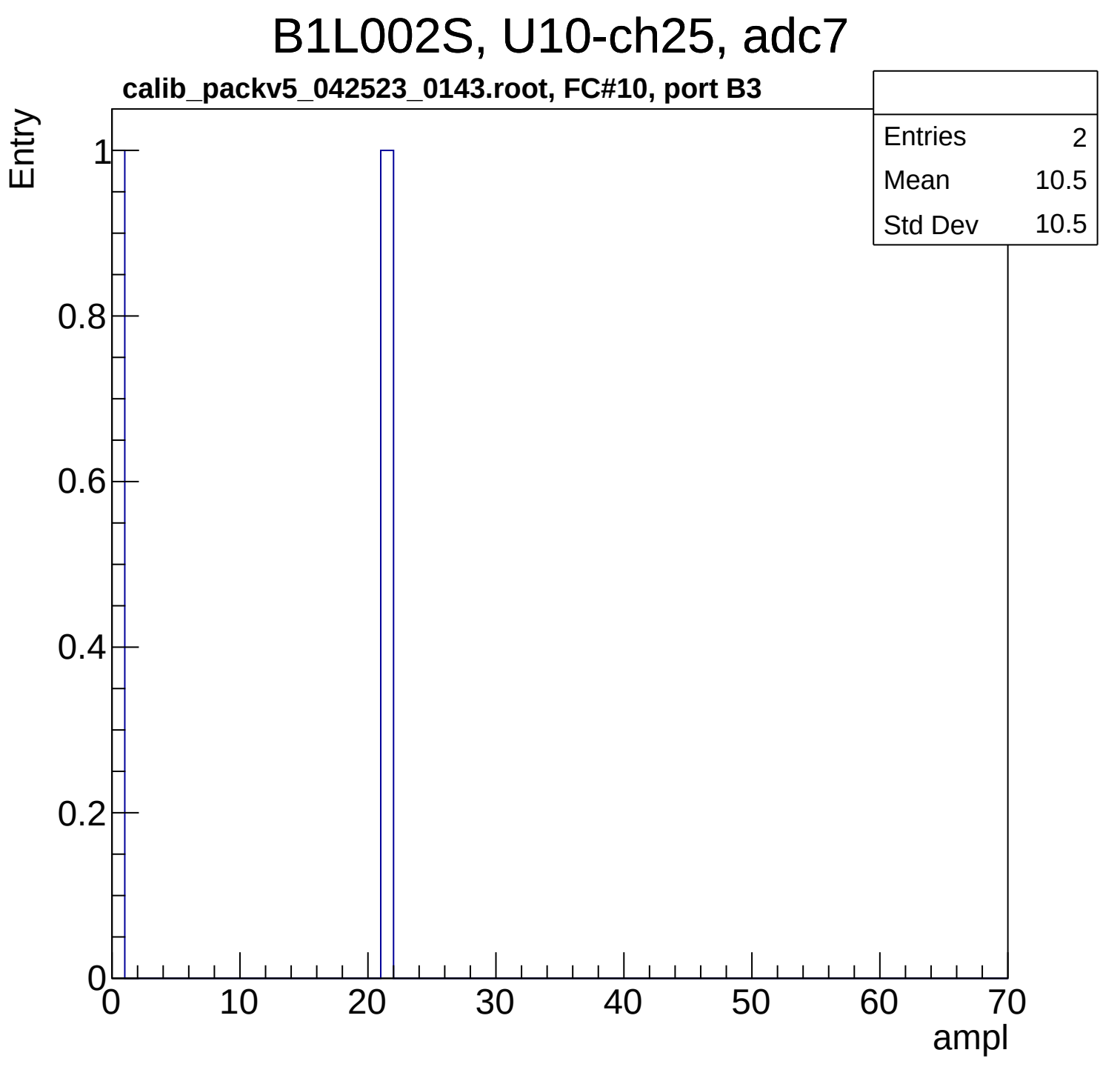
0
10
20
30
40
50
60
70


B1L002S, U10-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.81
Std Dev 14.98
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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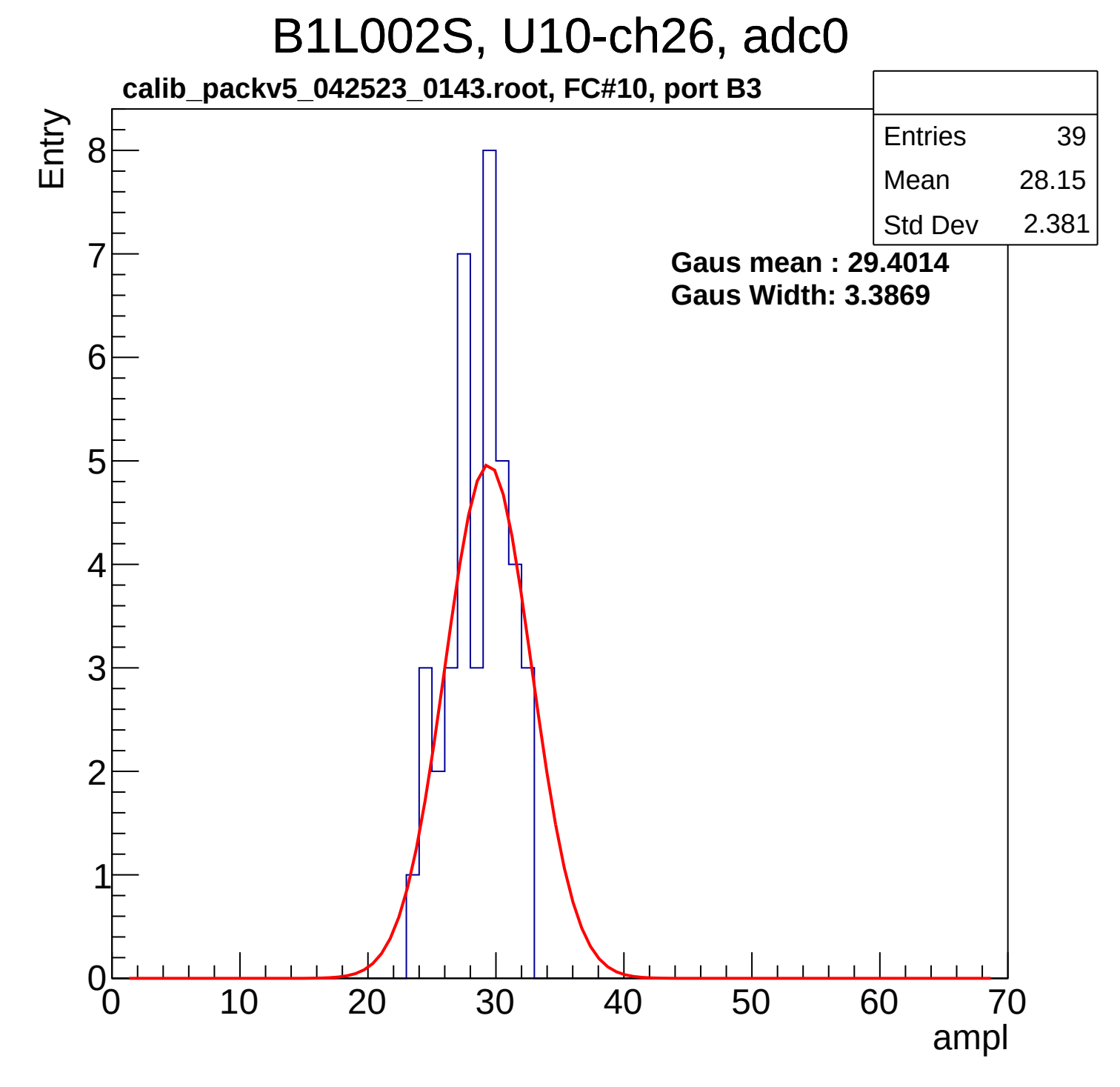
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 28.15
Std Dev 2.381
Gaus mean : 29.4014
Gaus Width: 3.3869
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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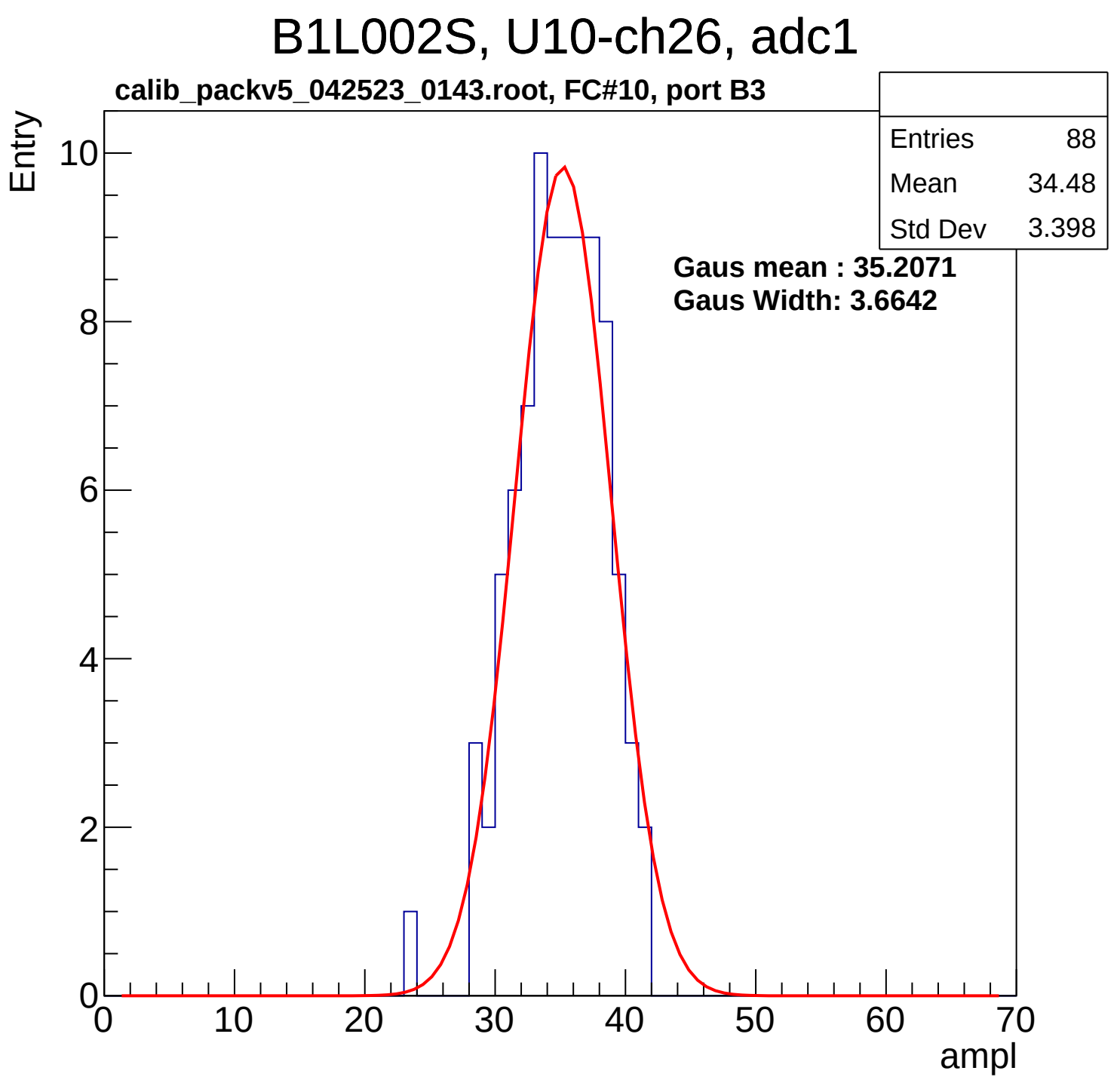
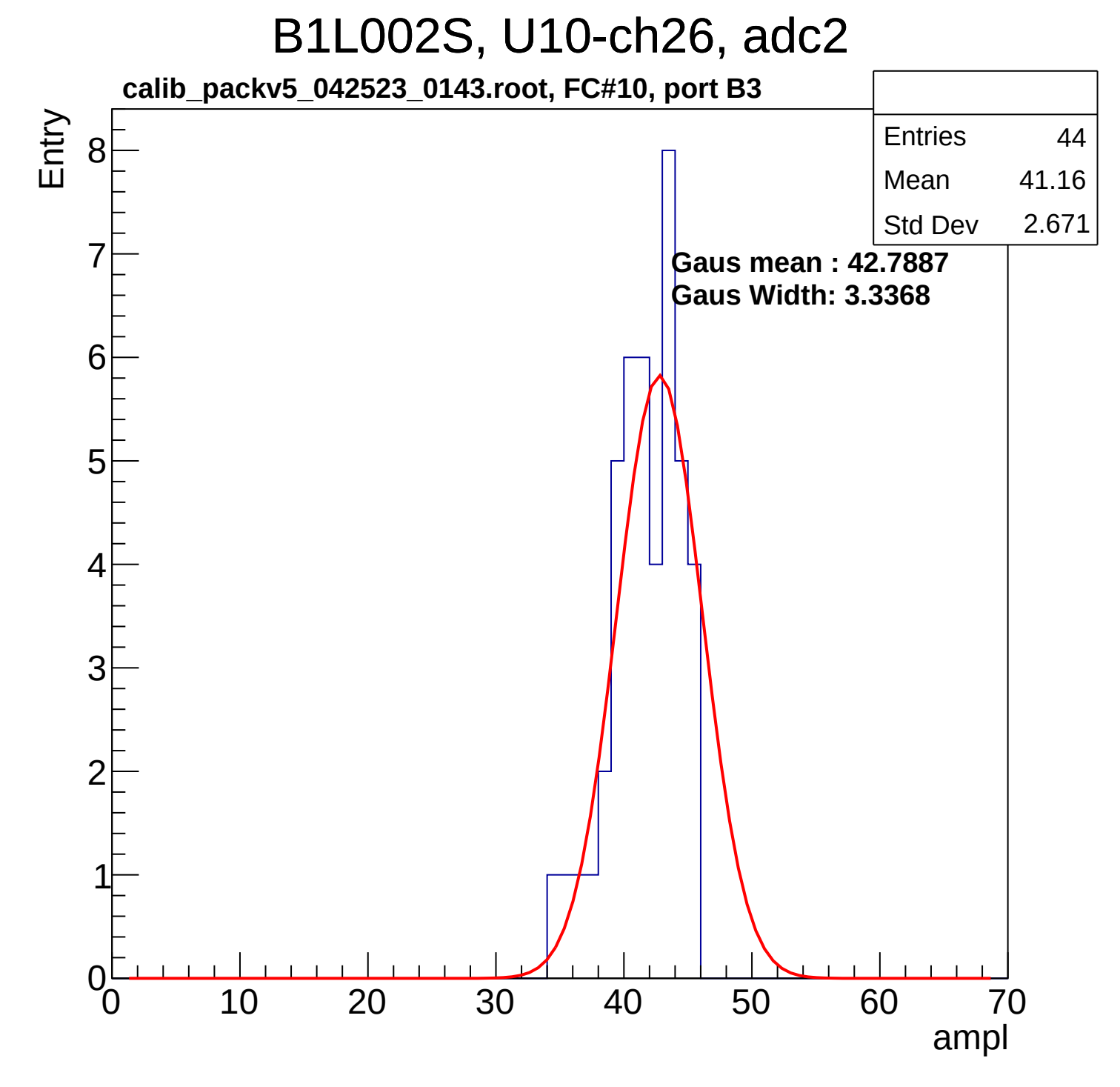

B1L002S, U10-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 88
Mean 34.48
Std Dev 3.398
Gaus mean : 35.2071
Gaus Width: 3.6642
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 41.16
Std Dev 2.671
Gaus mean : 42.7887
Gaus Width: 3.3368
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
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50
60
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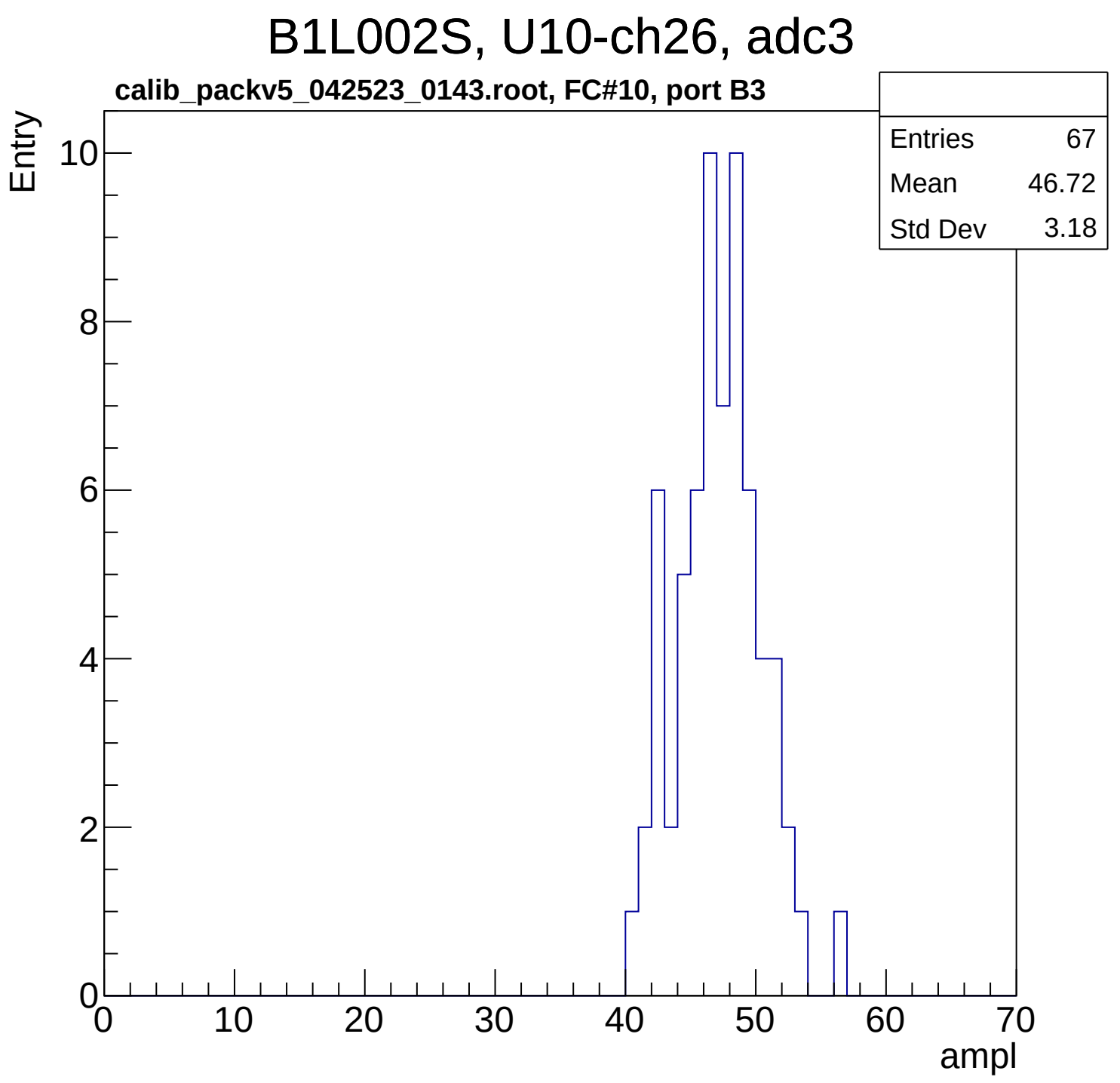
B1L002S, U10-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.72
Std Dev 3.18
Entry
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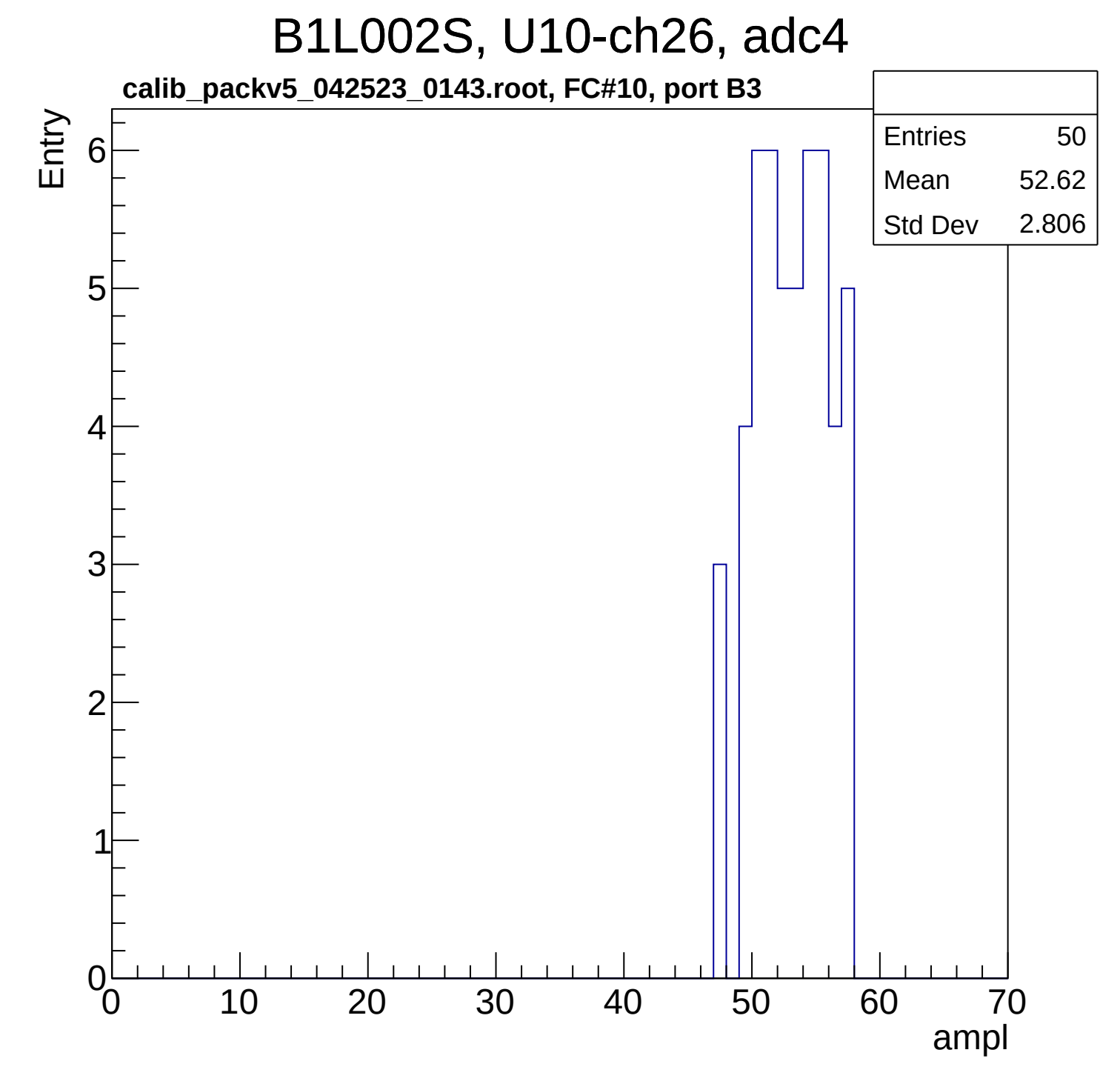
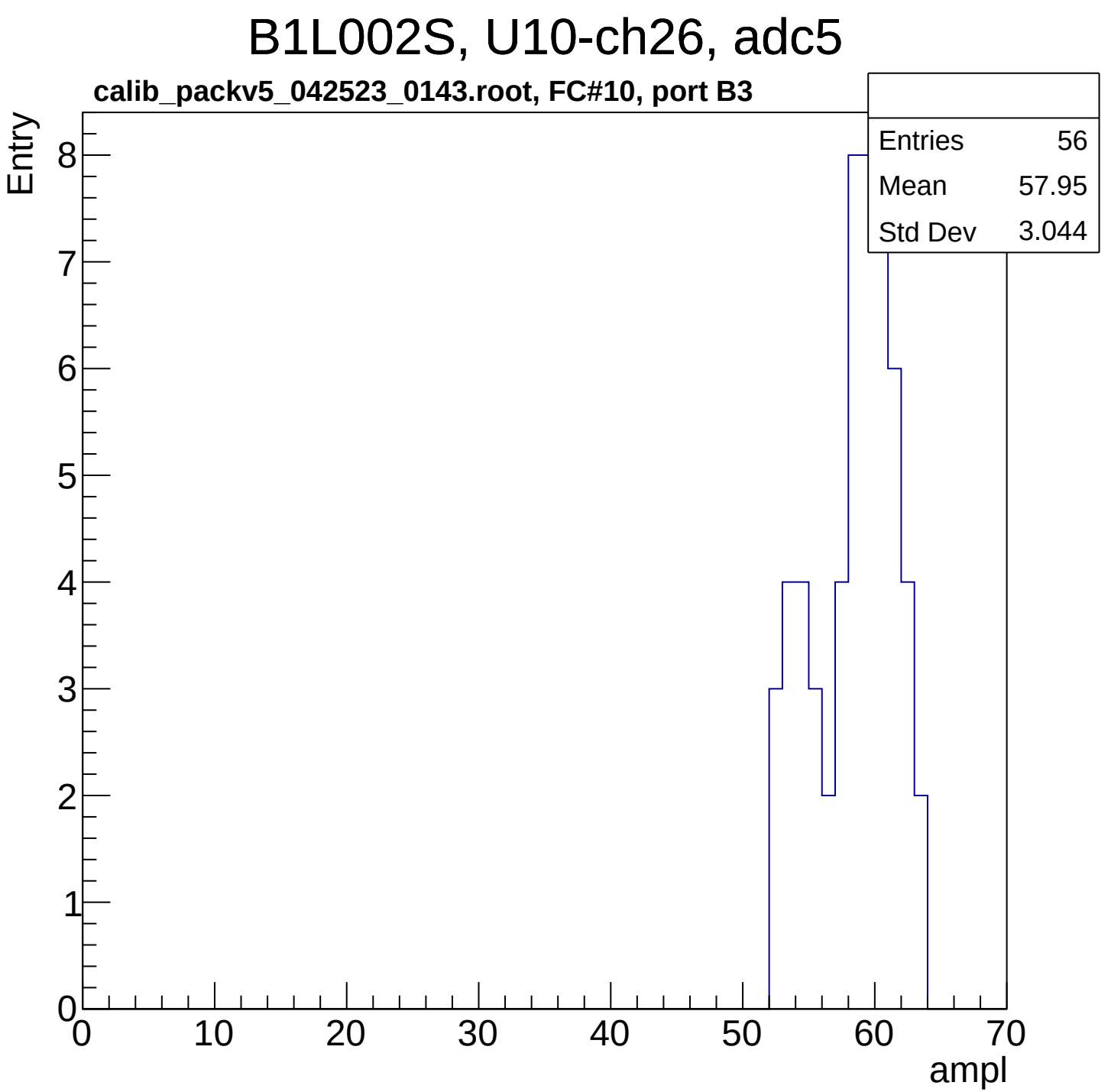
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.62
Std Dev 2.806
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.95
Std Dev 3.044
Entry
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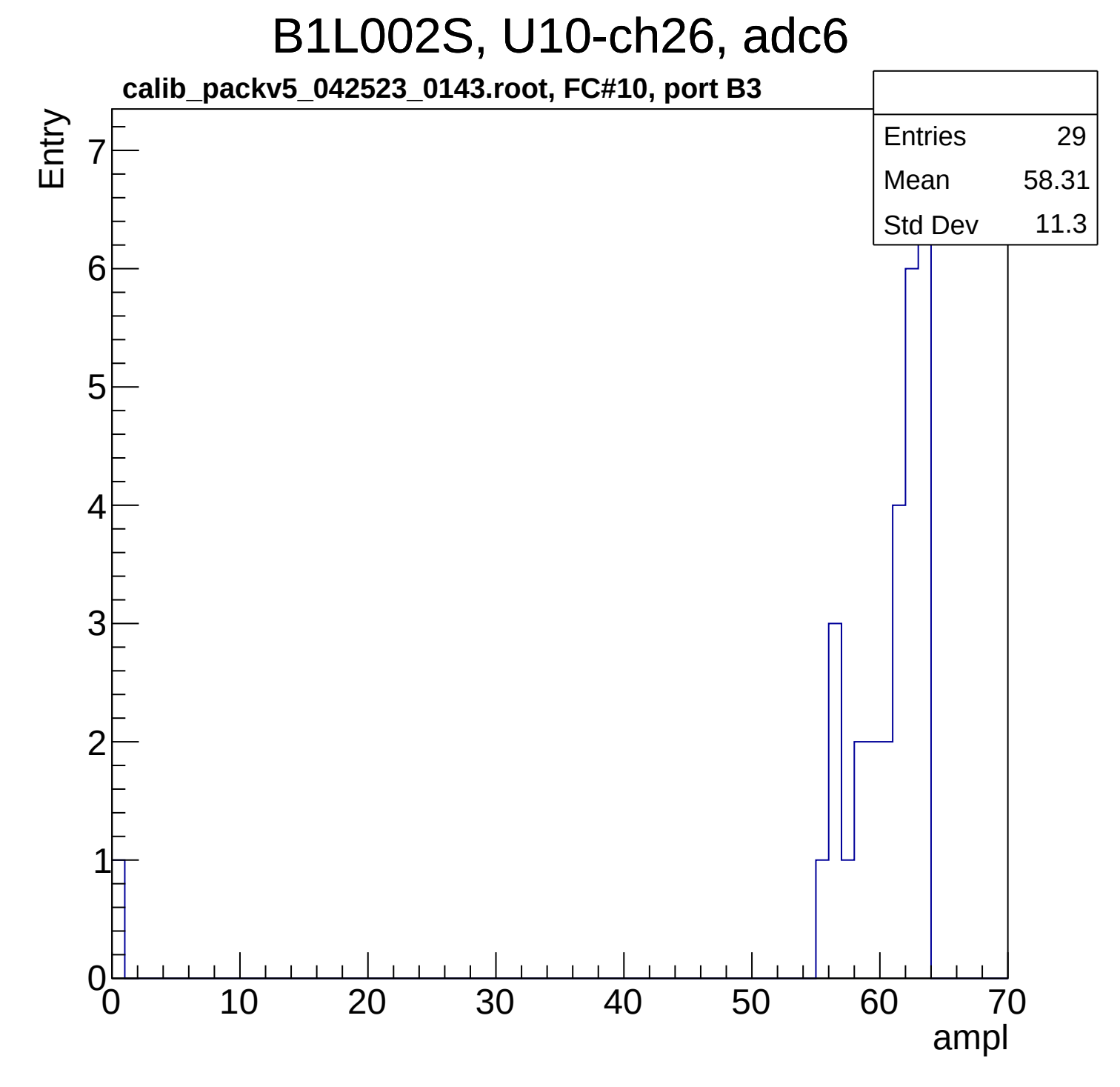
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.31
Std Dev 11.3
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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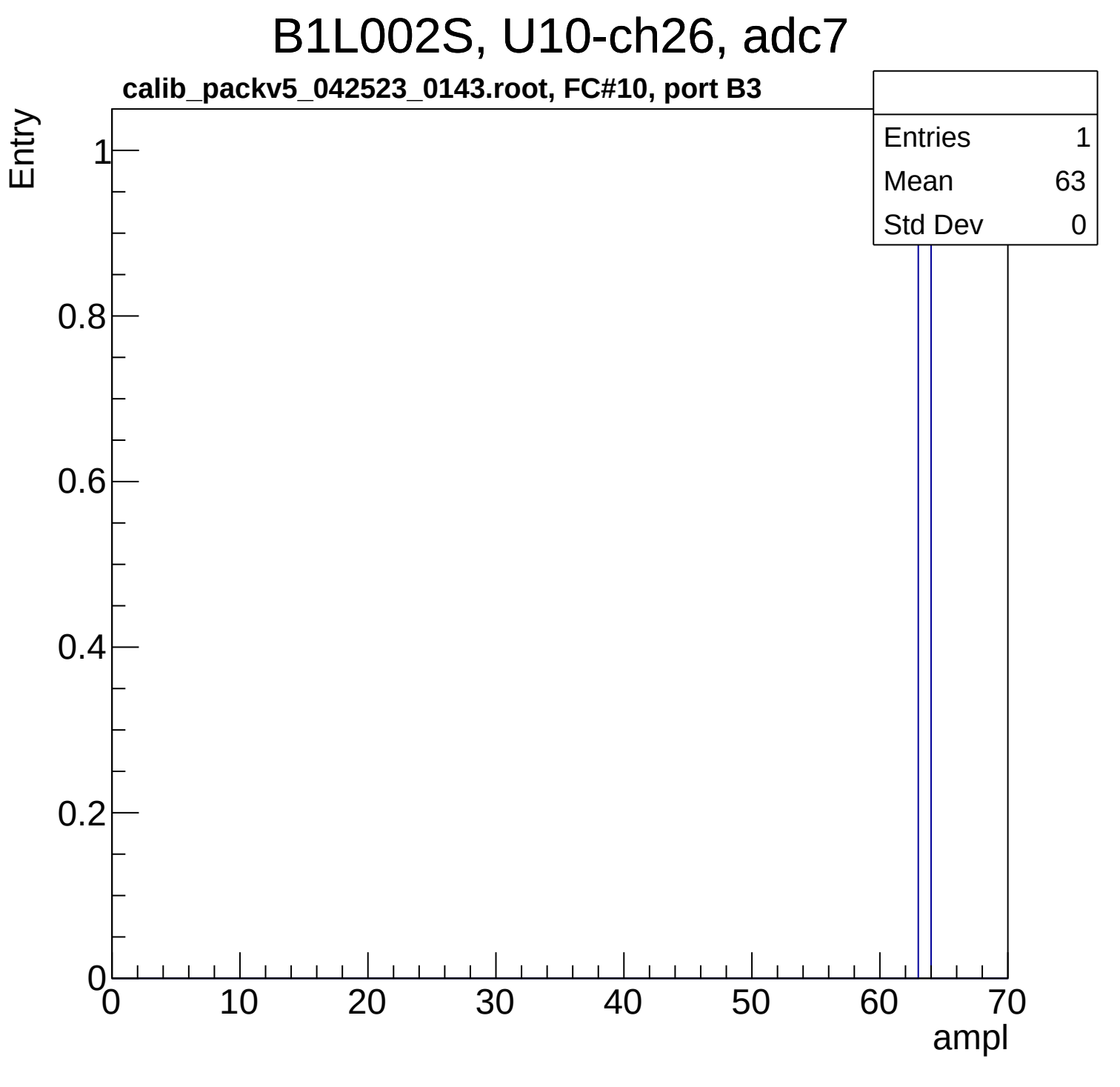
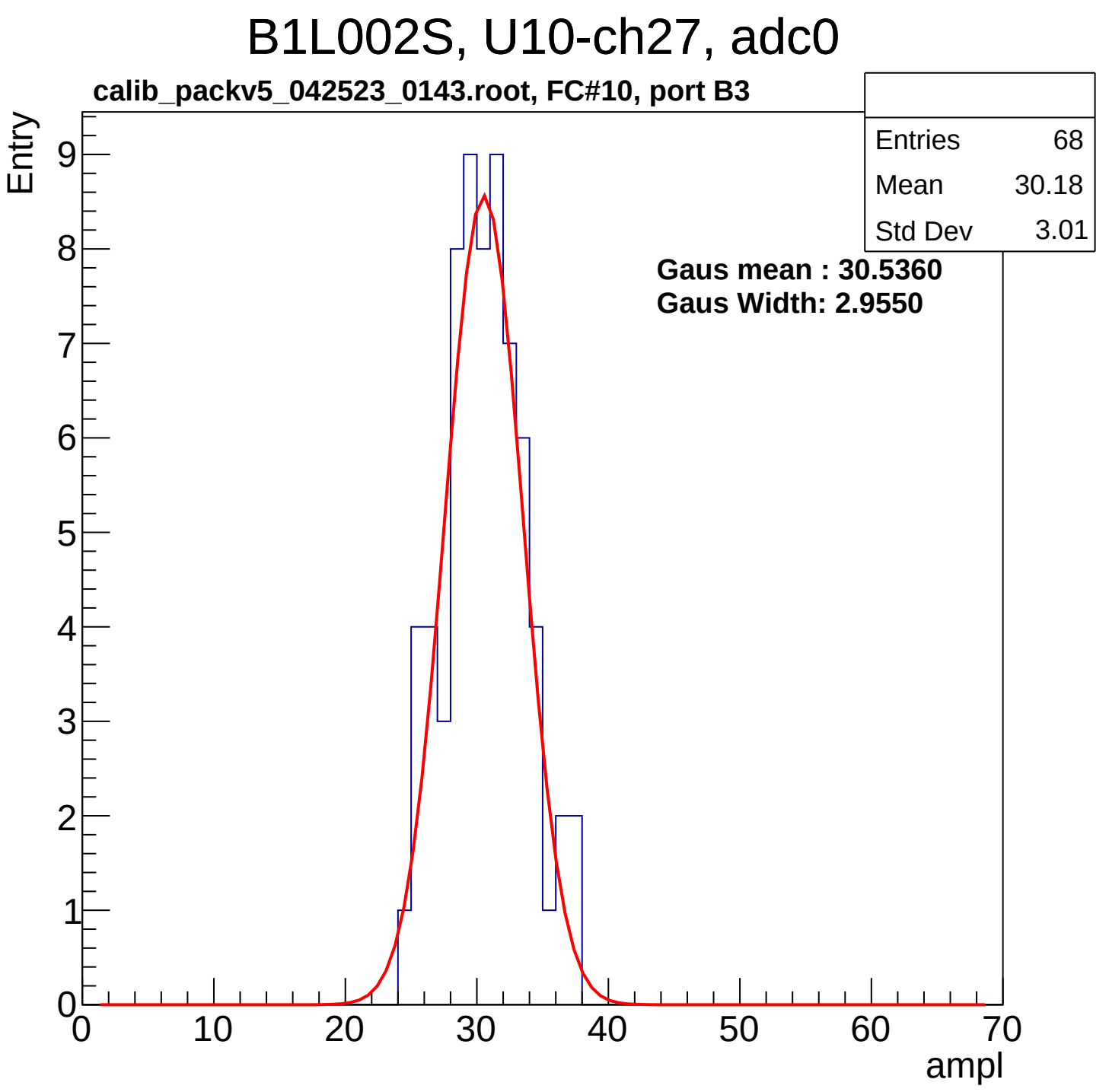

B1L002S, U10-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 30.18
Std Dev 3.01
Gaus mean : 30.5360
Gaus Width: 2.9550
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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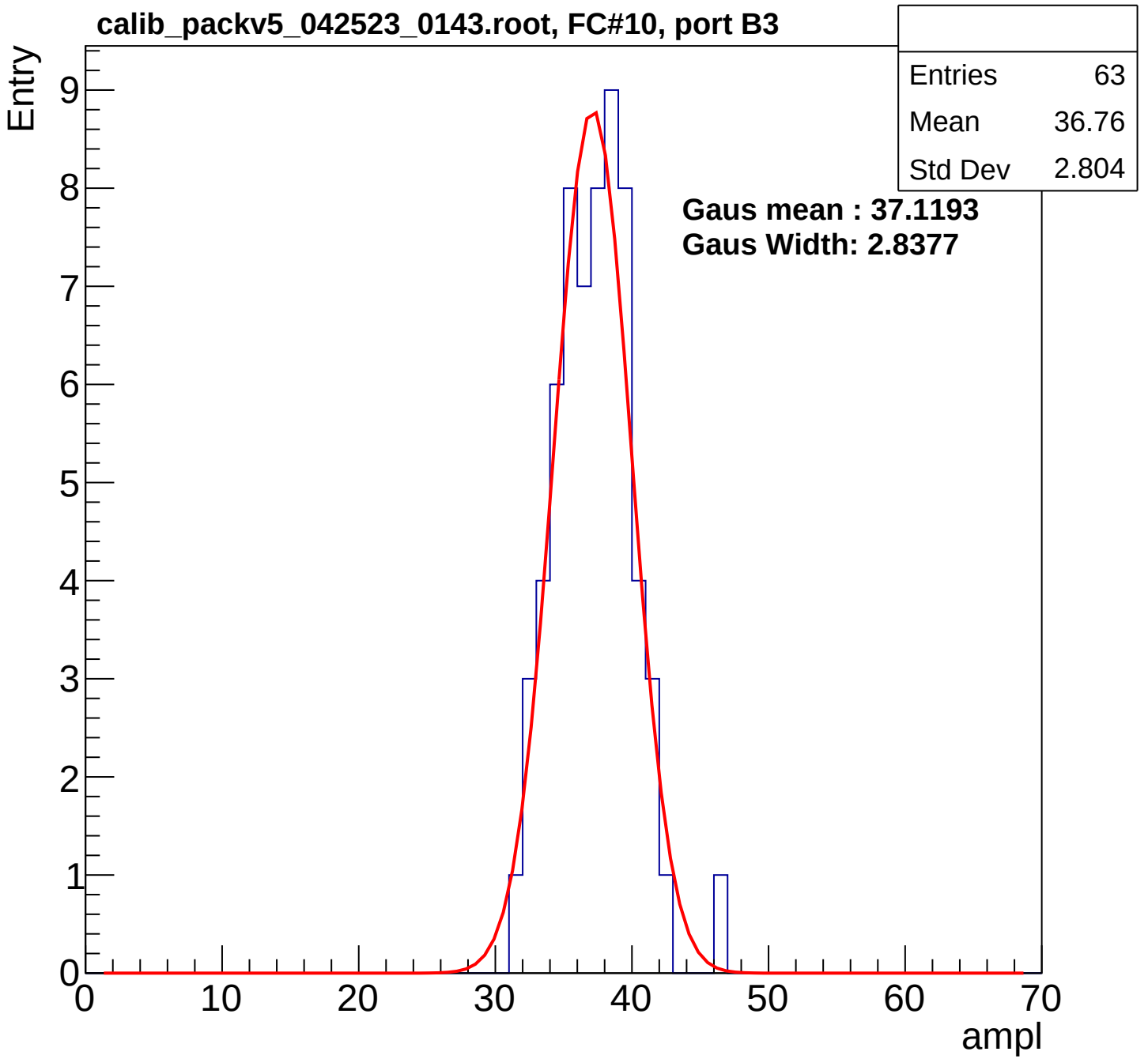
B1L002S, U10-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.76
Std Dev 2.804
Gaus mean : 37.1193
Gaus Width: 2.8377
Entry
ampl
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2
3
4
5
6
7
8
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0
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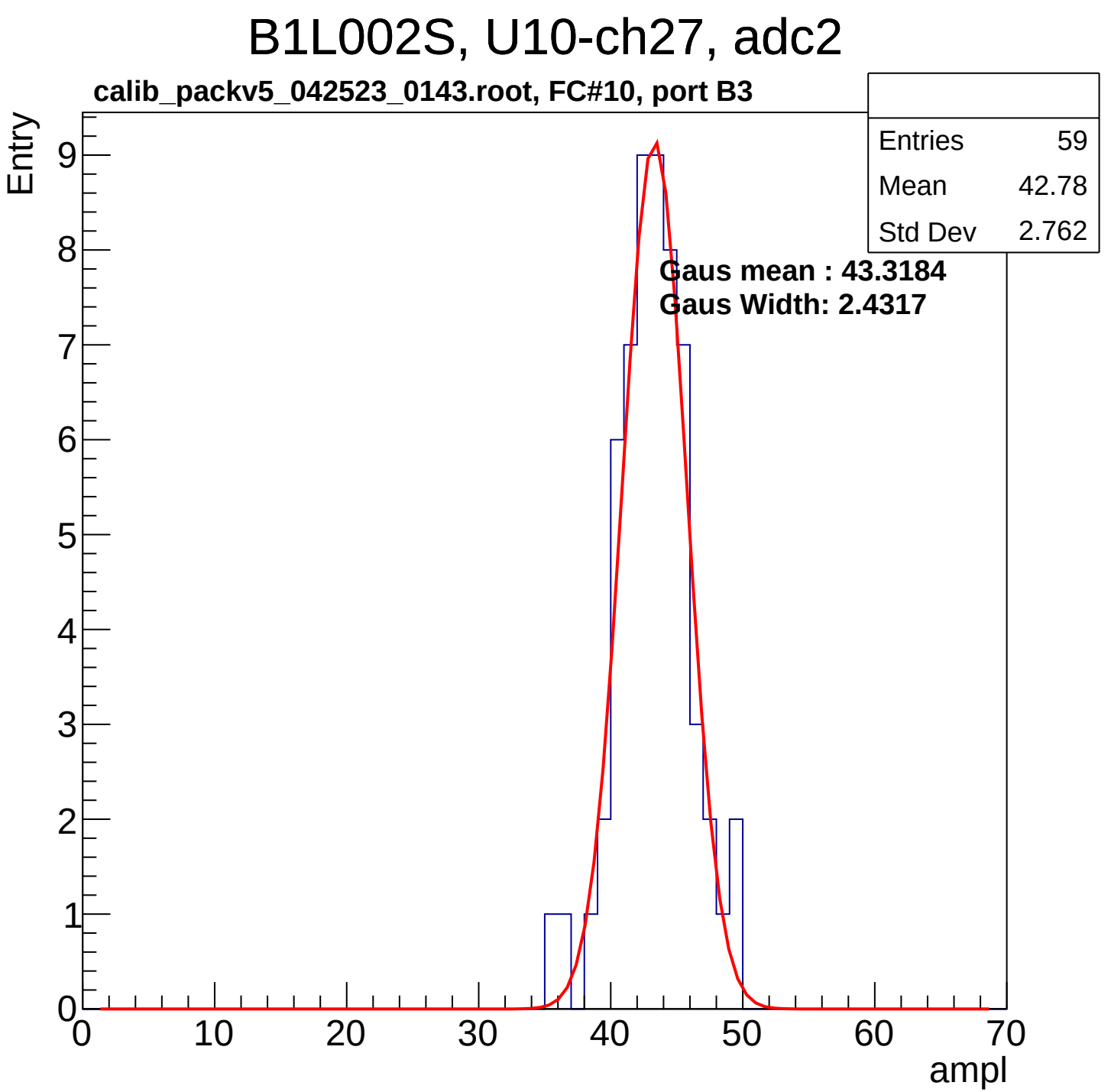
B1L002S, U10-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 42.78
Std Dev 2.762
Gaus mean : 43.3184
Gaus Width: 2.4317
Entry
ampl
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2
3
4
5
6
7
8
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0
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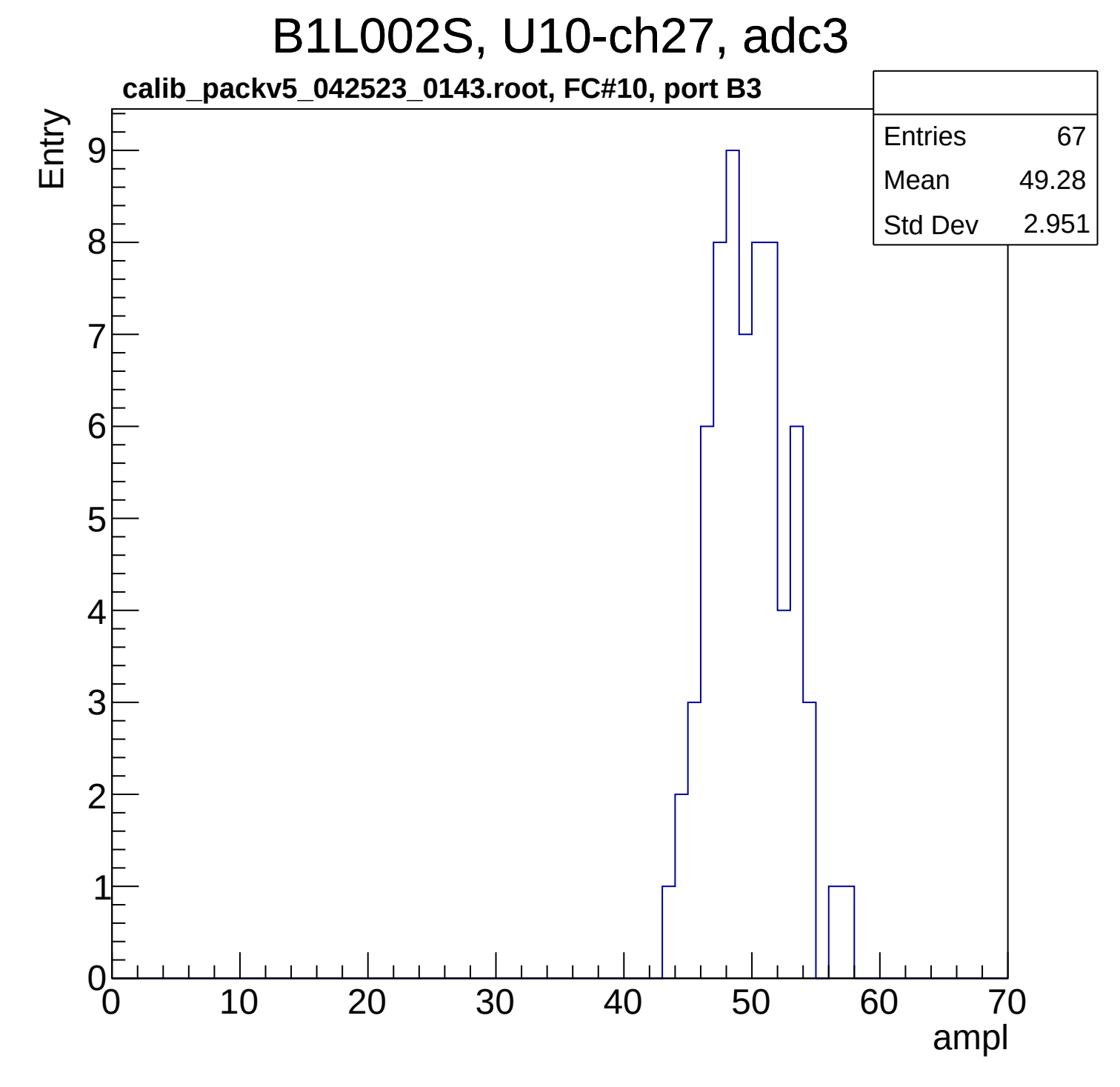
B1L002S, U10-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 49.28
Std Dev 2.951
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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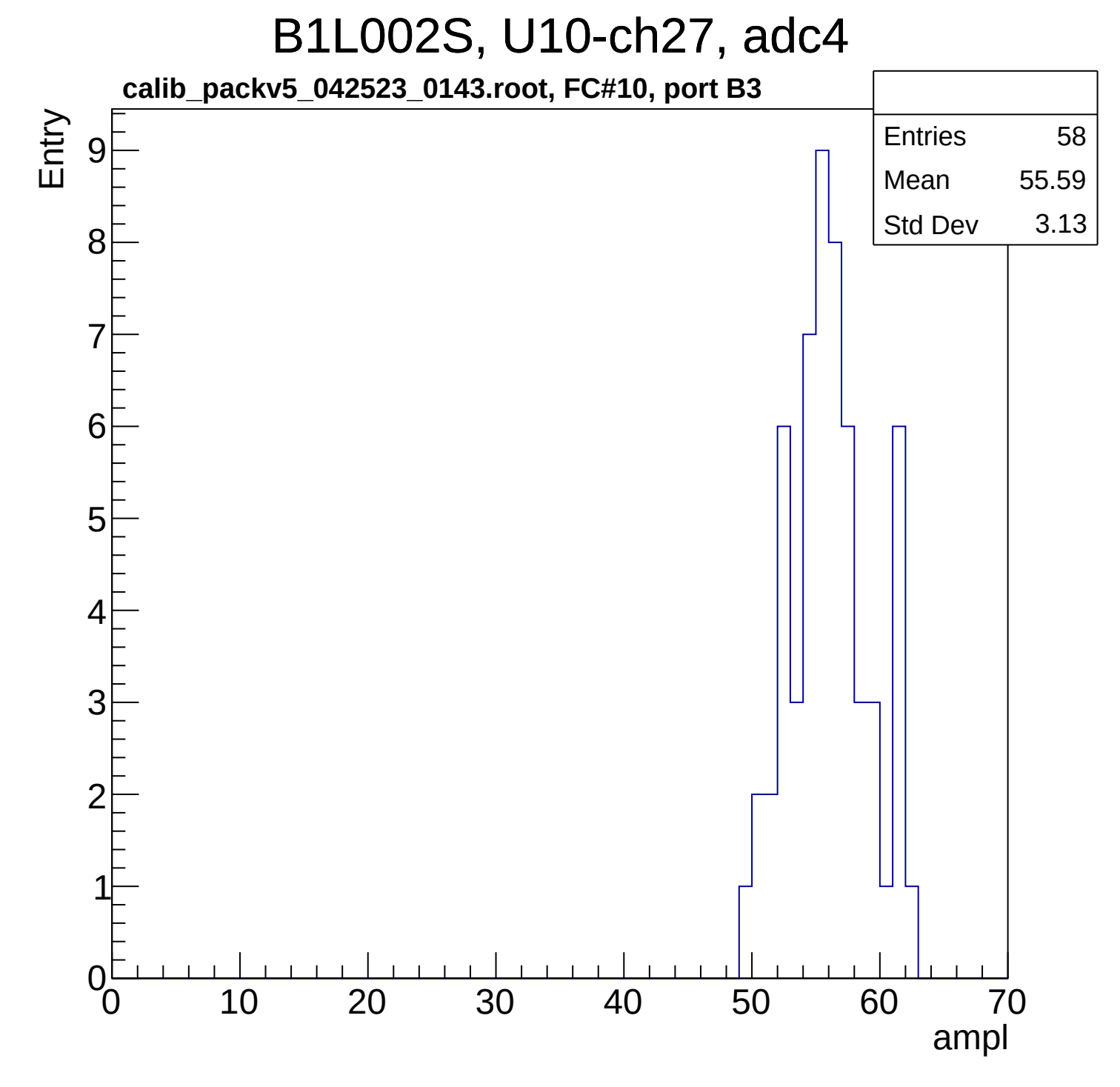
B1L002S, U10-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 55.59
Std Dev 3.13
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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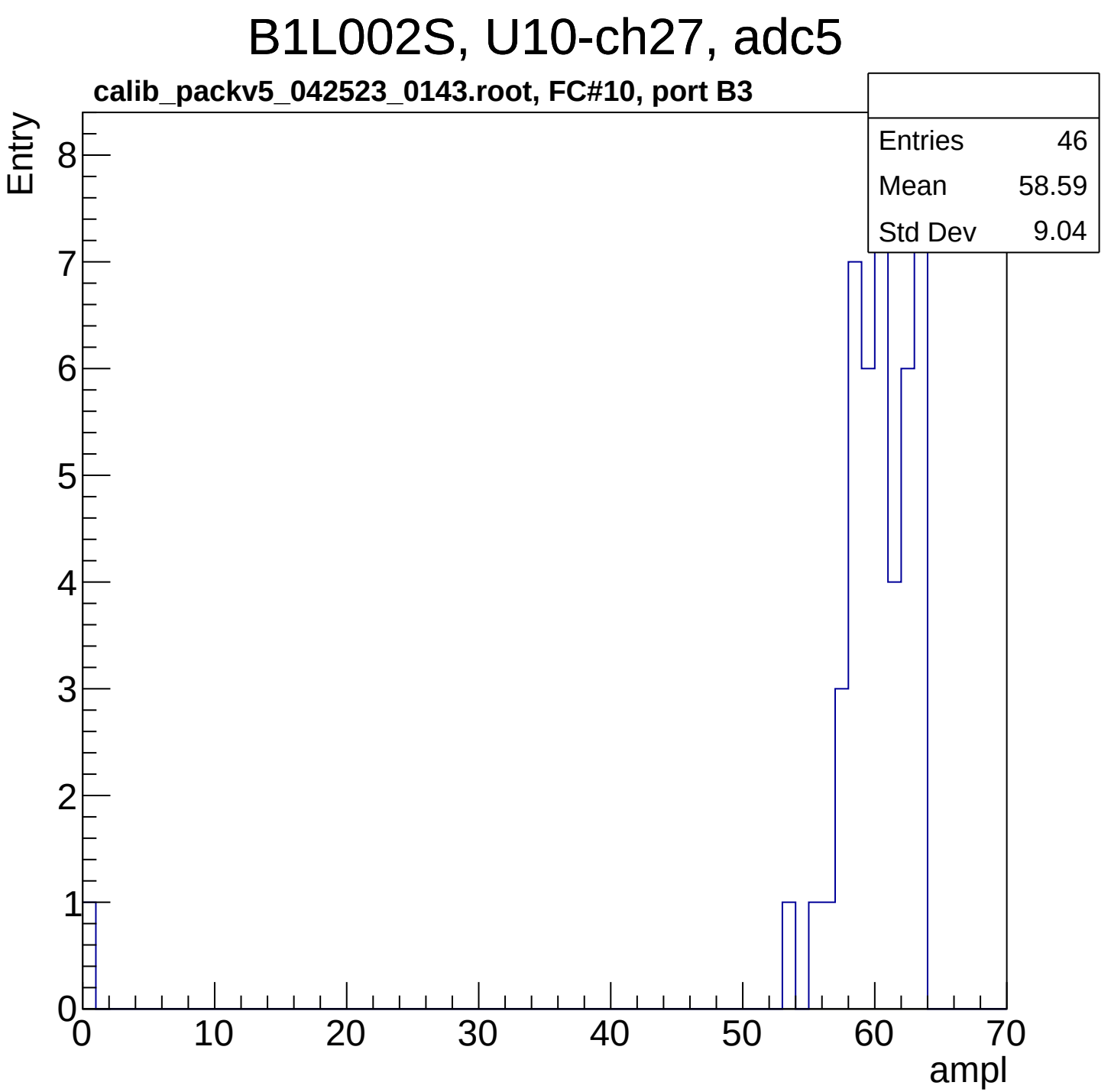
B1L002S, U10-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.59
Std Dev 9.04
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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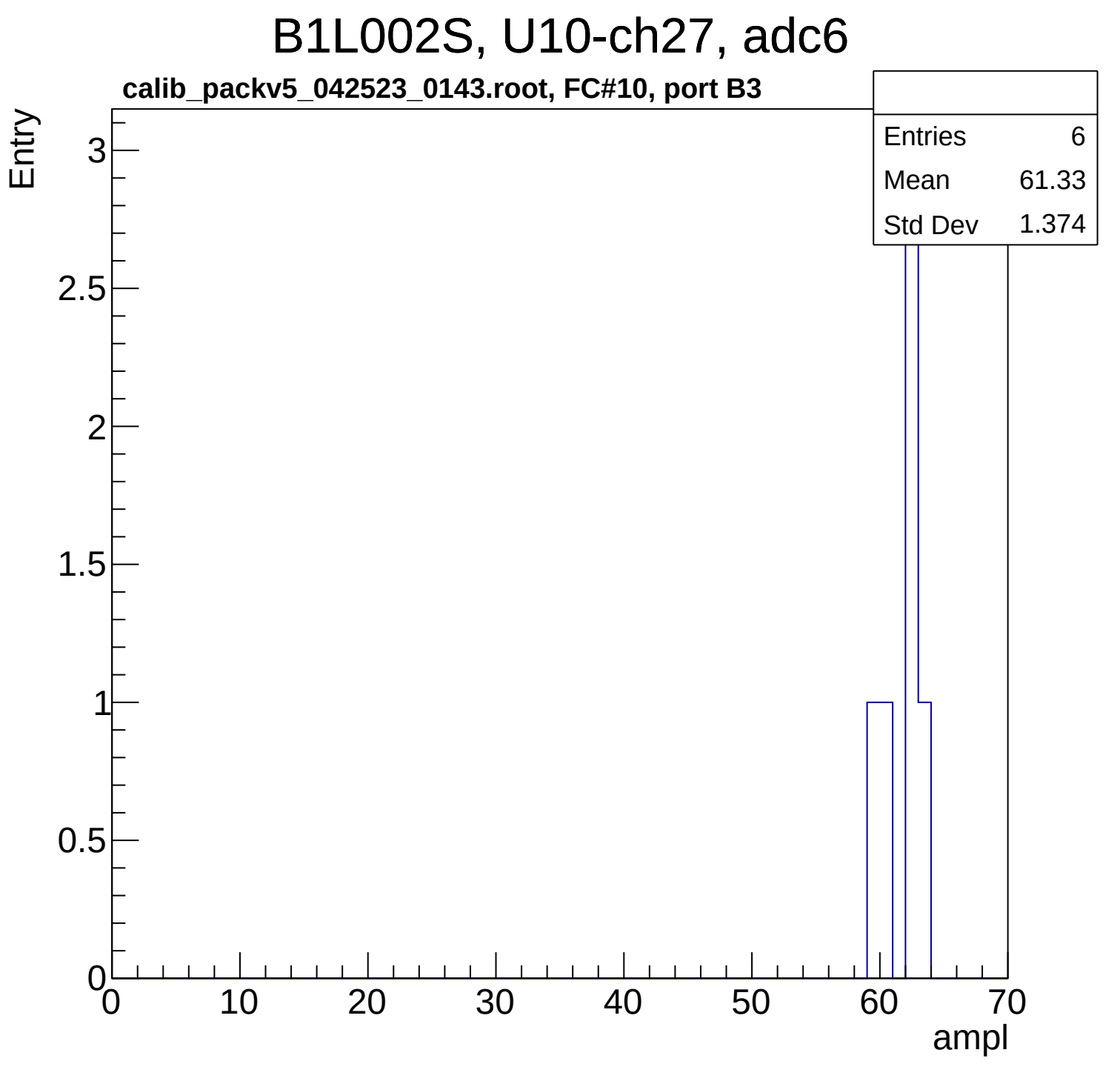
B1L002S, U10-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.33
Std Dev 1.374
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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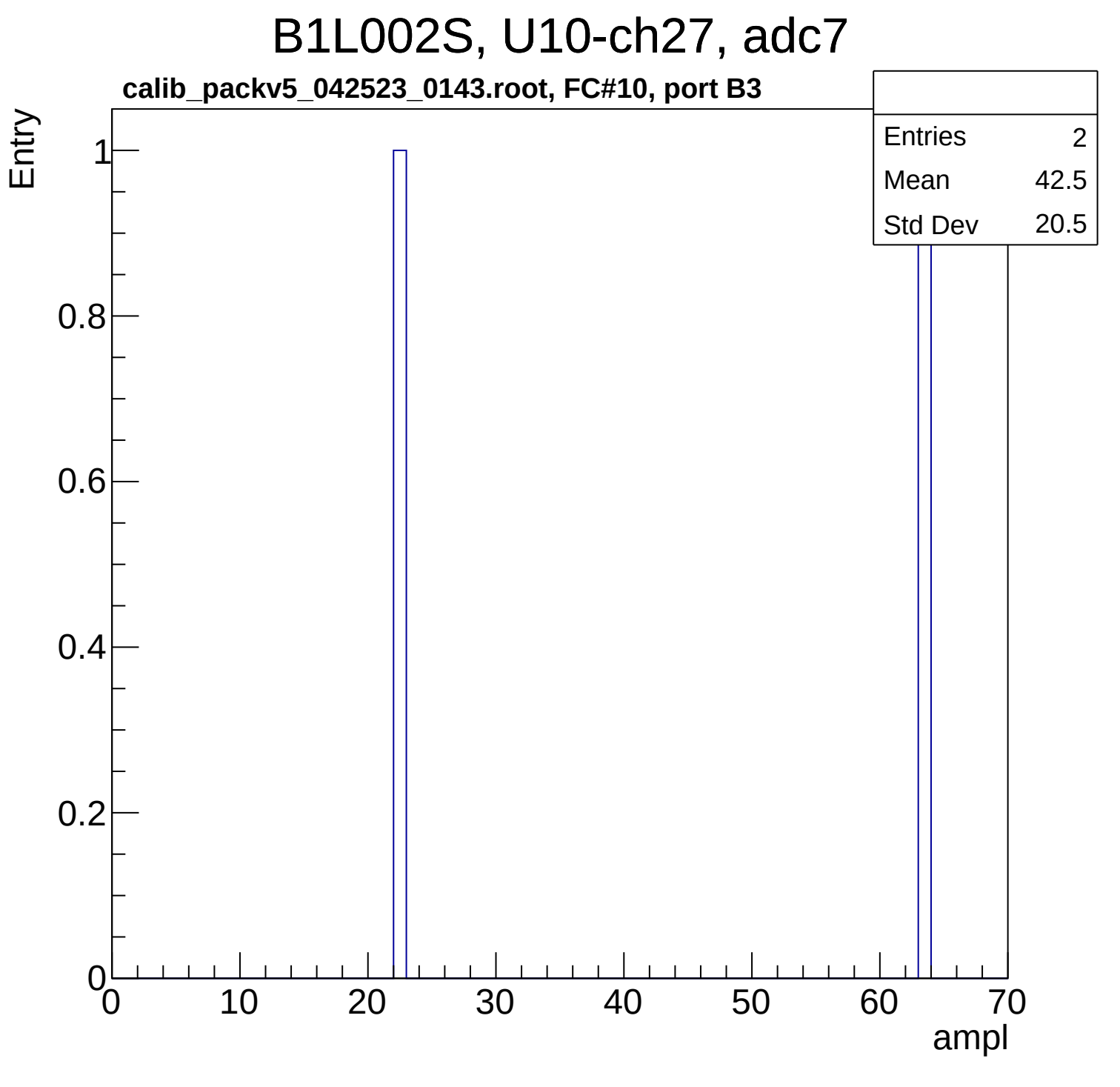
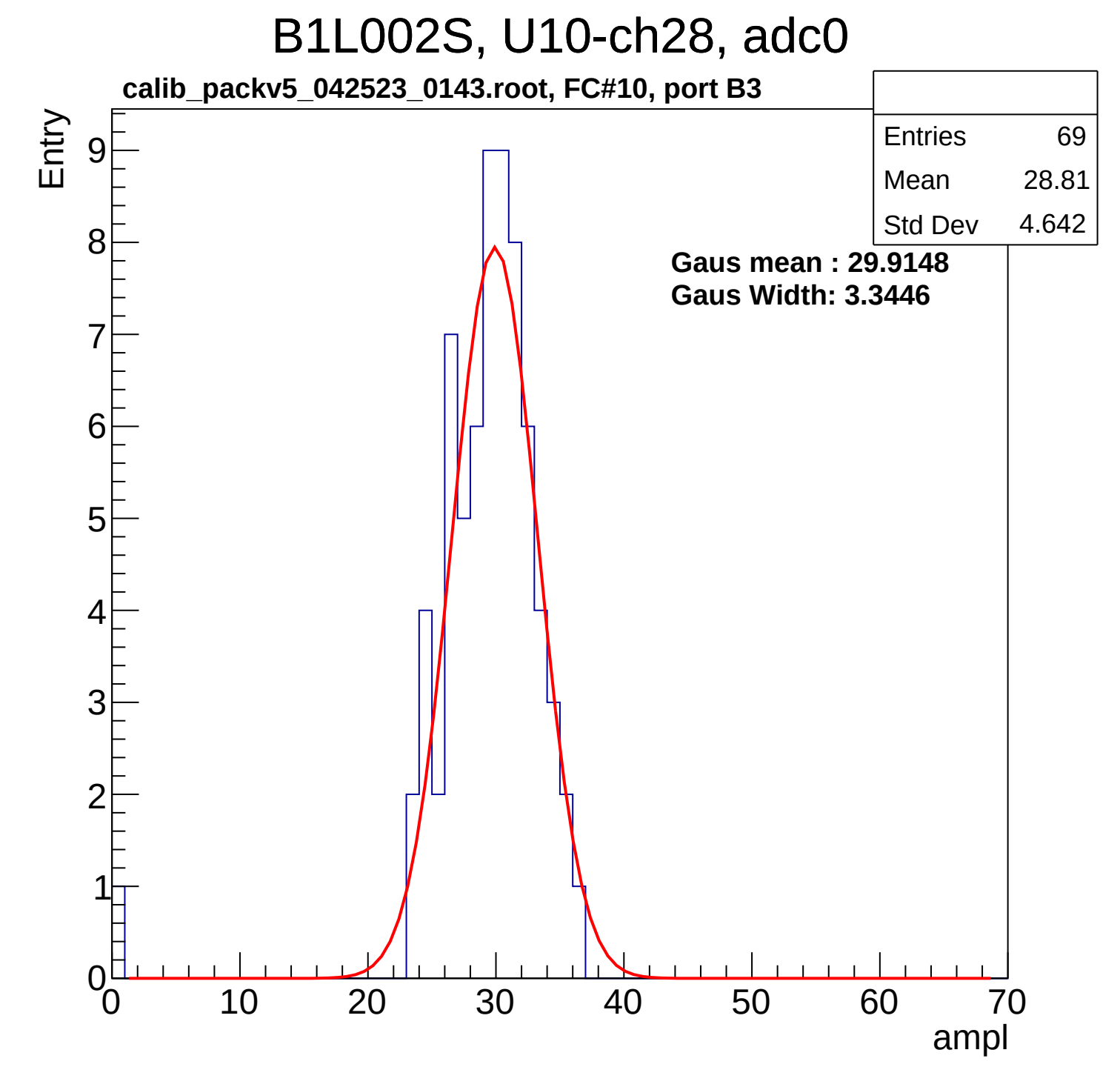
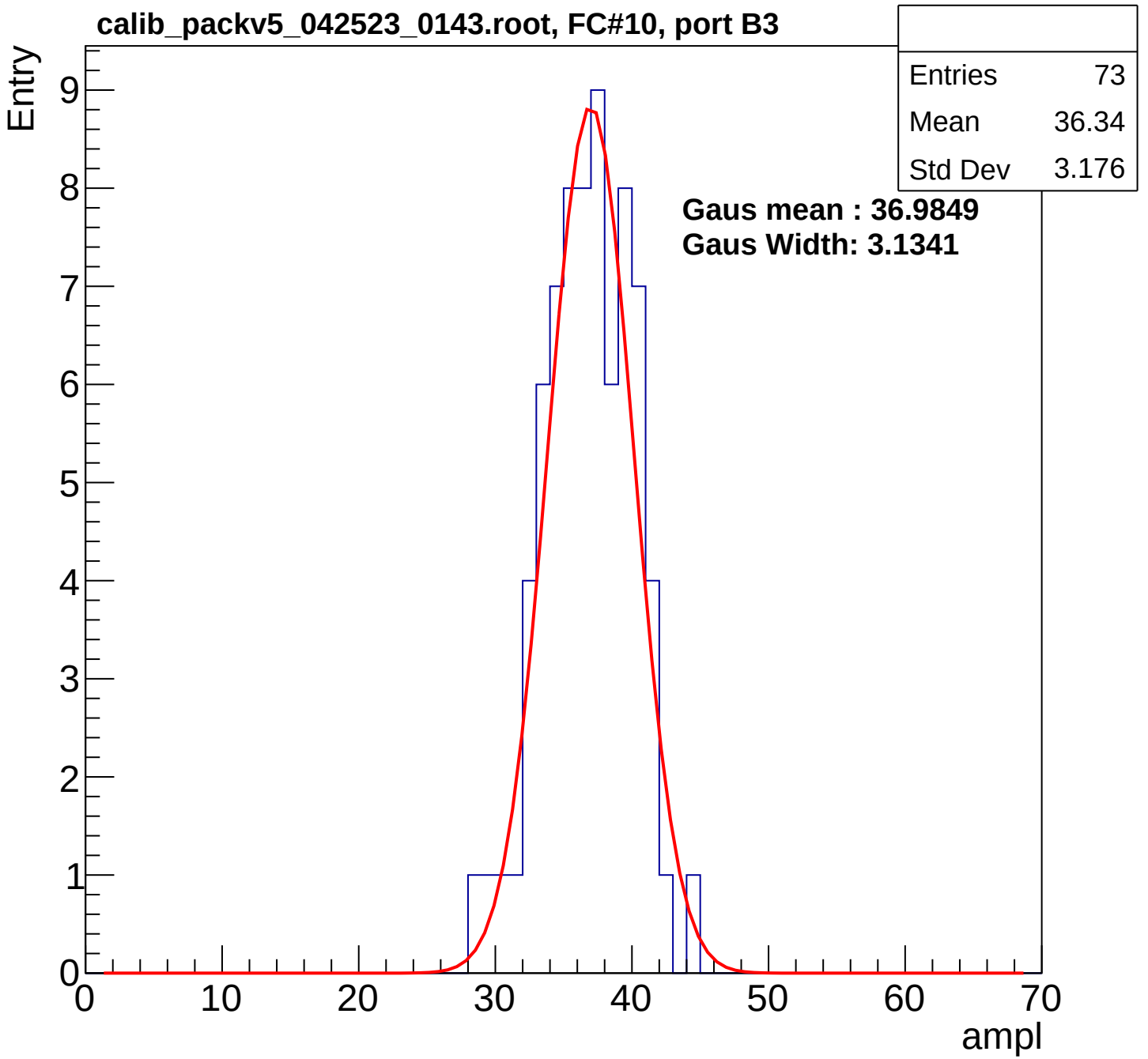

B1L002S, U10-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.81
Std Dev 4.642
Gaus mean : 29.9148
Gaus Width: 3.3446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.34
Std Dev 3.176
Gaus mean : 36.9849
Gaus Width: 3.1341
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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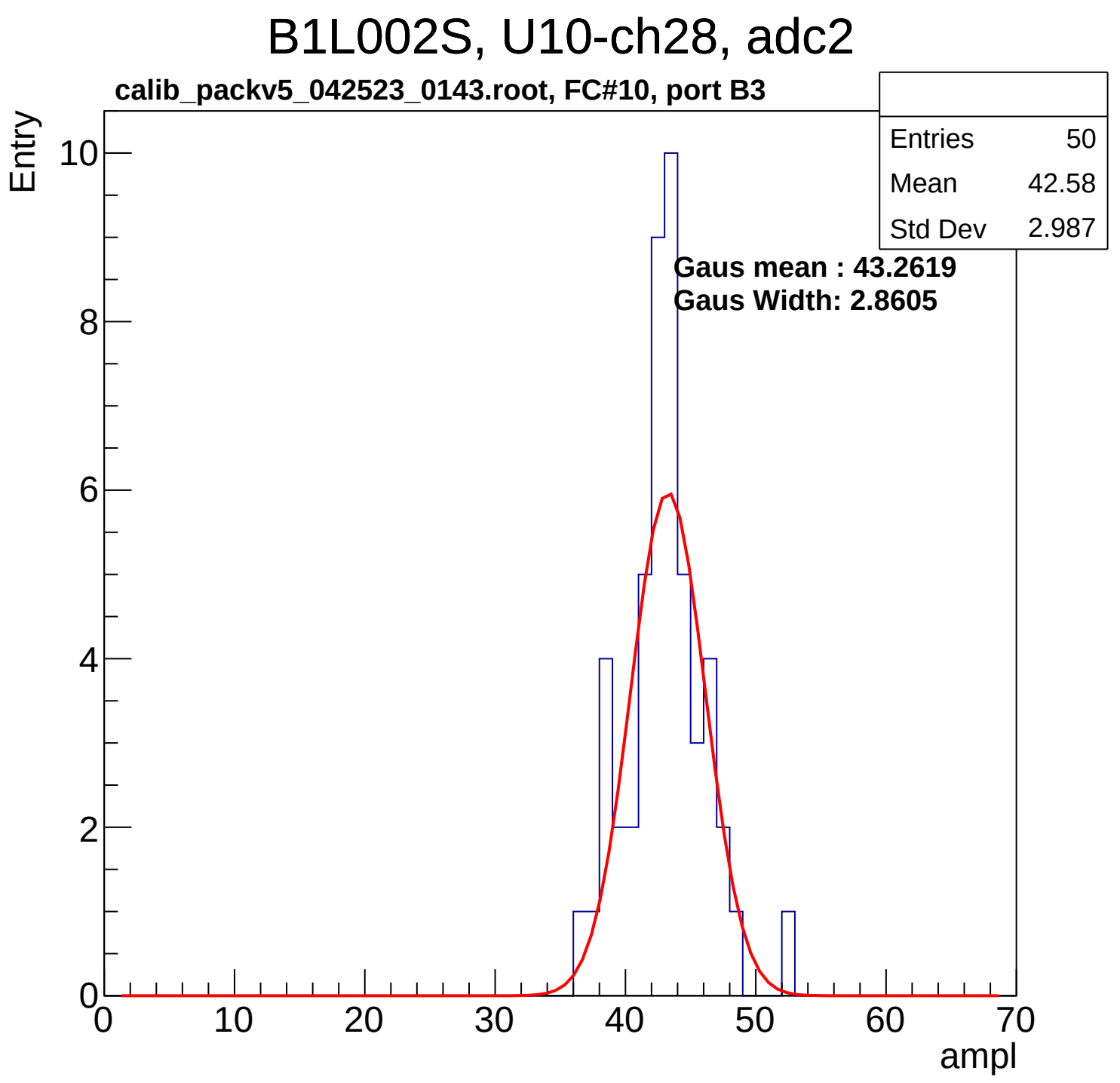
B1L002S, U10-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.58
Std Dev 2.987
Gaus mean : 43.2619
Gaus Width: 2.8605
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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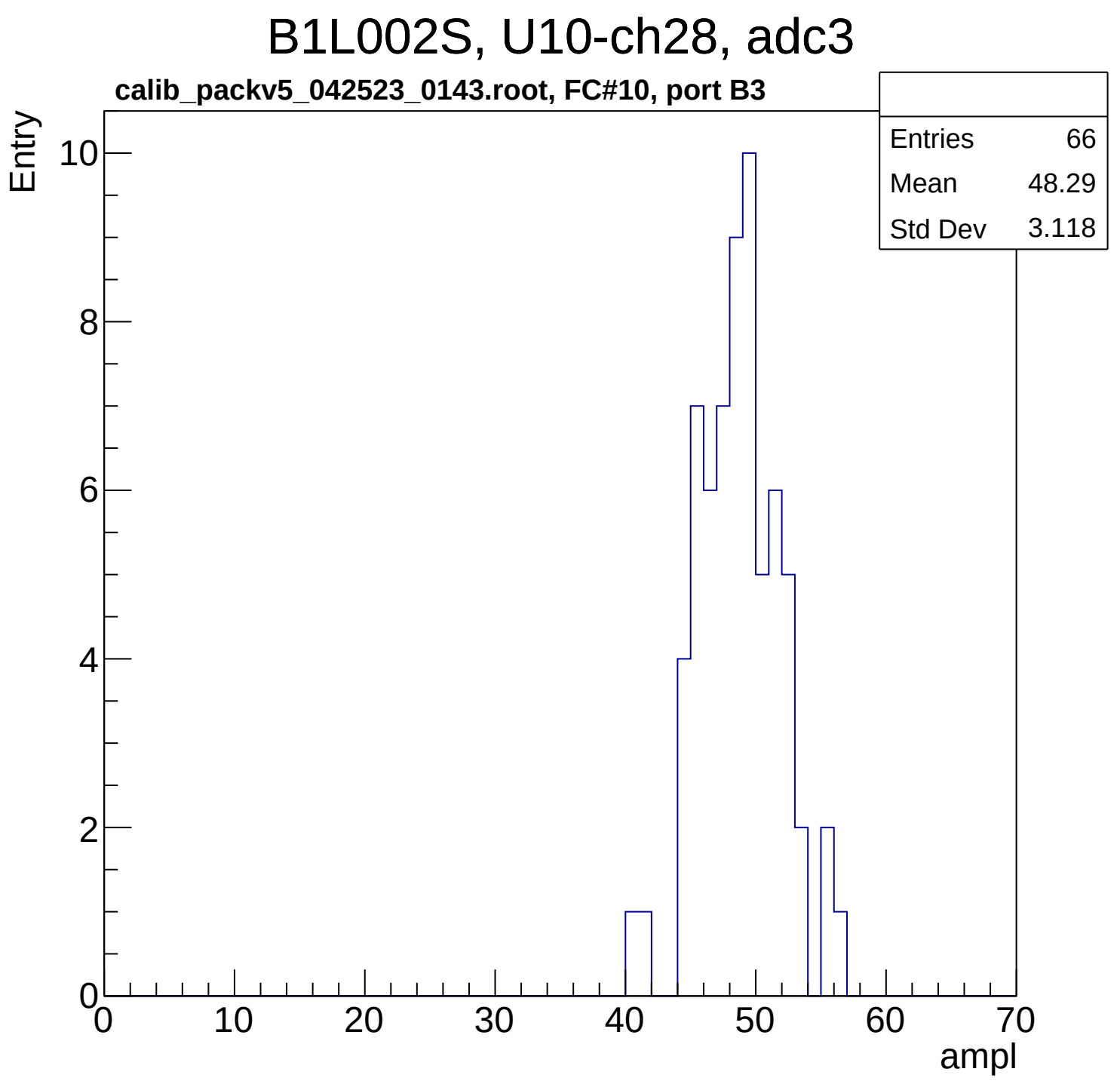
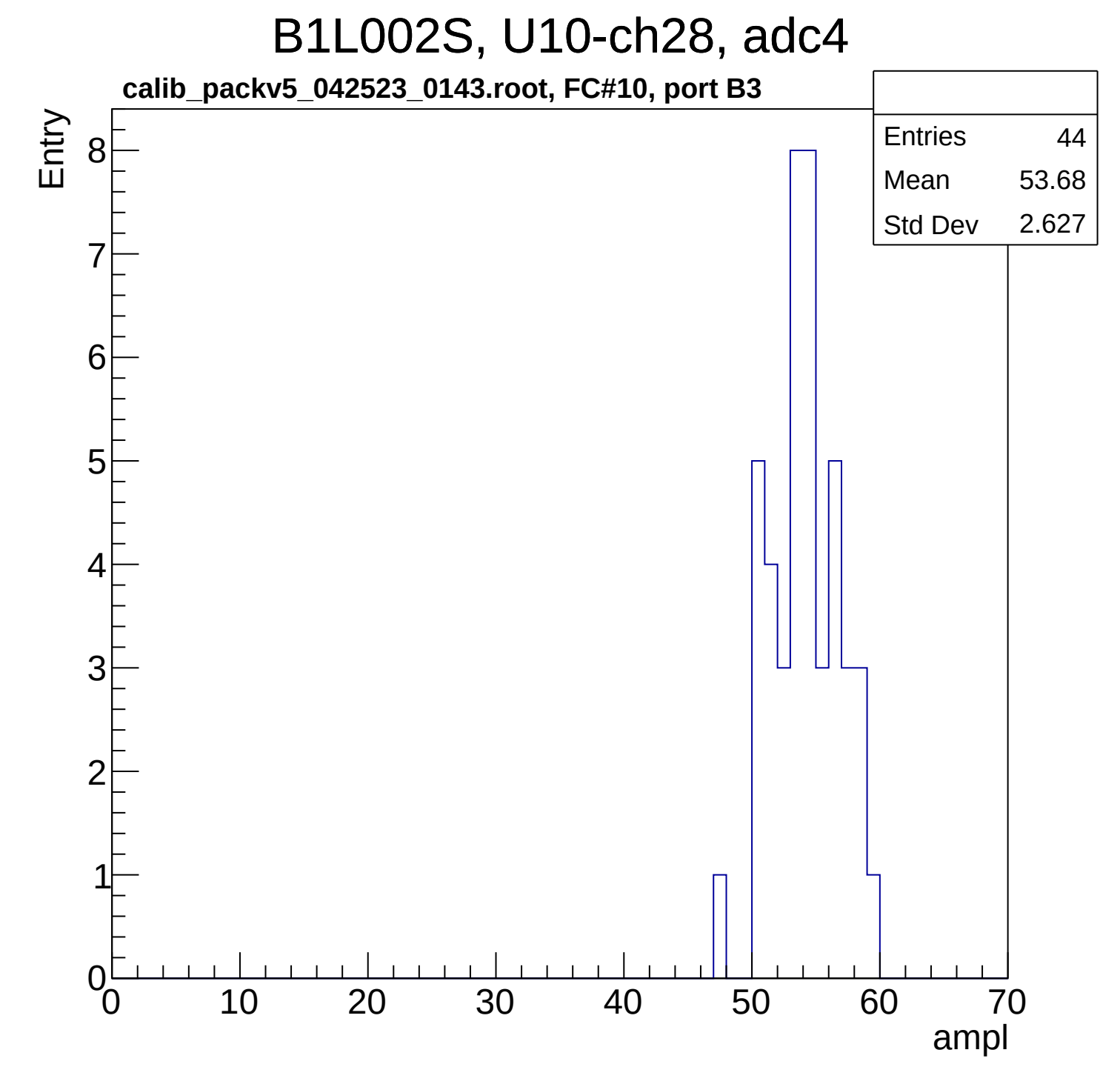

B1L002S, U10-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.29
Std Dev 3.118
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.68
Std Dev 2.627
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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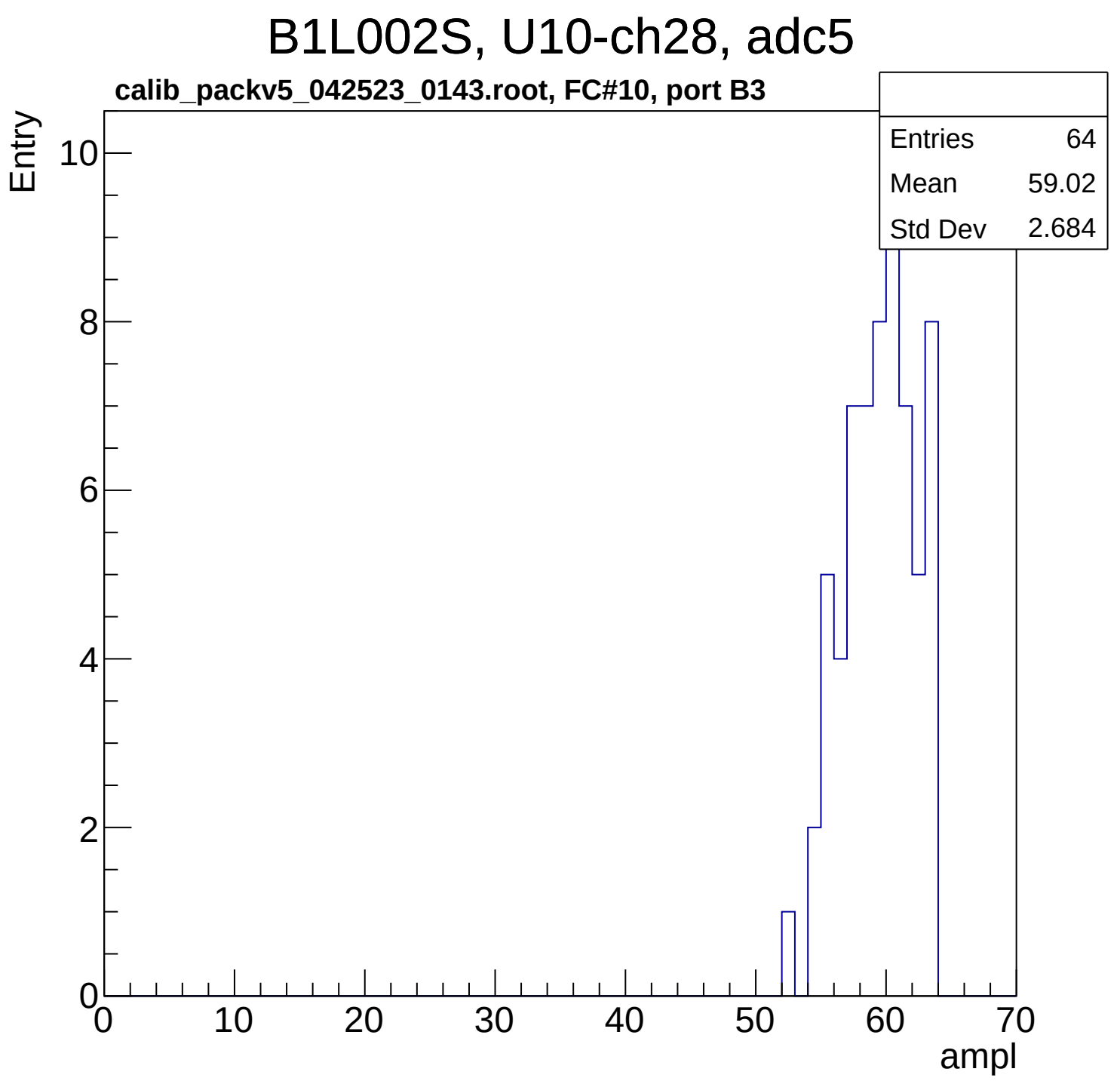
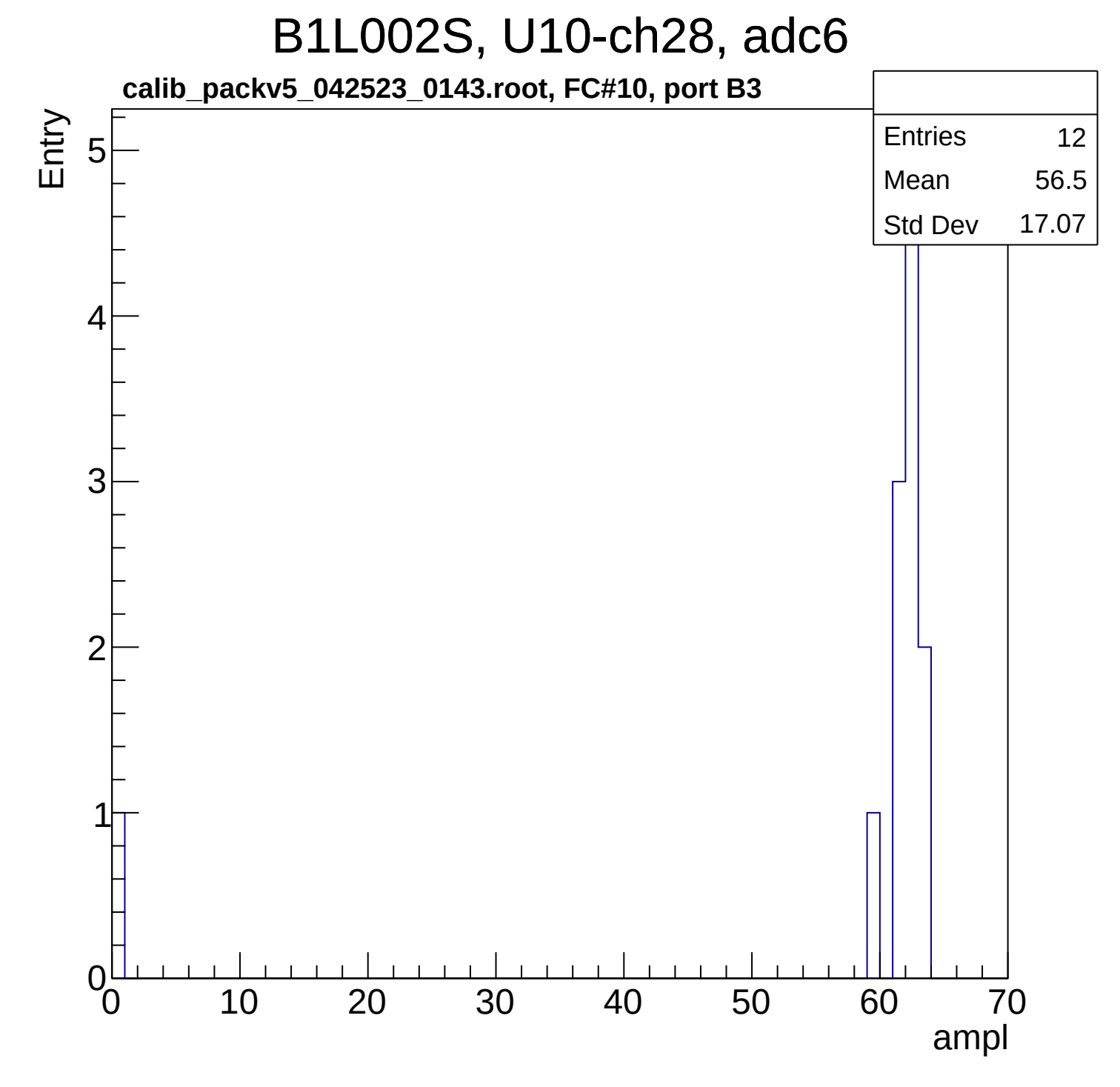
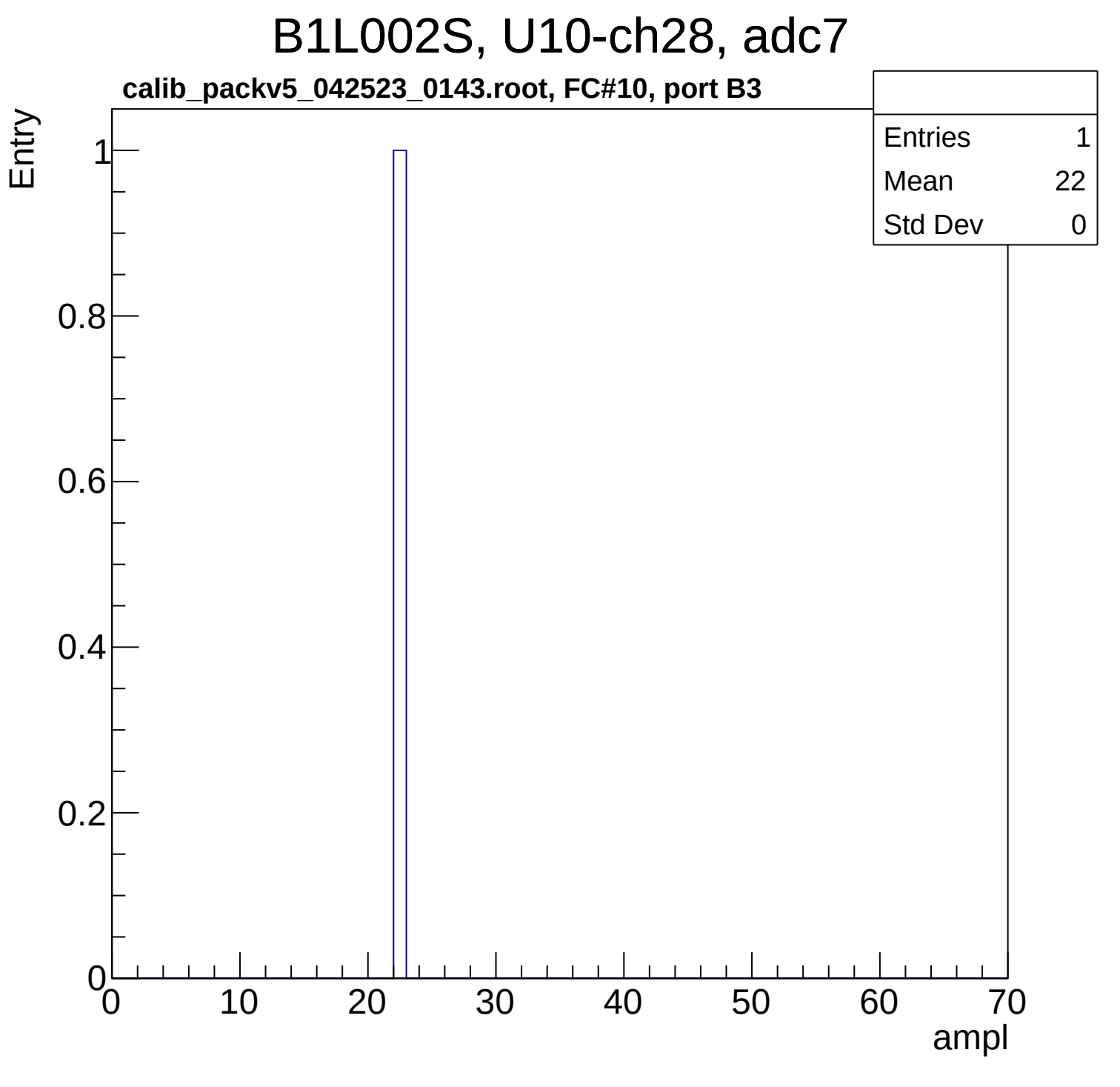

B1L002S, U10-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 59.02
Std Dev 2.684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.5
Std Dev 17.07
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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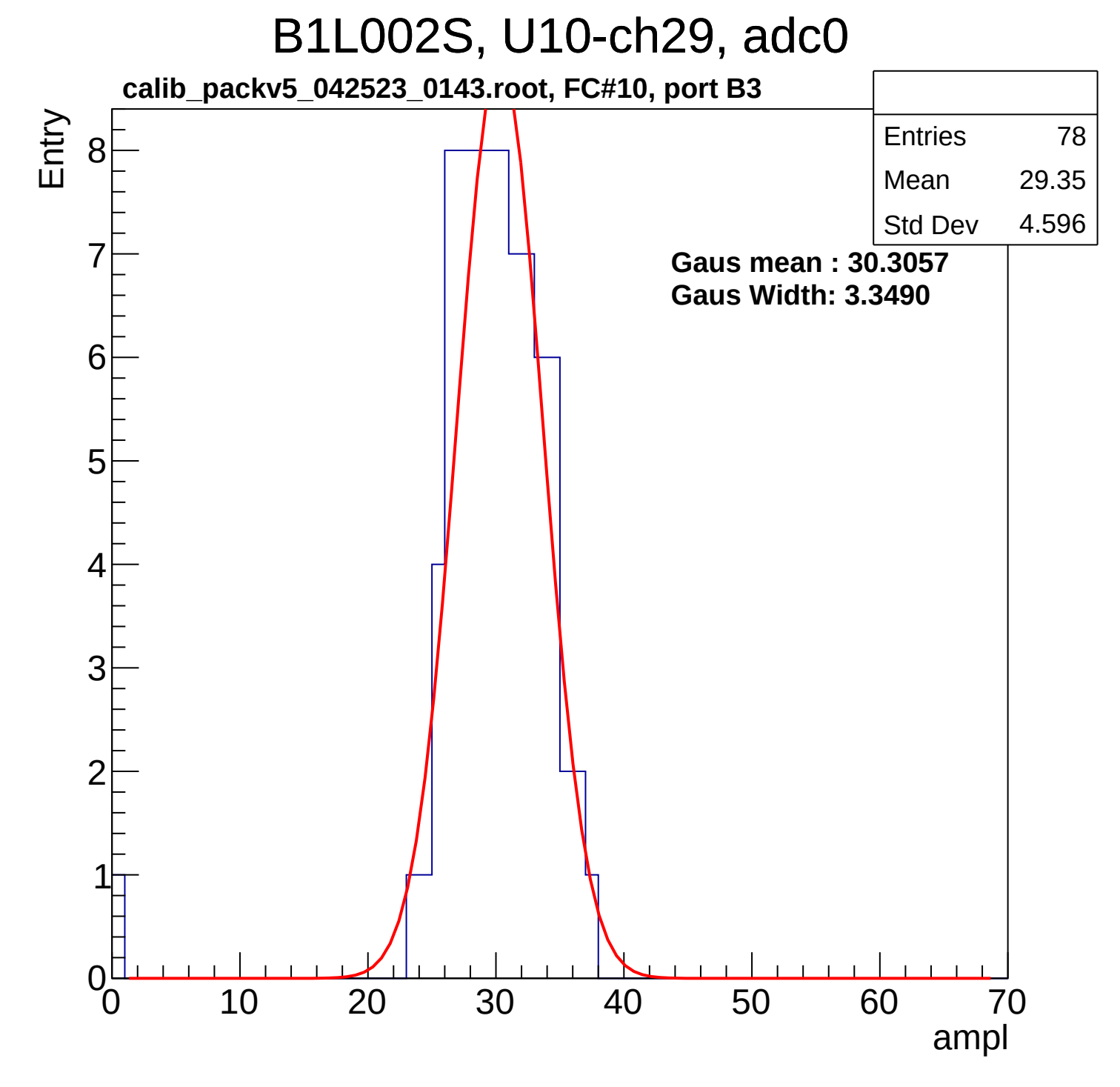
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 29.35
Std Dev 4.596
Gaus mean : 30.3057
Gaus Width: 3.3490
Entry
ampl
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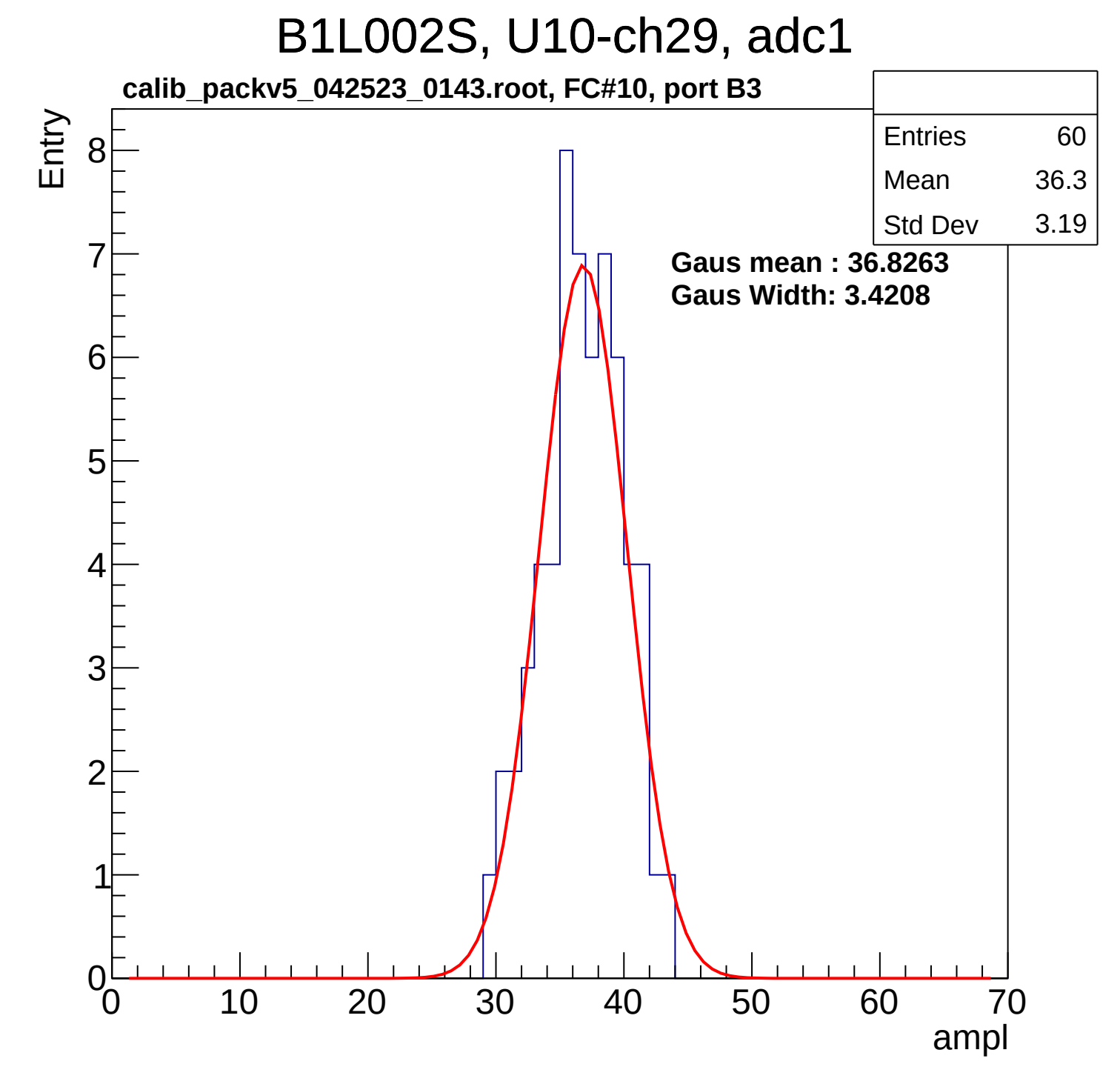
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 36.3
Std Dev 3.19
Gaus mean : 36.8263
Gaus Width: 3.4208
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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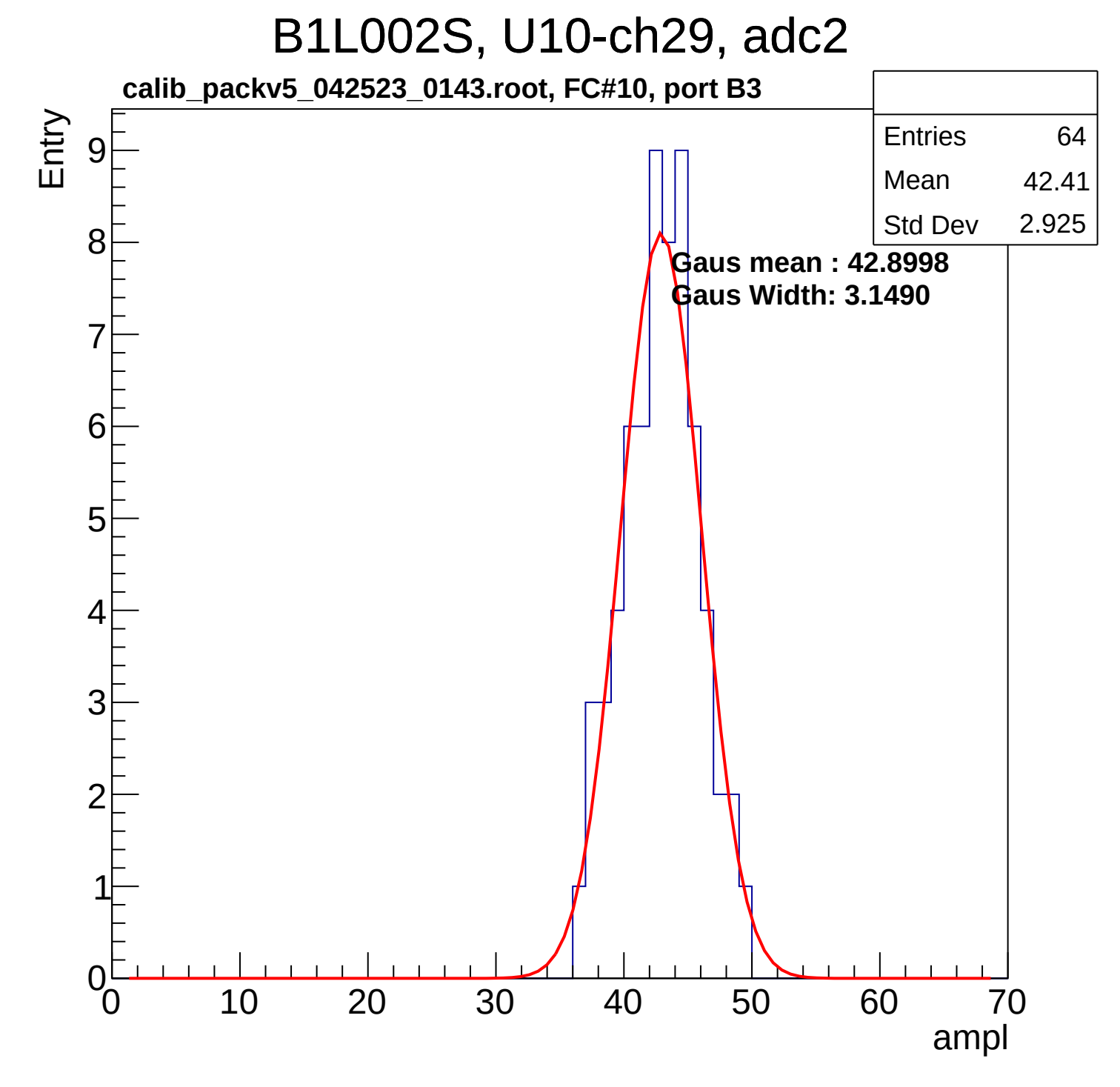
40
50
60
70


B1L002S, U10-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.41
Std Dev 2.925
Gaus mean : 42.8998
Gaus Width: 3.1490
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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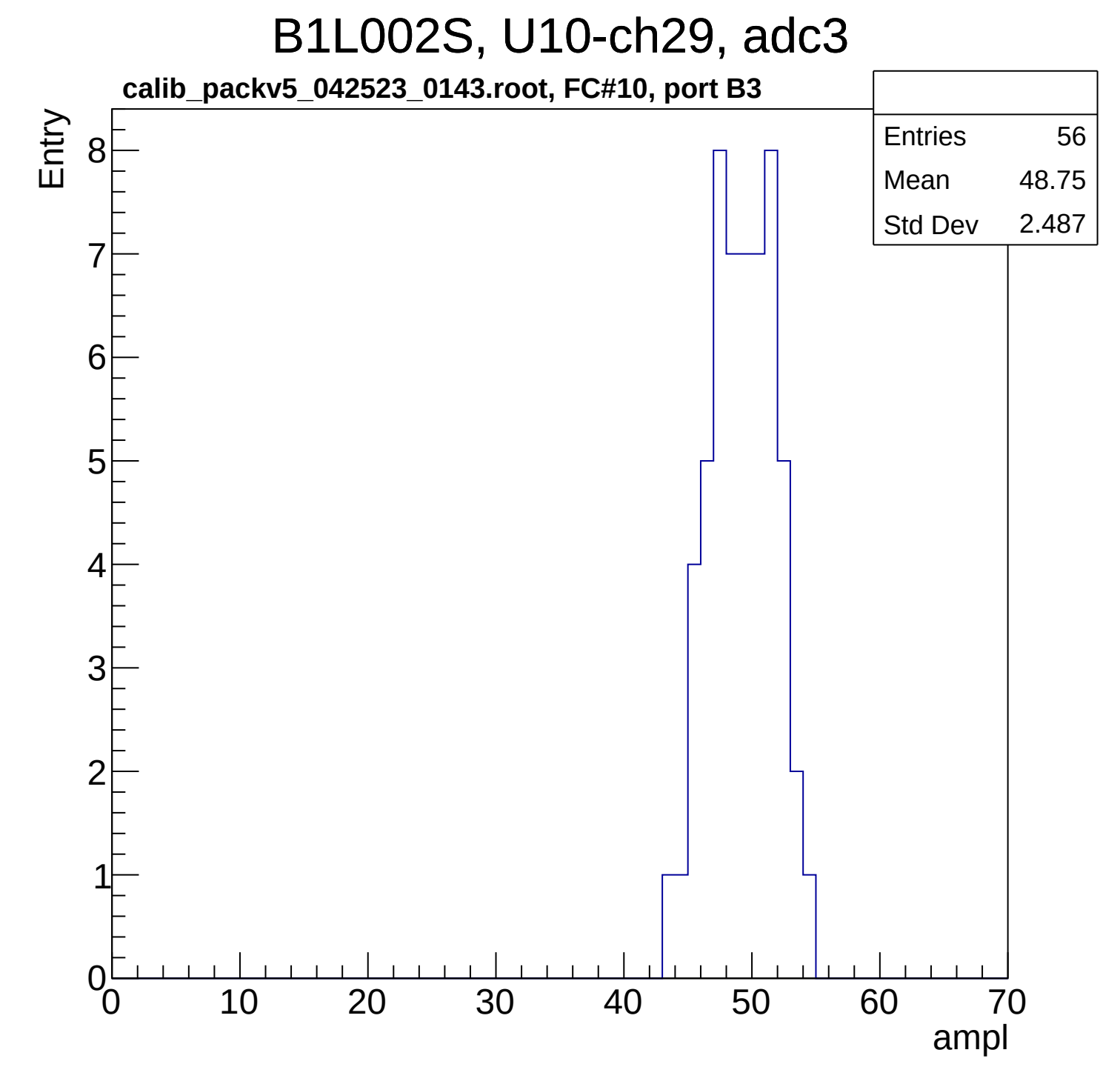
B1L002S, U10-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 48.75
Std Dev 2.487
Entry
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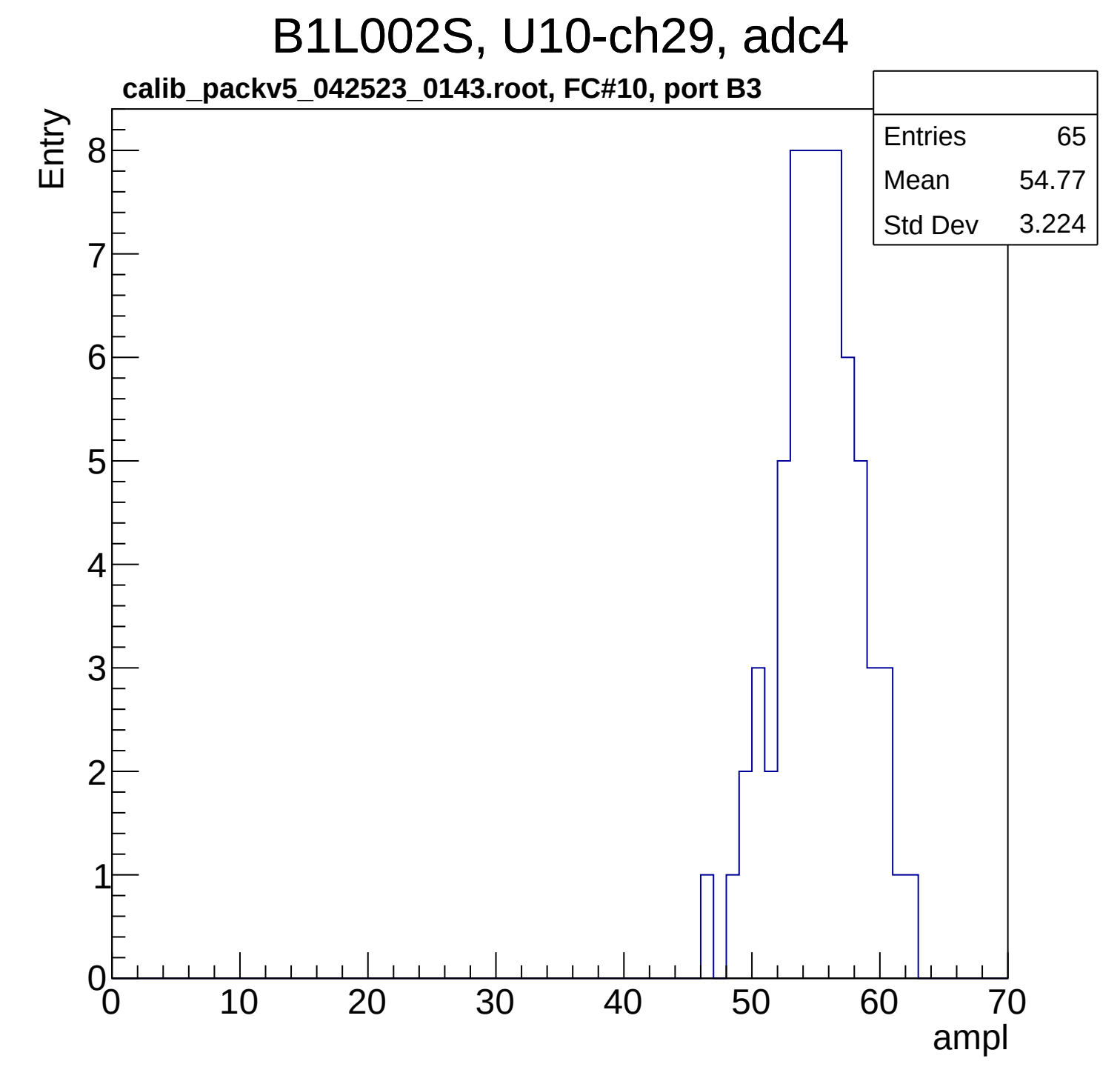
50
60
70
ampl


B1L002S, U10-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 54.77
Std Dev 3.224
Entry
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3
4
5
6
7
8
0
10
20
30
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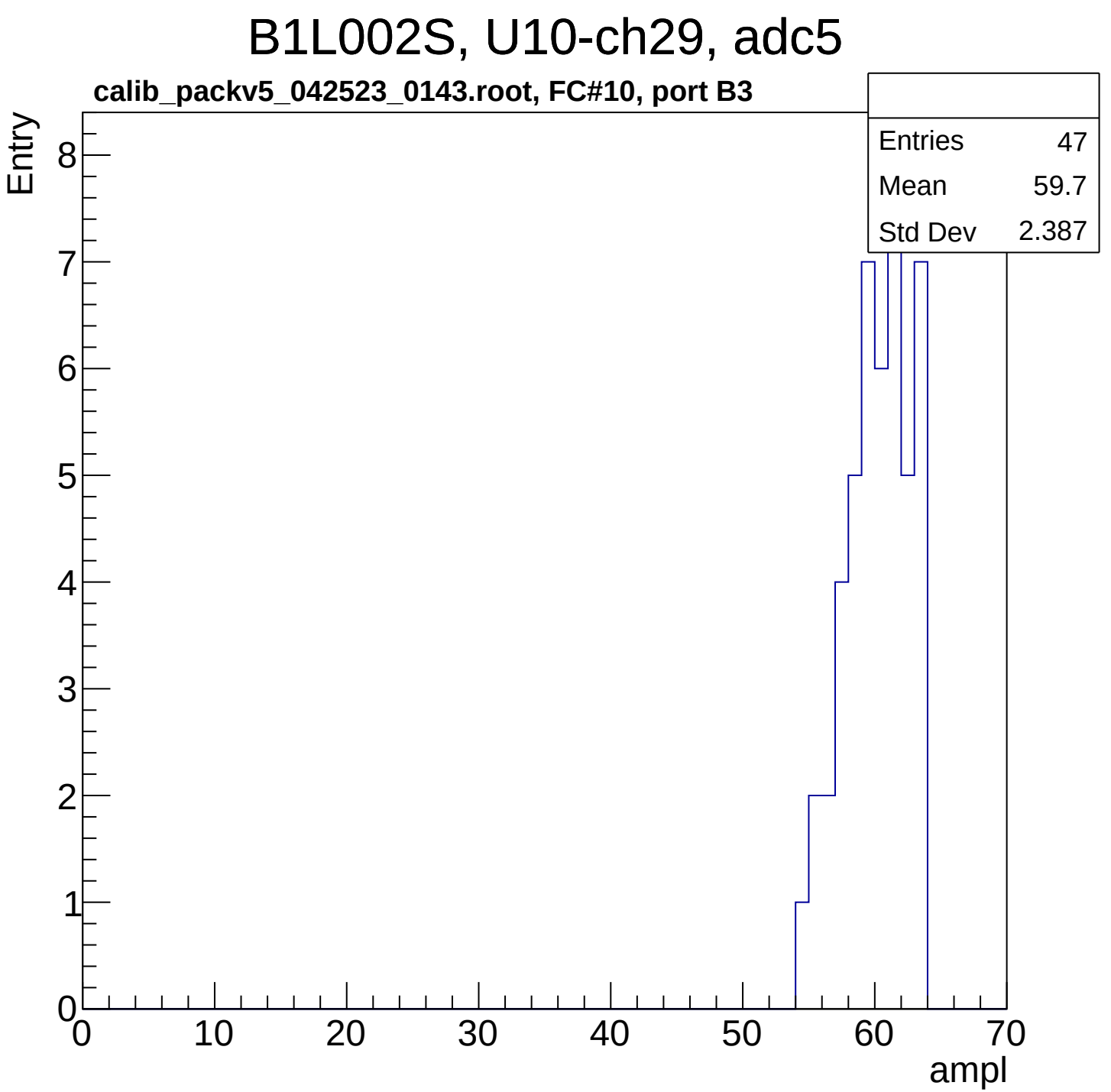
50
60
70
ampl


B1L002S, U10-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.7
Std Dev 2.387
Entry
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3
4
5
6
7
8
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10
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30
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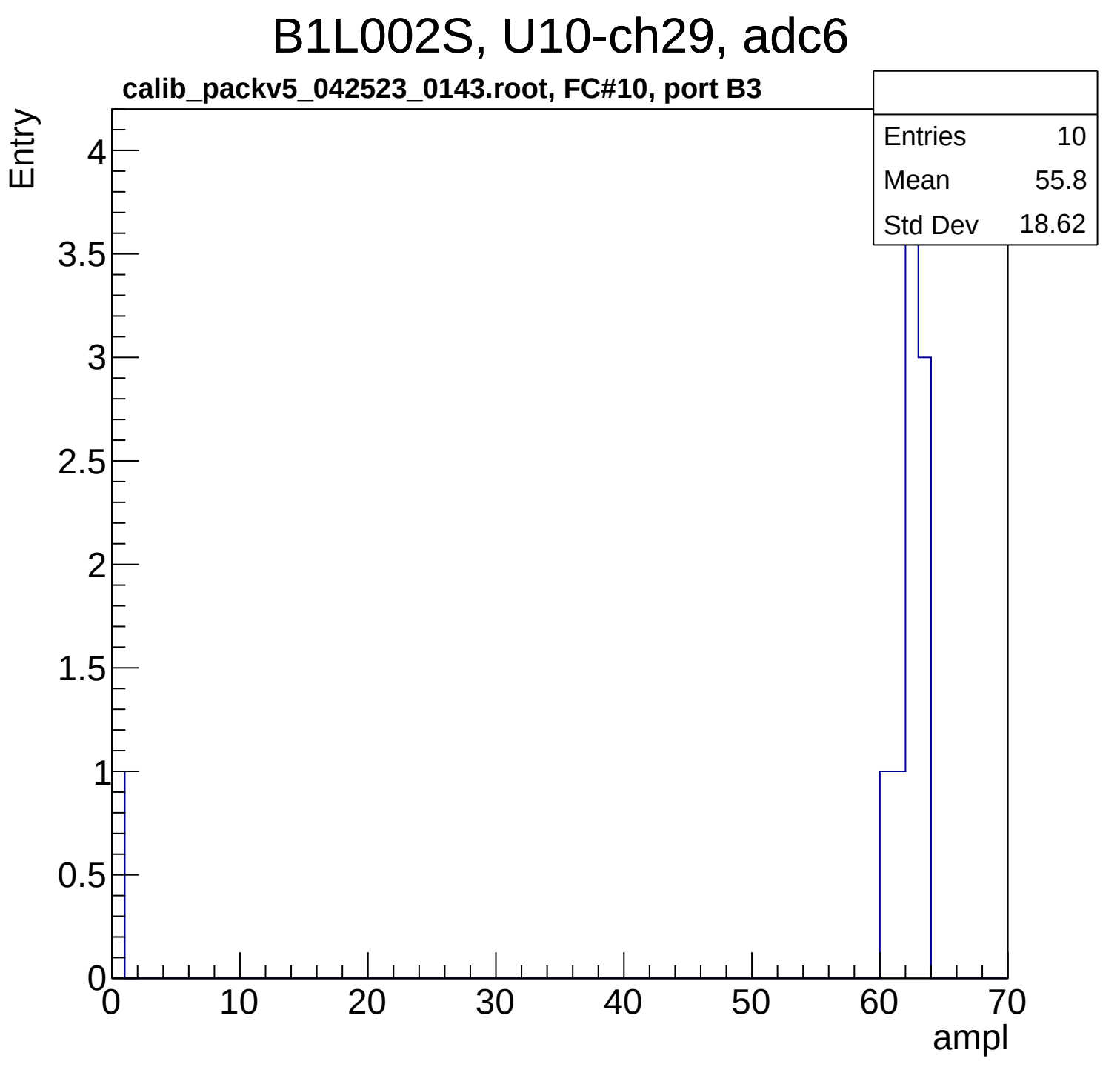
60
70
ampl


B1L002S, U10-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.62
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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50
60
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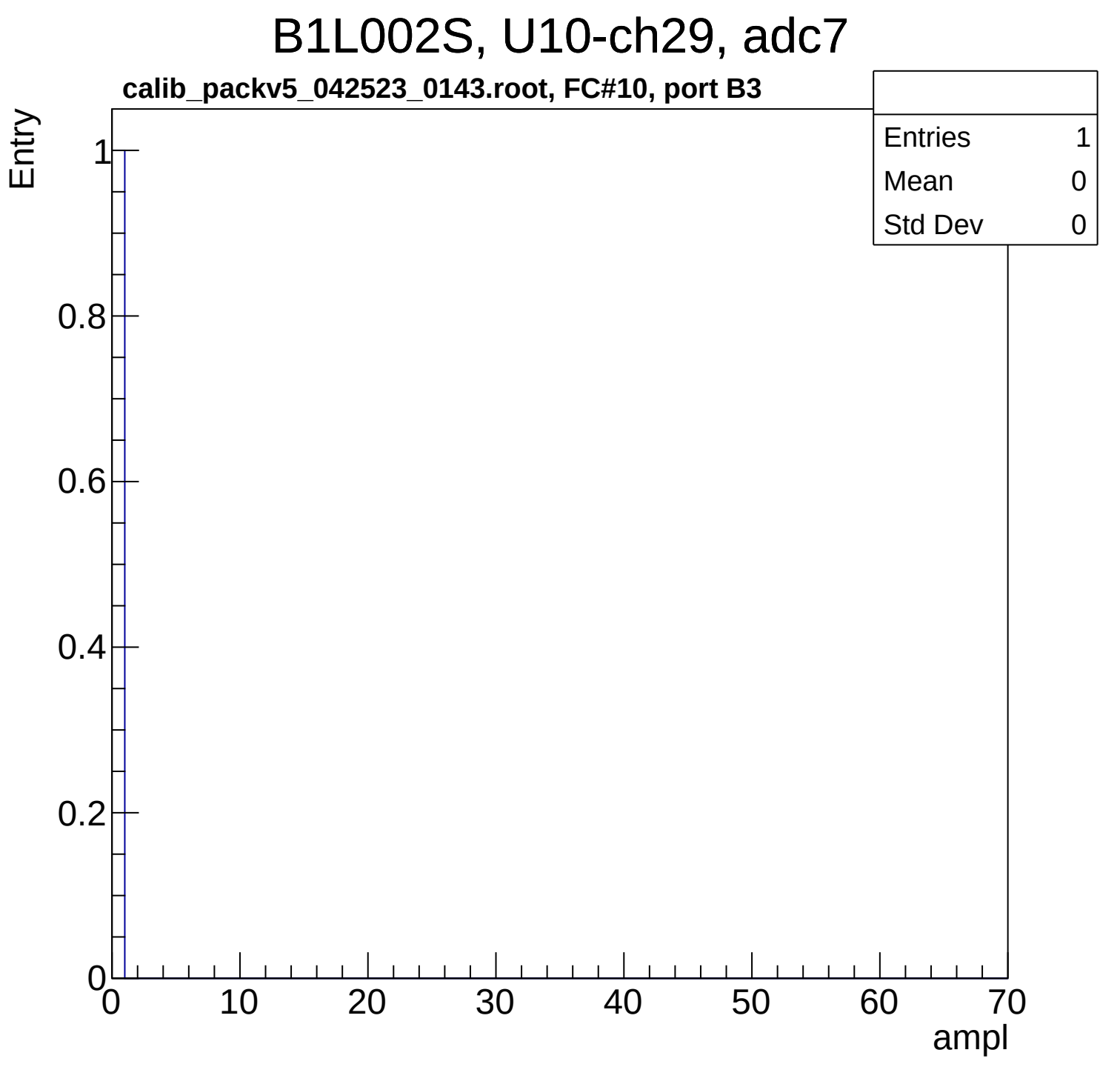
B1L002S, U10-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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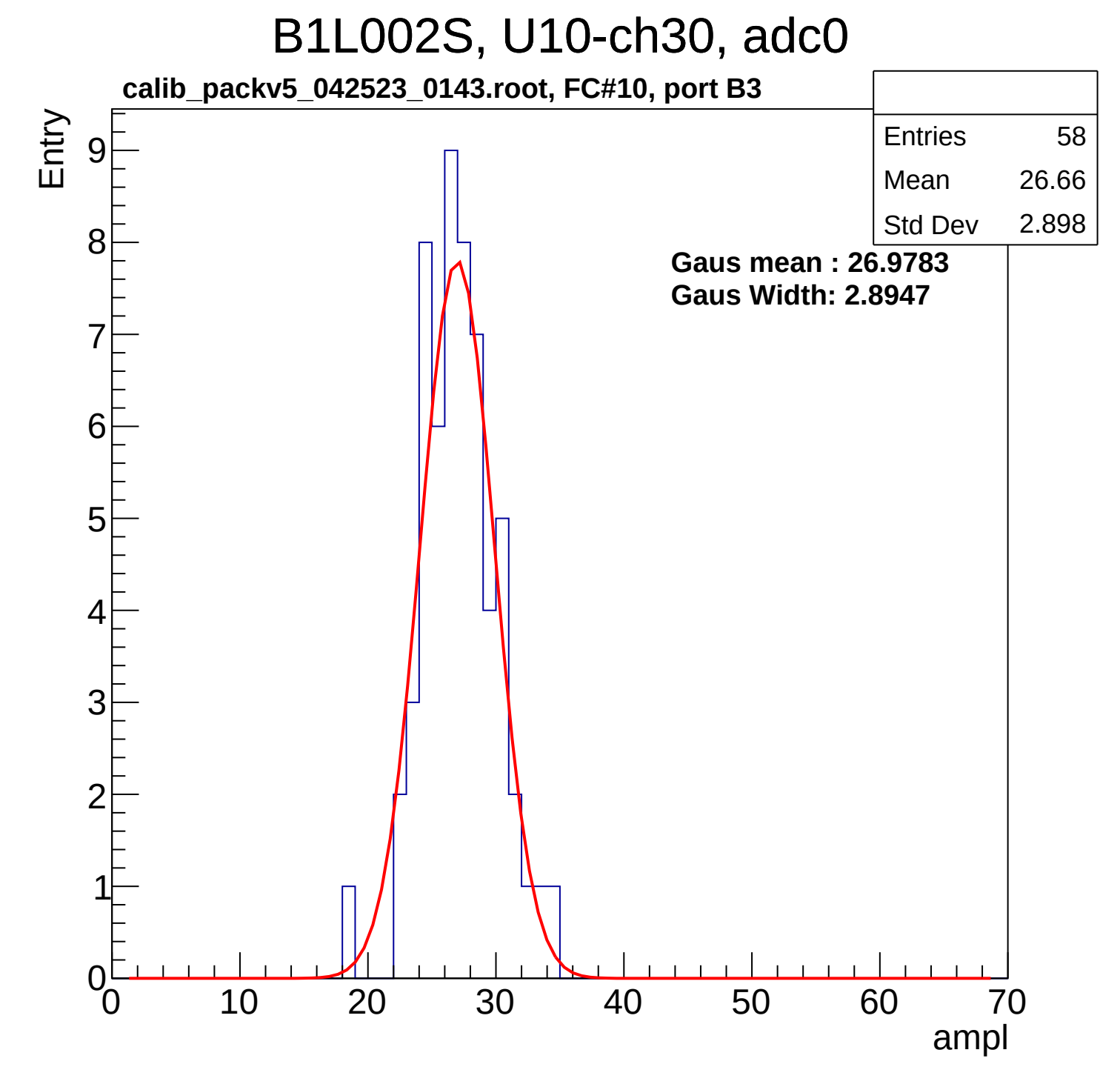
40
50
60
70
ampl


B1L002S, U10-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.66
Std Dev 2.898
Gaus mean : 26.9783
Gaus Width: 2.8947
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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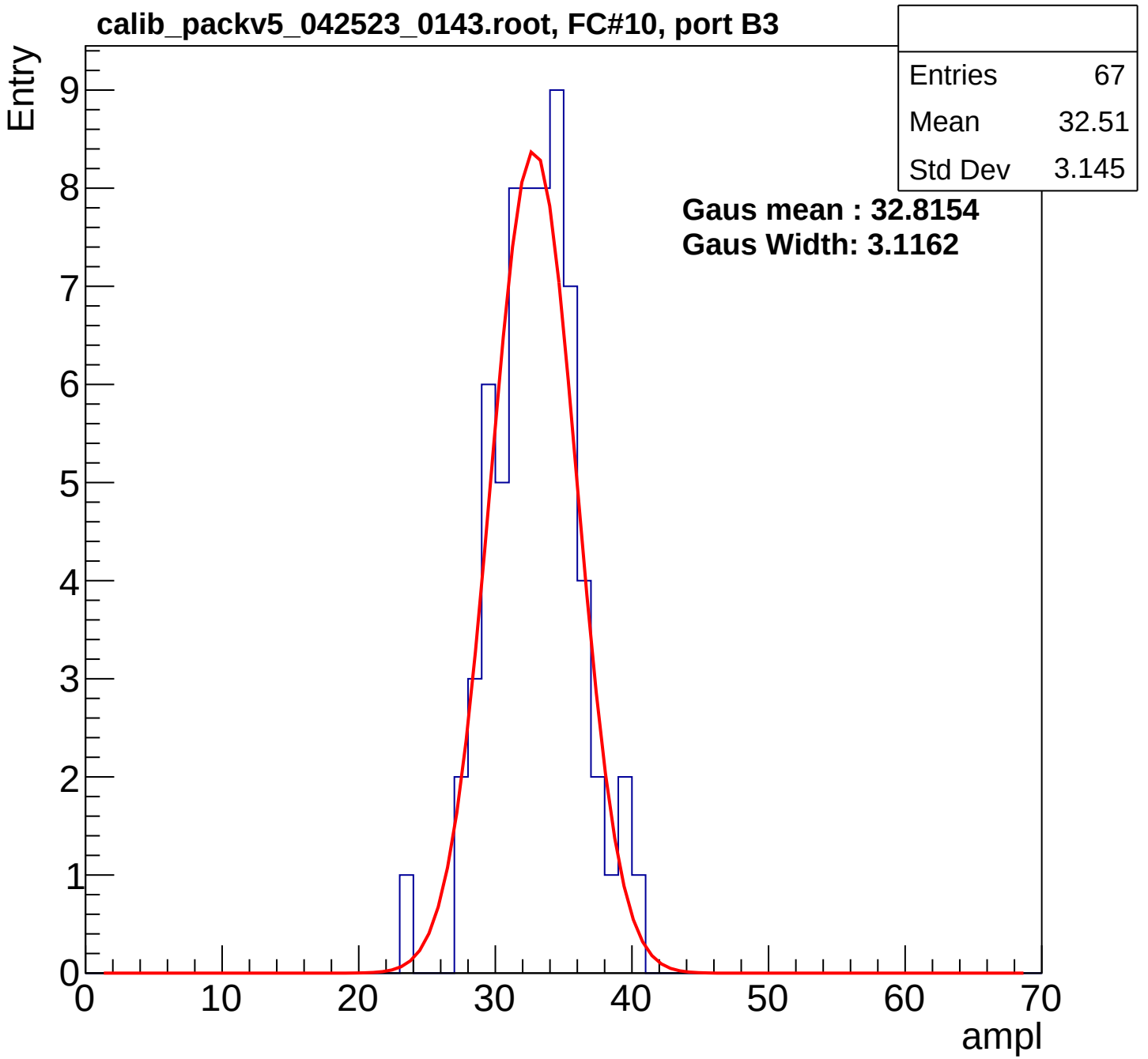
B1L002S, U10-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 32.51
Std Dev 3.145
Gaus mean : 32.8154
Gaus Width: 3.1162
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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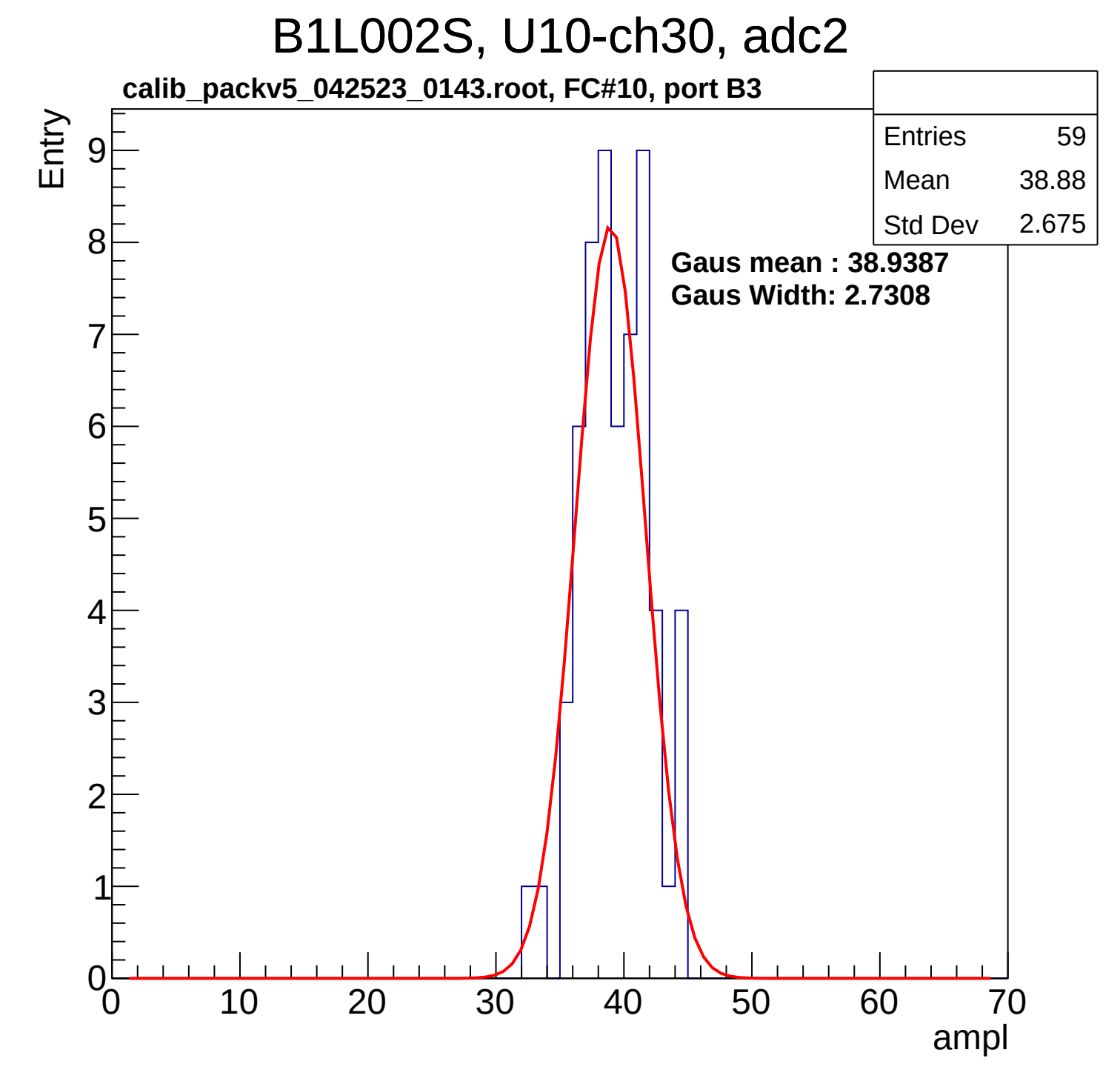
B1L002S, U10-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 38.88
Std Dev 2.675
Gaus mean : 38.9387
Gaus Width: 2.7308
Entry
ampl
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3
4
5
6
7
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0
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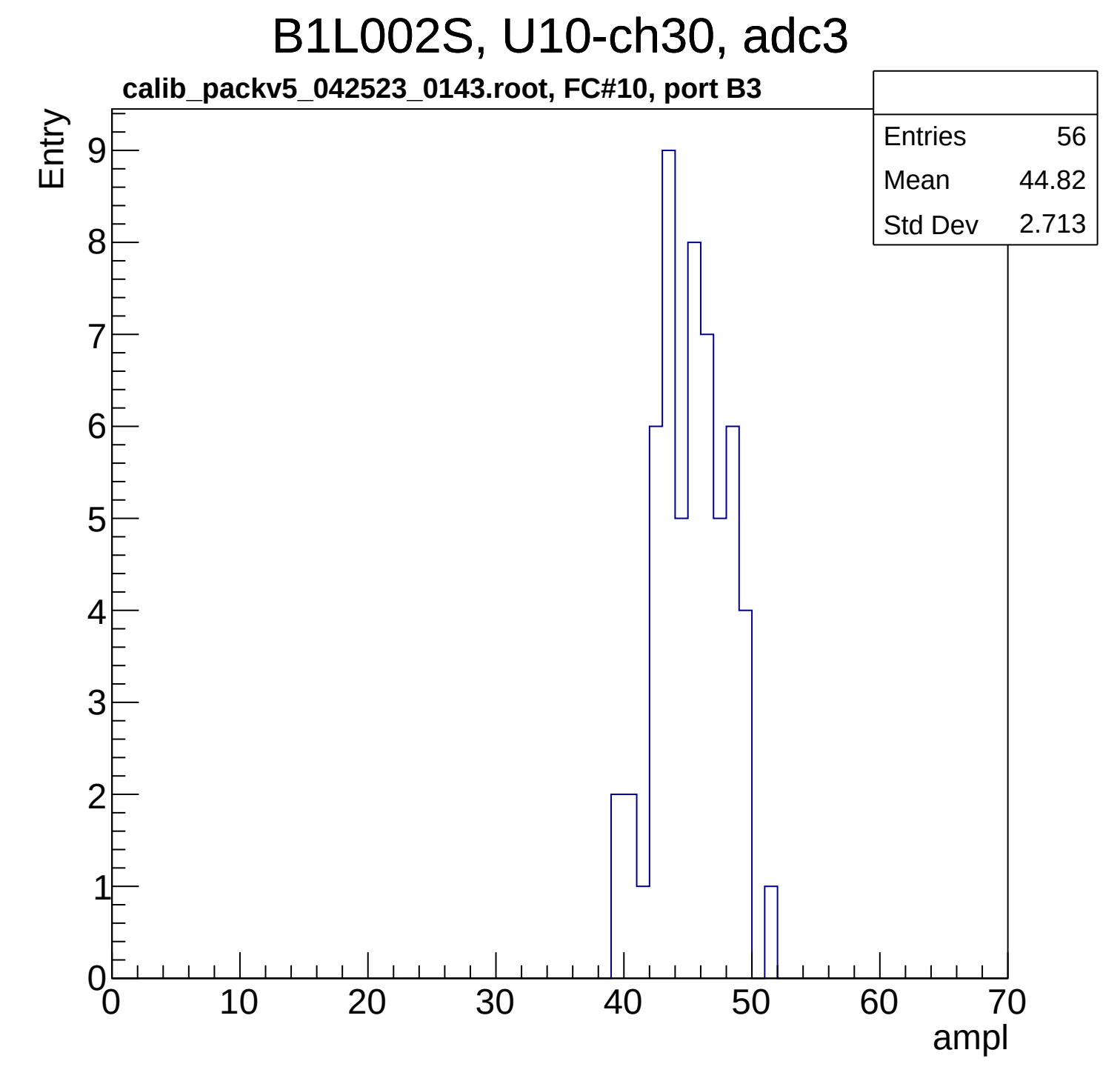
B1L002S, U10-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 44.82
Std Dev 2.713
Entry
ampl
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3
4
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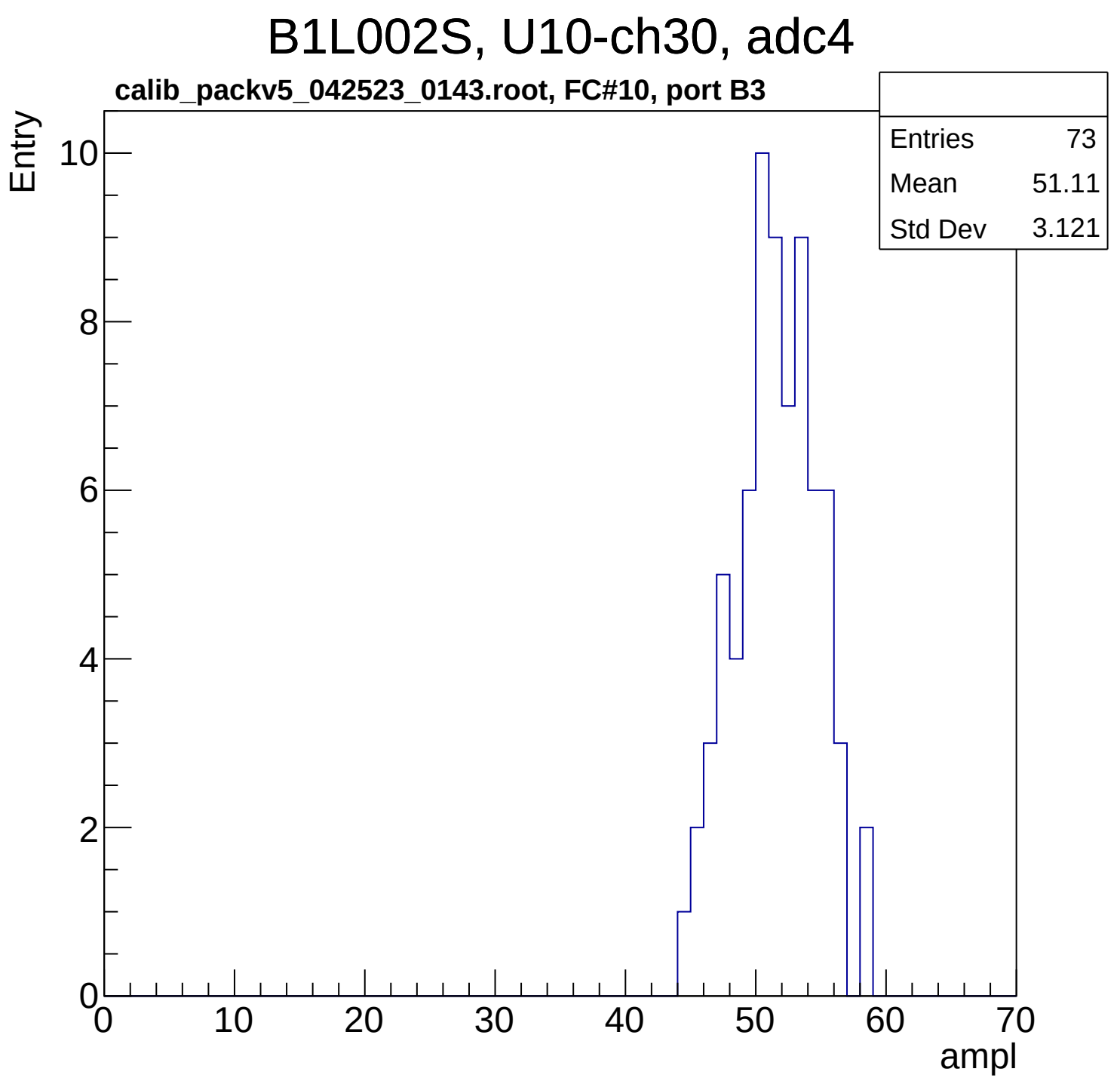
B1L002S, U10-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 51.11
Std Dev 3.121
Entry
0
2
4
6
8
10
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10
20
30
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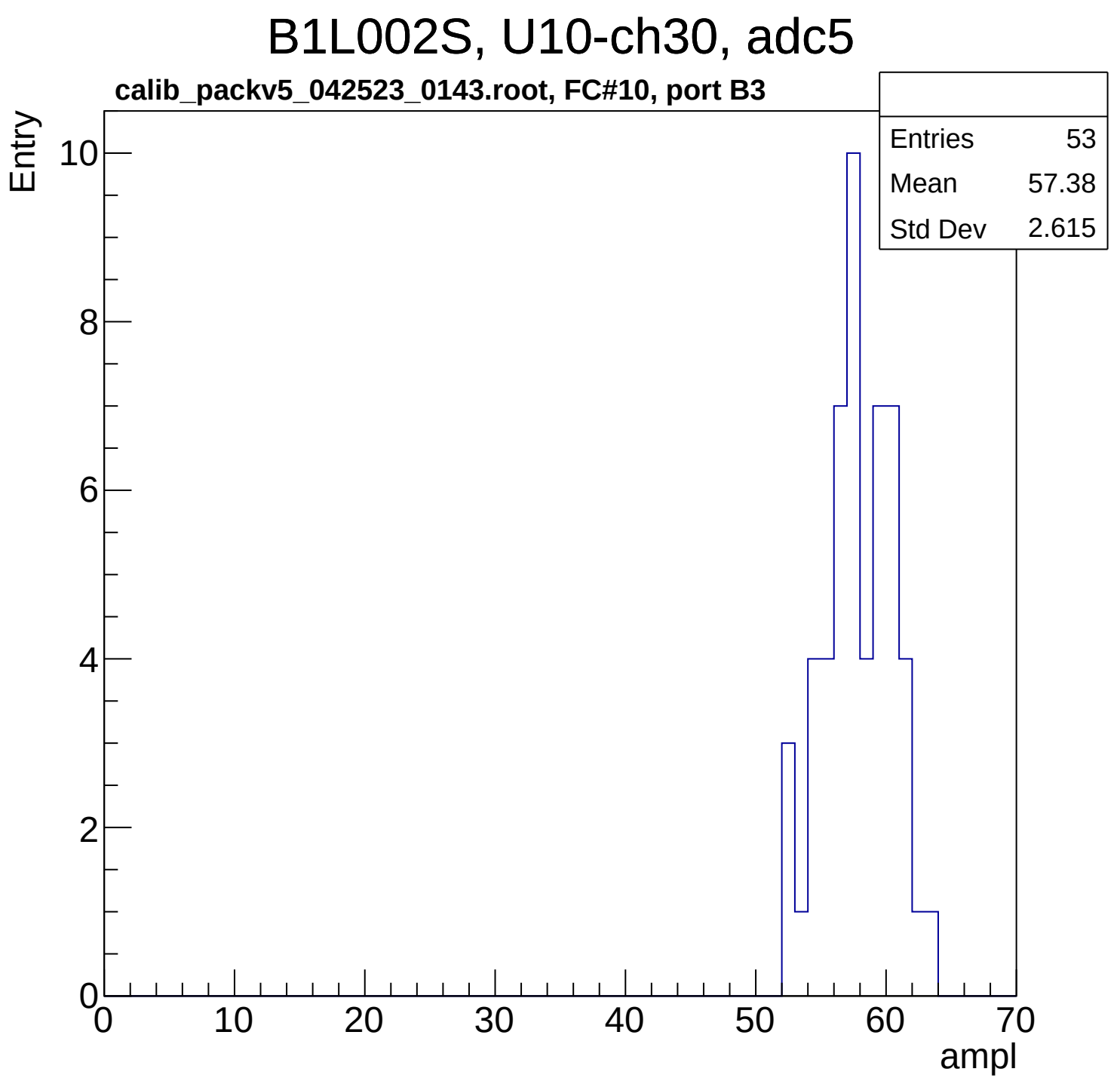
50
60
70
ampl


B1L002S, U10-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.38
Std Dev 2.615
Entry
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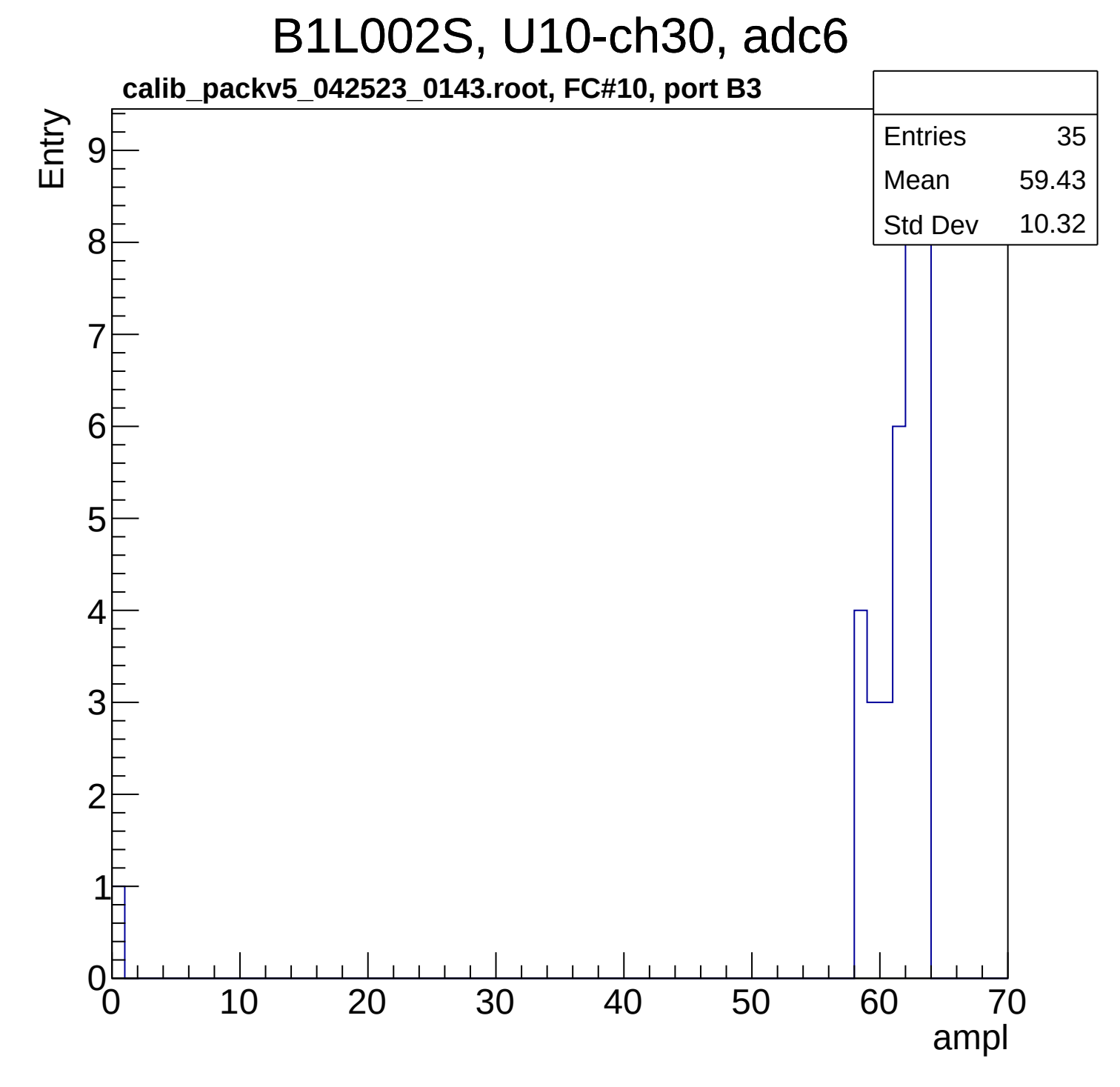
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 59.43
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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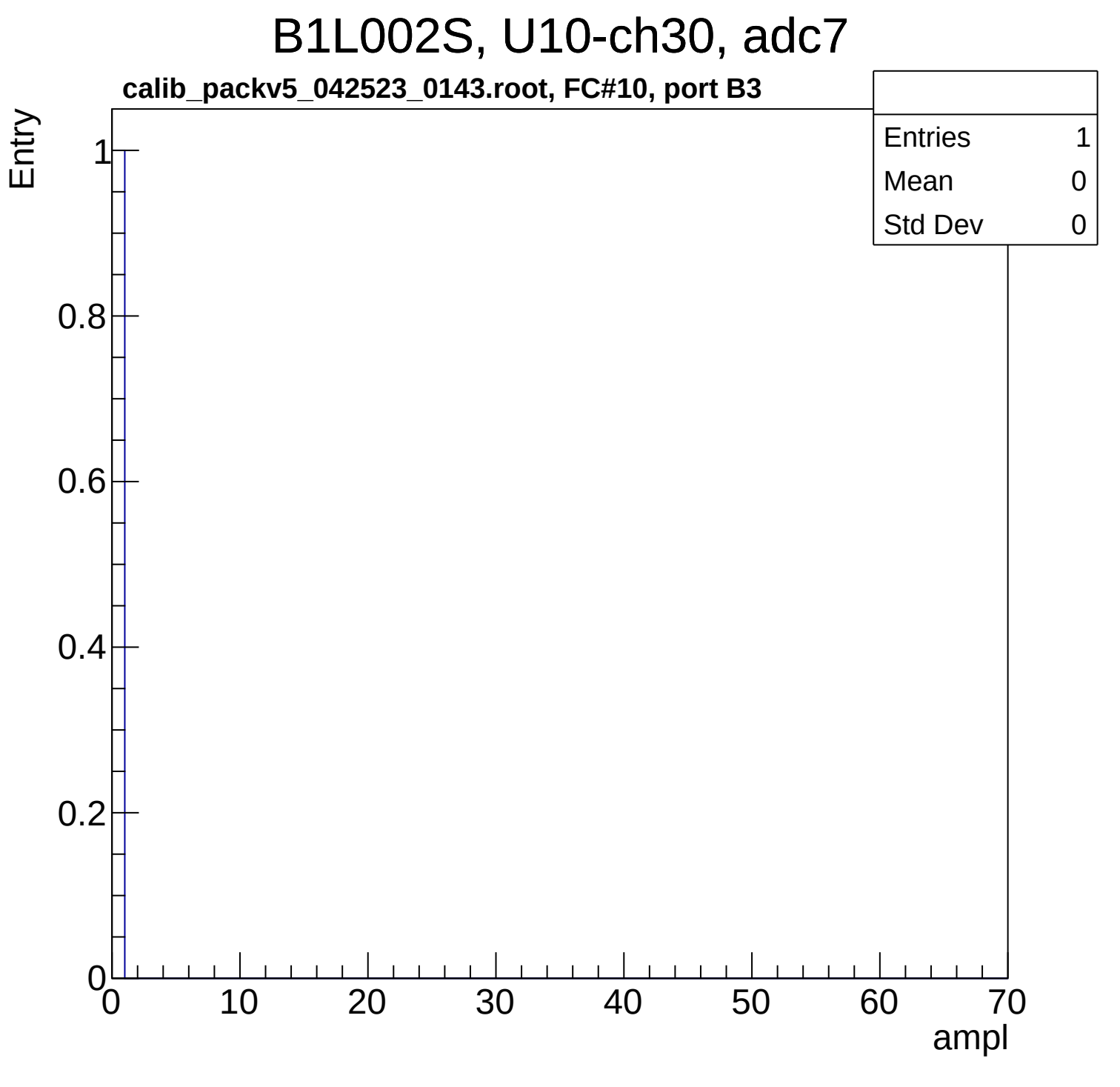
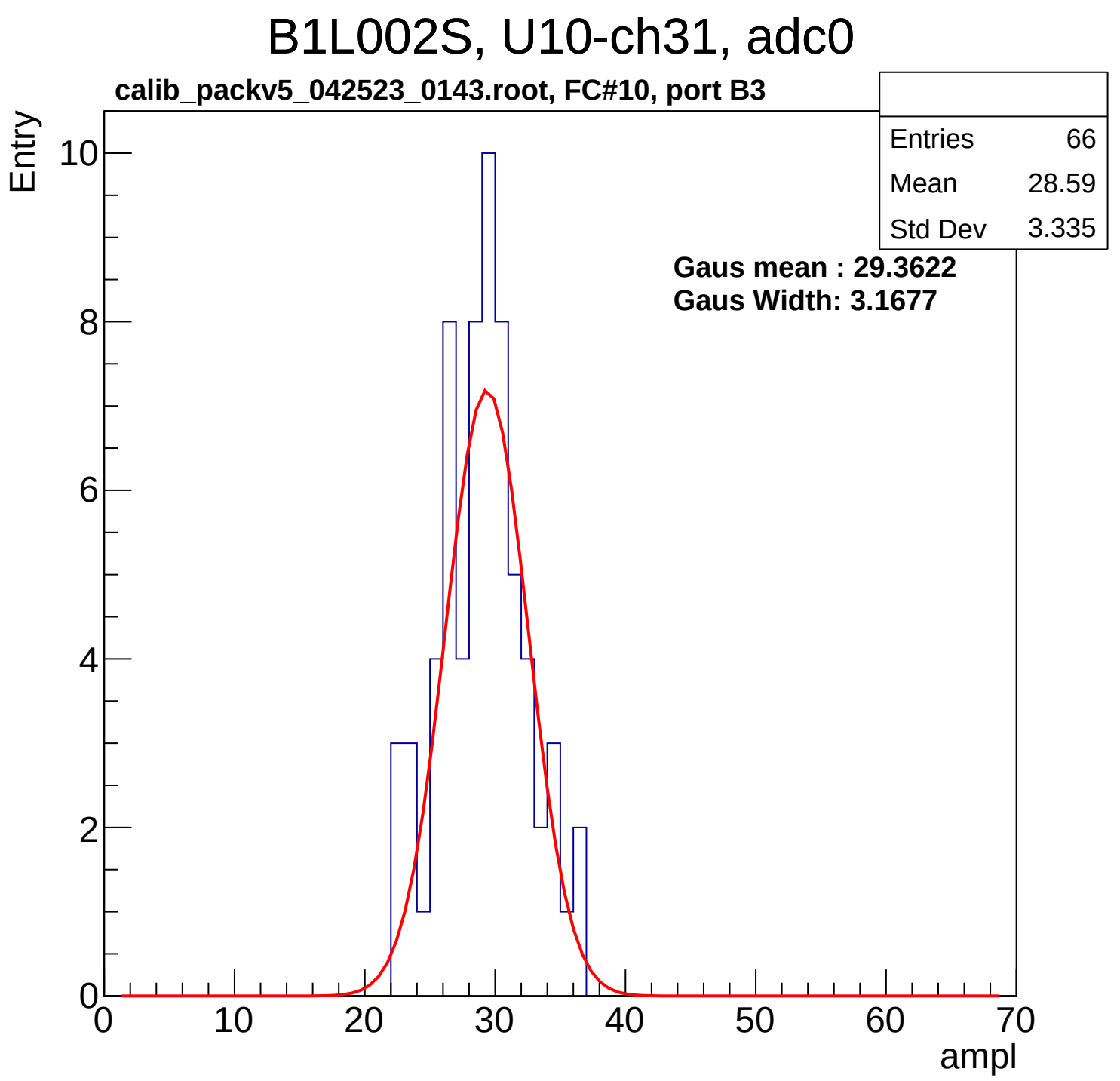
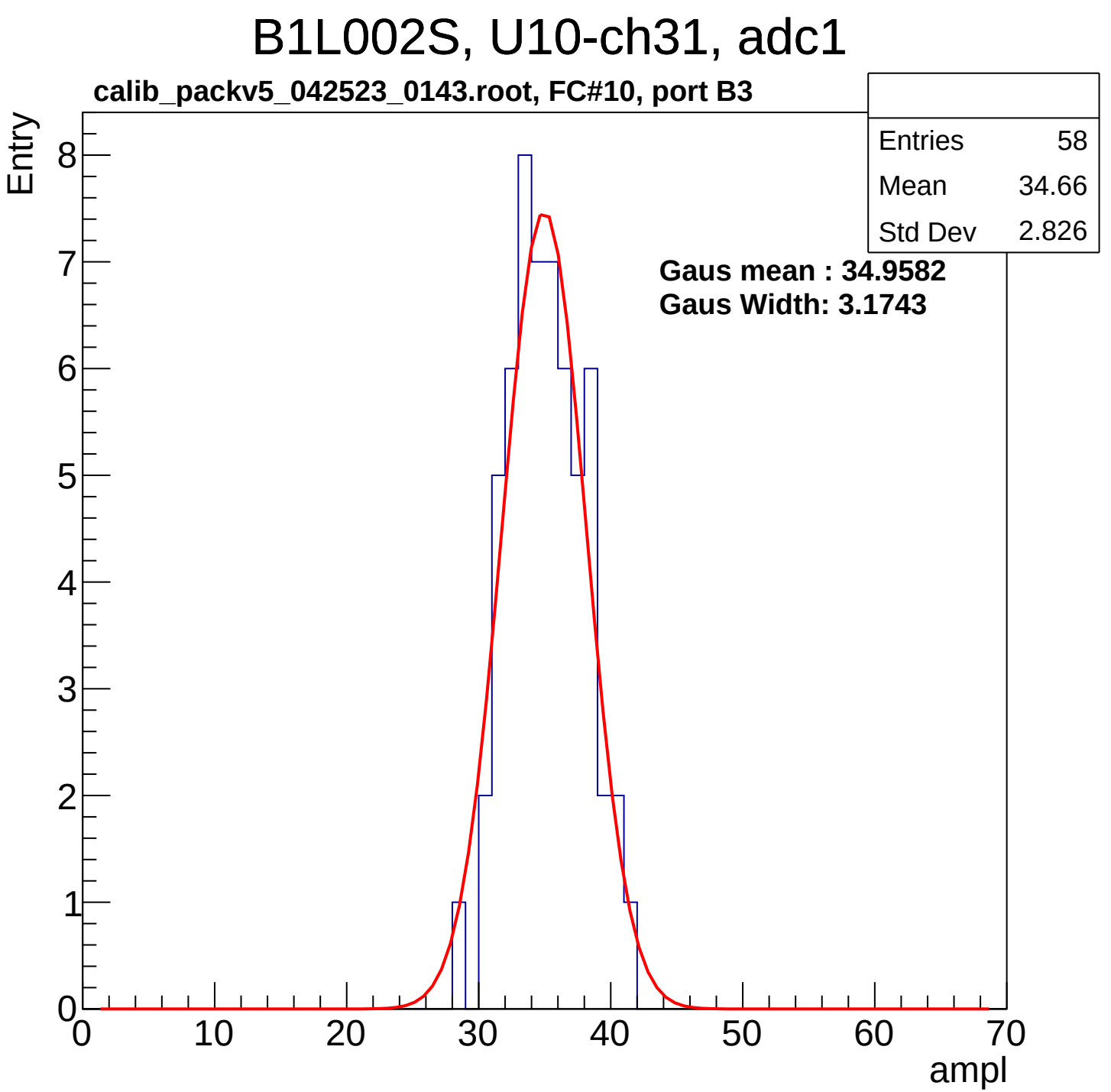
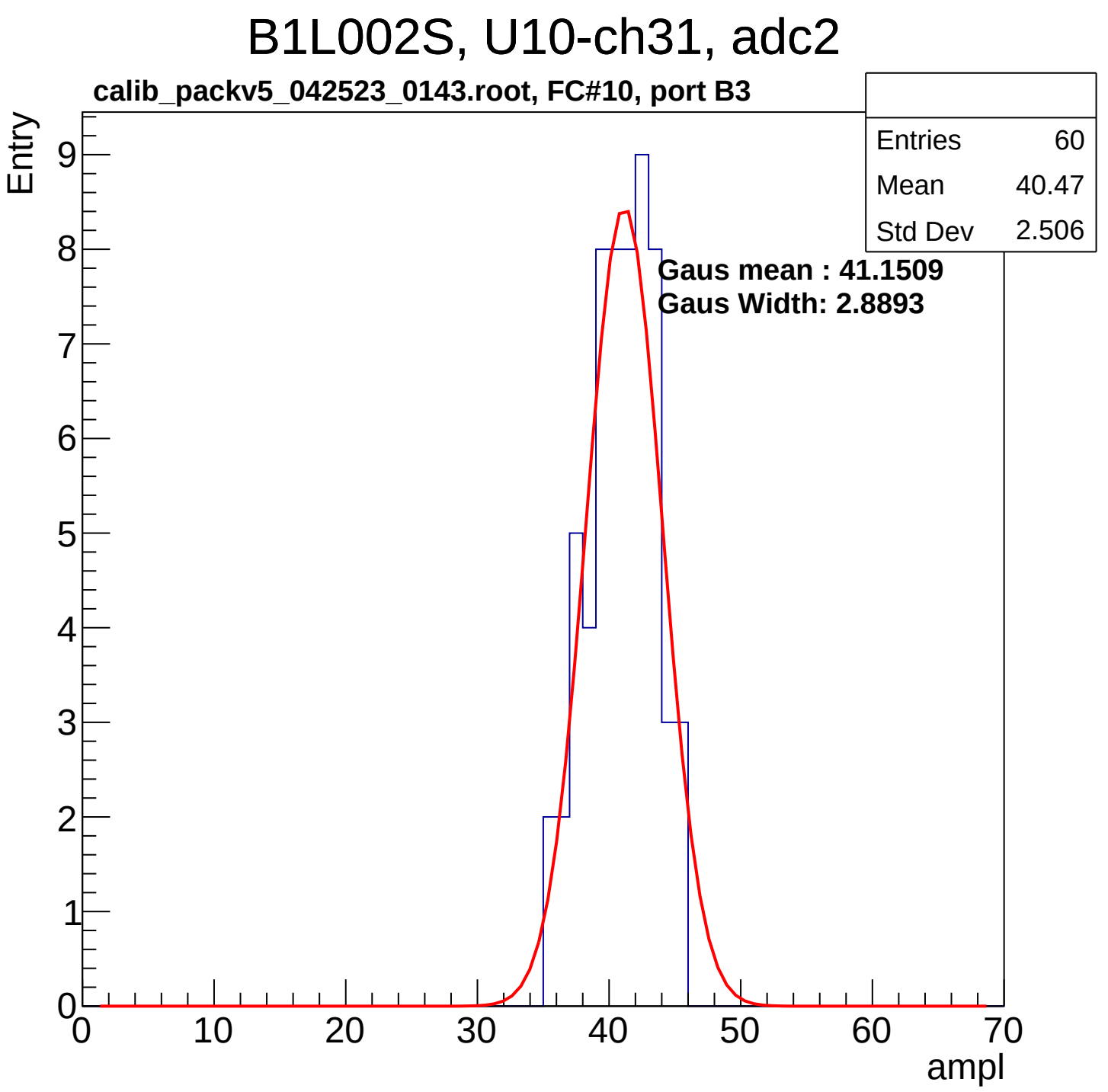

B1L002S, U10-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.59
Std Dev 3.335
Gaus mean : 29.3622
Gaus Width: 3.1677
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.66
Std Dev 2.826
Gaus mean : 34.9582
Gaus Width: 3.1743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.47
Std Dev 2.506
Gaus mean : 41.1509
Gaus Width: 2.8893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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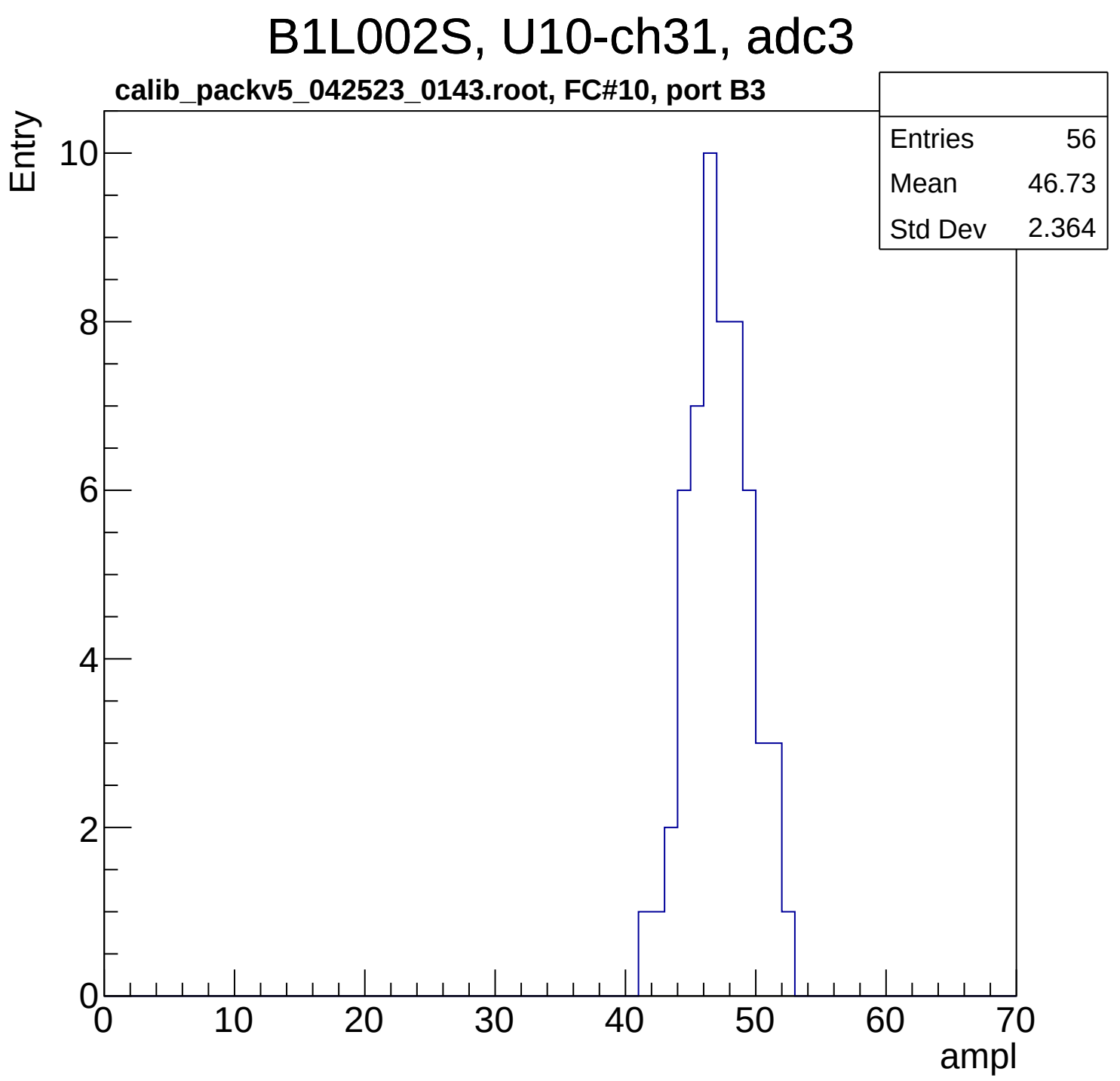
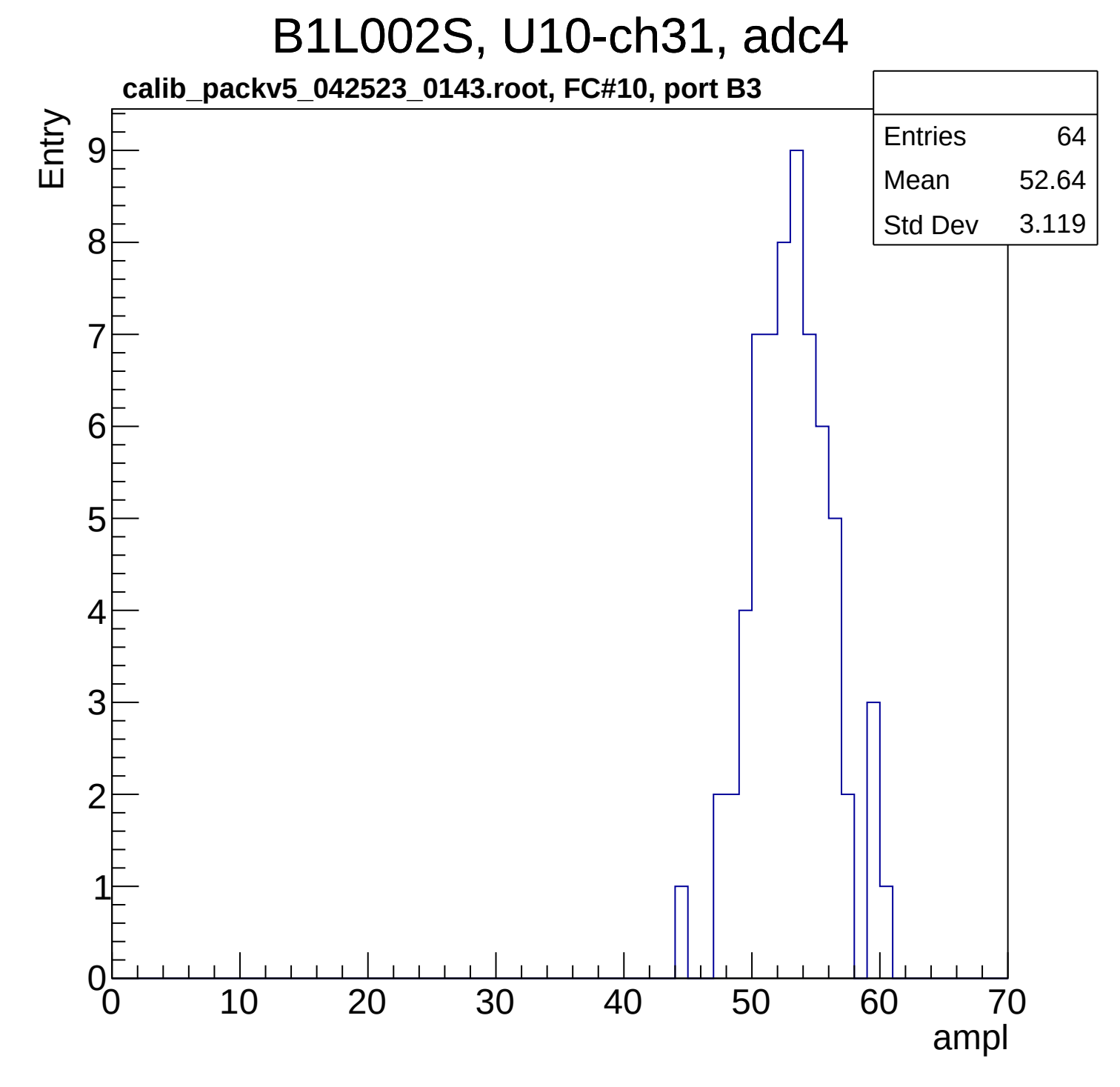

B1L002S, U10-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.73
Std Dev 2.364
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.64
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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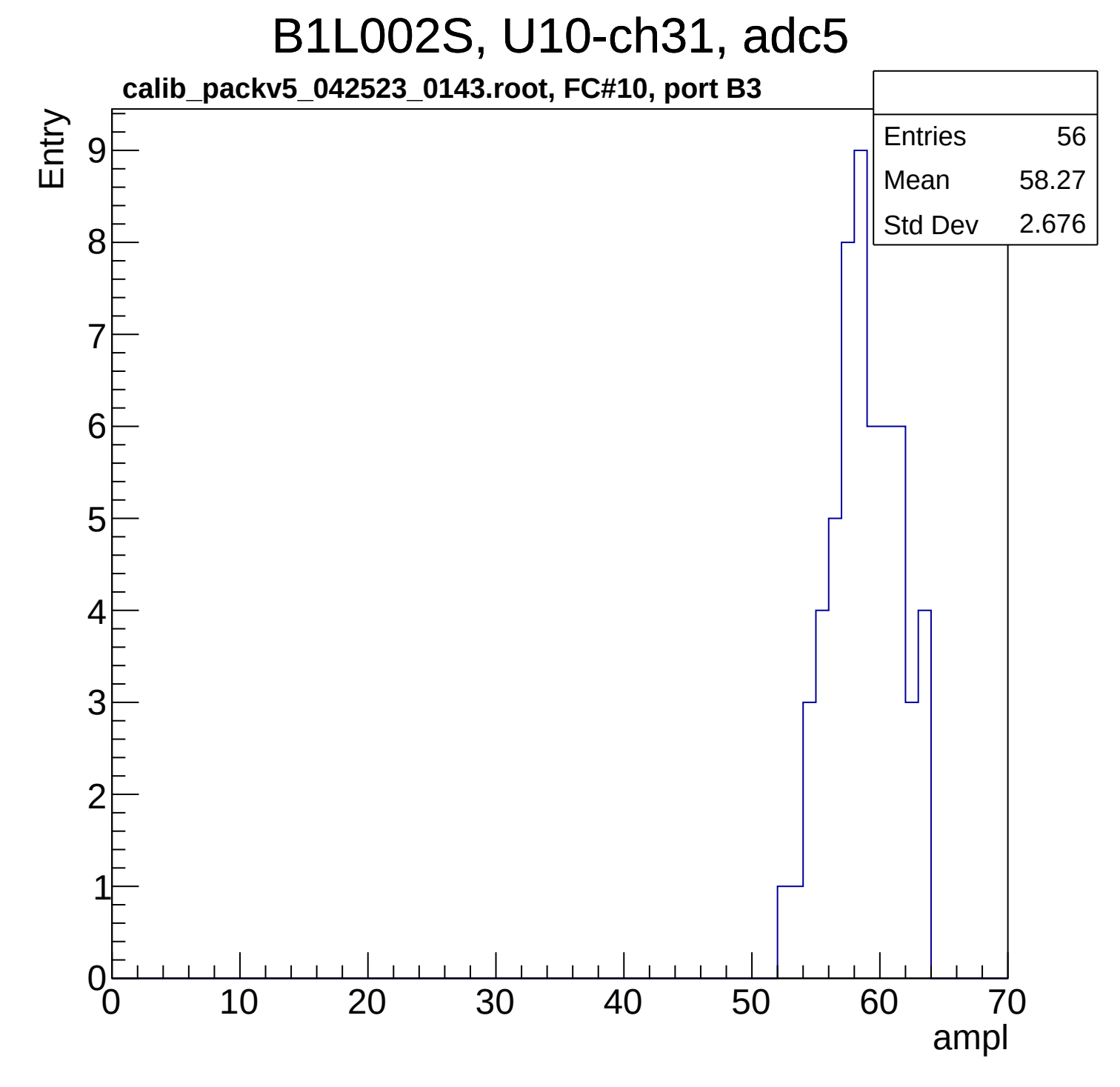
B1L002S, U10-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.27
Std Dev 2.676
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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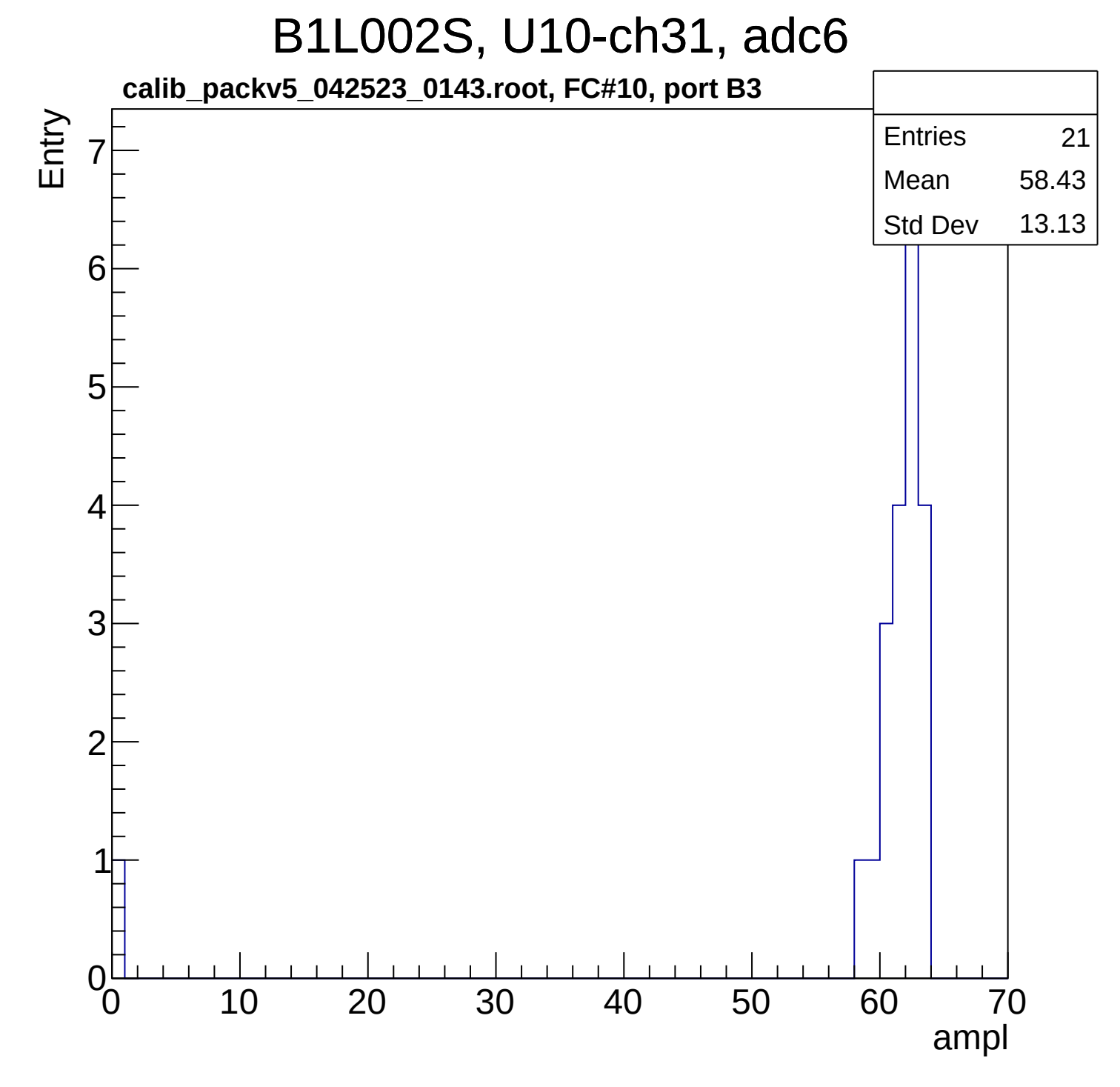
B1L002S, U10-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.43
Std Dev 13.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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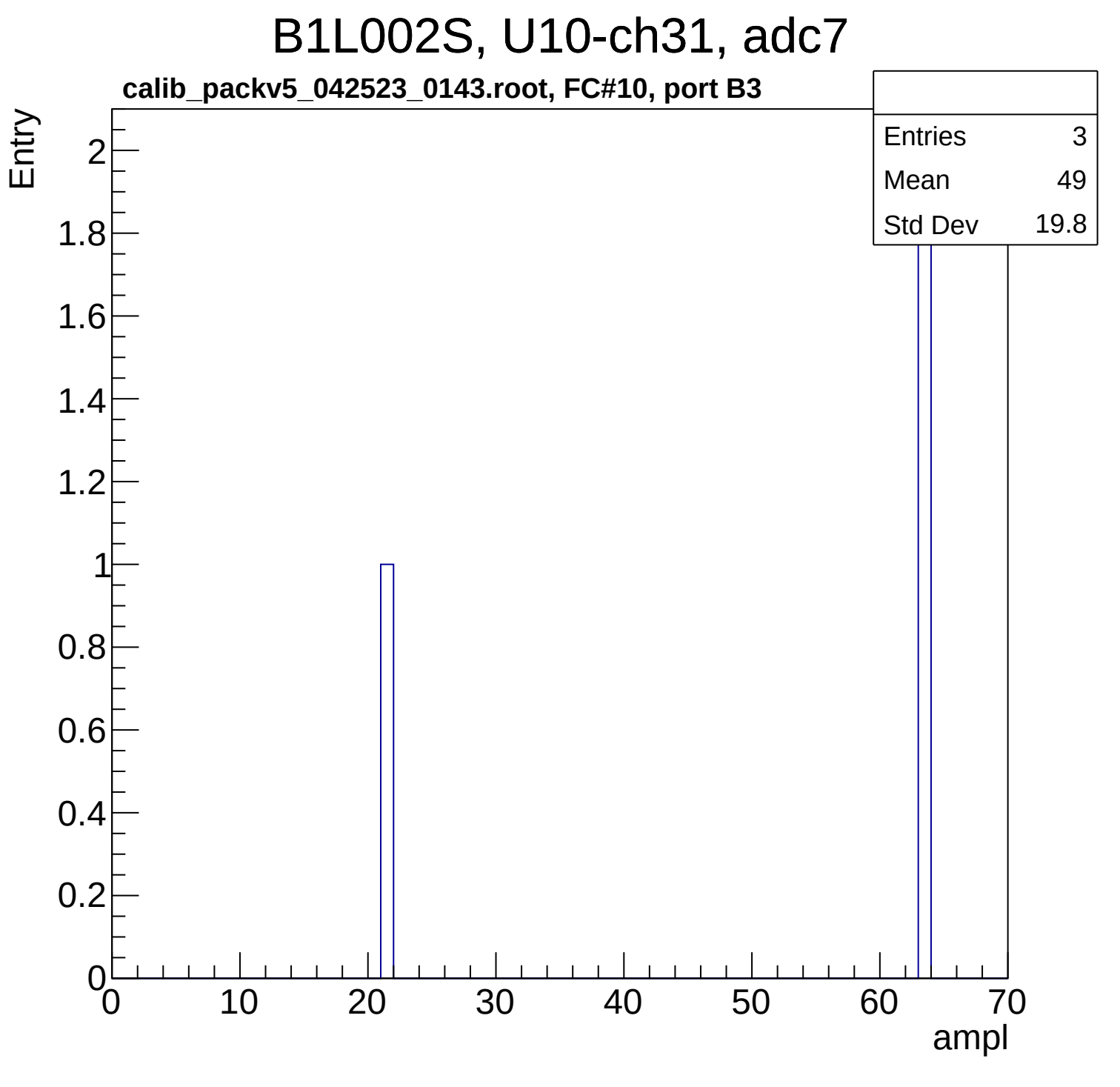
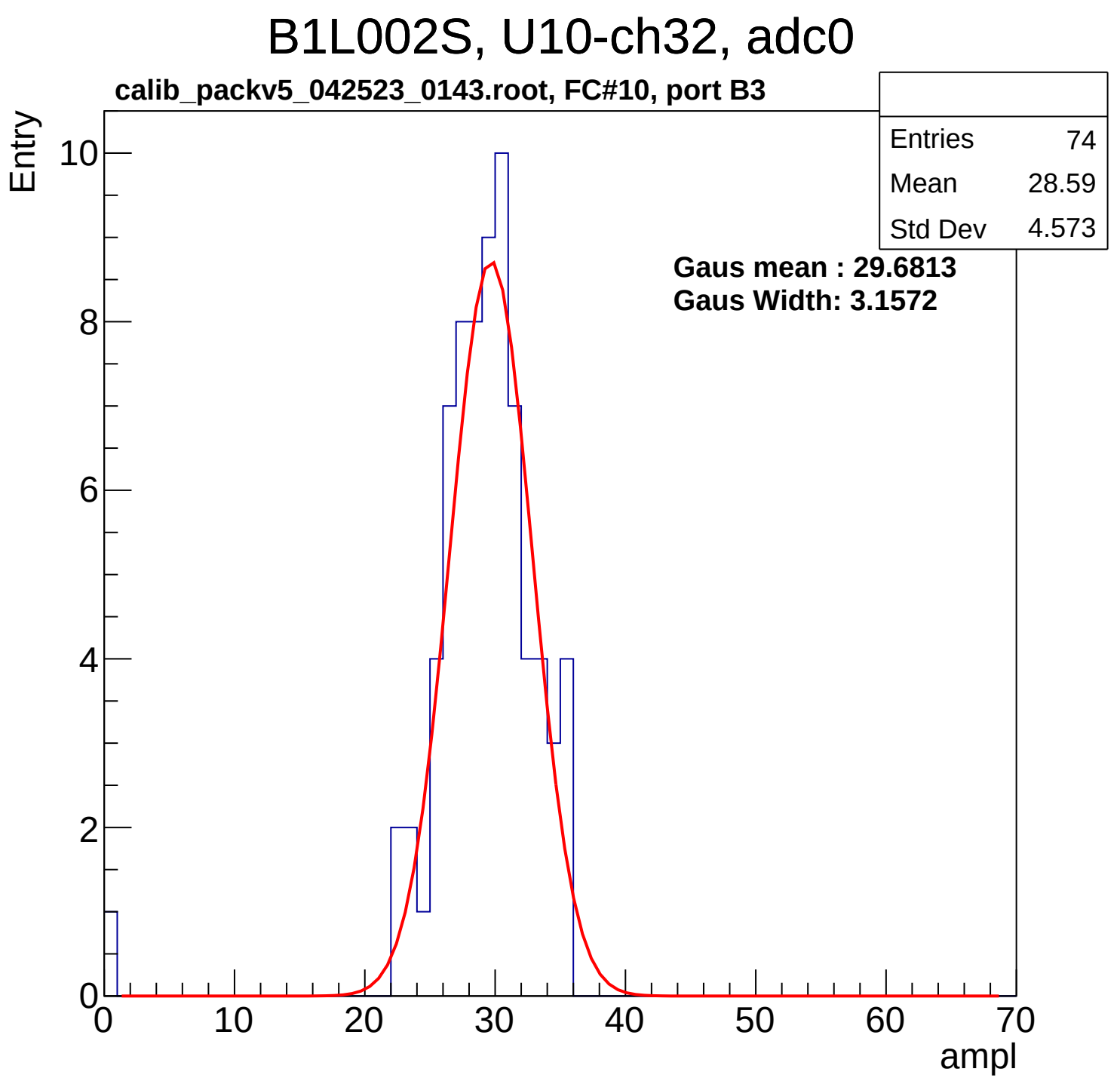

B1L002S, U10-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 49
Std Dev 19.8
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.59
Std Dev 4.573
Gaus mean : 29.6813
Gaus Width: 3.1572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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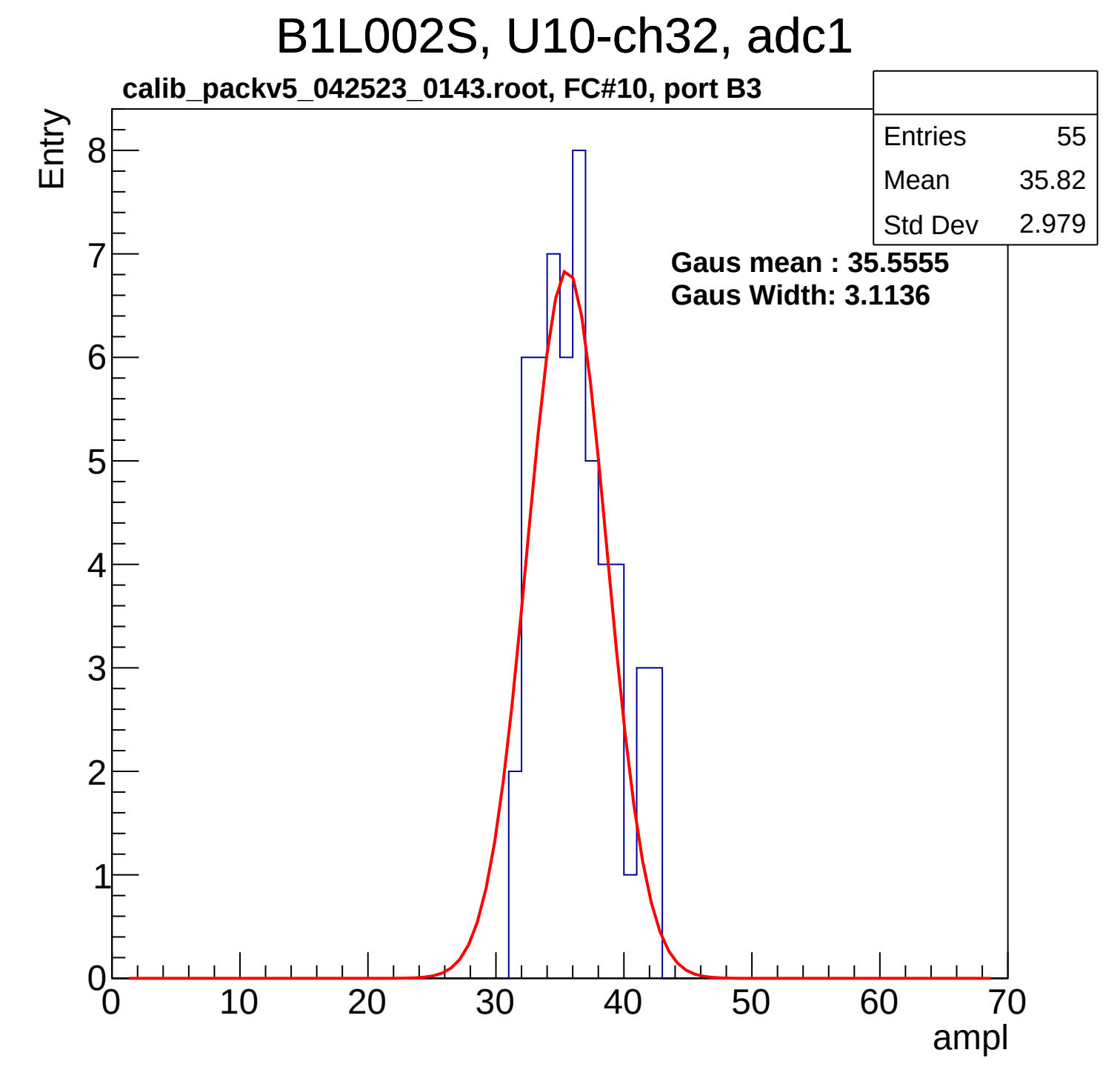
B1L002S, U10-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.82
Std Dev 2.979
Gaus mean : 35.5555
Gaus Width: 3.1136
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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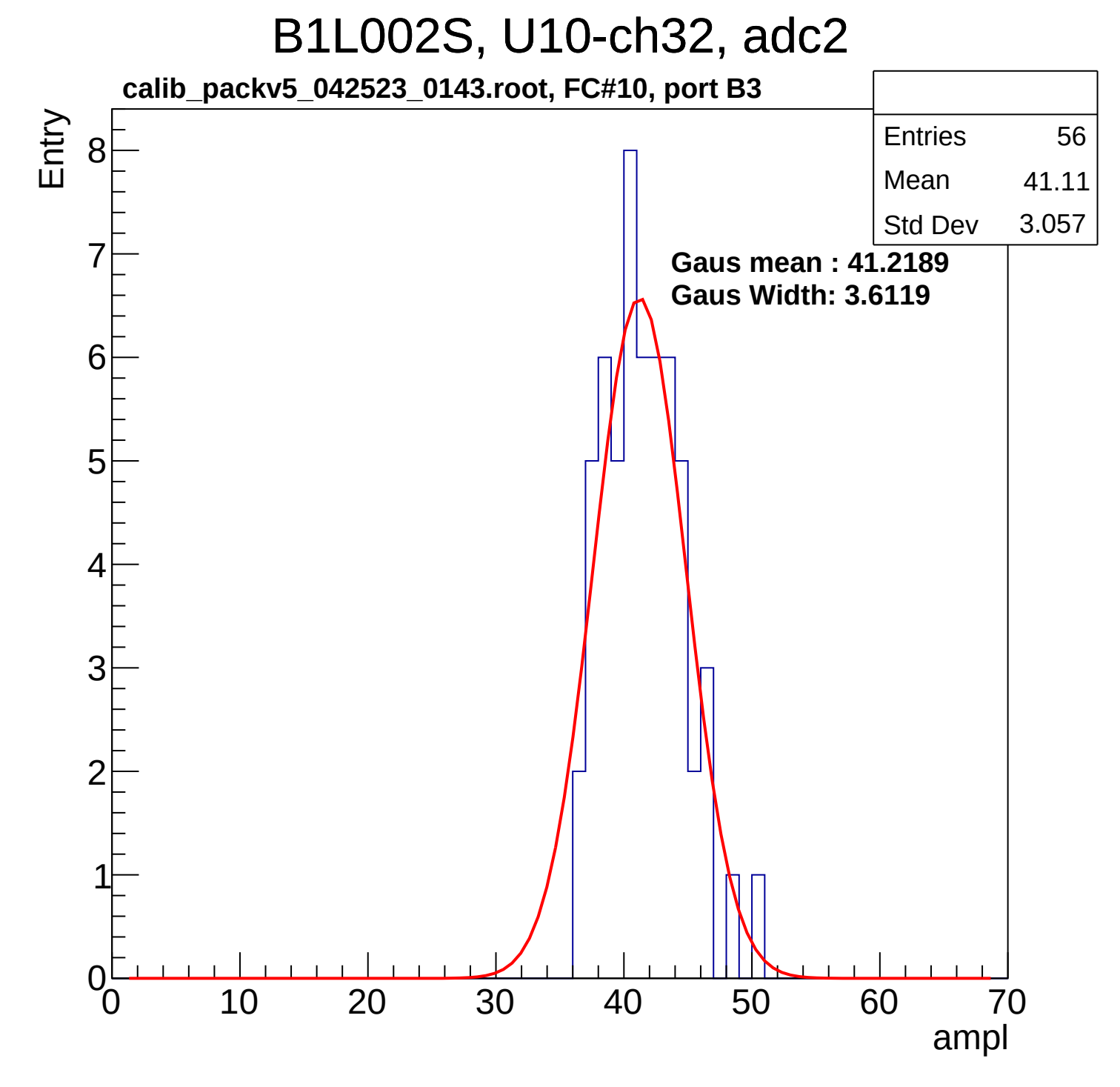
B1L002S, U10-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.11
Std Dev 3.057
Gaus mean : 41.2189
Gaus Width: 3.6119
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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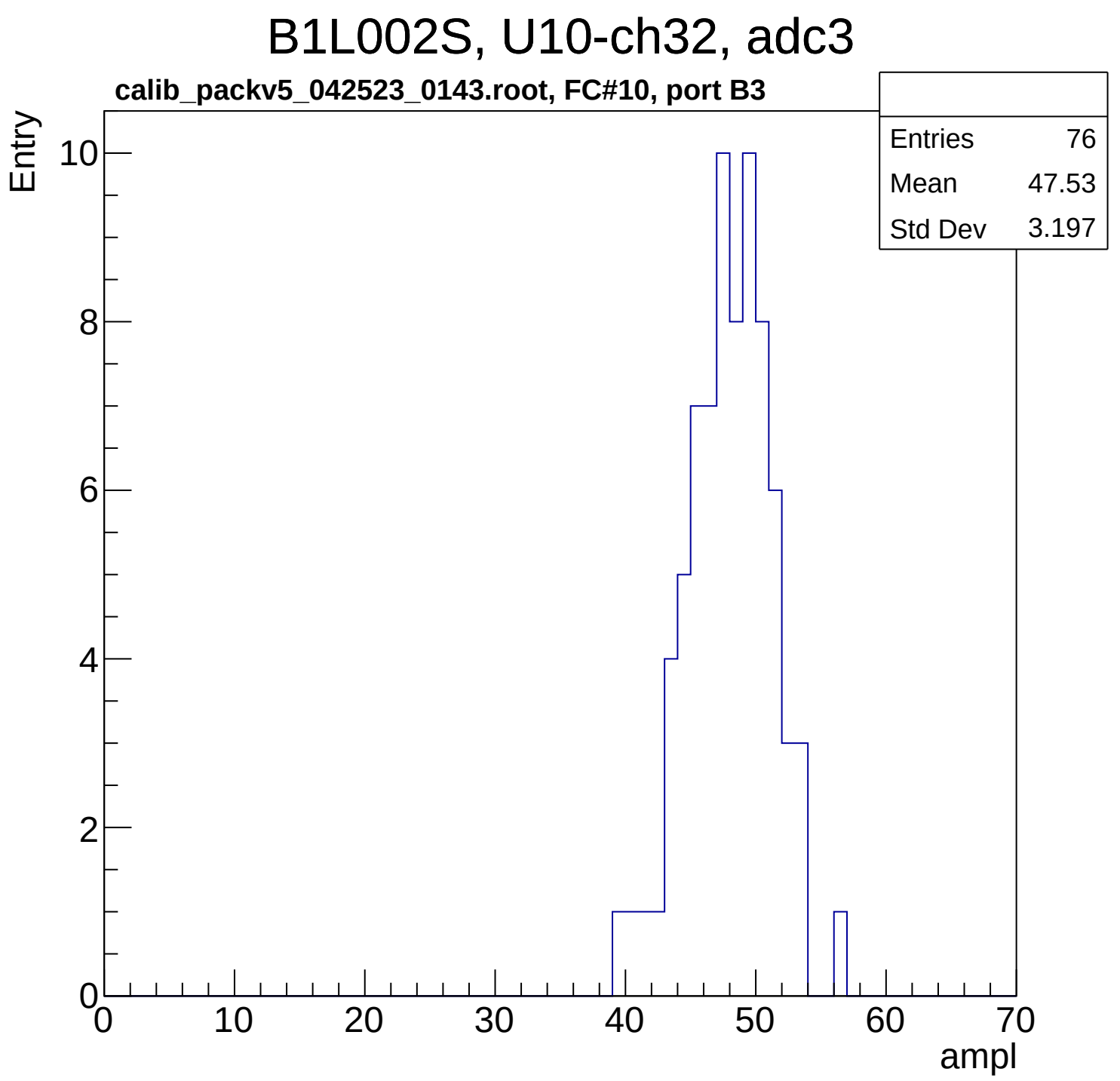
B1L002S, U10-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.53
Std Dev 3.197
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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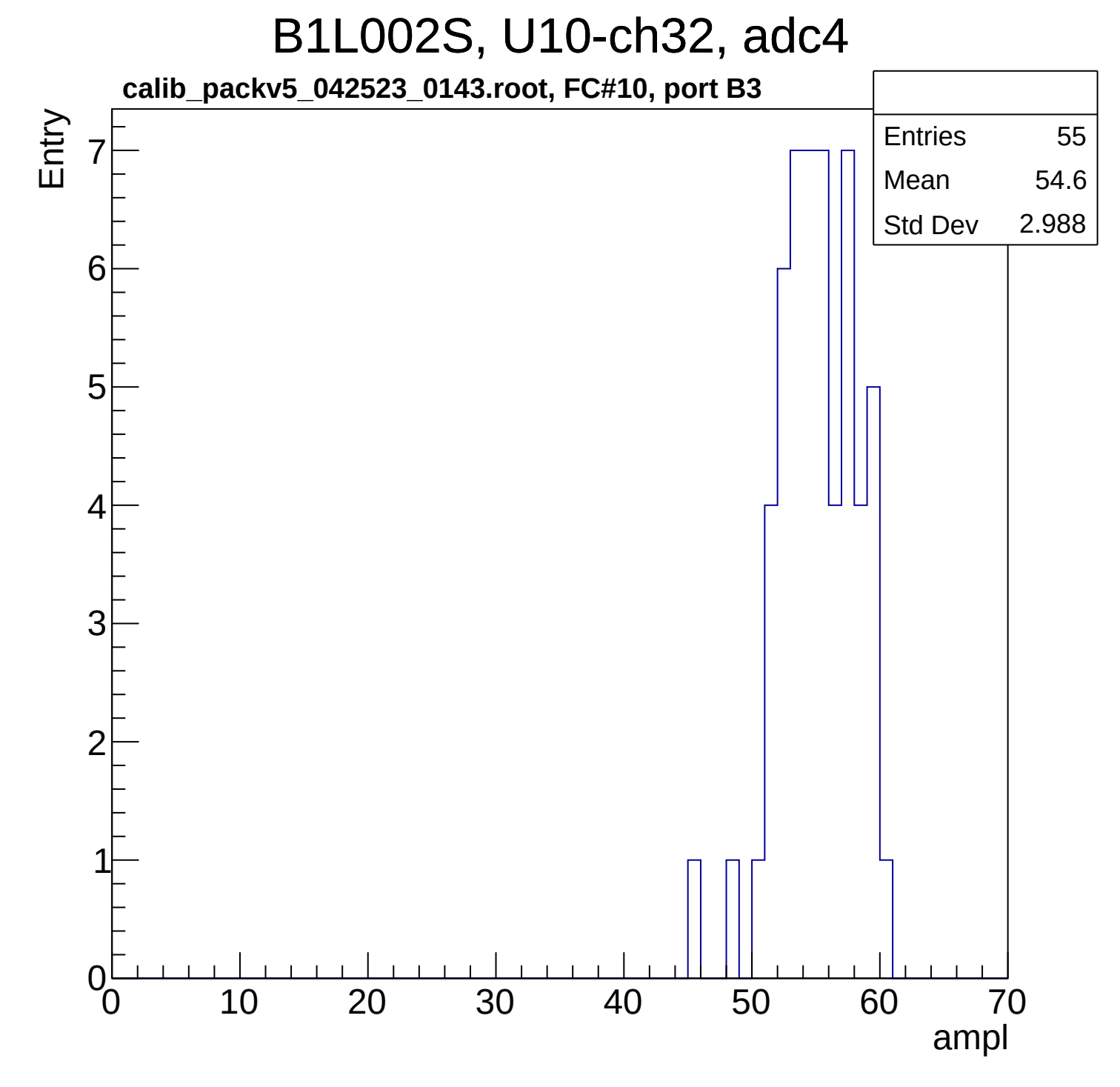
B1L002S, U10-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.6
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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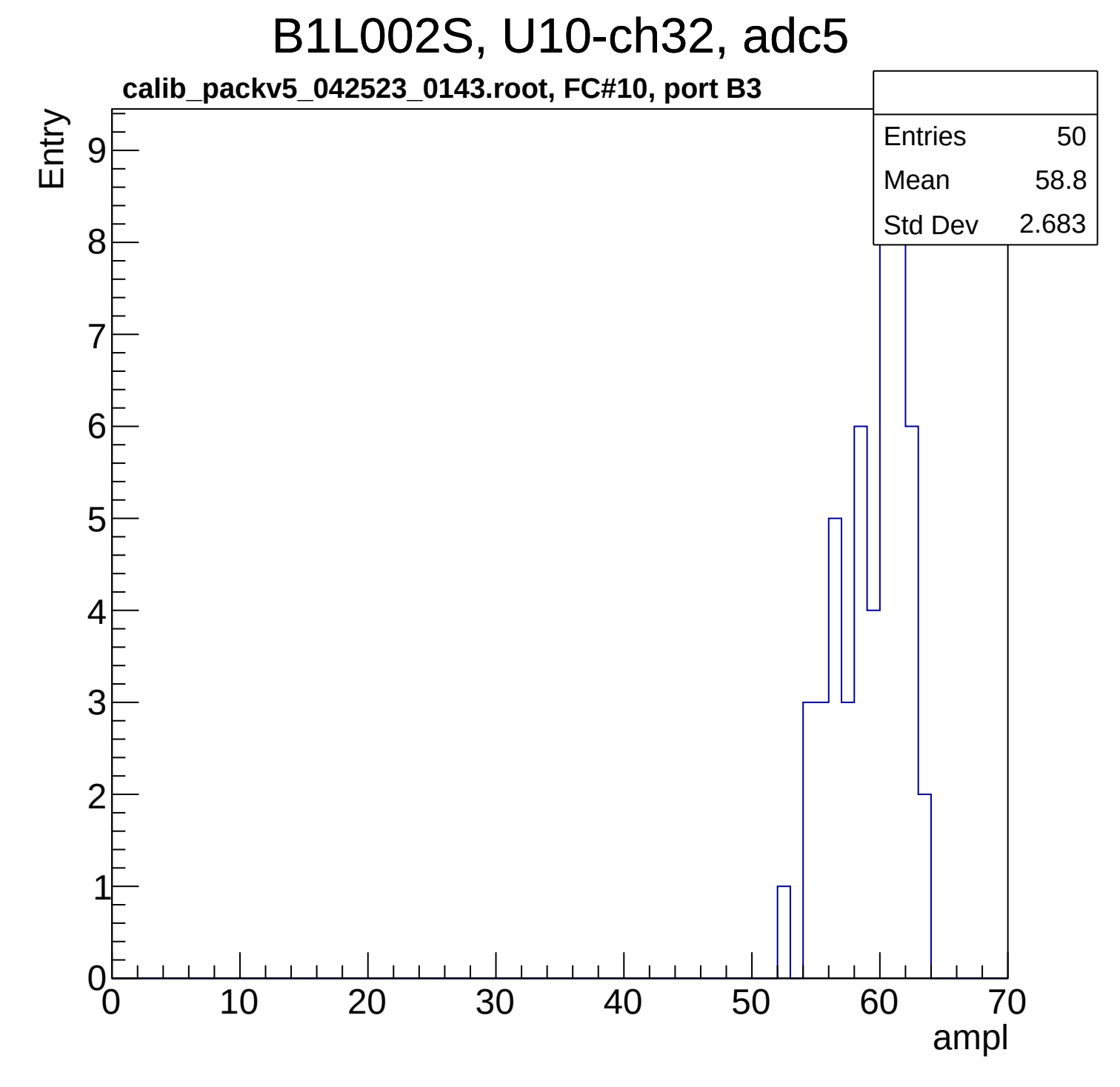
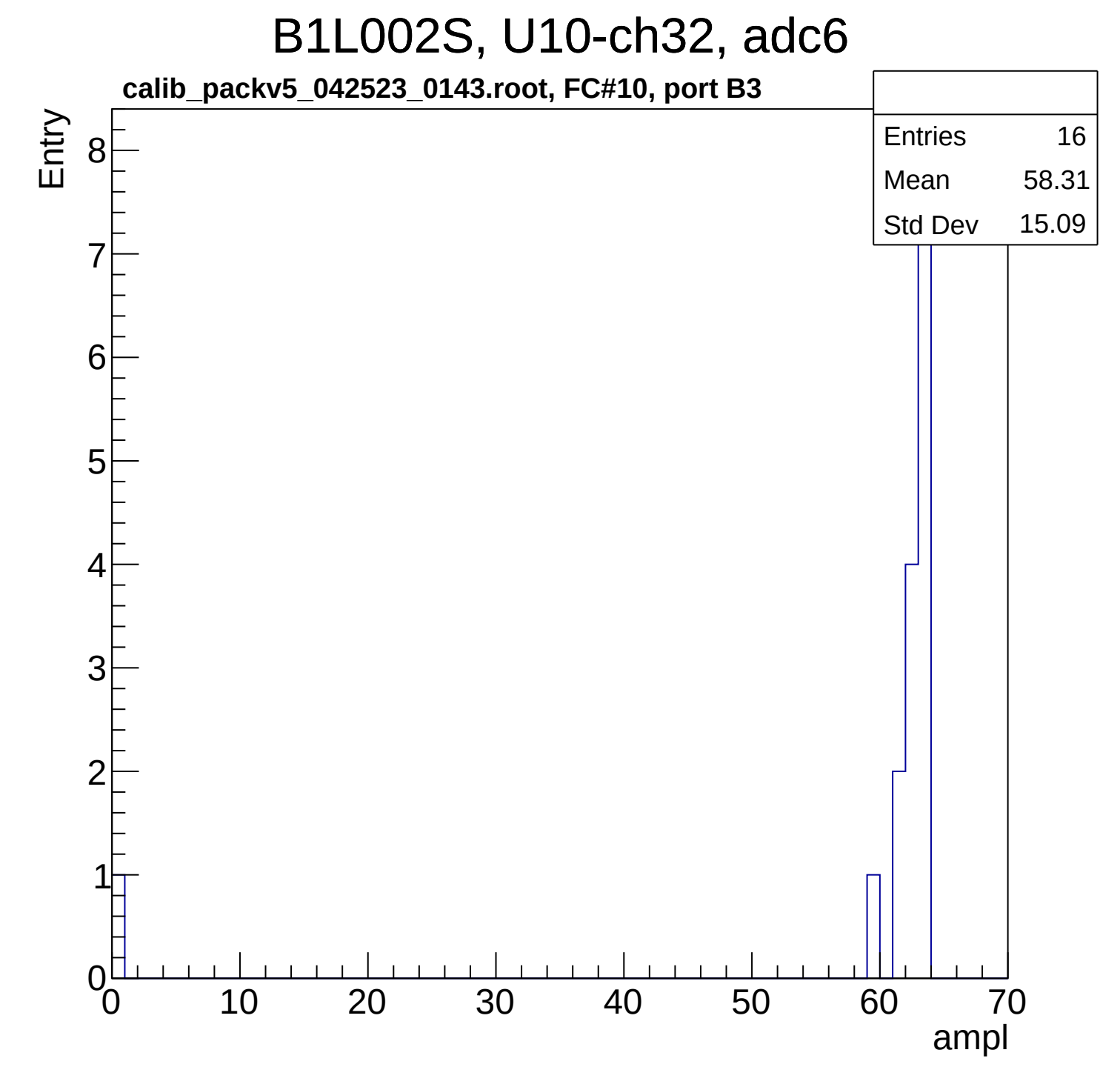

B1L002S, U10-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.8
Std Dev 2.683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 58.31
Std Dev 15.09
Entry
ampl
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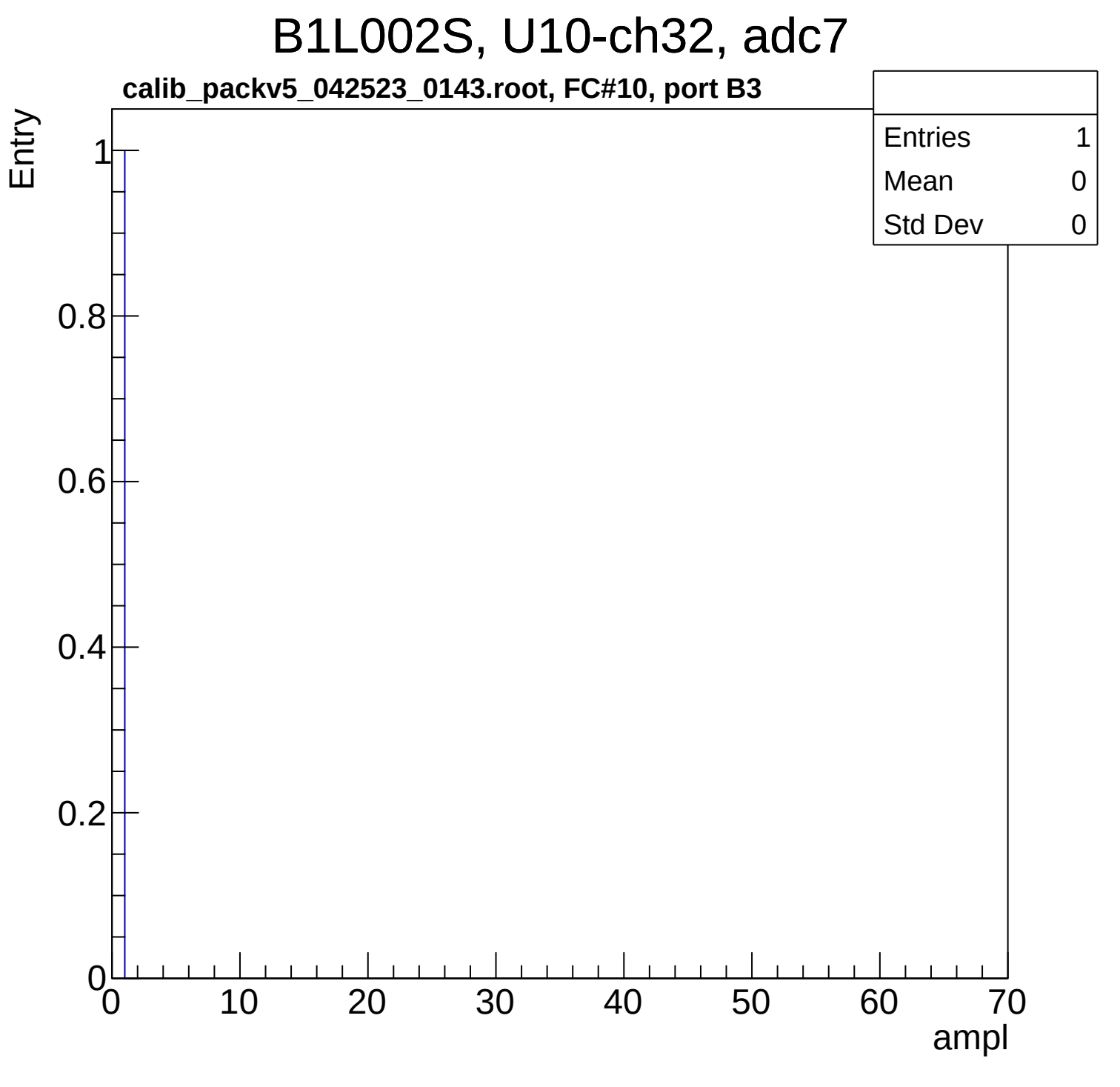
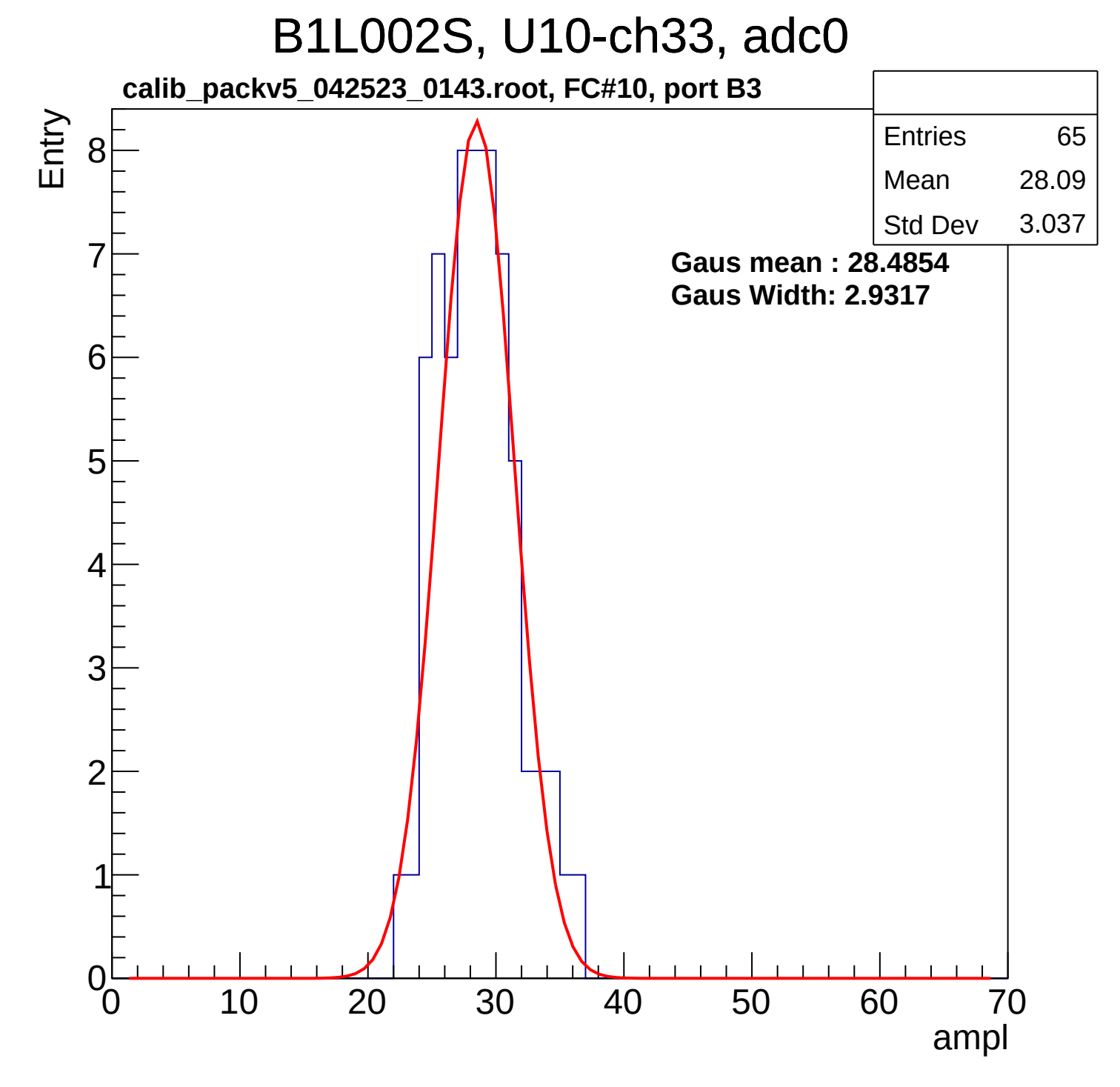
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.09
Std Dev 3.037
Gaus mean : 28.4854
Gaus Width: 2.9317
Entry
ampl
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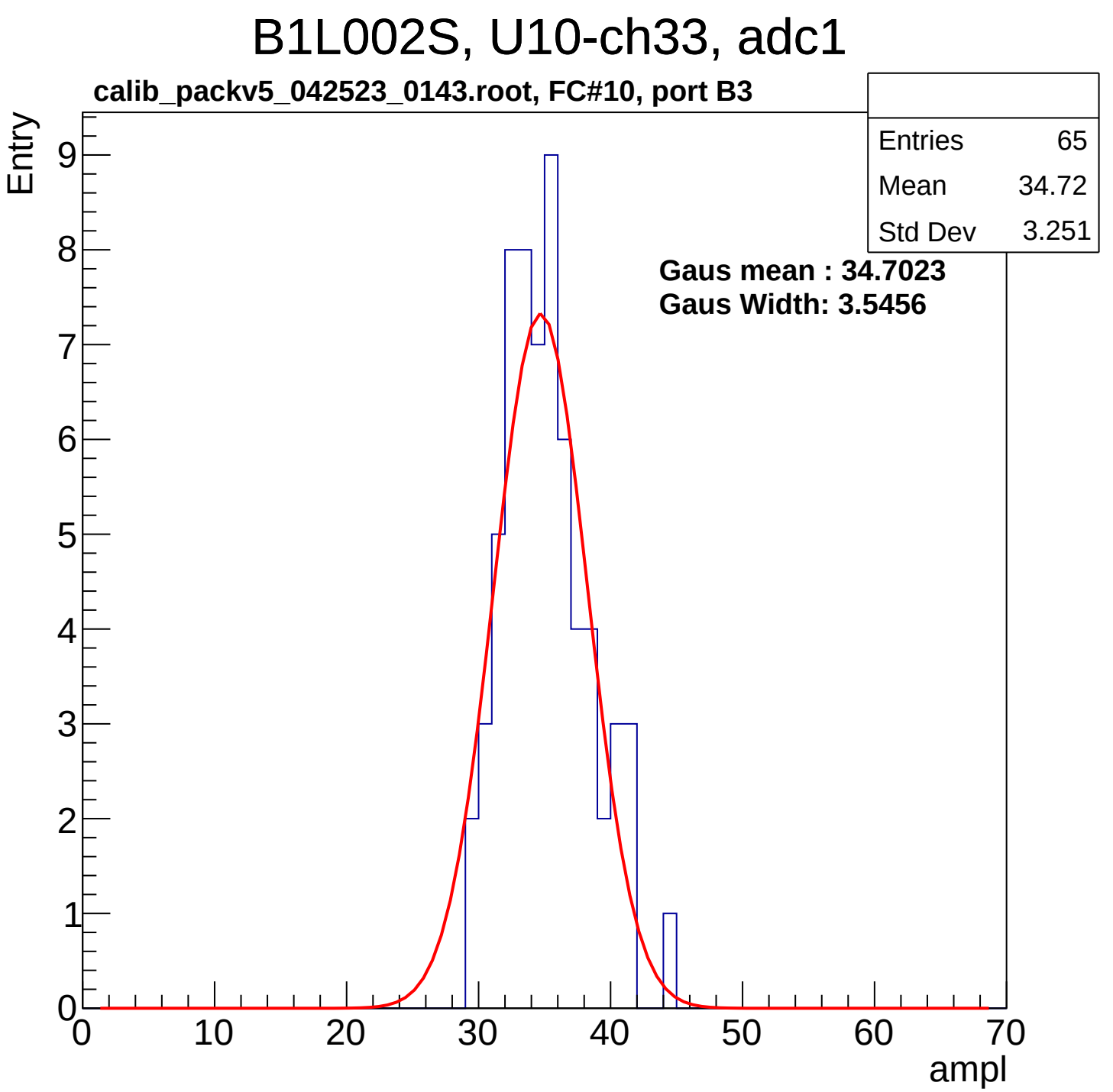
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.72
Std Dev 3.251
Gaus mean : 34.7023
Gaus Width: 3.5456
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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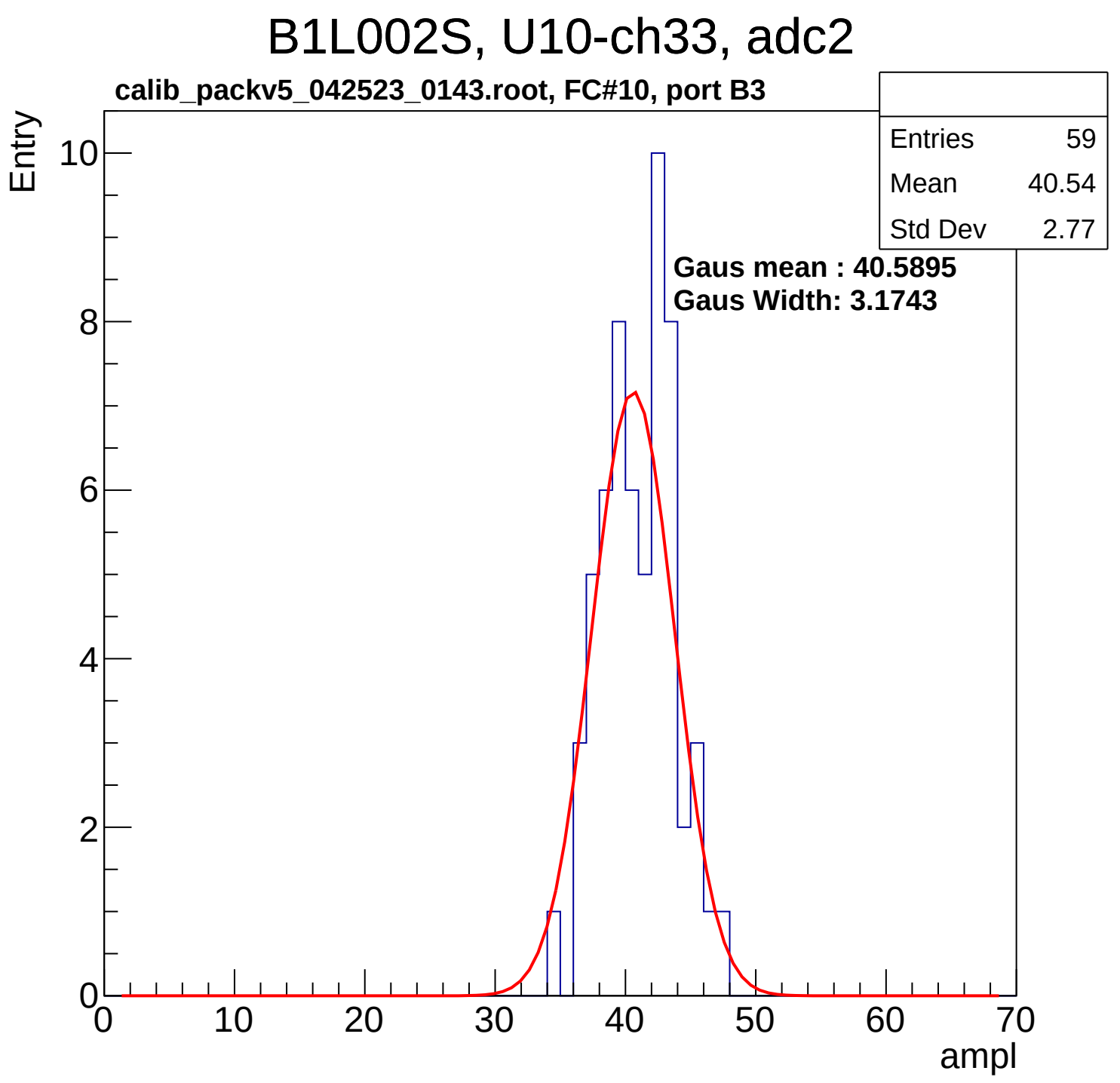
B1L002S, U10-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.54
Std Dev 2.77
Gaus mean : 40.5895
Gaus Width: 3.1743
Entry
ampl
0
2
4
6
8
10
0
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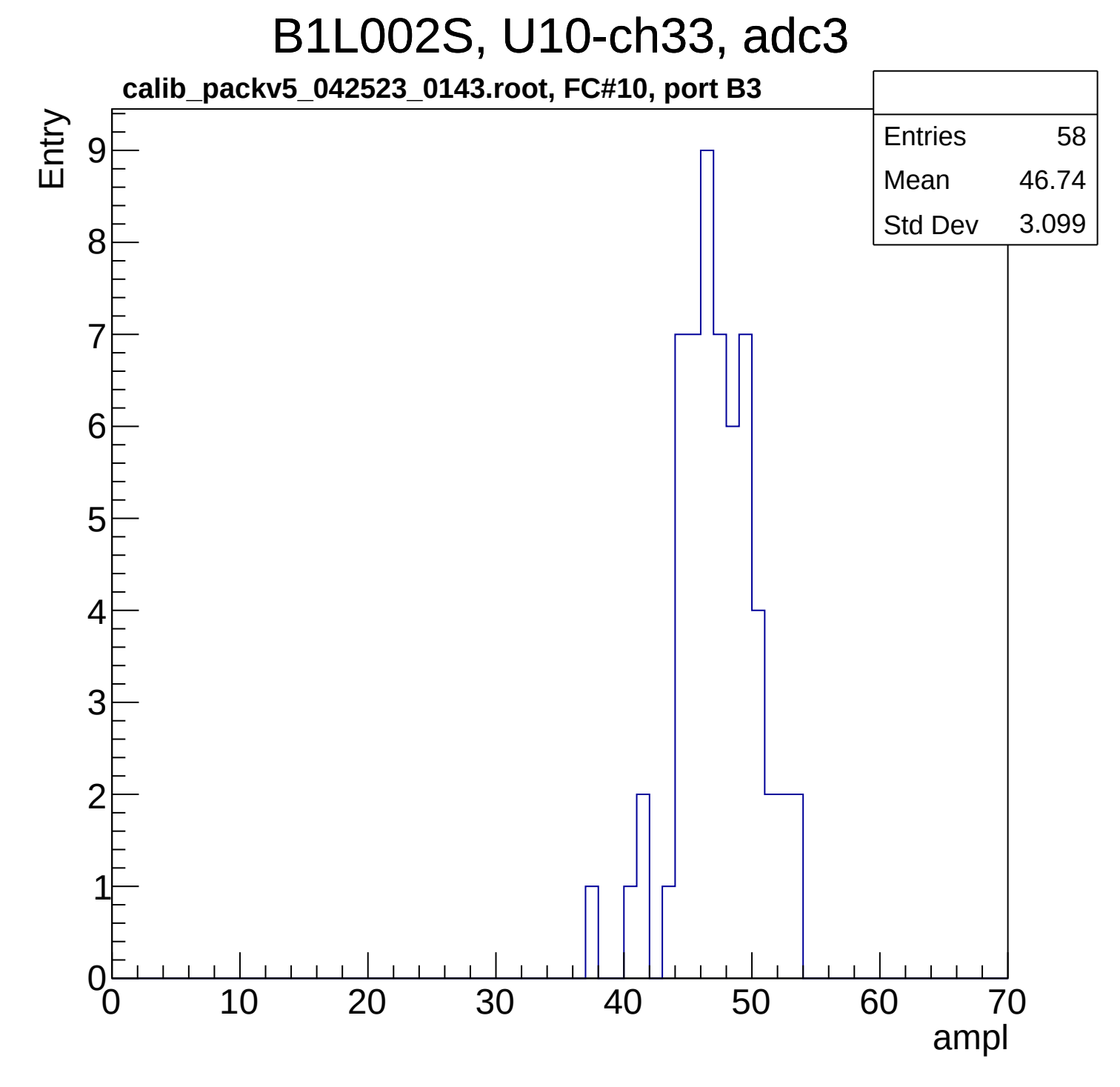
20
30
40
50
60
70


B1L002S, U10-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.74
Std Dev 3.099
Entry
ampl
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0
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60
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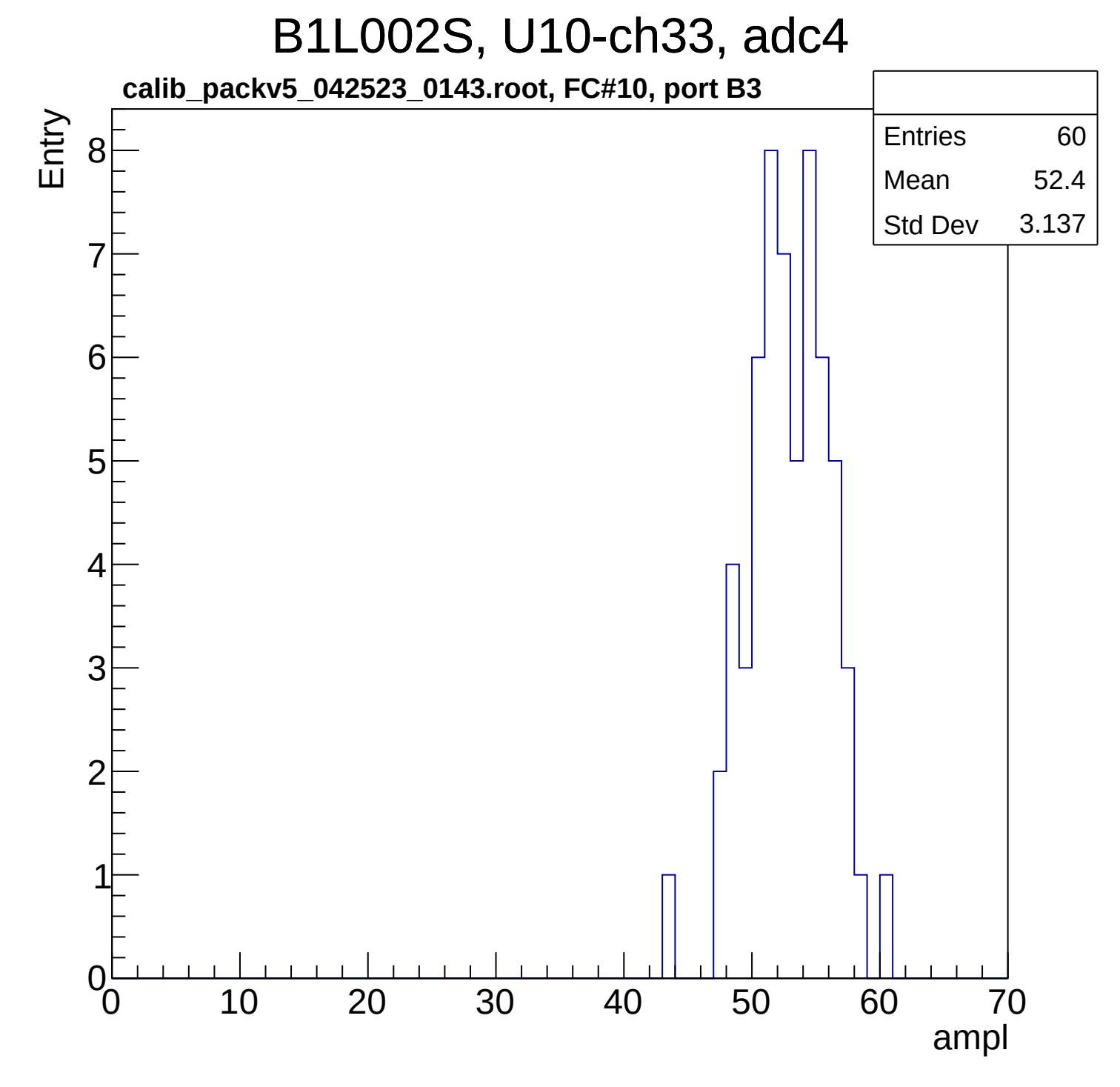
B1L002S, U10-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.4
Std Dev 3.137
Entry
ampl
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1
2
3
4
5
6
7
8
0
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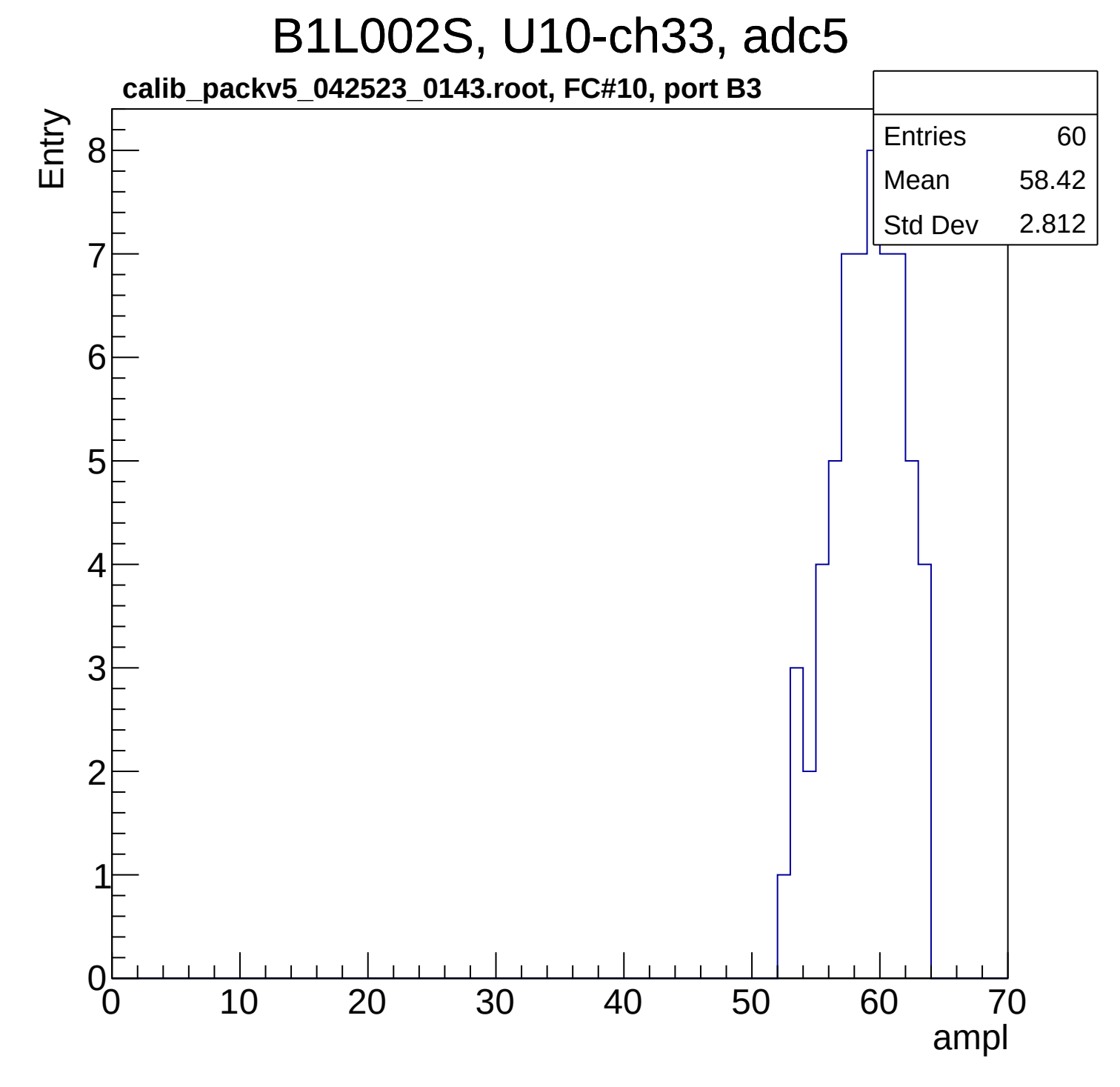
20
30
40
50
60
70


B1L002S, U10-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.42
Std Dev 2.812
Entry
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2
3
4
5
6
7
8
0
10
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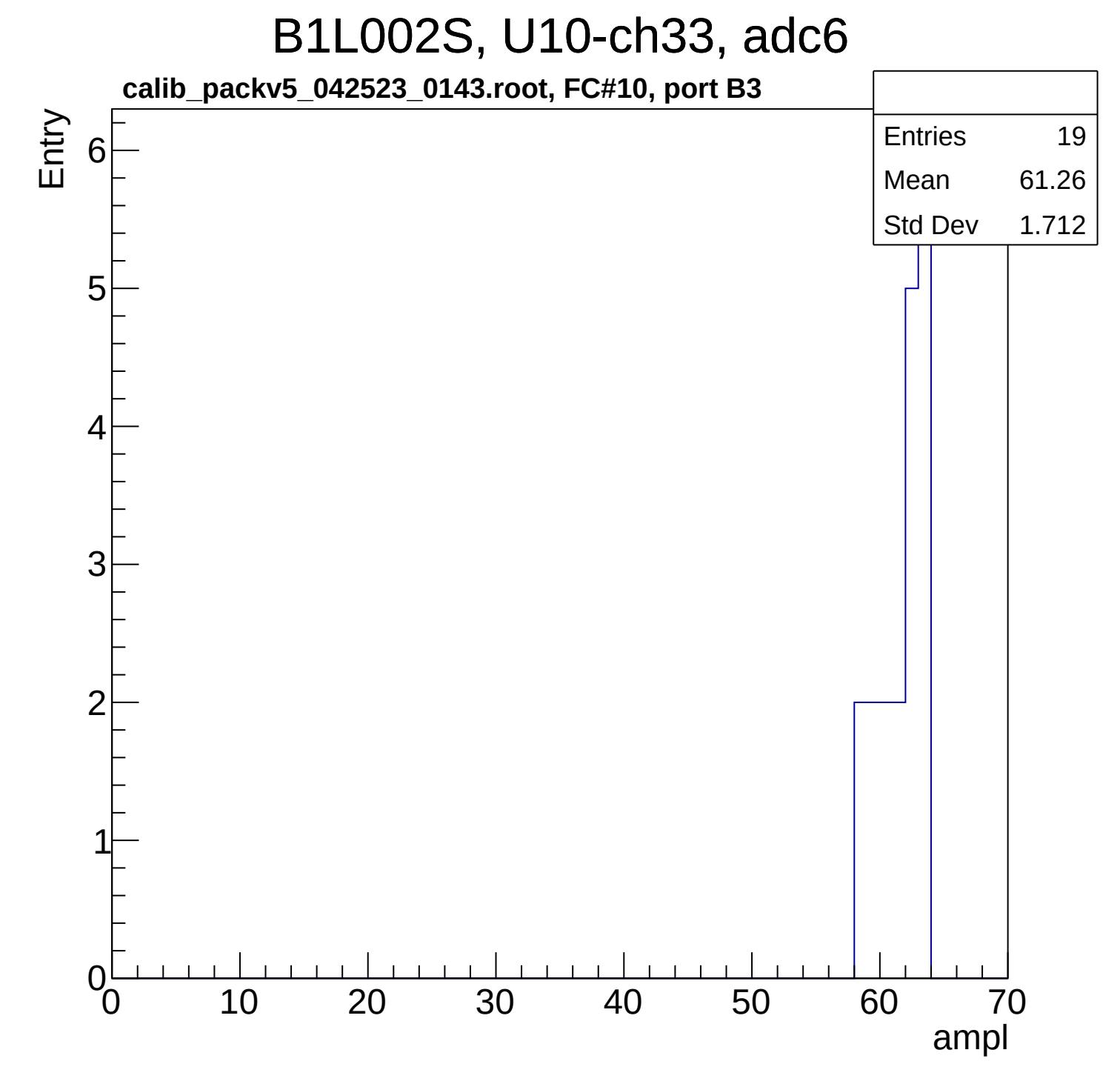
30
40
50
60
70
ampl


B1L002S, U10-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.26
Std Dev 1.712
Entry
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2
3
4
5
6
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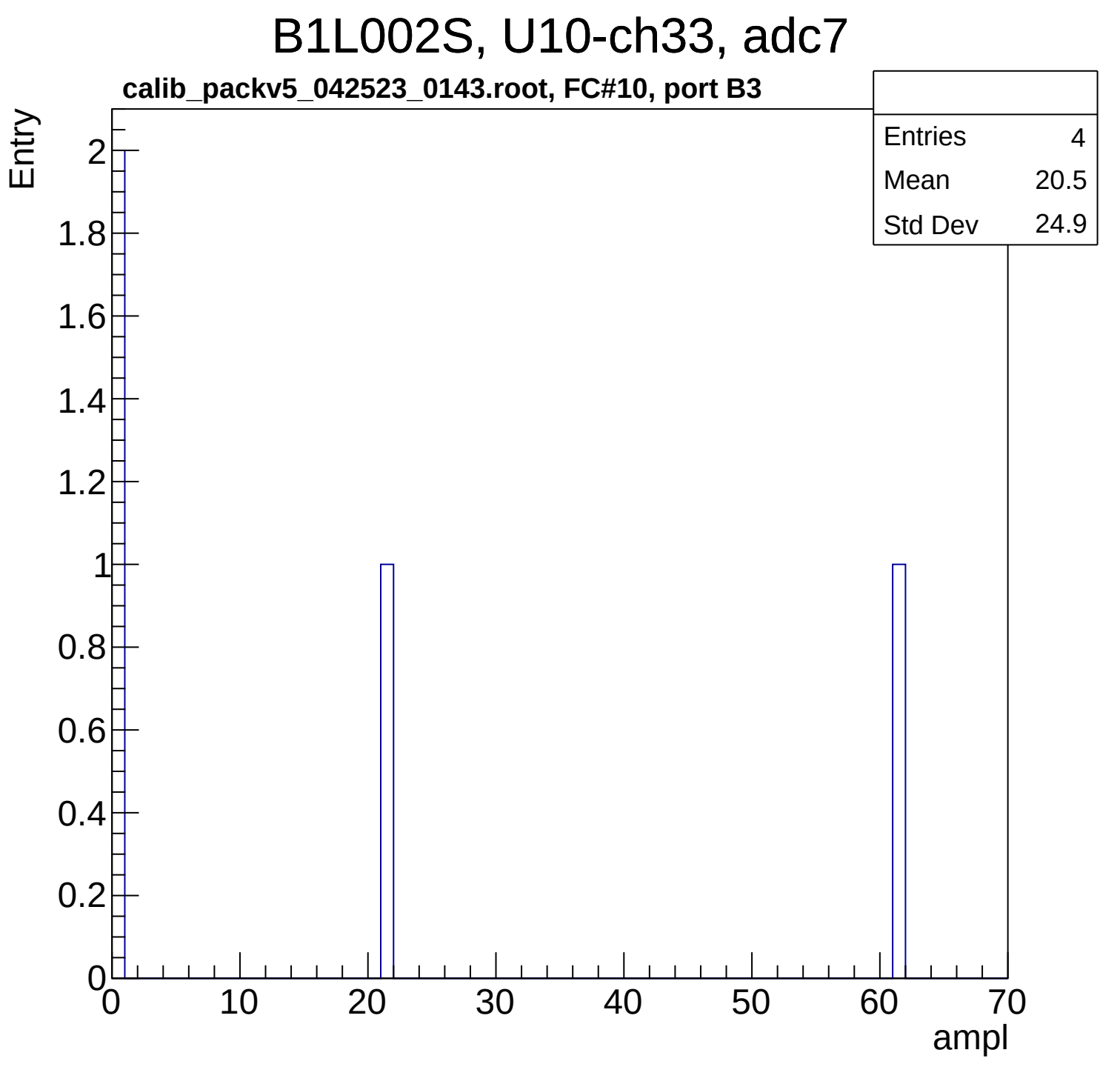
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.5
Std Dev 24.9
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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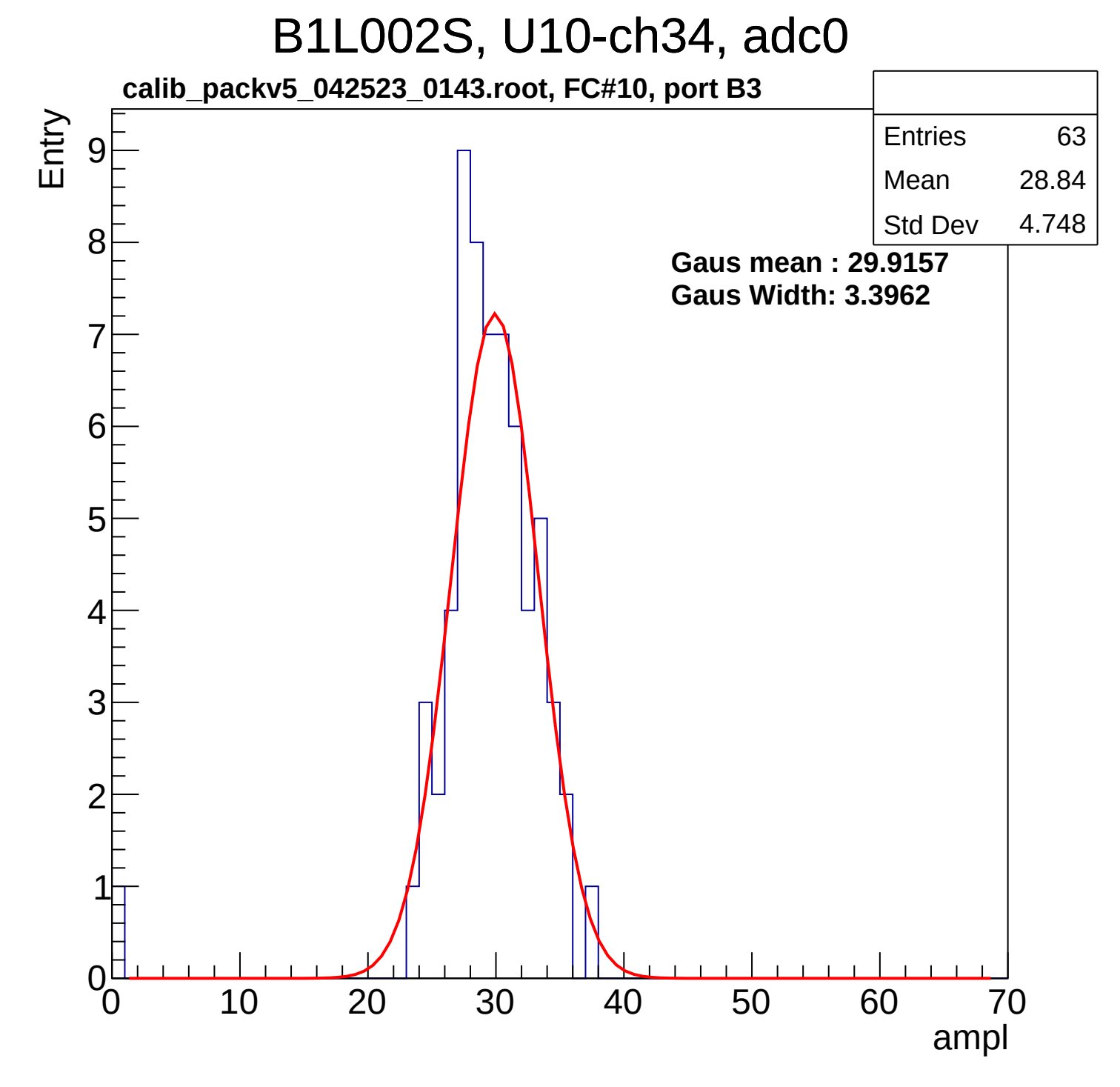
B1L002S, U10-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.84
Std Dev 4.748
Gaus mean : 29.9157
Gaus Width: 3.3962
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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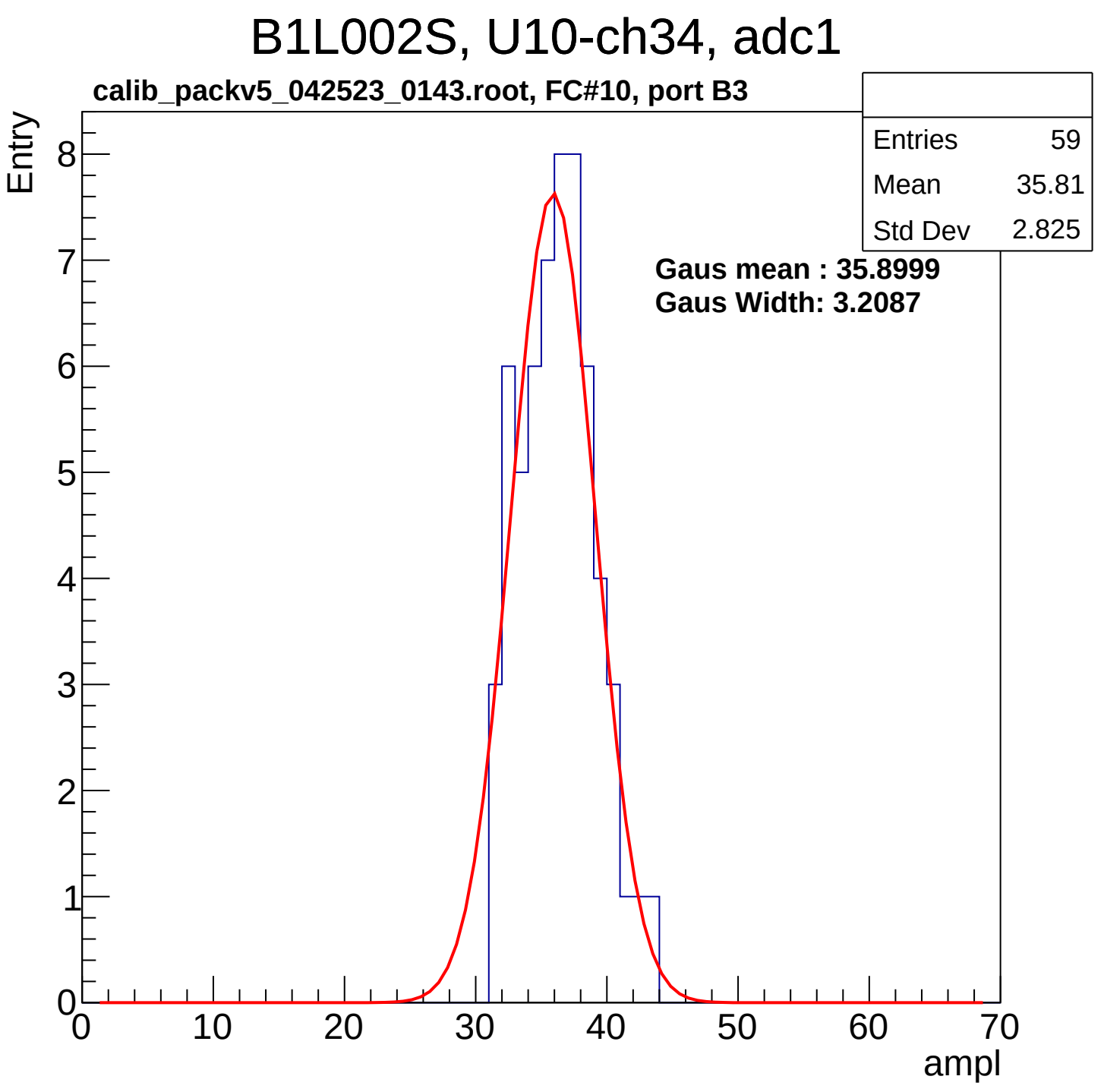
B1L002S, U10-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.81
Std Dev 2.825
Gaus mean : 35.8999
Gaus Width: 3.2087
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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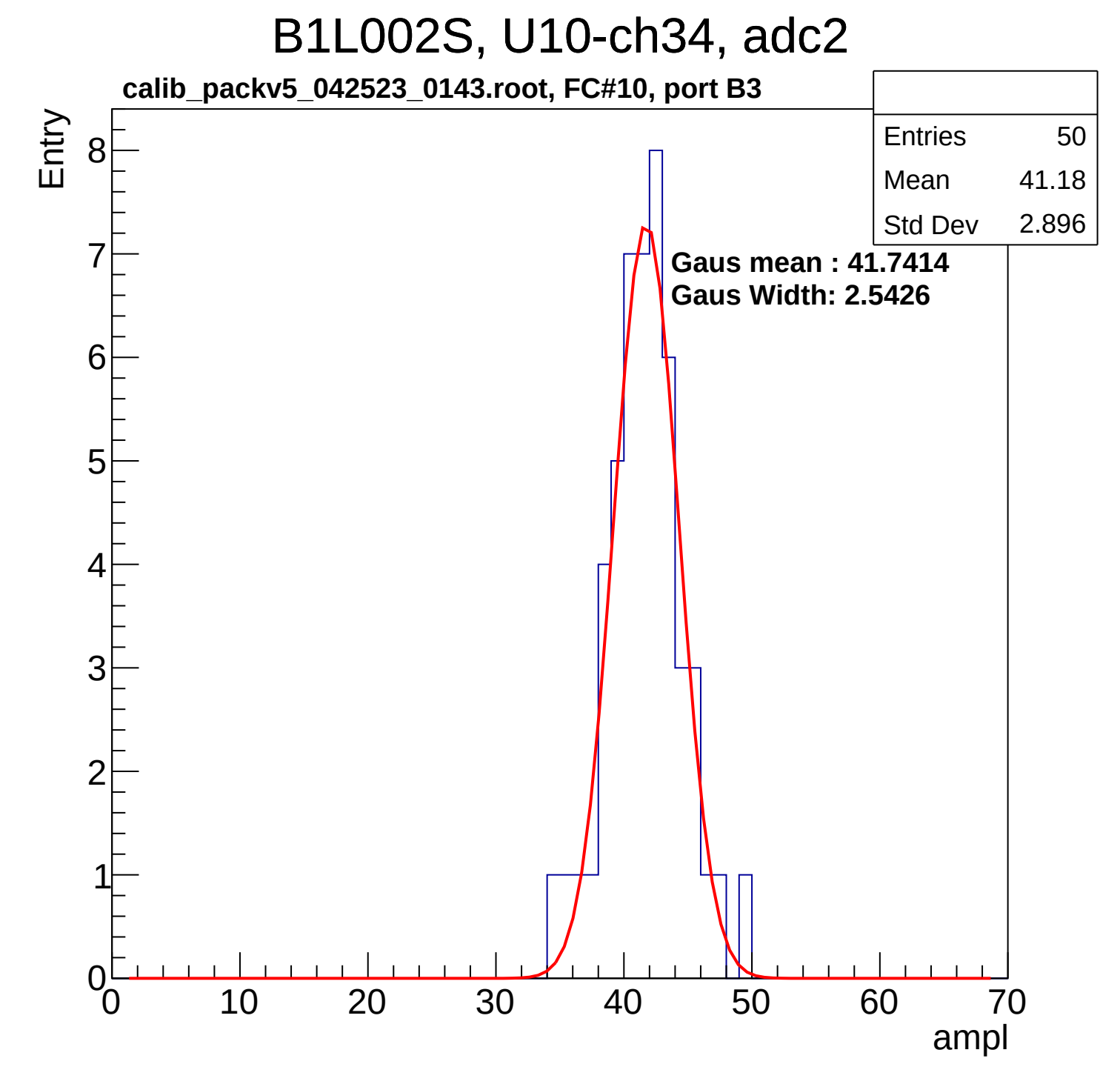
B1L002S, U10-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.18
Std Dev 2.896
Gaus mean : 41.7414
Gaus Width: 2.5426
Entry
ampl
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1
2
3
4
5
6
7
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0
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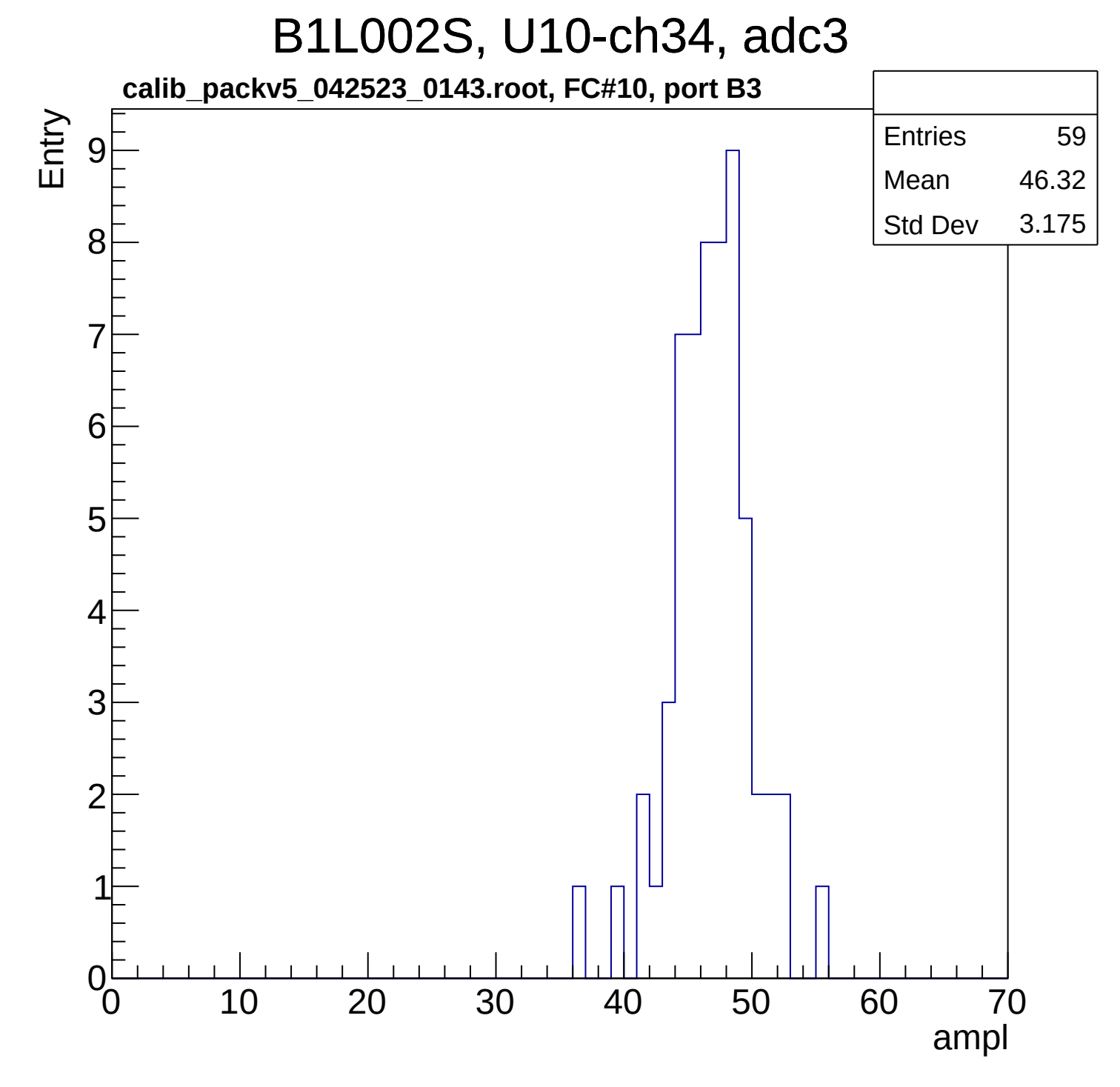
B1L002S, U10-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.32
Std Dev 3.175
Entry
ampl
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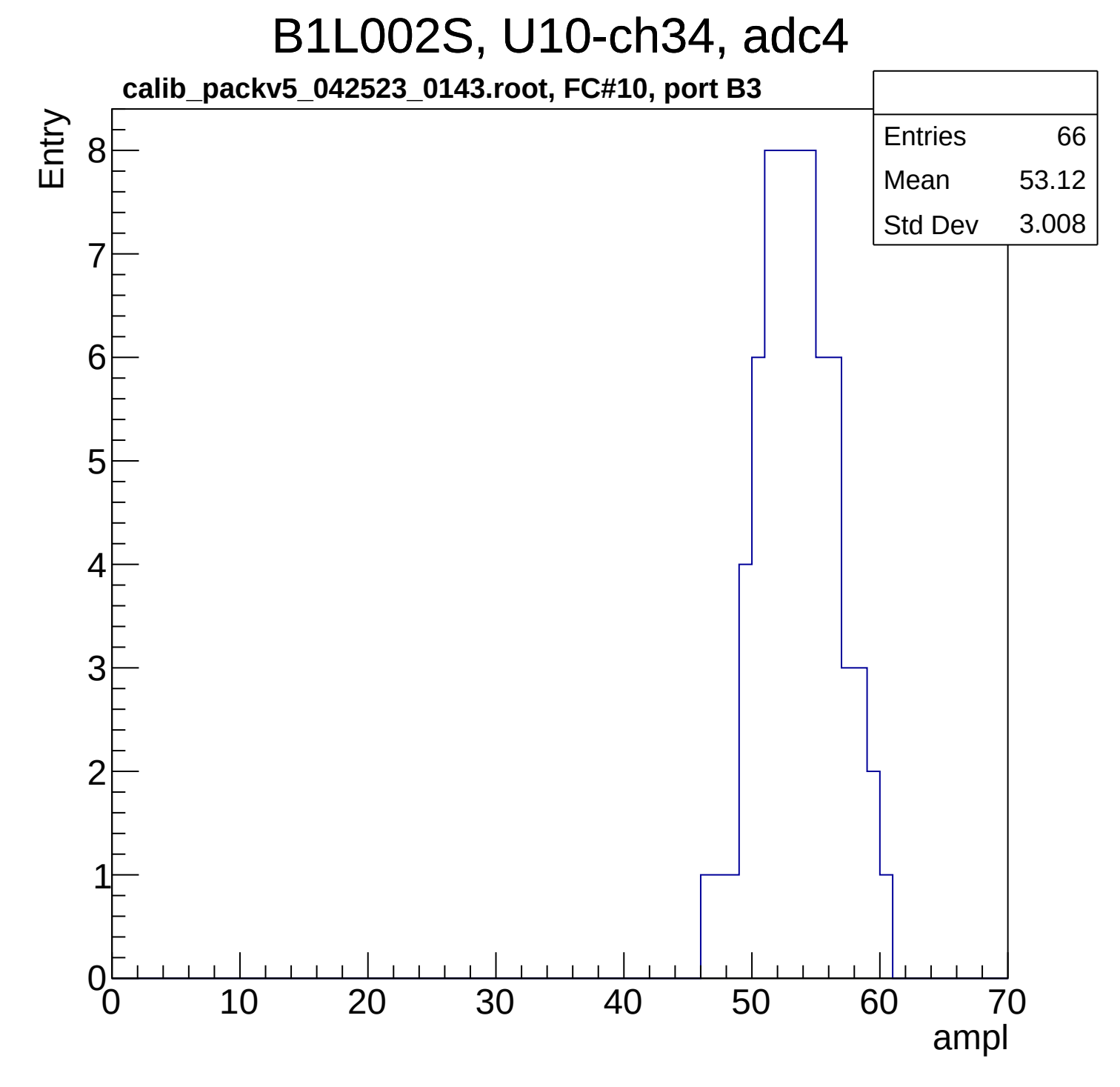
B1L002S, U10-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.12
Std Dev 3.008
Entry
ampl
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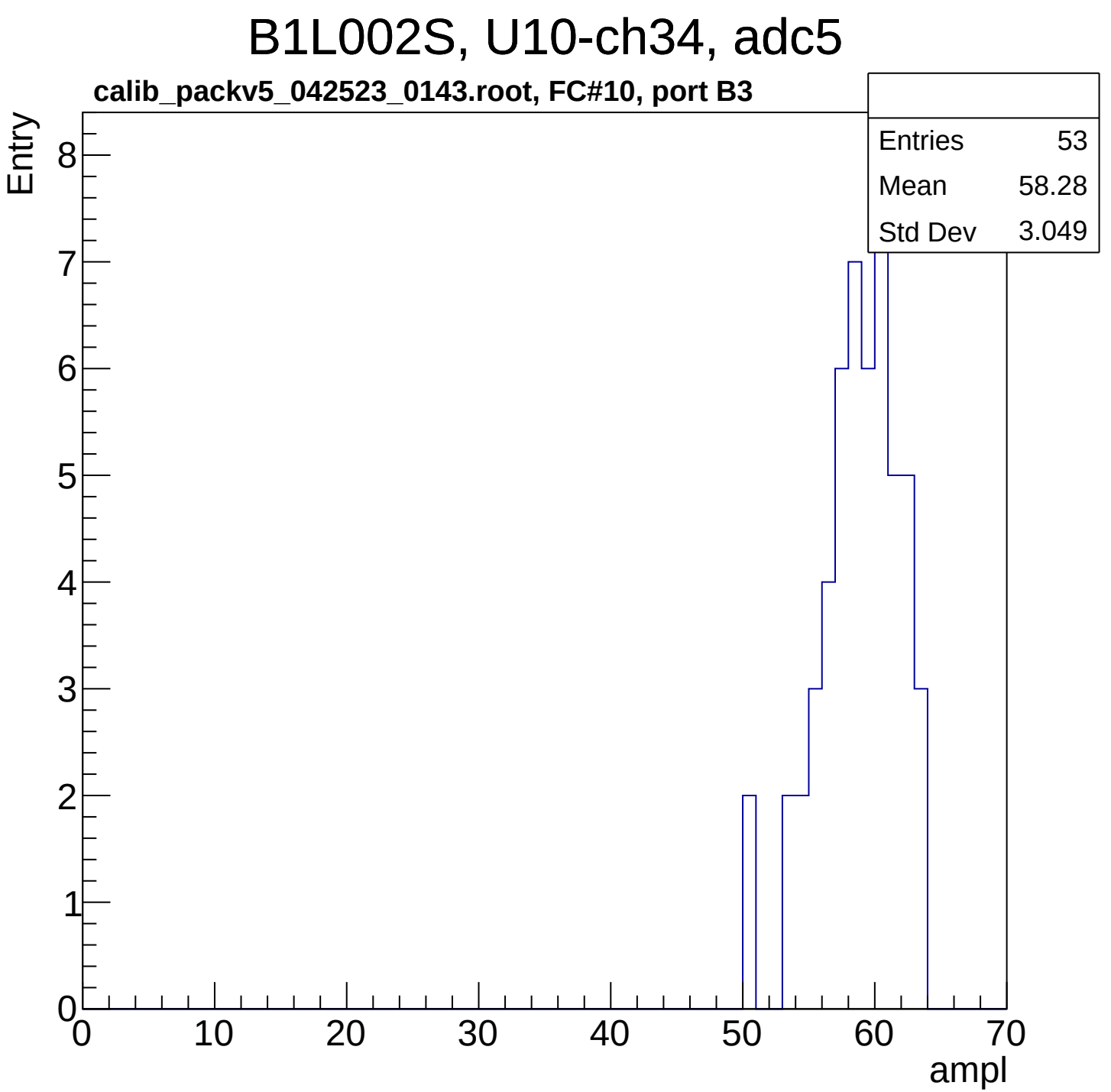
B1L002S, U10-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.28
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
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60
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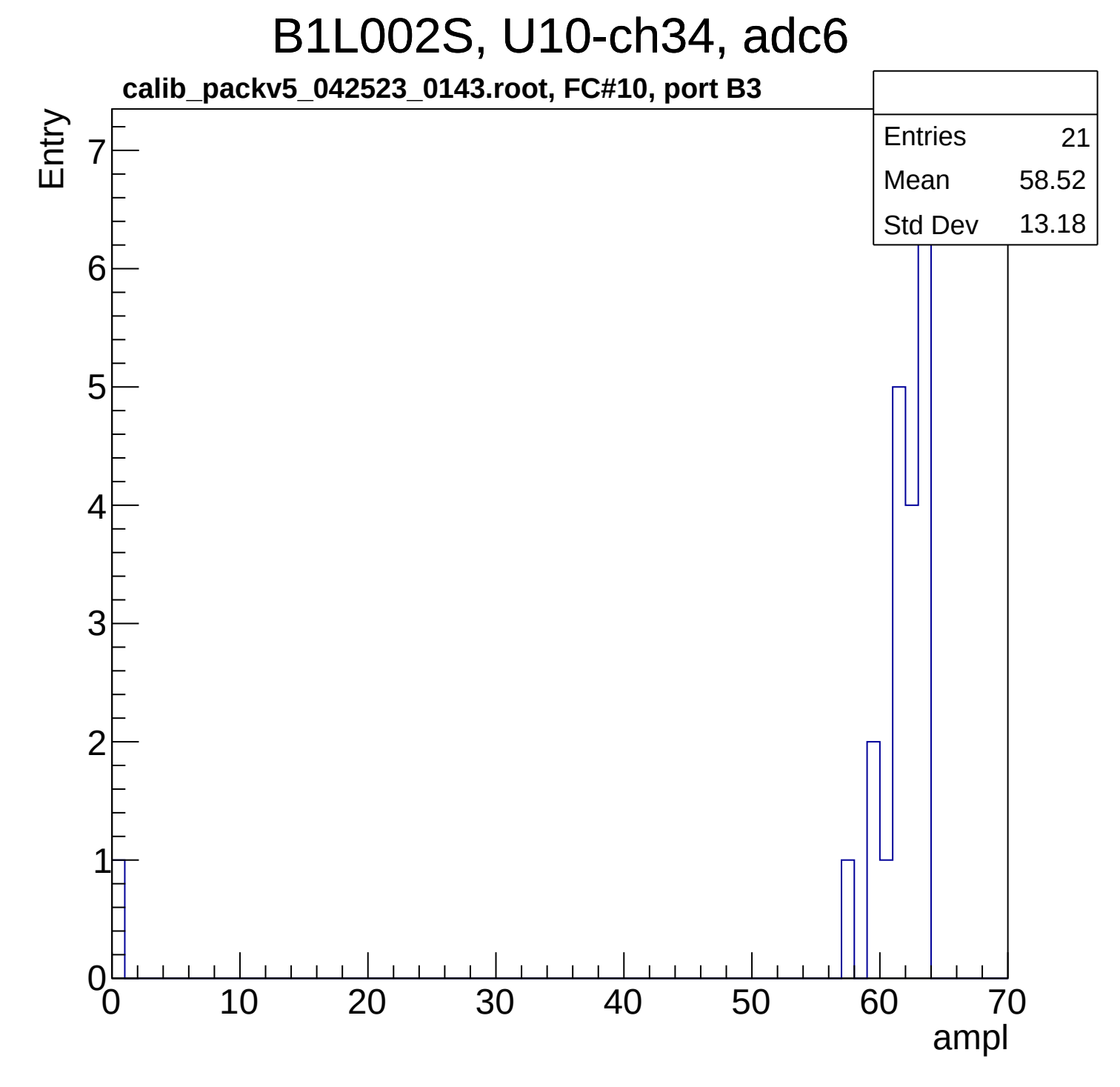
B1L002S, U10-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.52
Std Dev 13.18
Entry
0
1
2
3
4
5
6
7
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10
20
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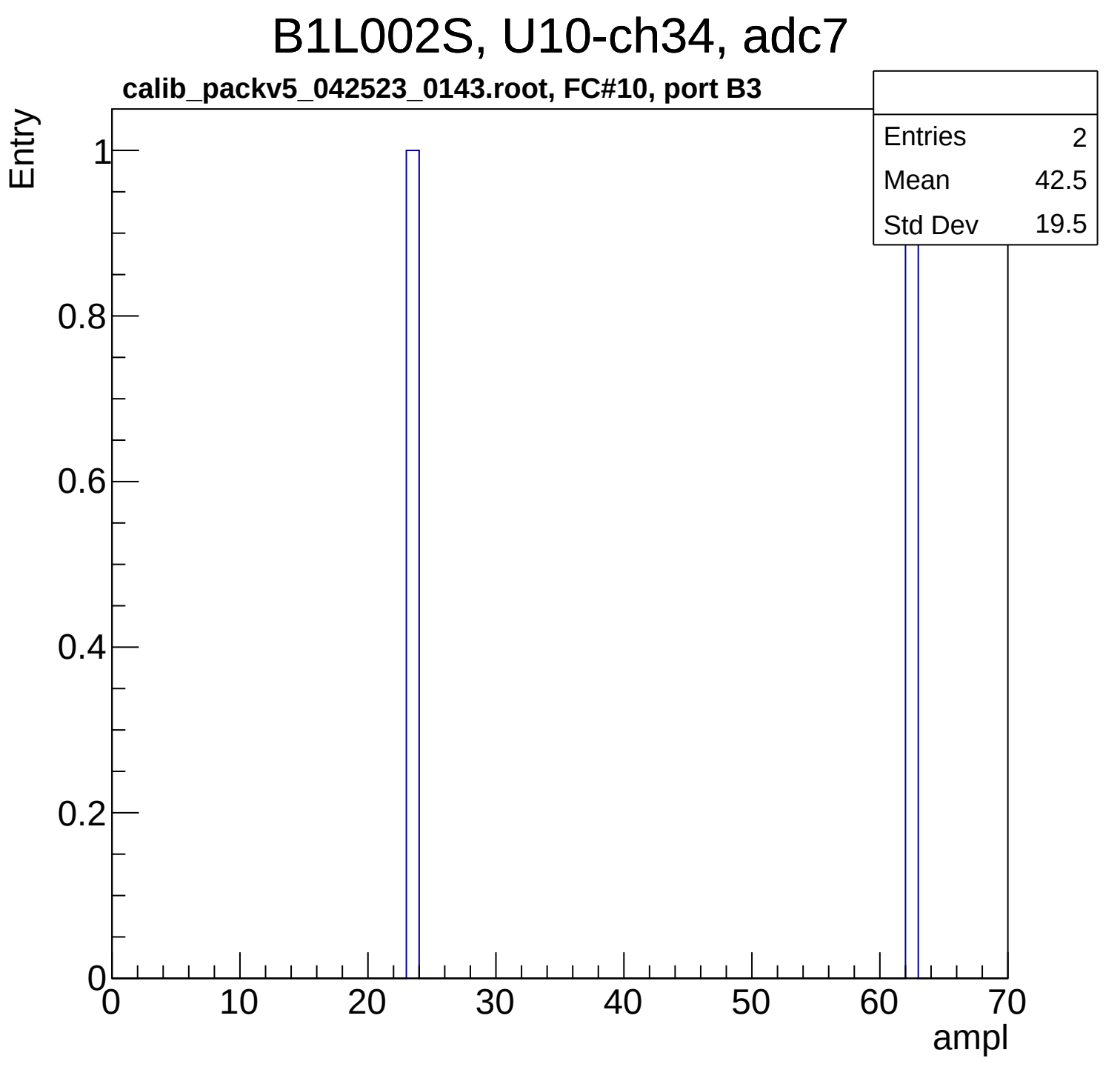
60
70
ampl


B1L002S, U10-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 19.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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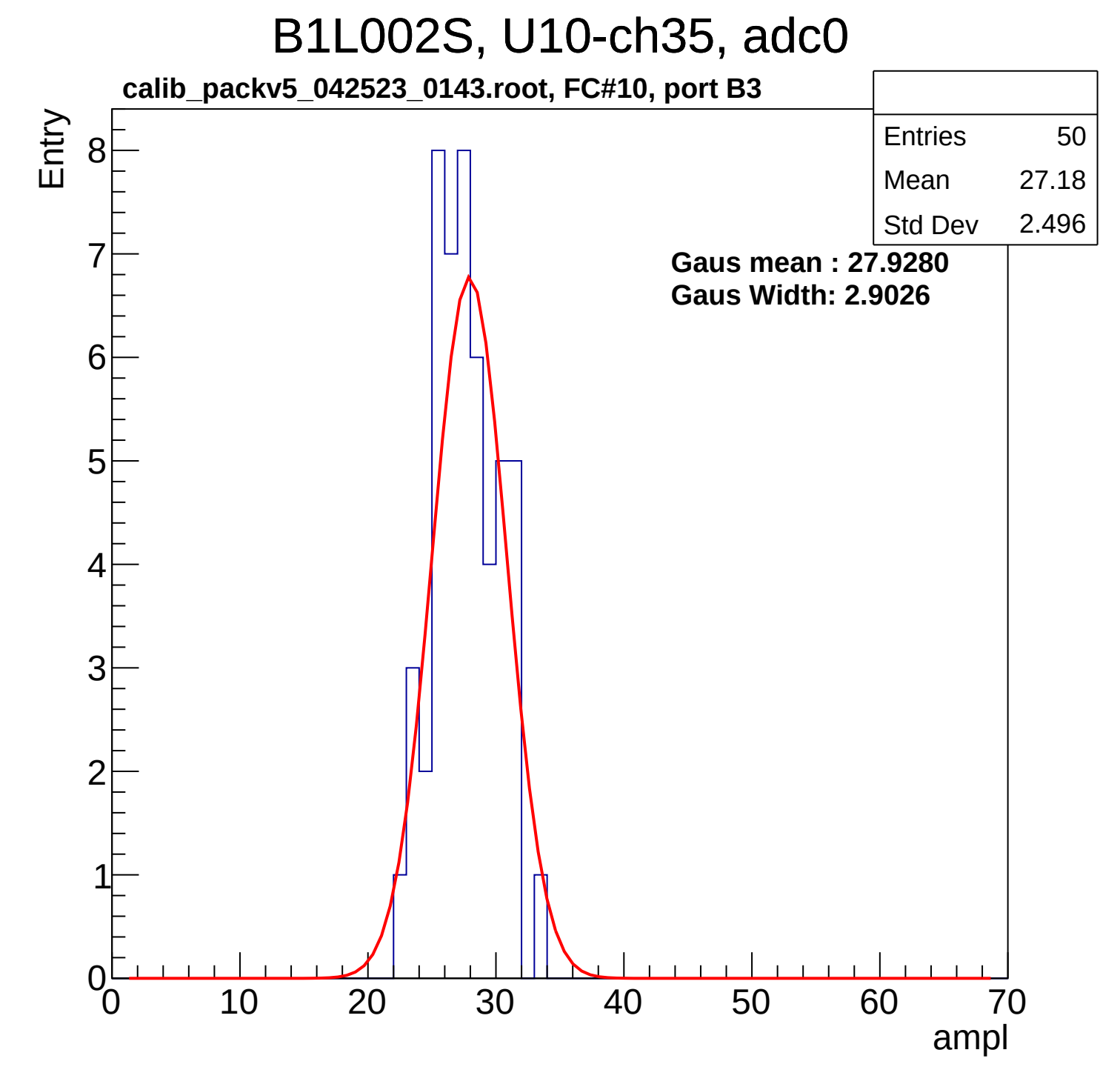
50
60
70
ampl


B1L002S, U10-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 27.18
Std Dev 2.496
Gaus mean : 27.9280
Gaus Width: 2.9026
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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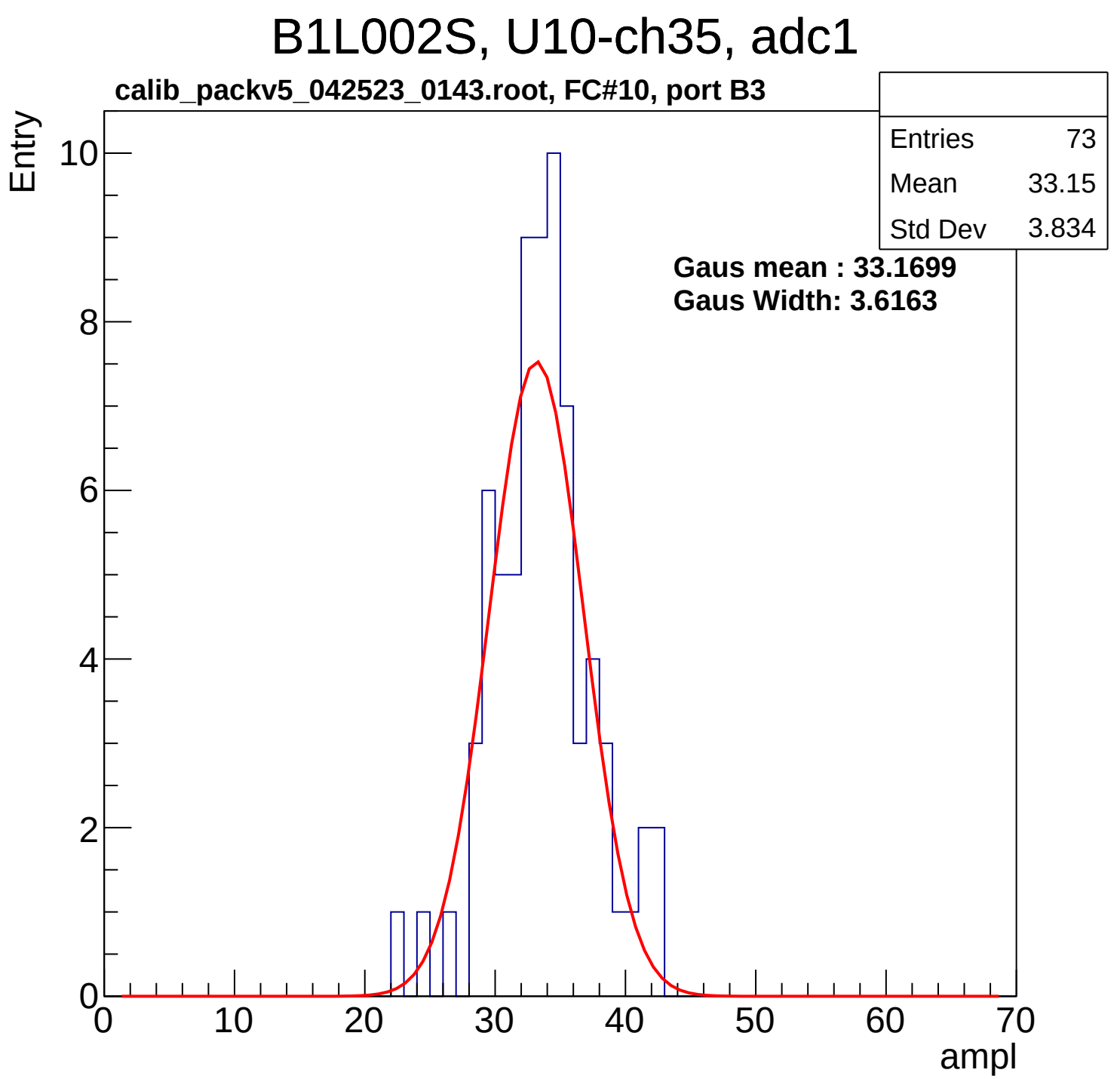
B1L002S, U10-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 33.15
Std Dev 3.834
Gaus mean : 33.1699
Gaus Width: 3.6163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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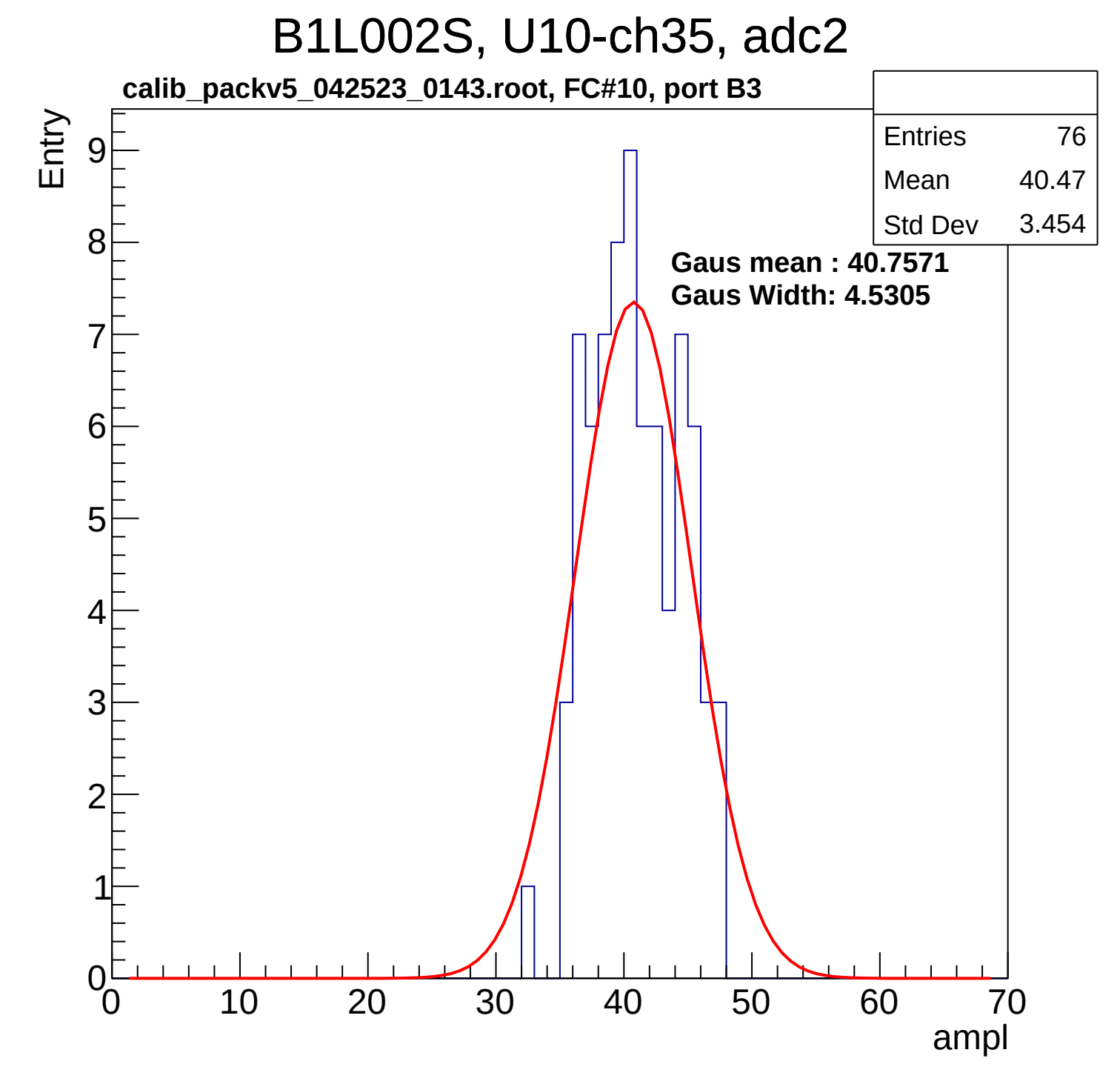
B1L002S, U10-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 40.47
Std Dev 3.454
Gaus mean : 40.7571
Gaus Width: 4.5305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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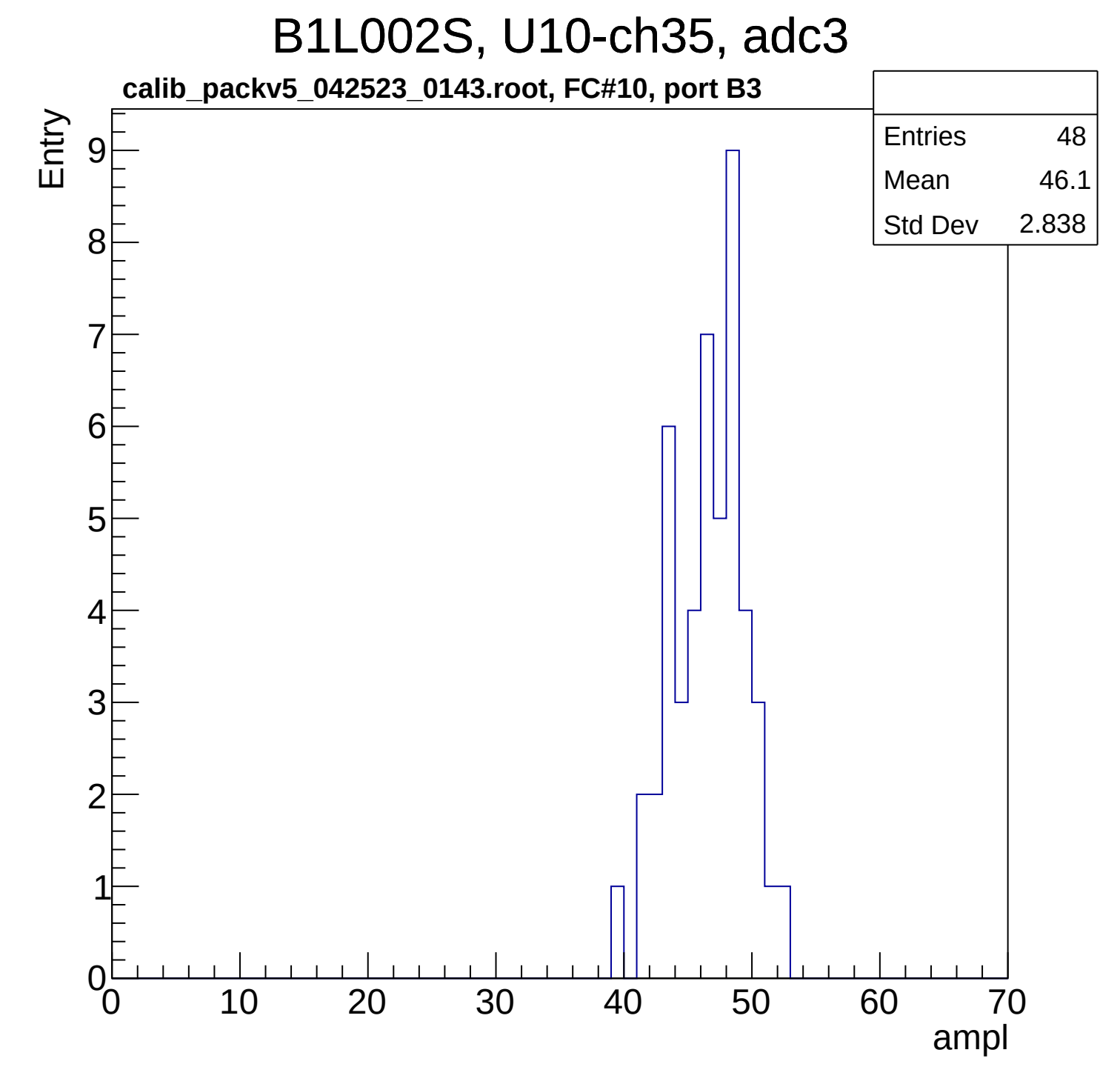
B1L002S, U10-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 46.1
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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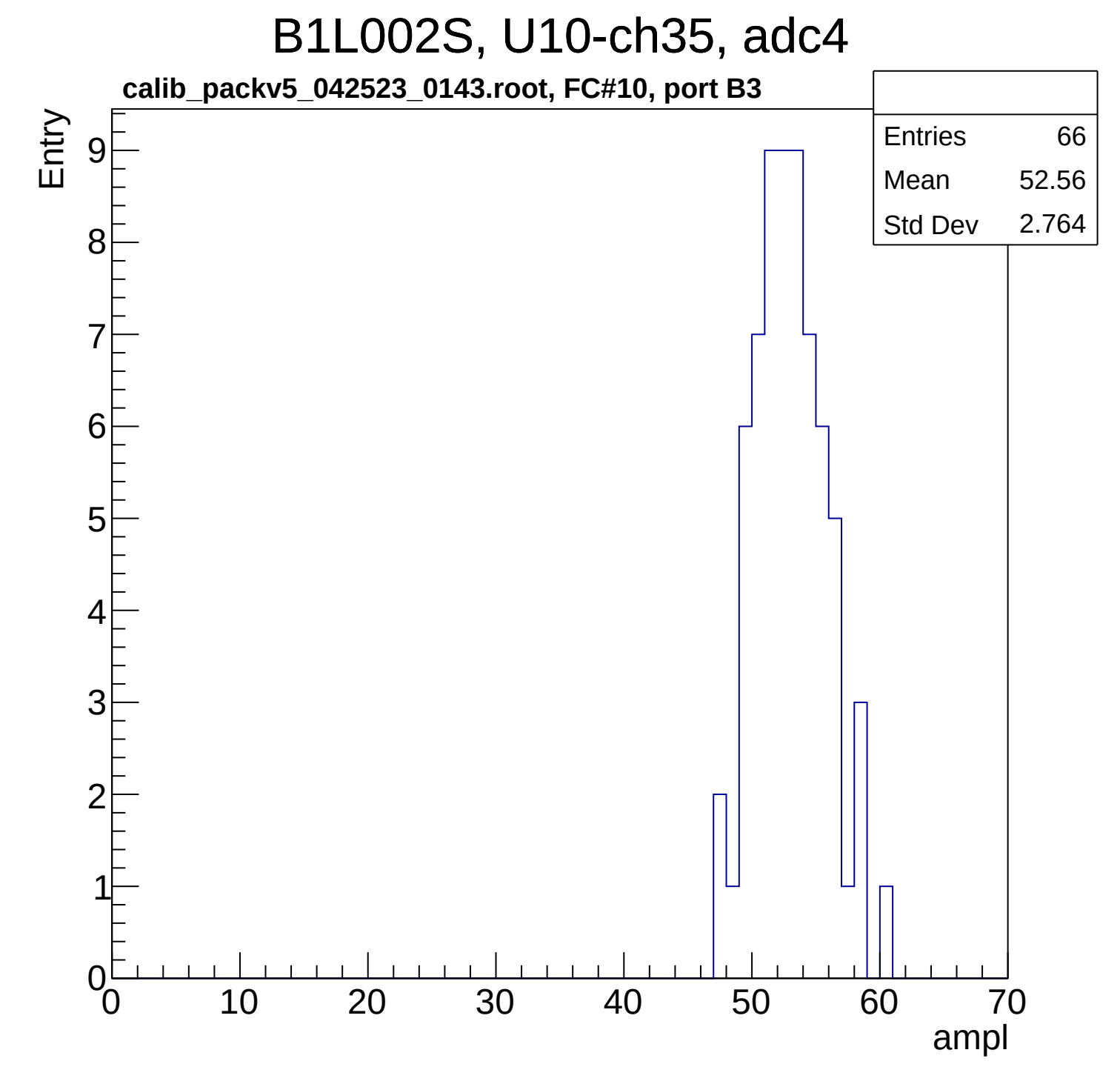
B1L002S, U10-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.56
Std Dev 2.764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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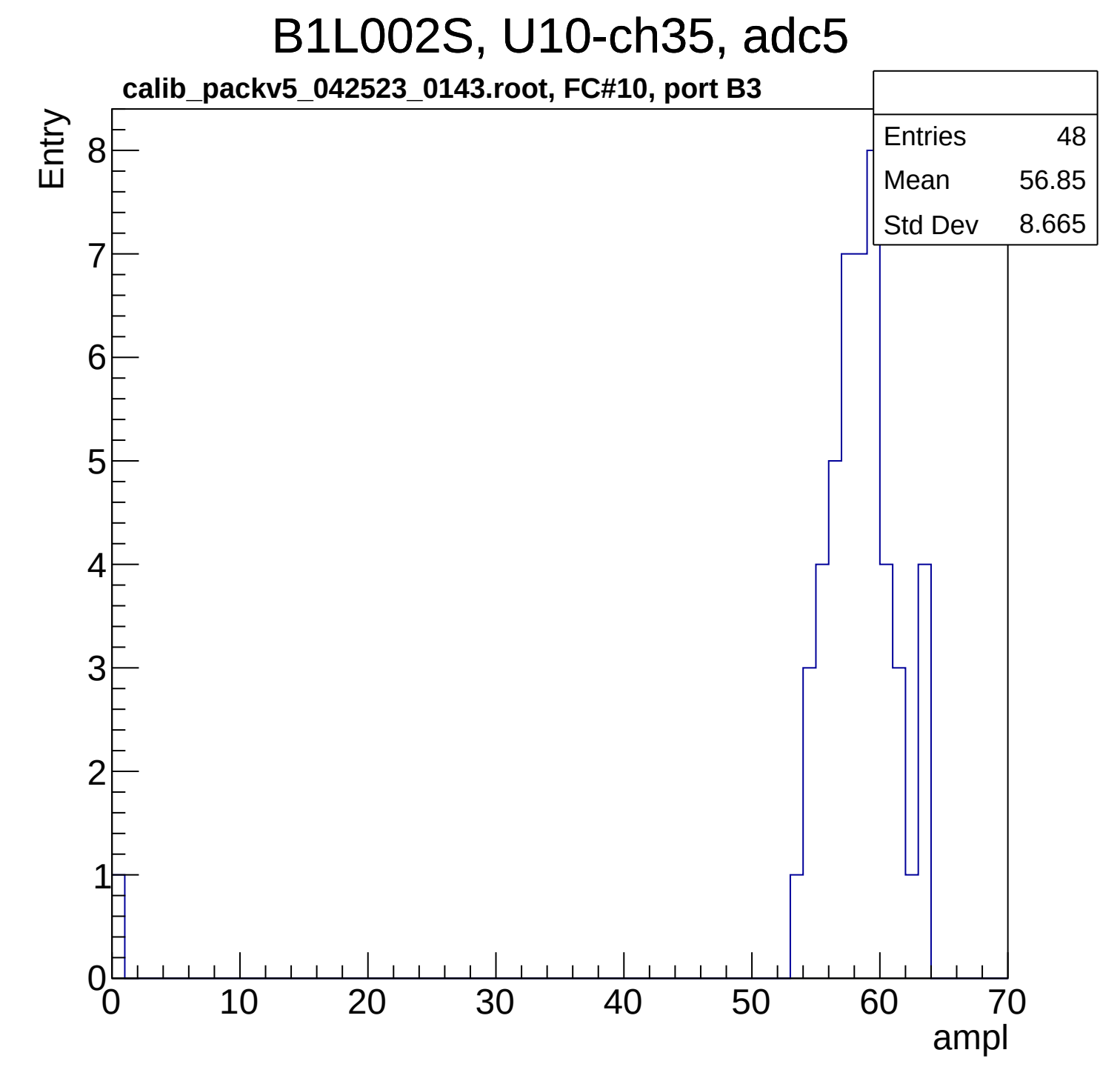
B1L002S, U10-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 56.85
Std Dev 8.665
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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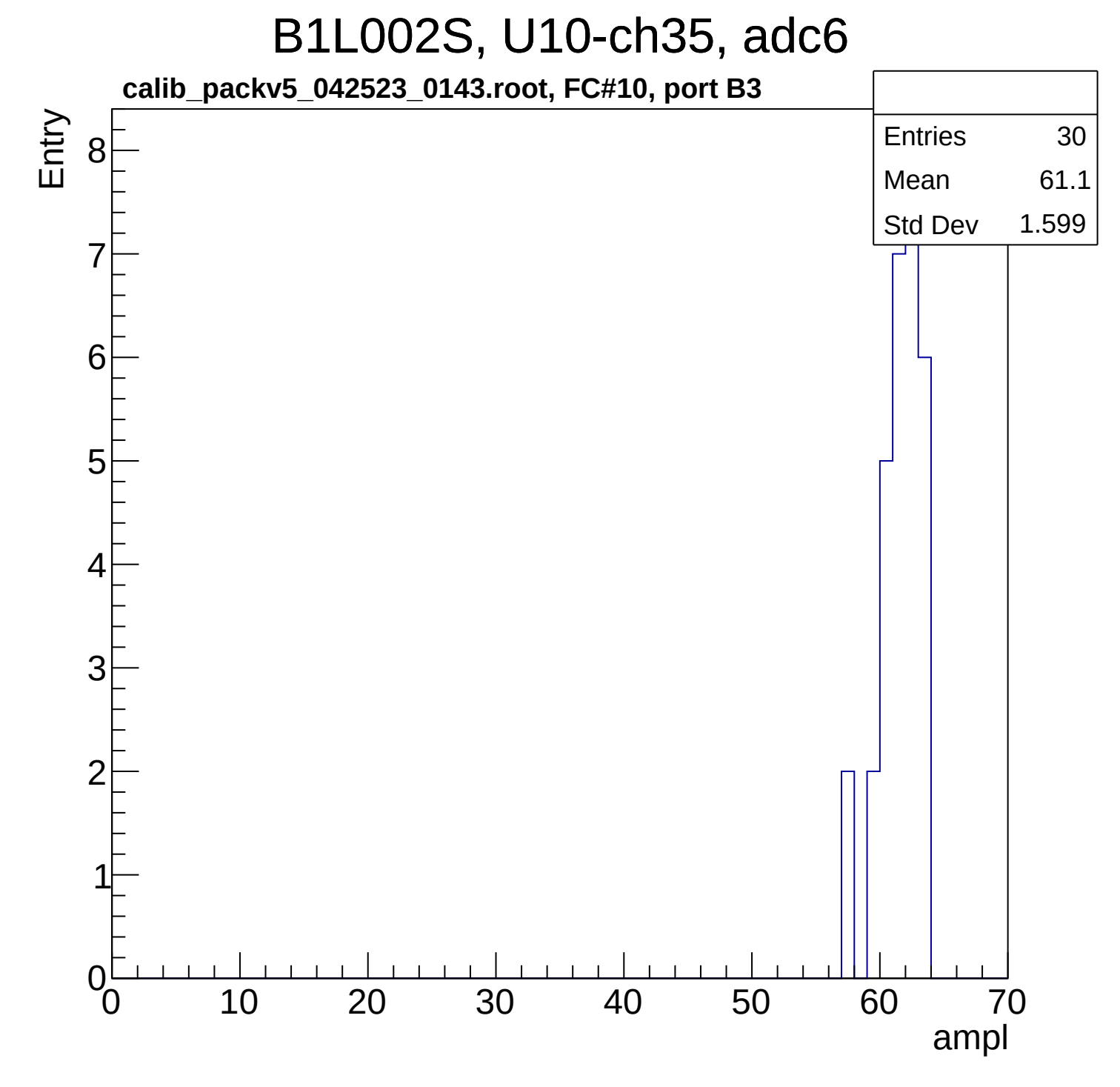
B1L002S, U10-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 61.1
Std Dev 1.599
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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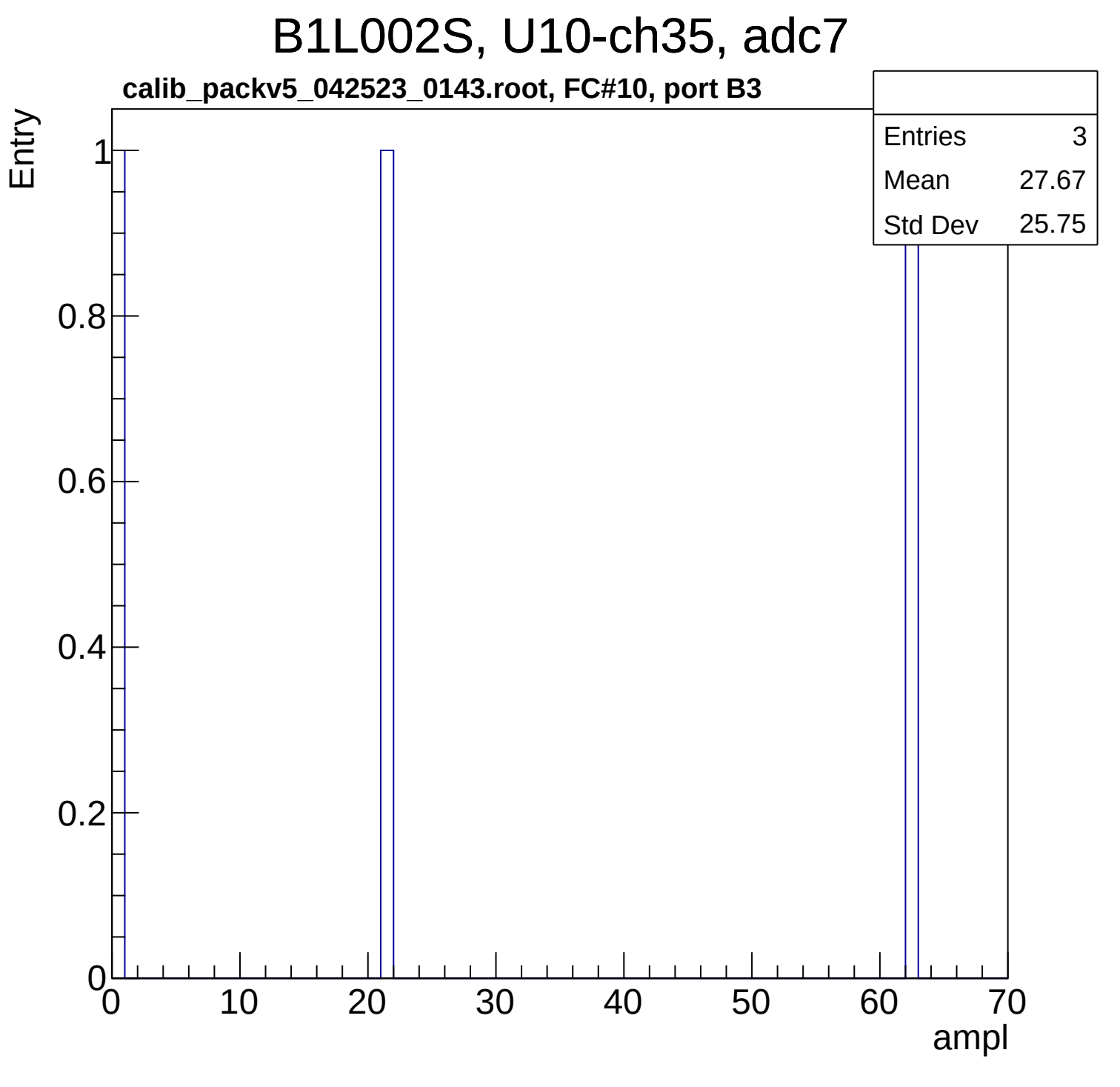
B1L002S, U10-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.67
Std Dev 25.75
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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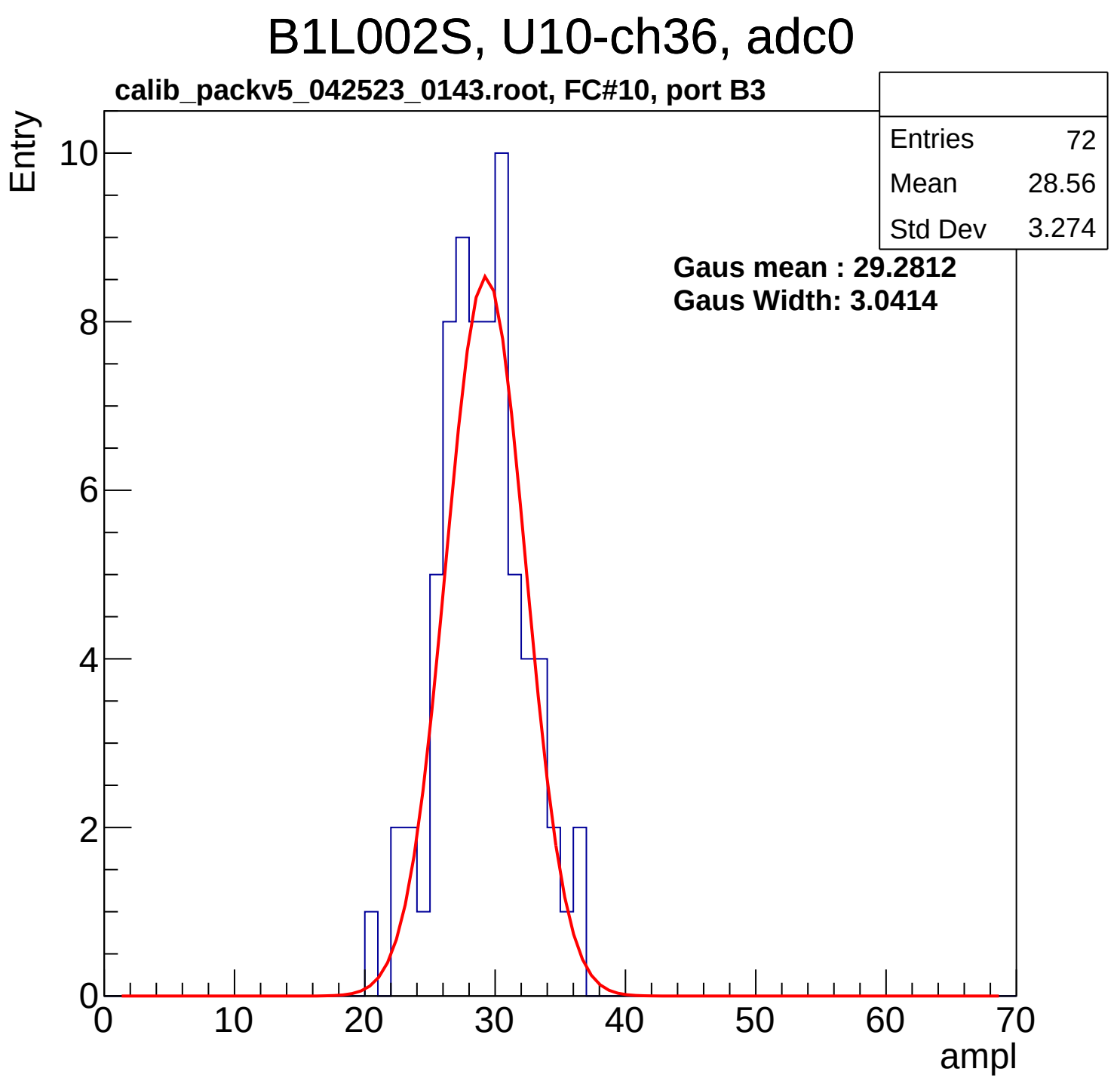
B1L002S, U10-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.56
Std Dev 3.274
Gaus mean : 29.2812
Gaus Width: 3.0414
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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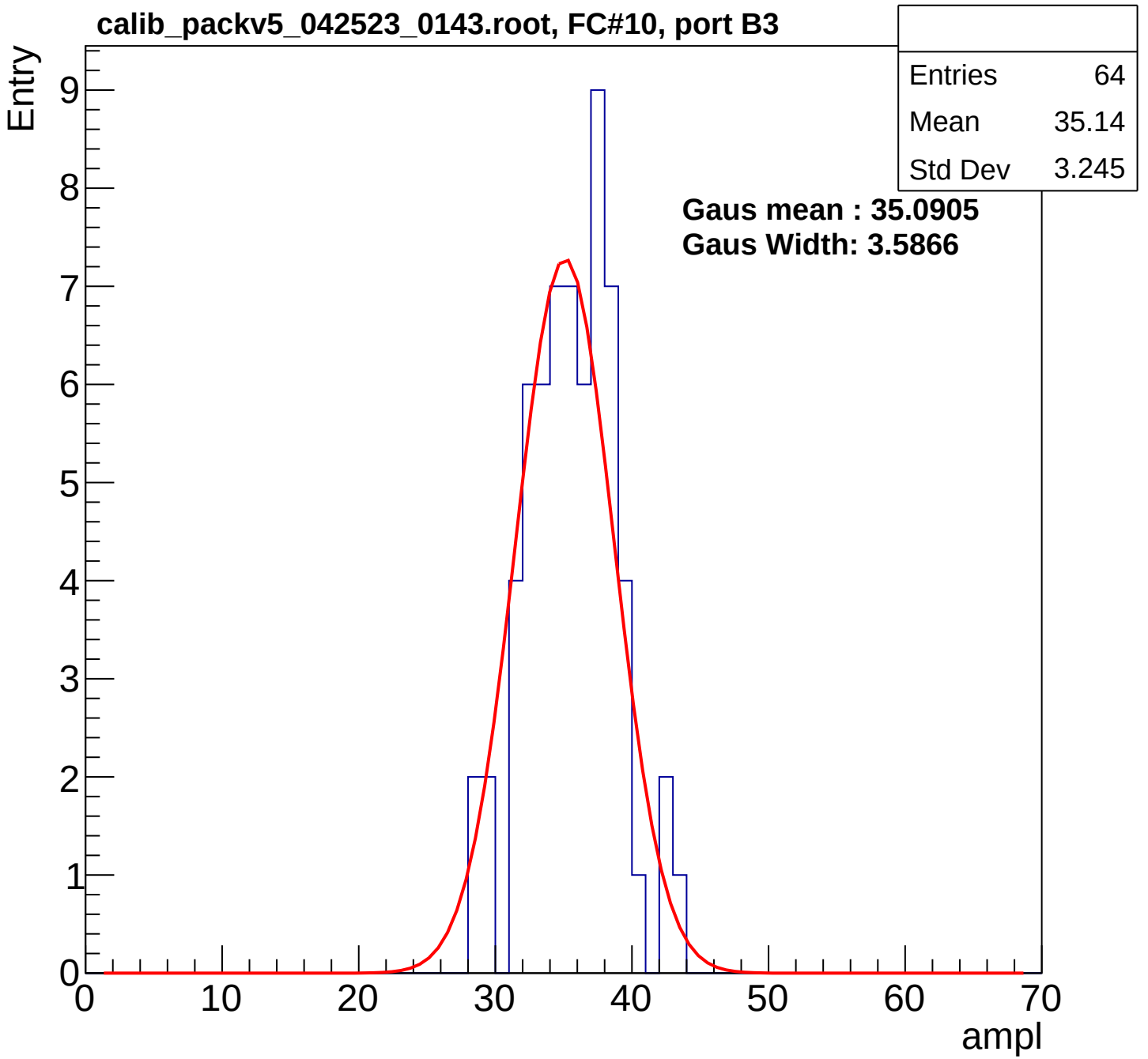
B1L002S, U10-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.14
Std Dev 3.245
Gaus mean : 35.0905
Gaus Width: 3.5866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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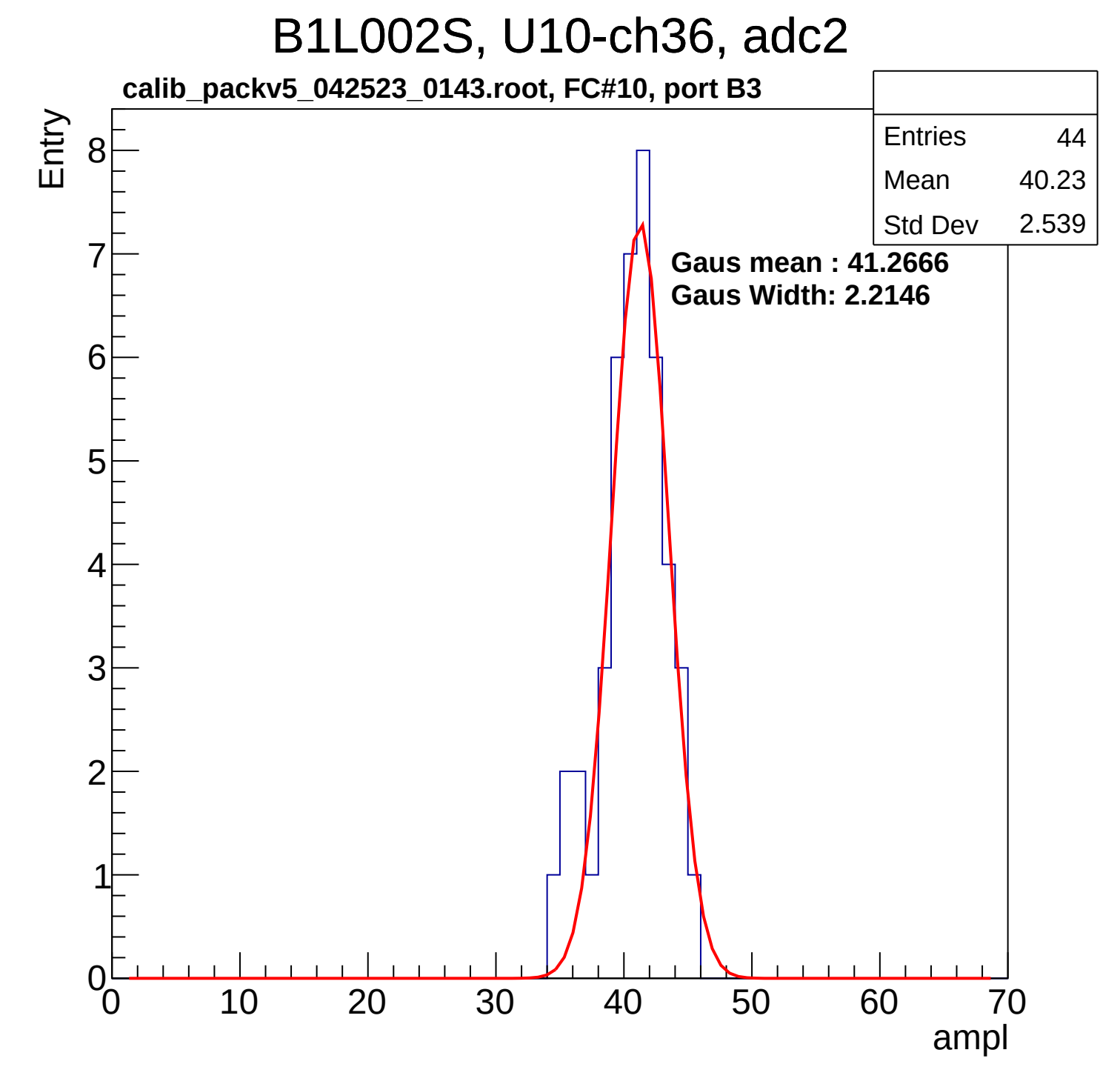
B1L002S, U10-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 40.23
Std Dev 2.539
Gaus mean : 41.2666
Gaus Width: 2.2146
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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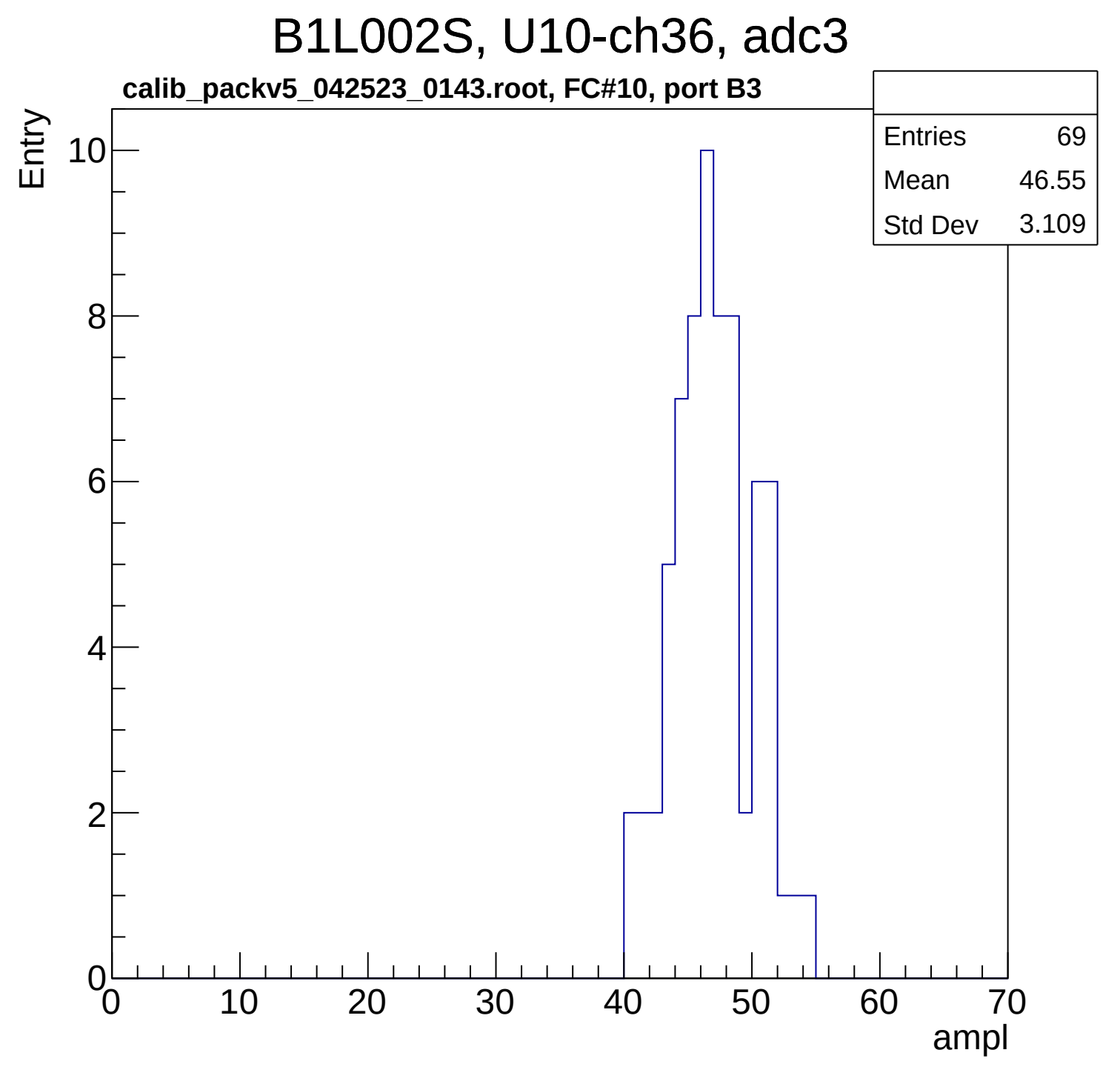
B1L002S, U10-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.55
Std Dev 3.109
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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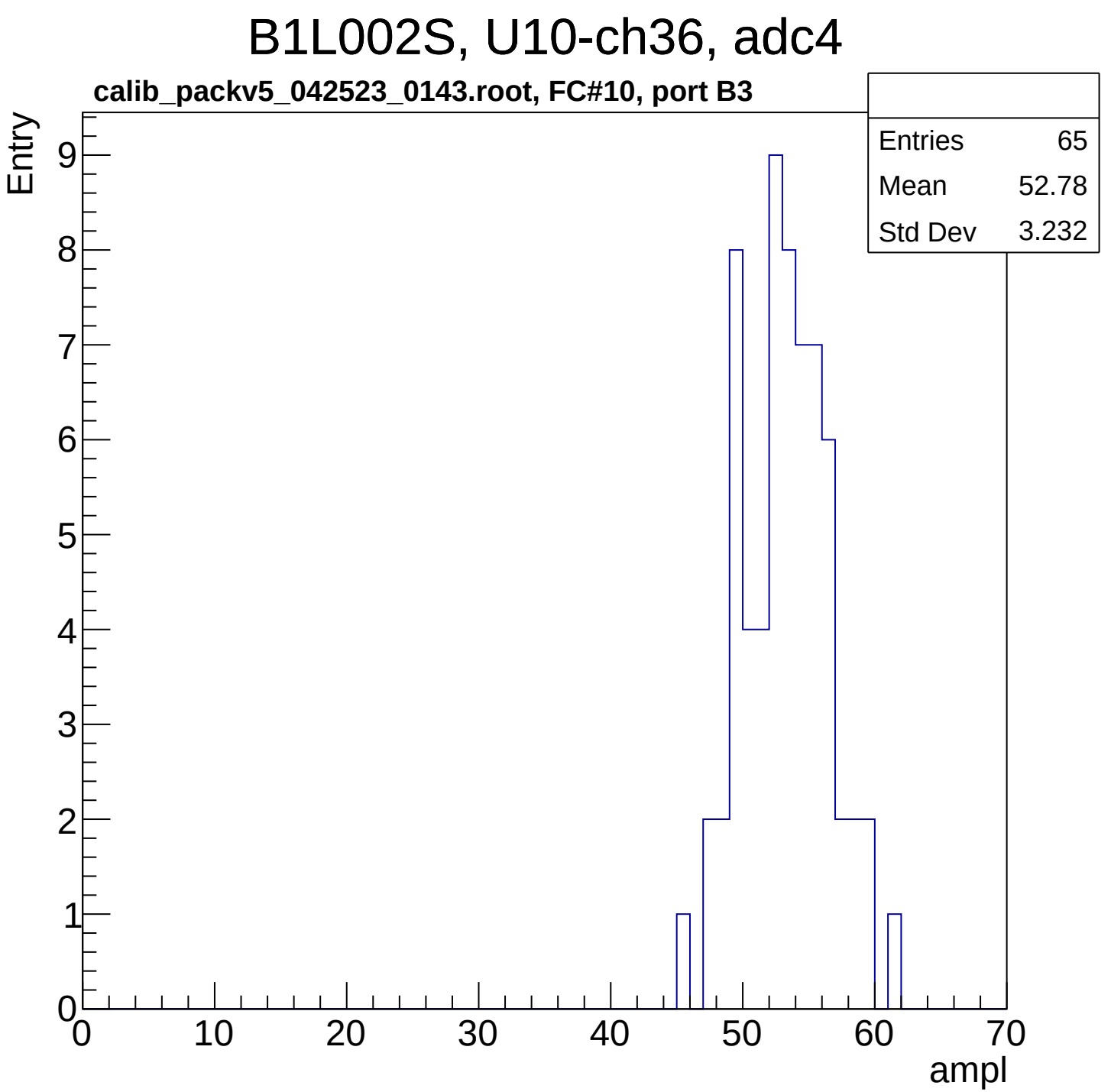
B1L002S, U10-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 52.78
Std Dev 3.232
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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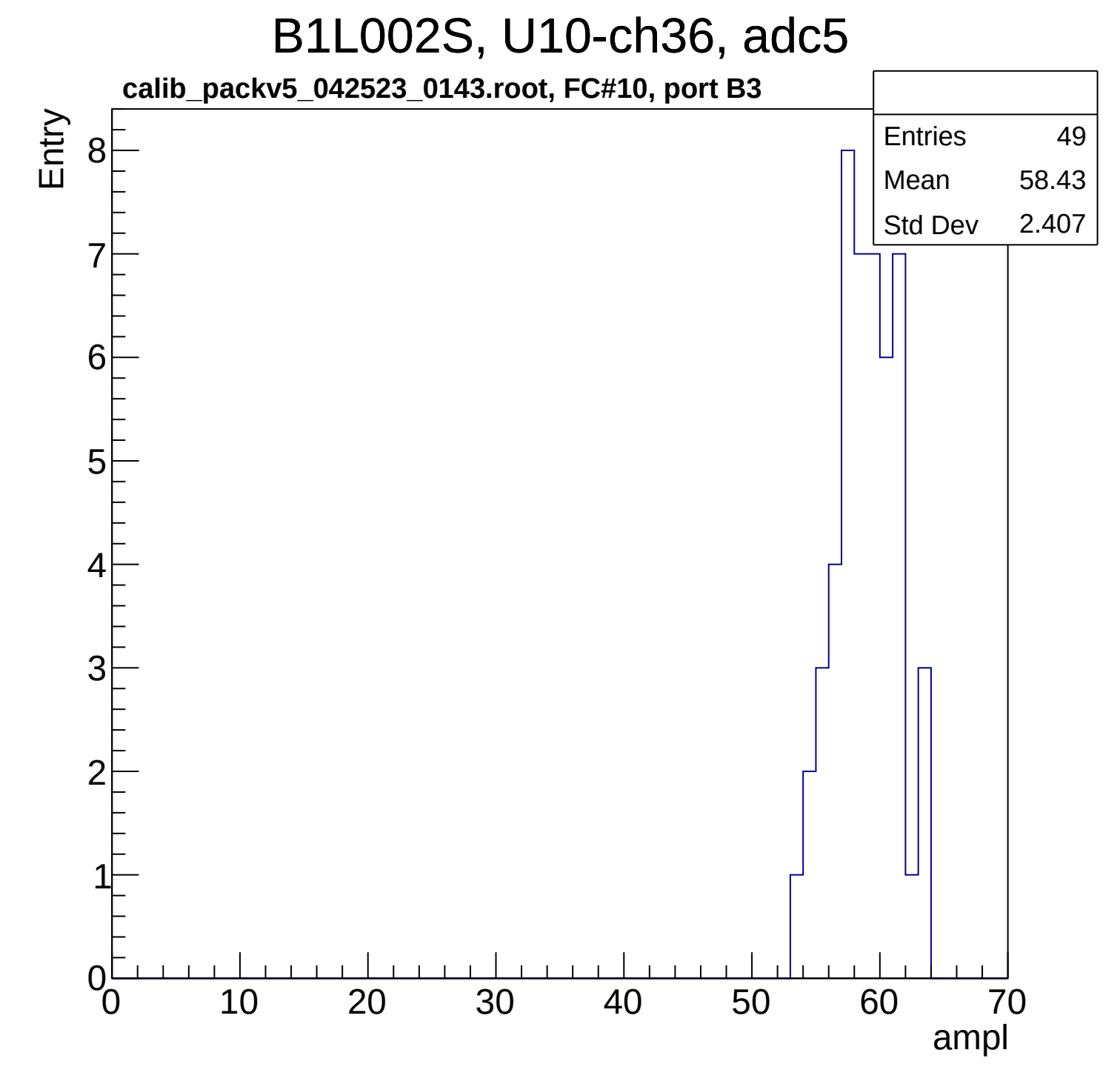
B1L002S, U10-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.43
Std Dev 2.407
Entry
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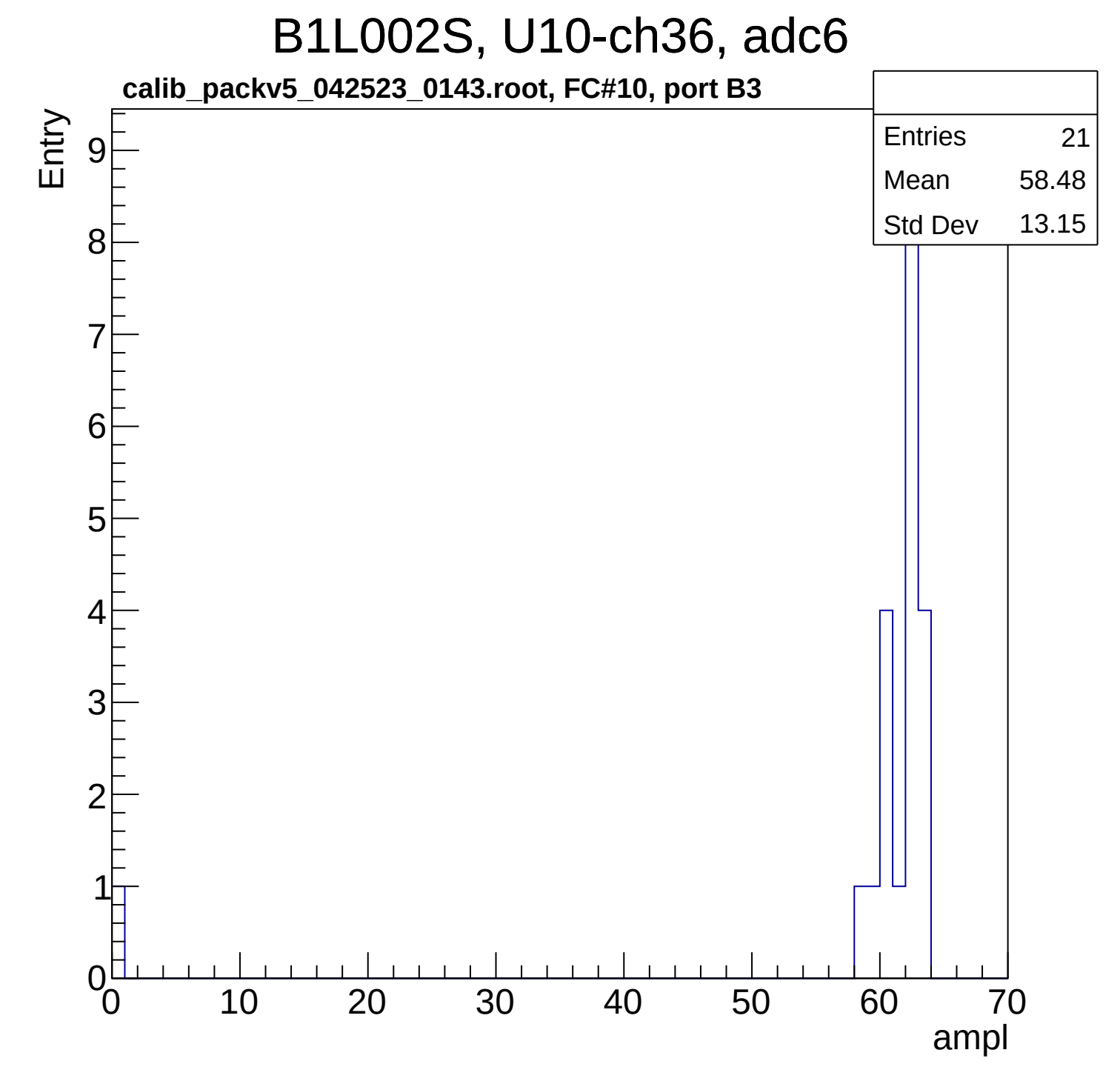
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.48
Std Dev 13.15
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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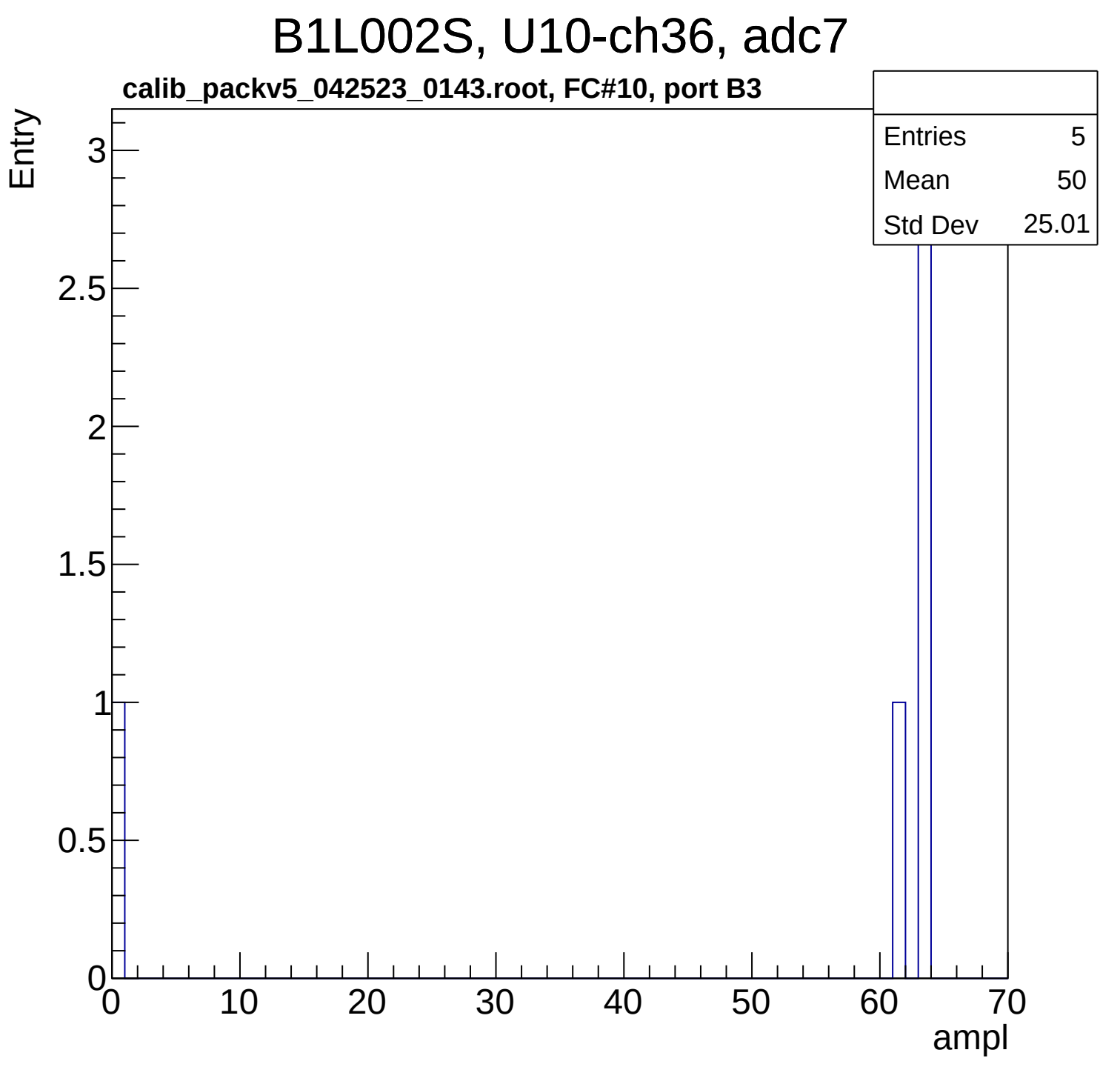
B1L002S, U10-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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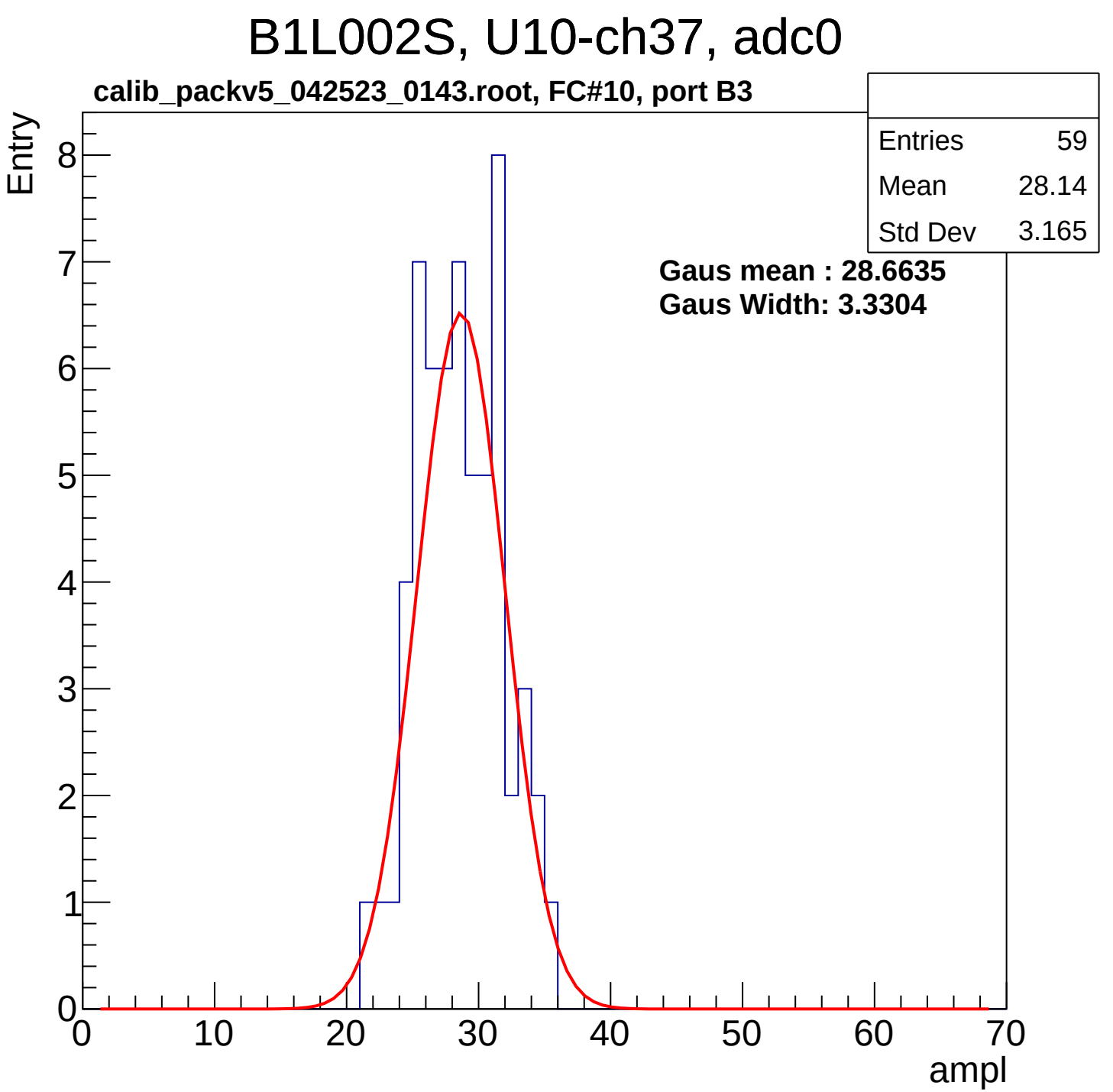
20
30
40
50
60
70


B1L002S, U10-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.14
Std Dev 3.165
Gaus mean : 28.6635
Gaus Width: 3.3304
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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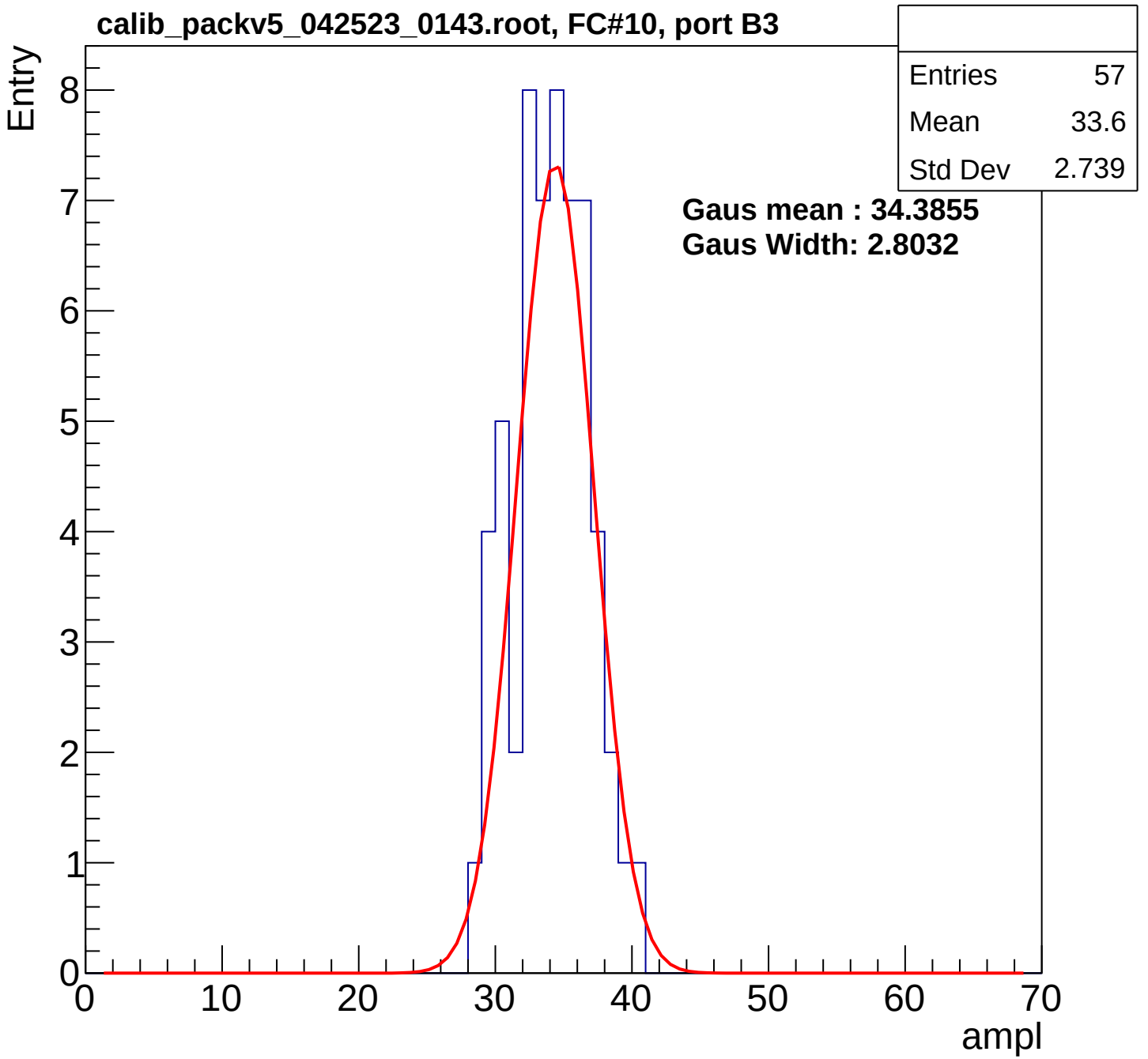
40
50
60
70


B1L002S, U10-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.6
Std Dev 2.739
Gaus mean : 34.3855
Gaus Width: 2.8032
Entry
ampl
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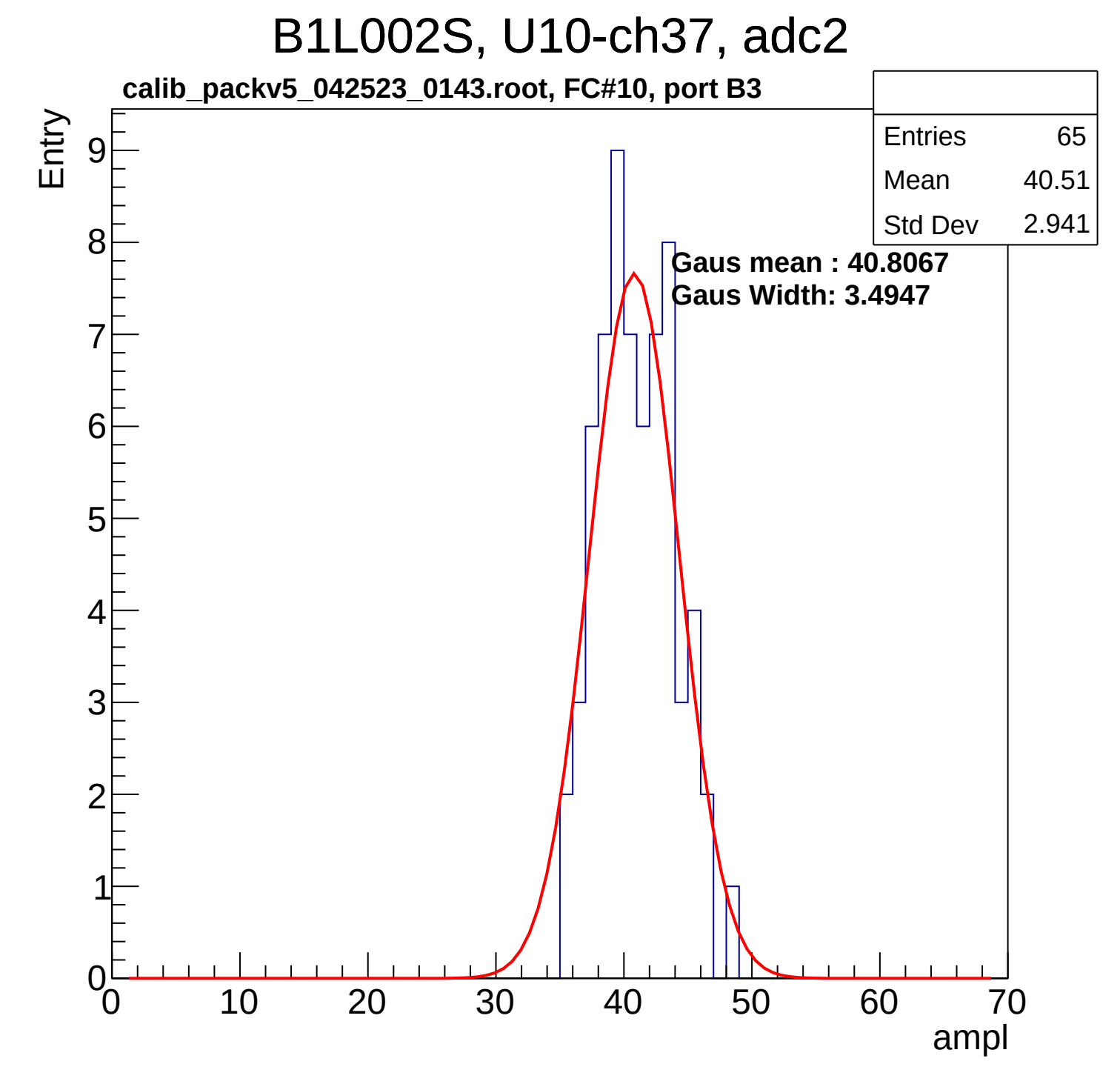
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.51
Std Dev 2.941
Gaus mean : 40.8067
Gaus Width: 3.4947
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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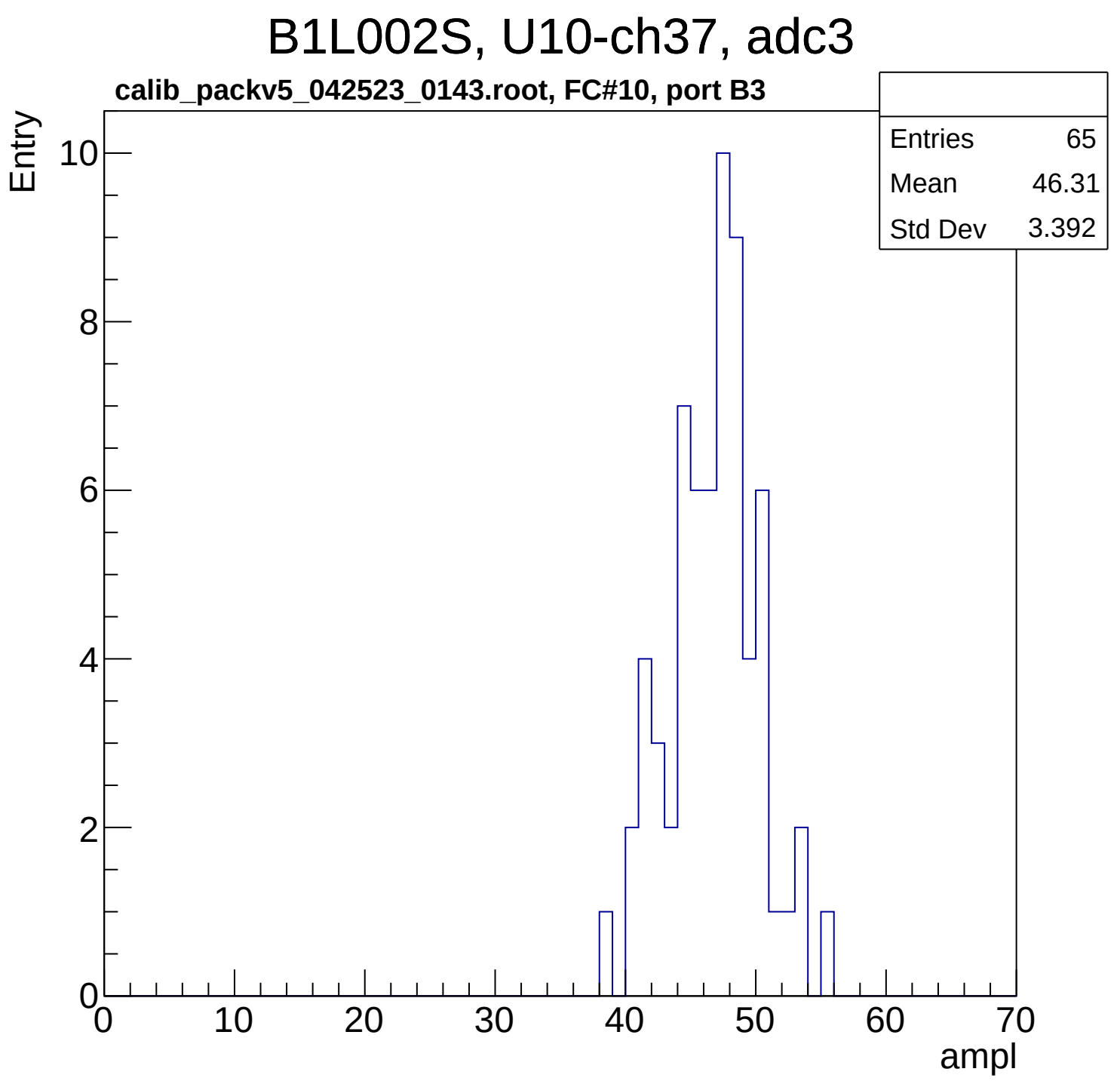
B1L002S, U10-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.31
Std Dev 3.392
Entry
ampl
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6
8
10
0
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40
50
60
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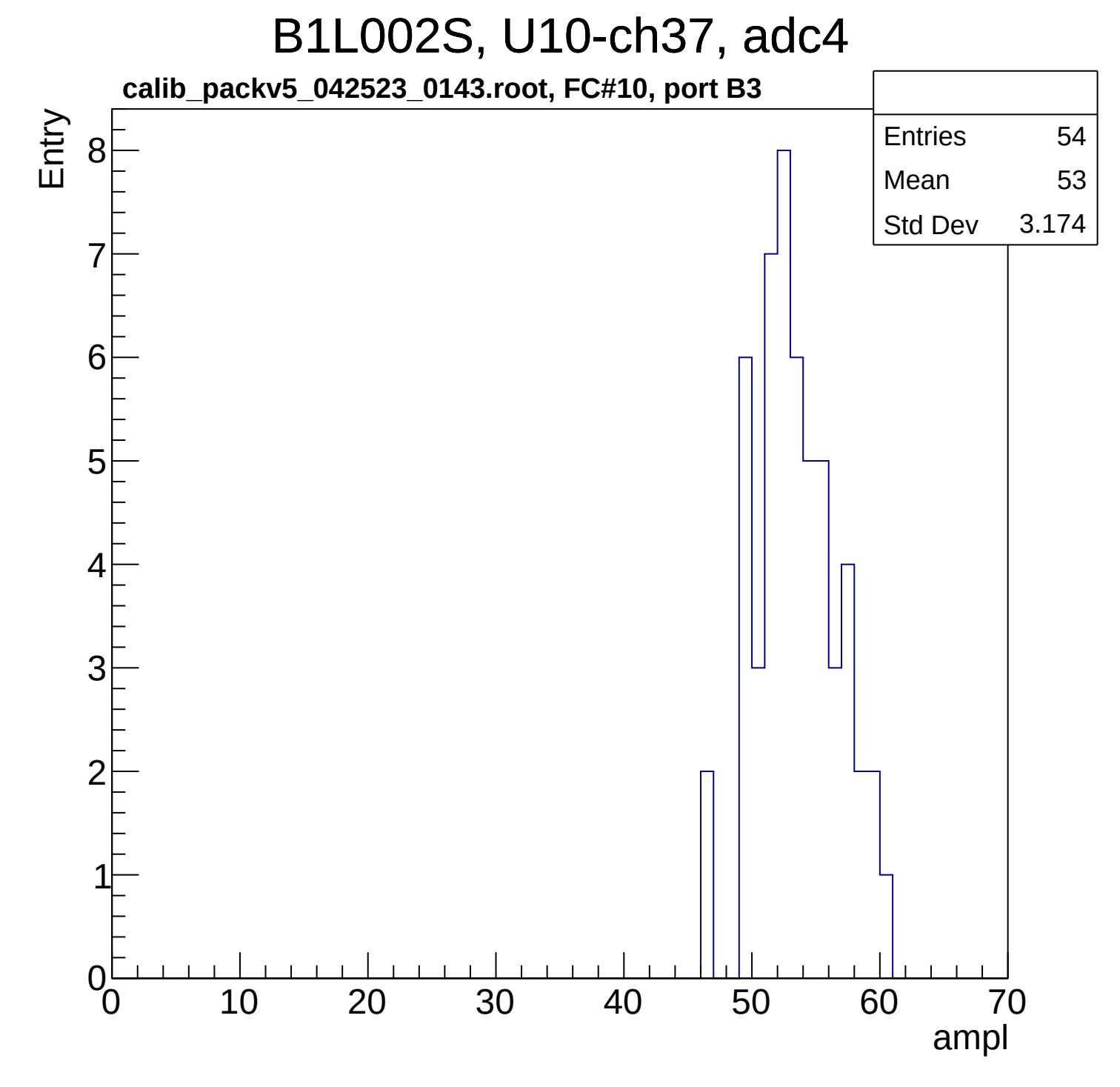
B1L002S, U10-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53
Std Dev 3.174
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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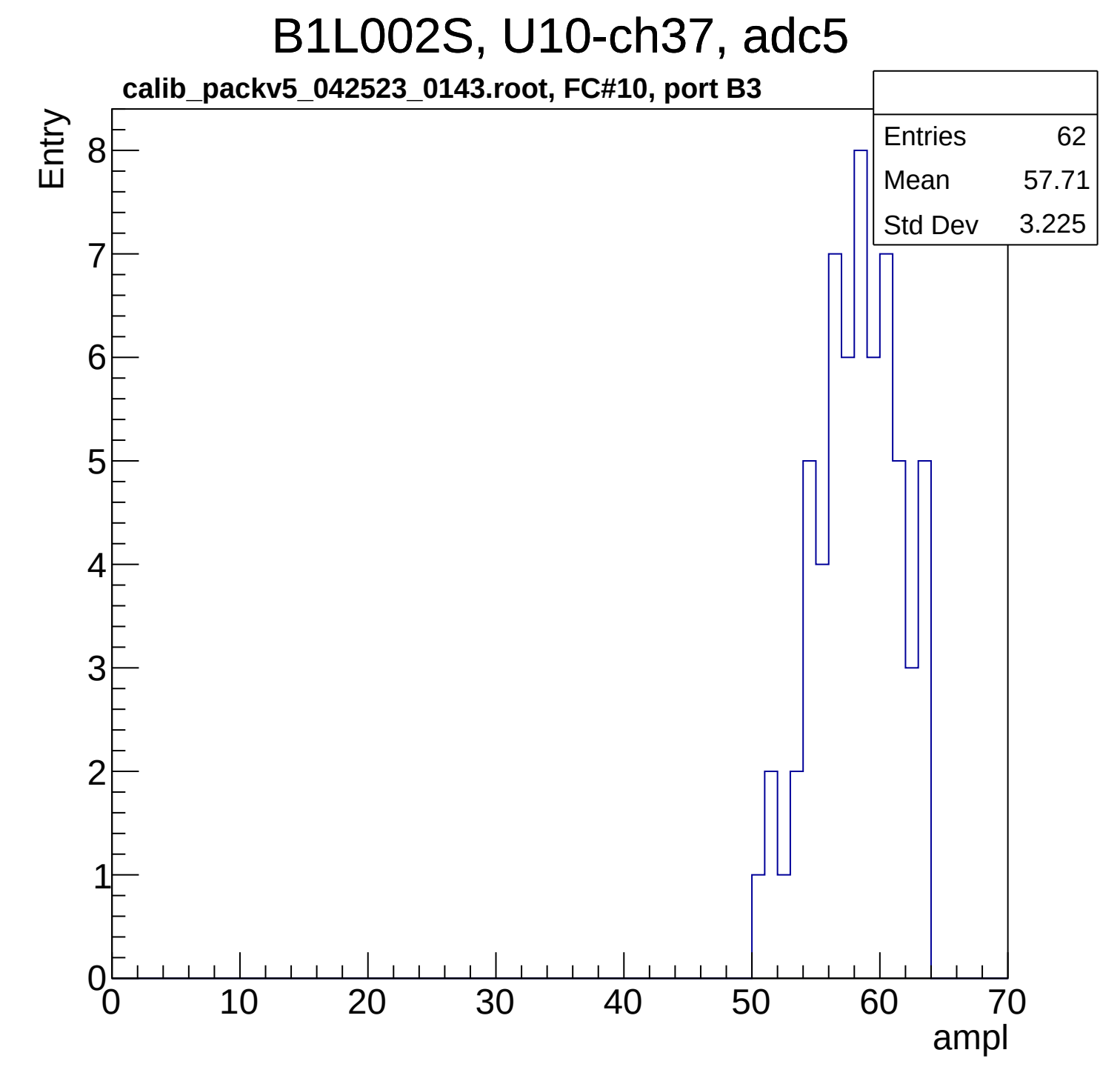
B1L002S, U10-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.71
Std Dev 3.225
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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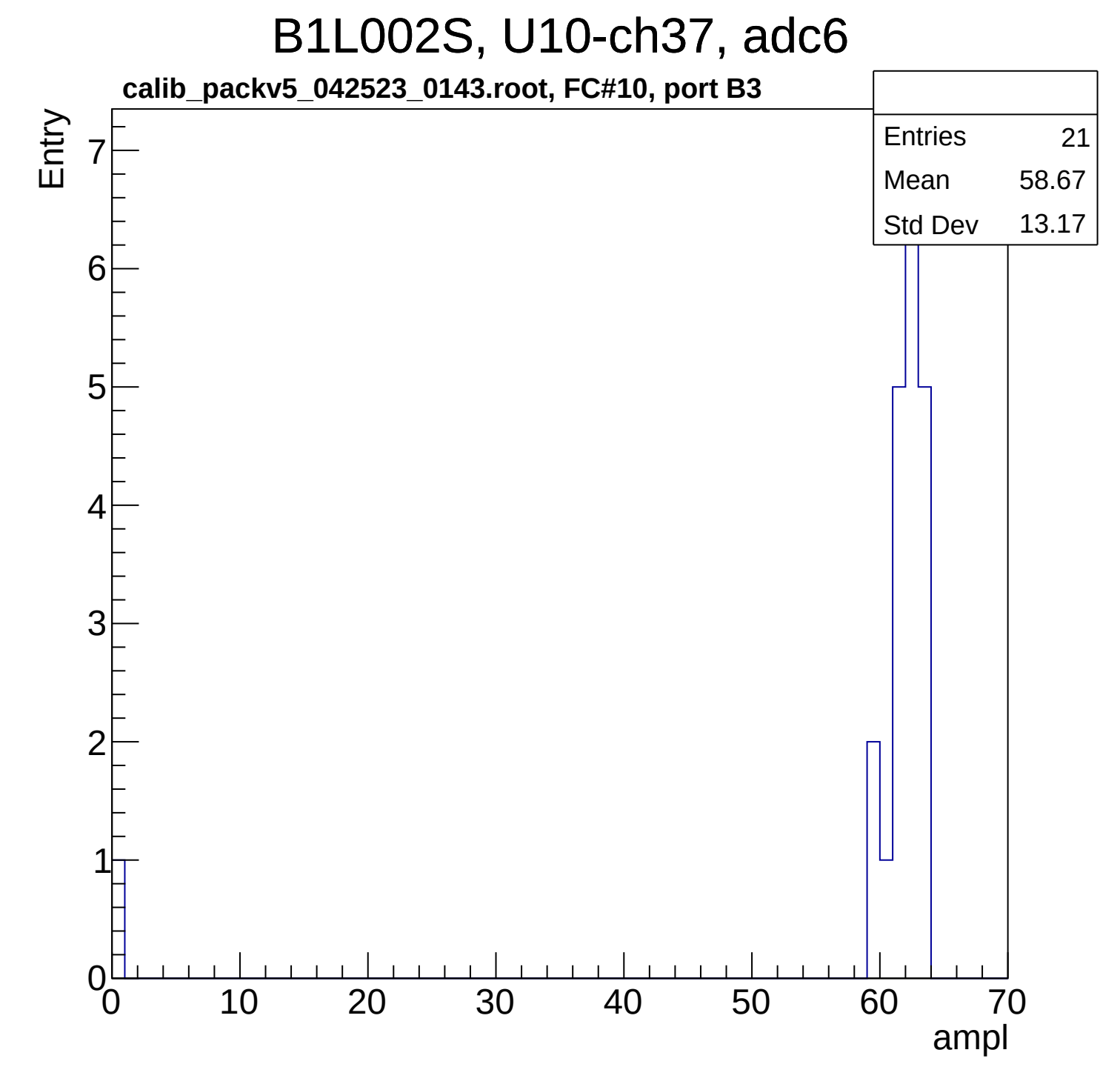
B1L002S, U10-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.67
Std Dev 13.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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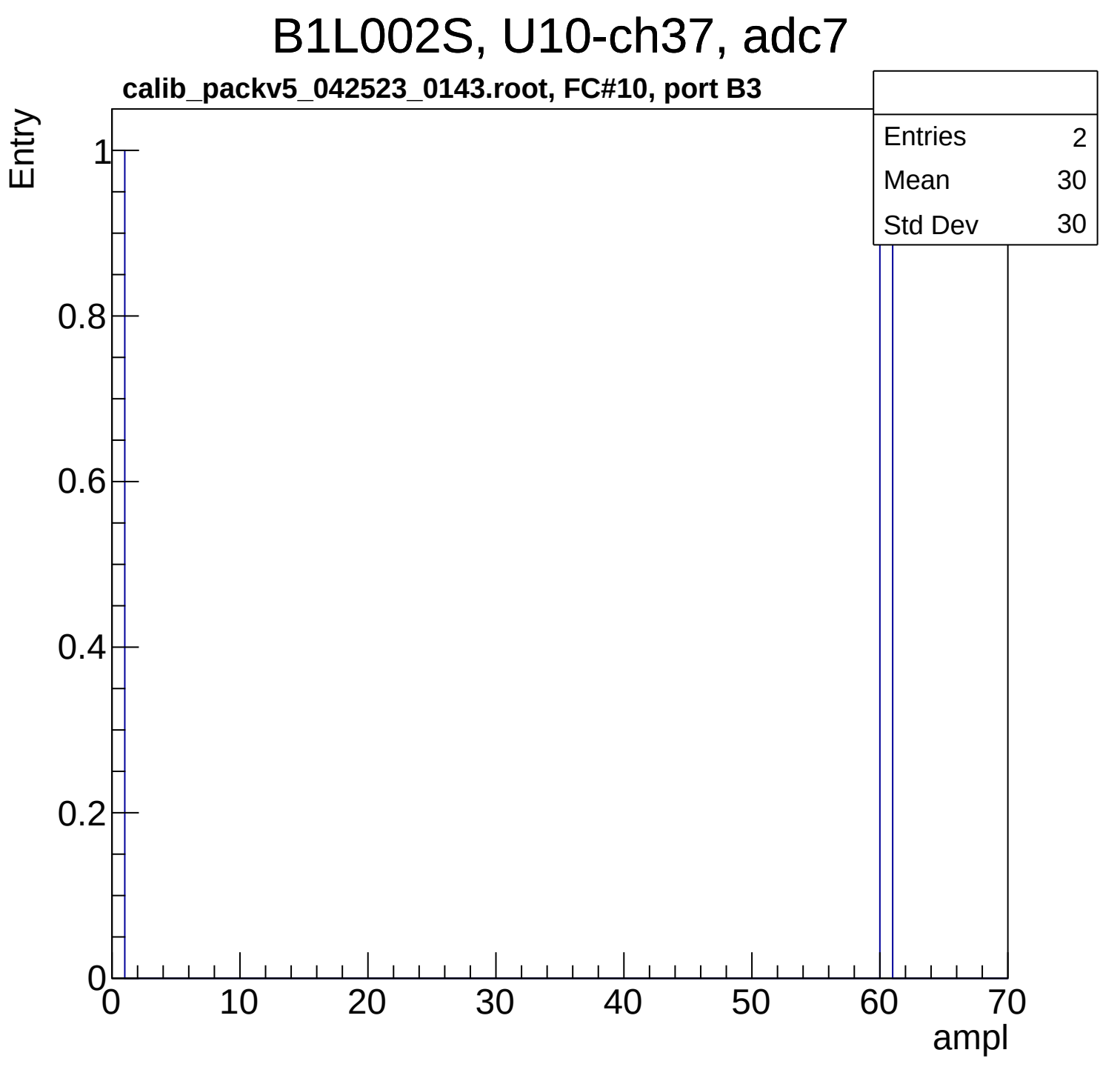
B1L002S, U10-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30
Std Dev 30
Entry
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0.6
0.4
0.2
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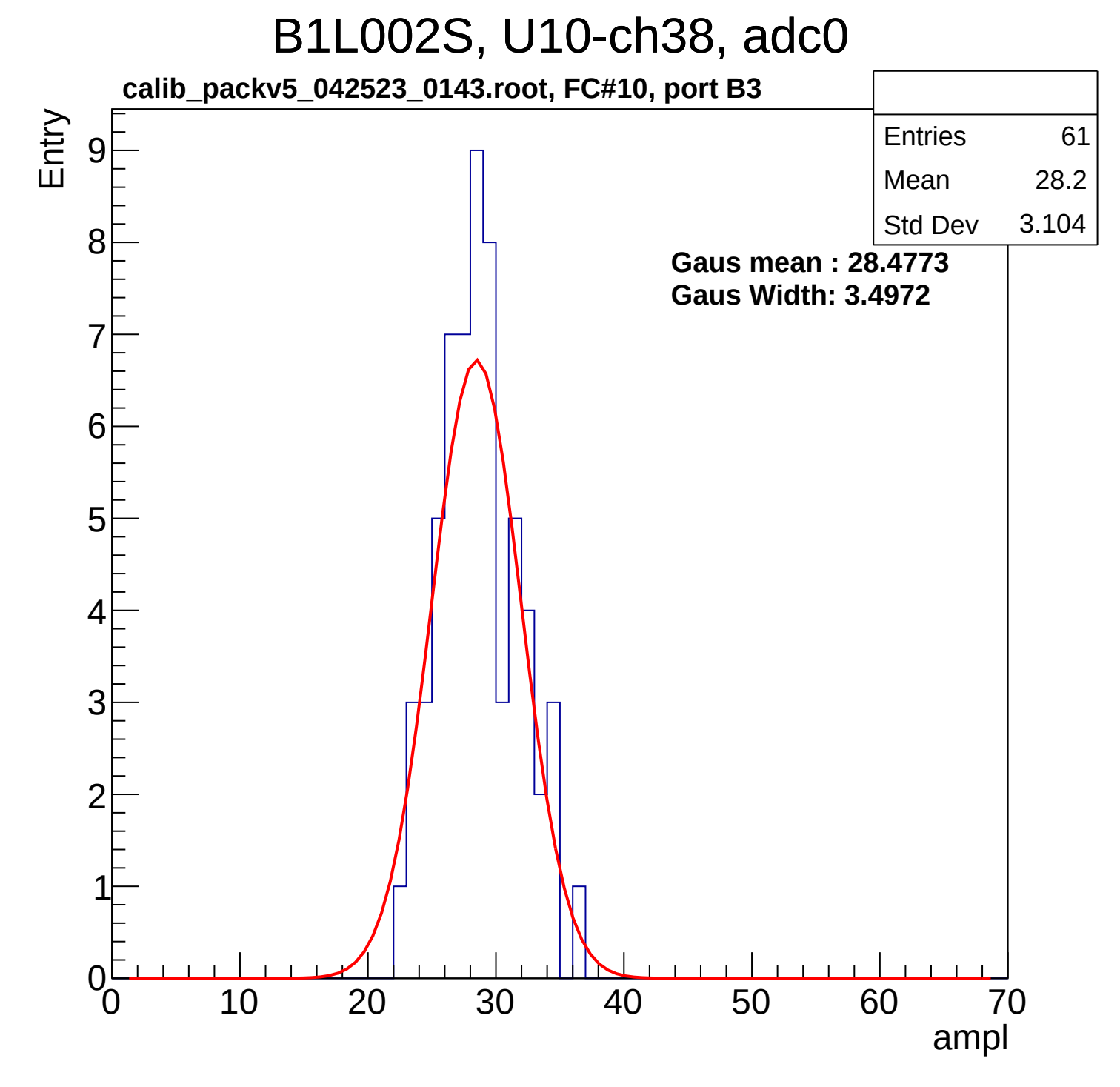
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.2
Std Dev 3.104
Gaus mean : 28.4773
Gaus Width: 3.4972
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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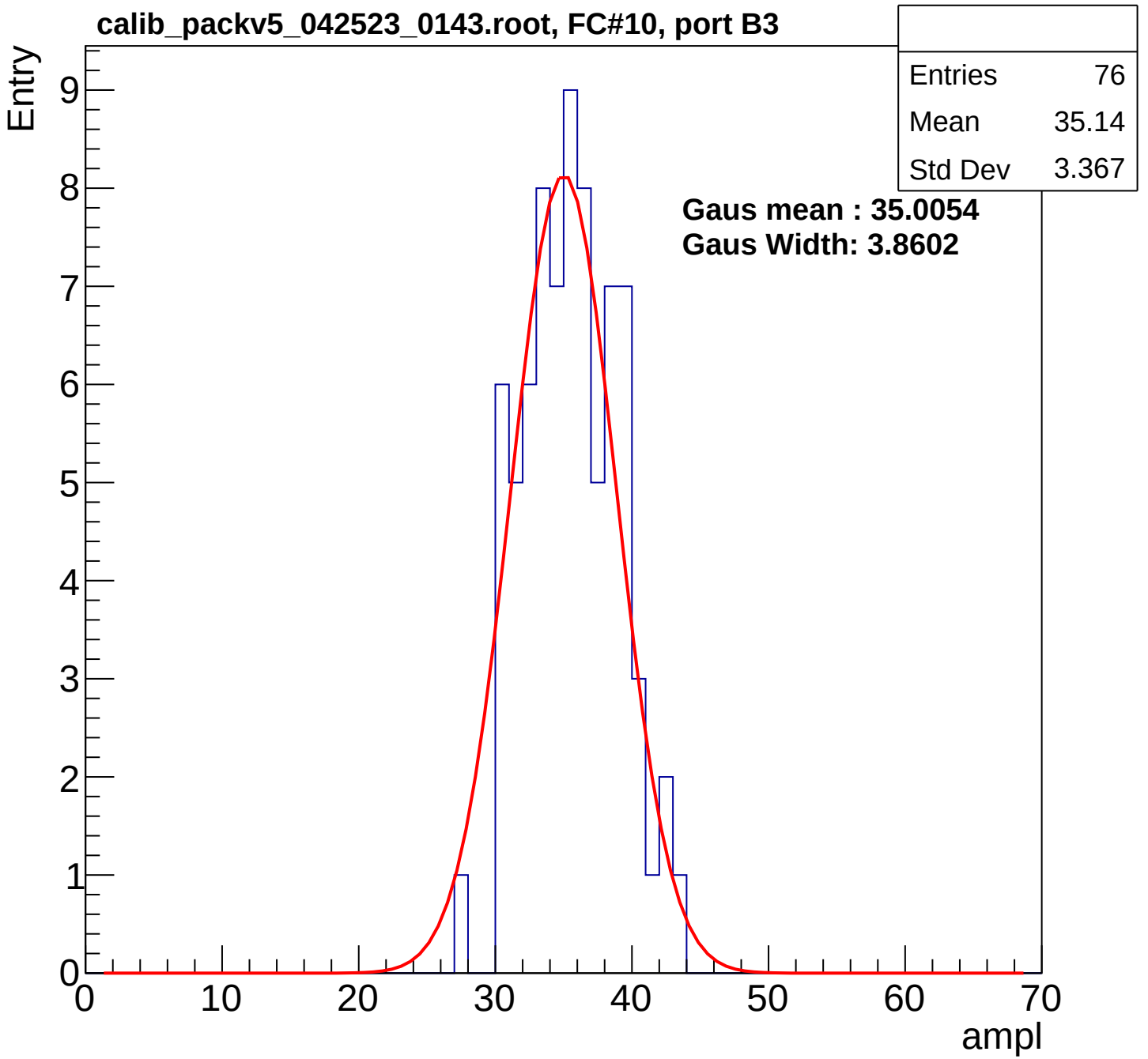
B1L002S, U10-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35.14
Std Dev 3.367
Gaus mean : 35.0054
Gaus Width: 3.8602
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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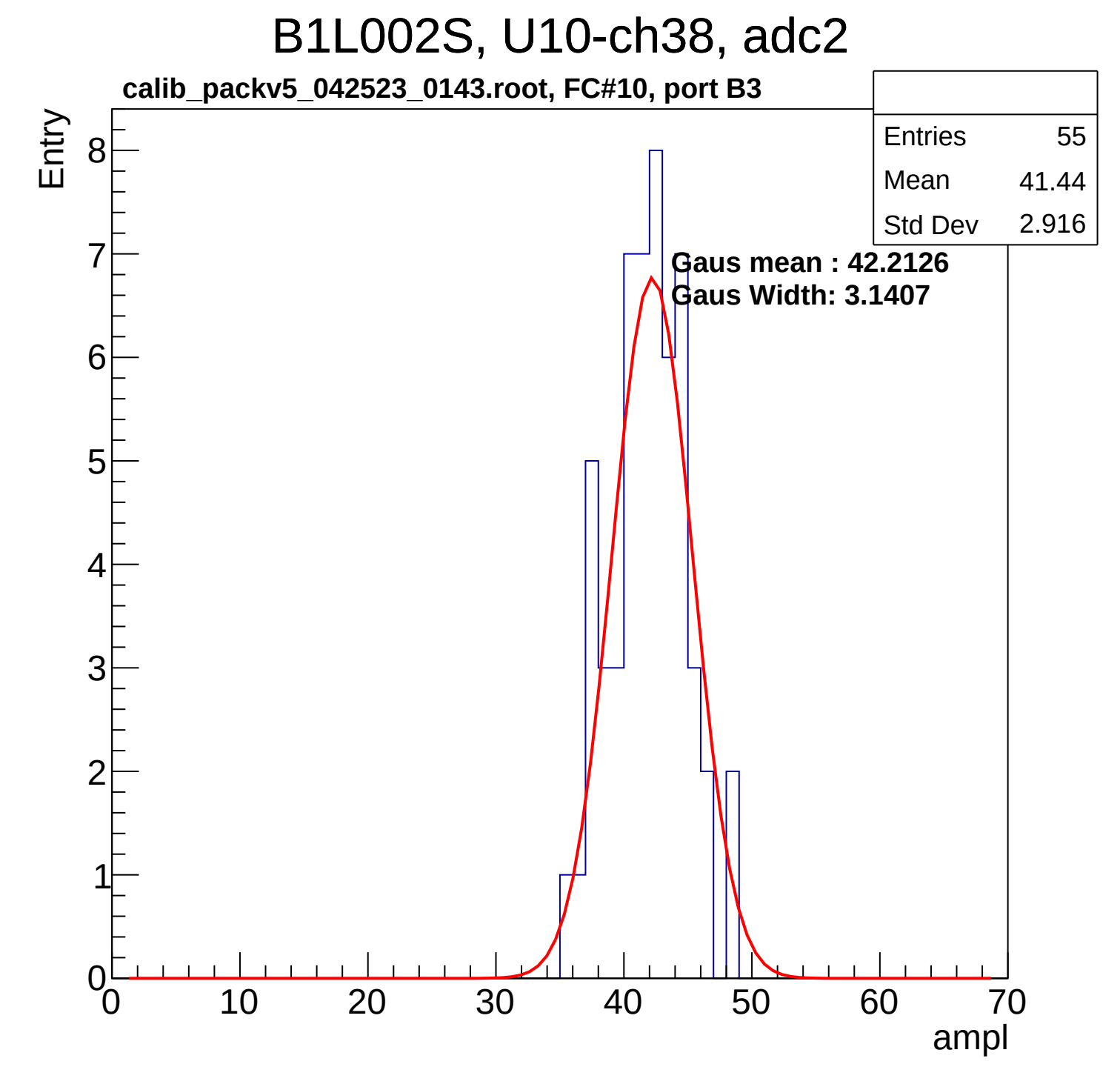
B1L002S, U10-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.44
Std Dev 2.916
Gaus mean : 42.2126
Gaus Width: 3.1407
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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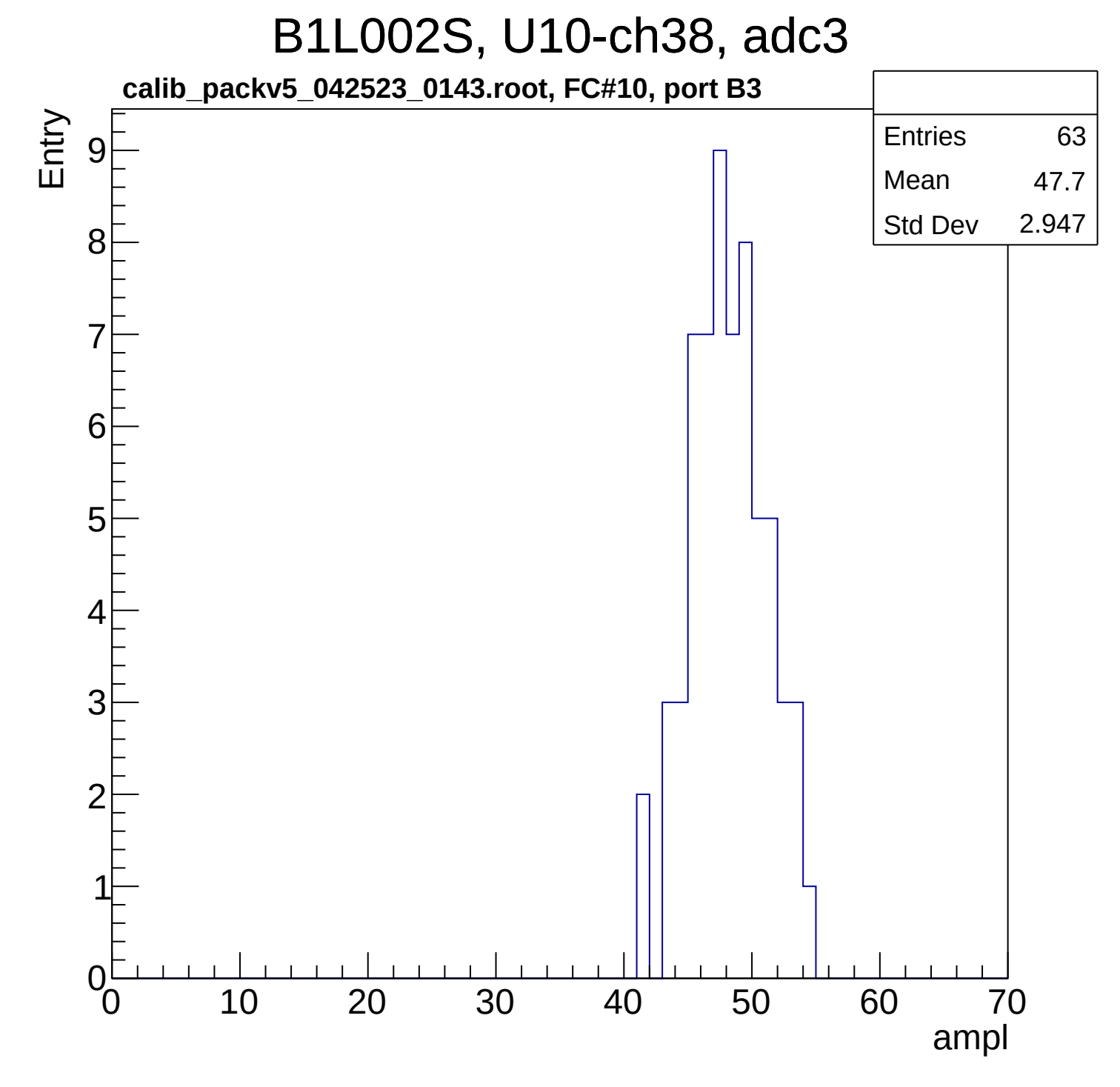
B1L002S, U10-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.7
Std Dev 2.947
Entry
ampl
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3
4
5
6
7
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10
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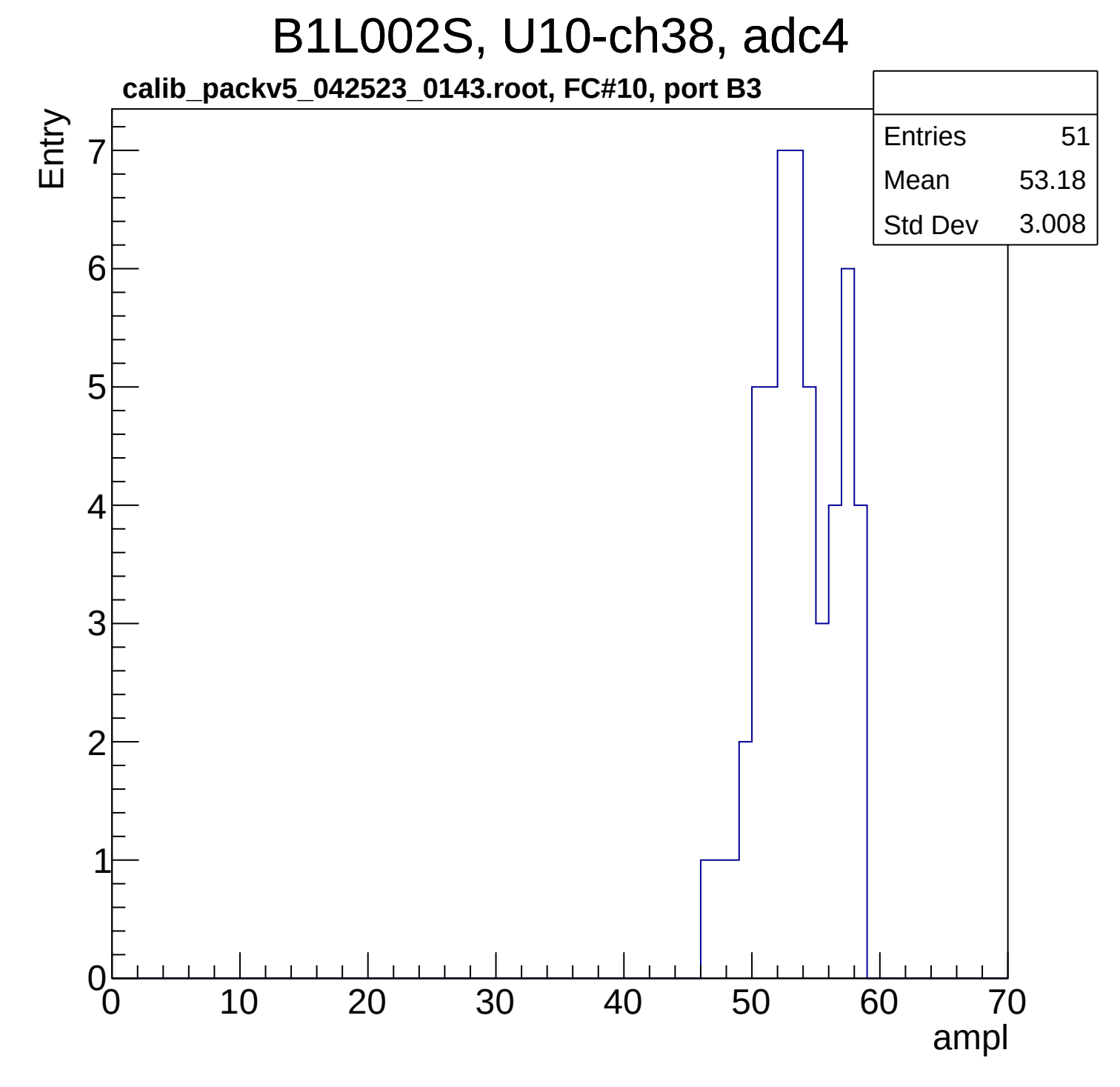
B1L002S, U10-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.18
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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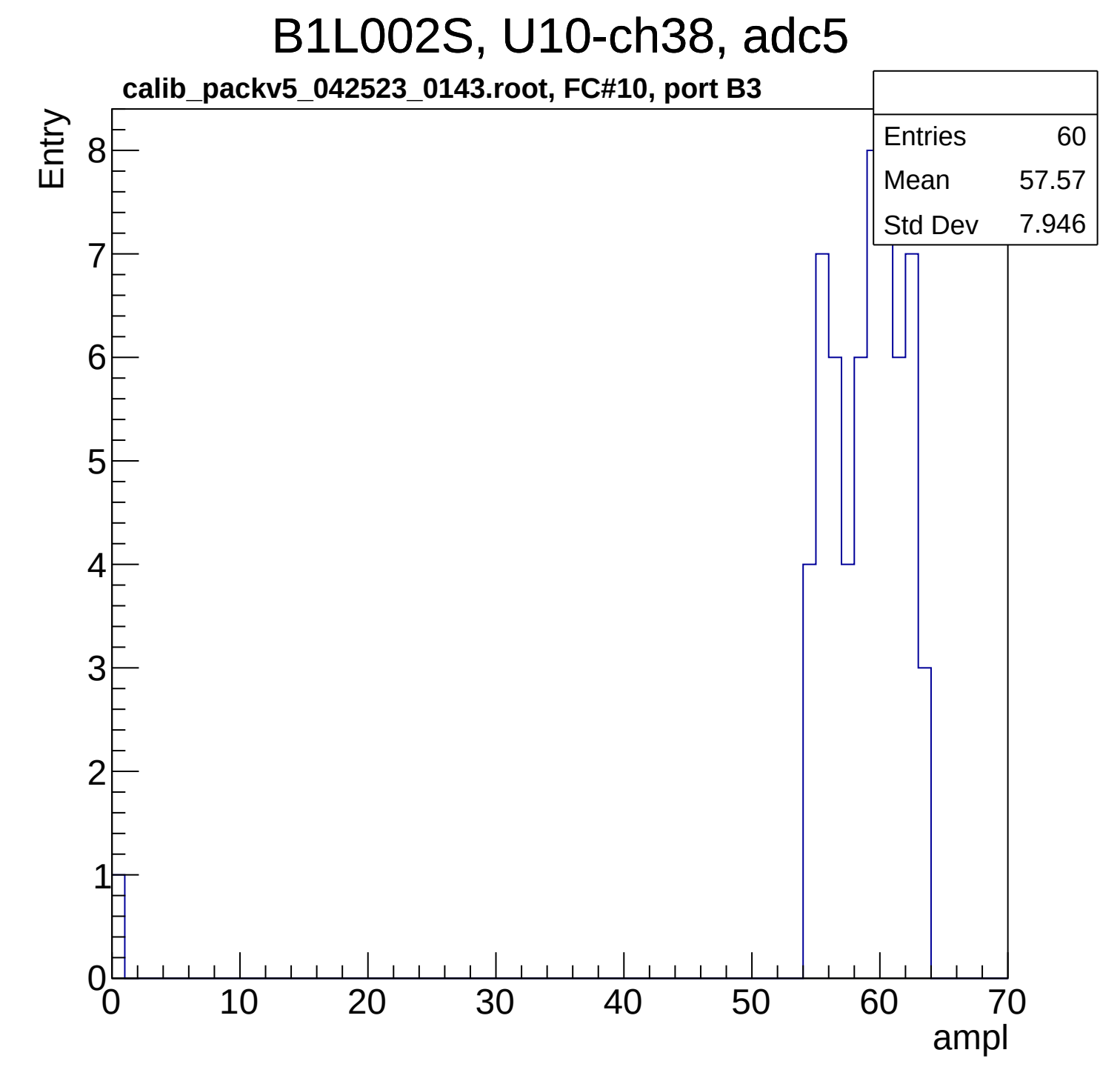
40
50
60
70


B1L002S, U10-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.57
Std Dev 7.946
Entry
ampl
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1
2
3
4
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7
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60
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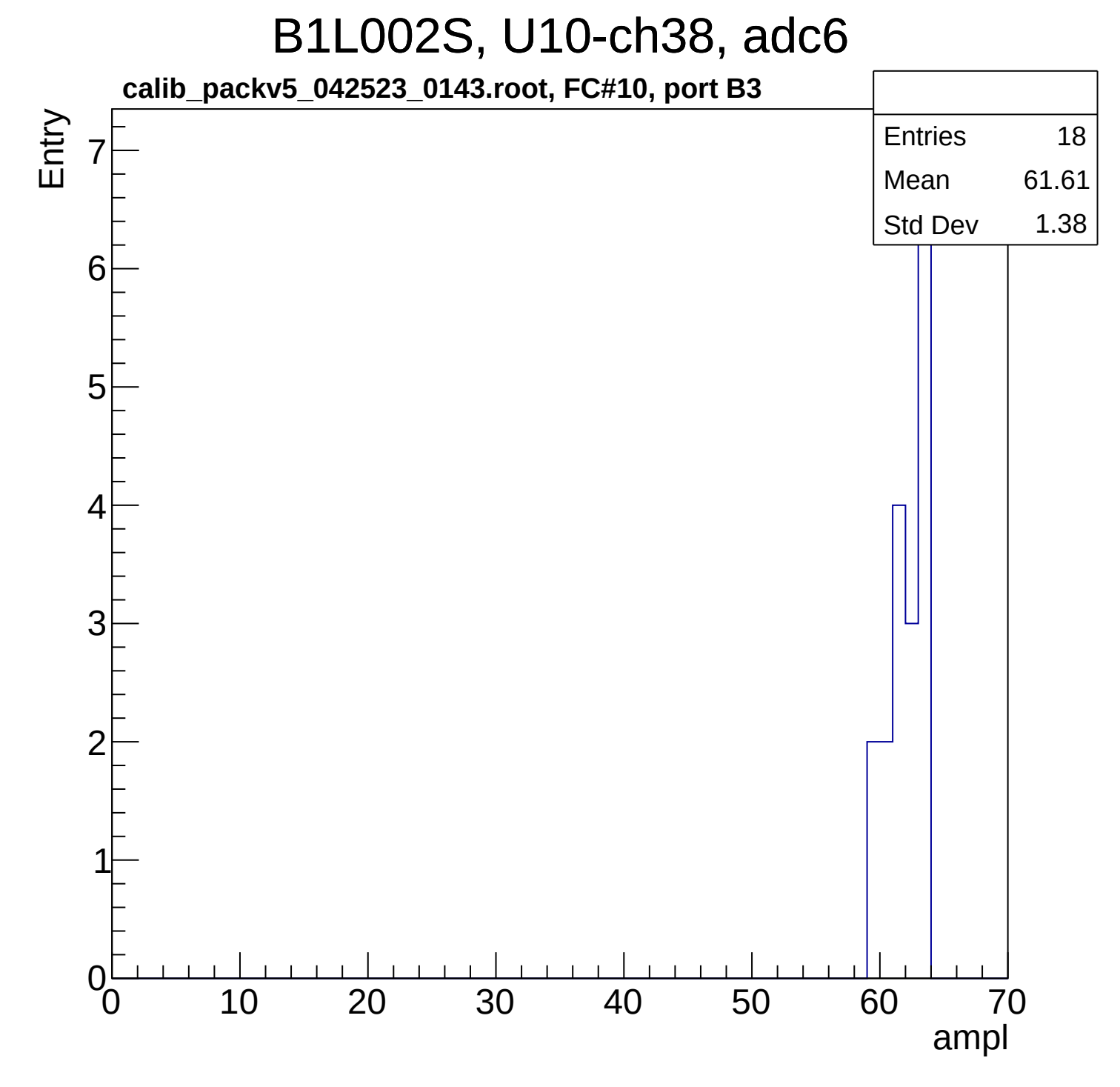
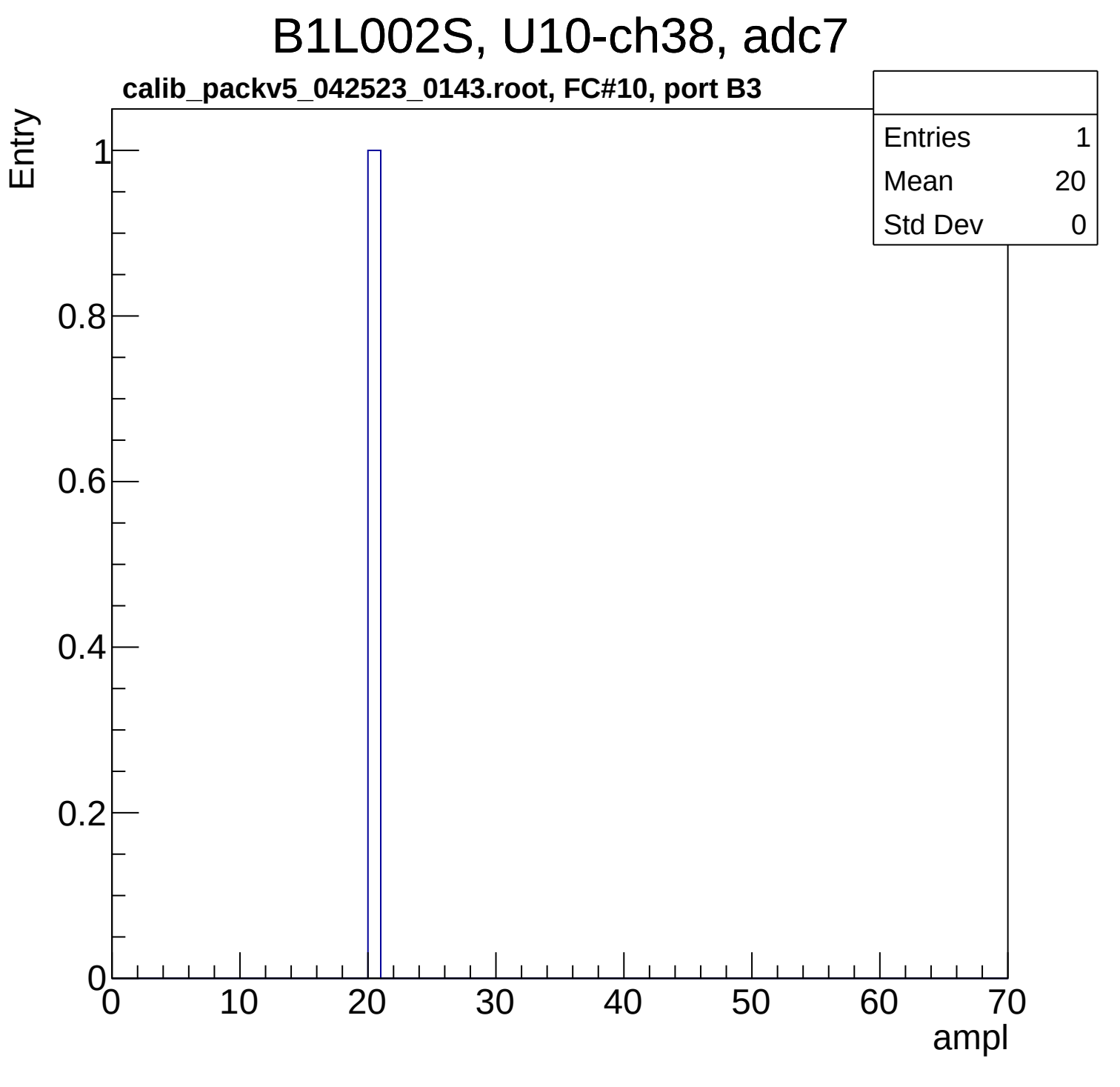
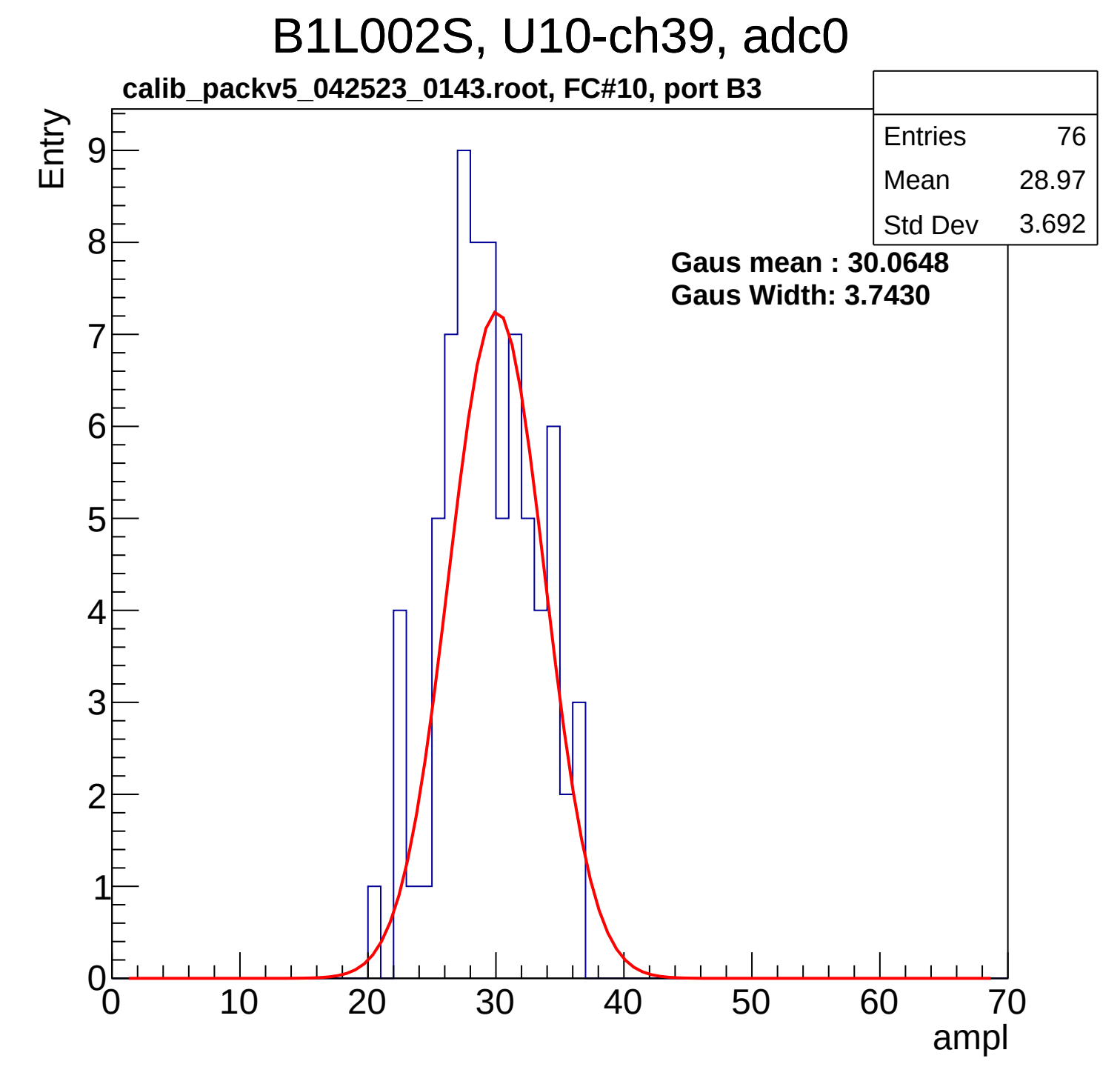

B1L002S, U10-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.61
Std Dev 1.38
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.97
Std Dev 3.692
Gaus mean : 30.0648
Gaus Width: 3.7430
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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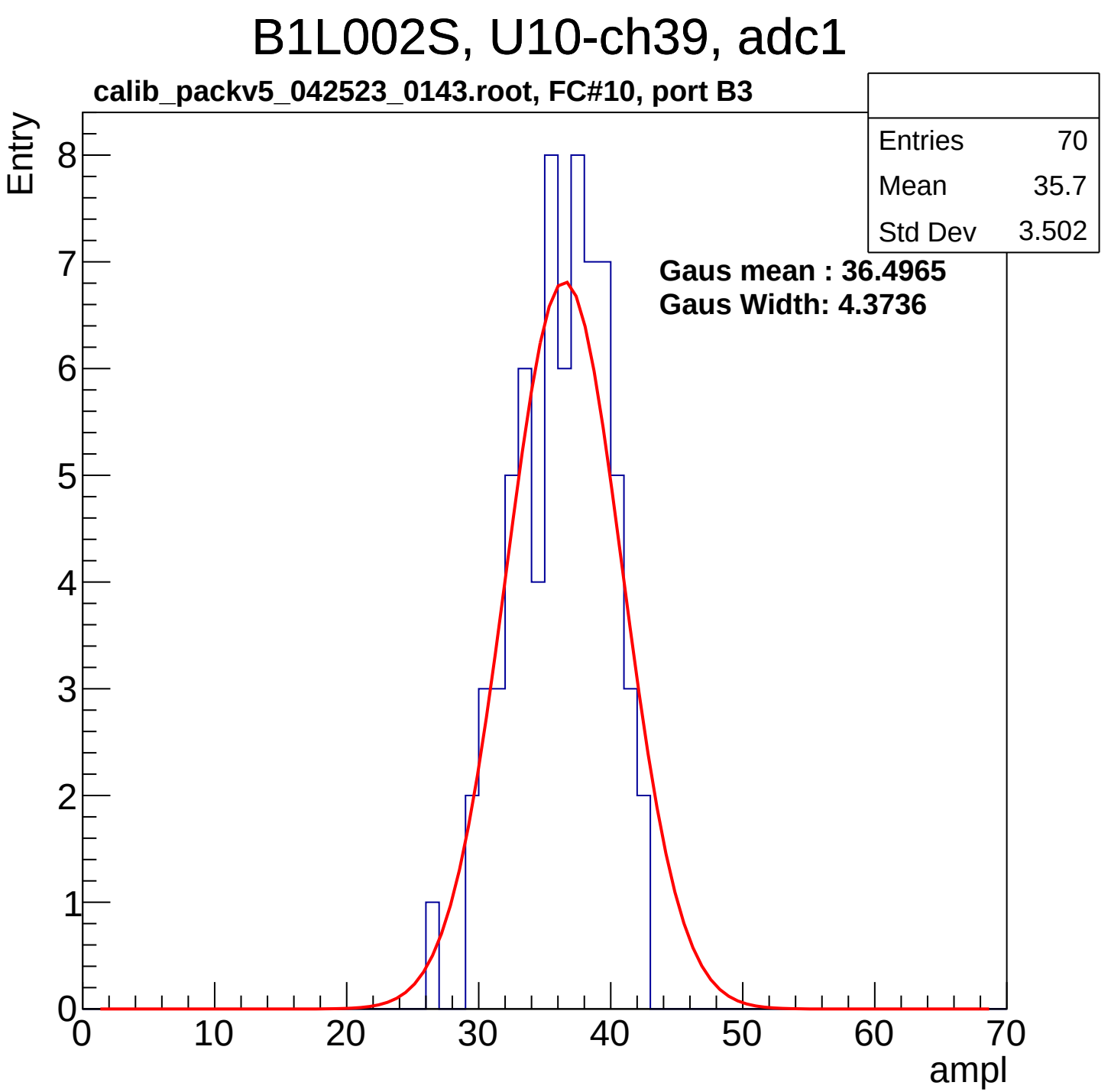
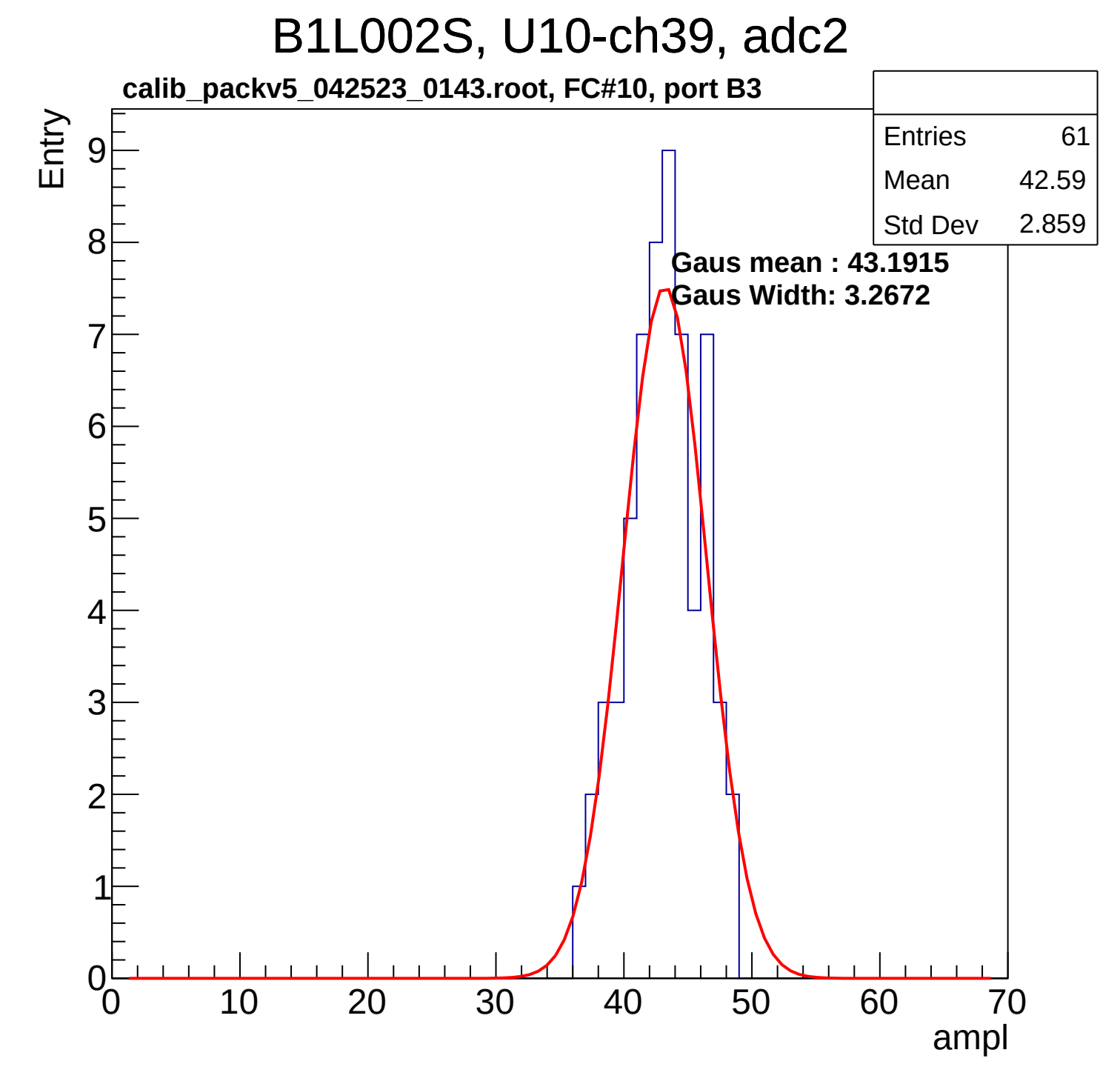

B1L002S, U10-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.7
Std Dev 3.502
Gaus mean : 36.4965
Gaus Width: 4.3736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 42.59
Std Dev 2.859
Gaus mean : 43.1915
Gaus Width: 3.2672
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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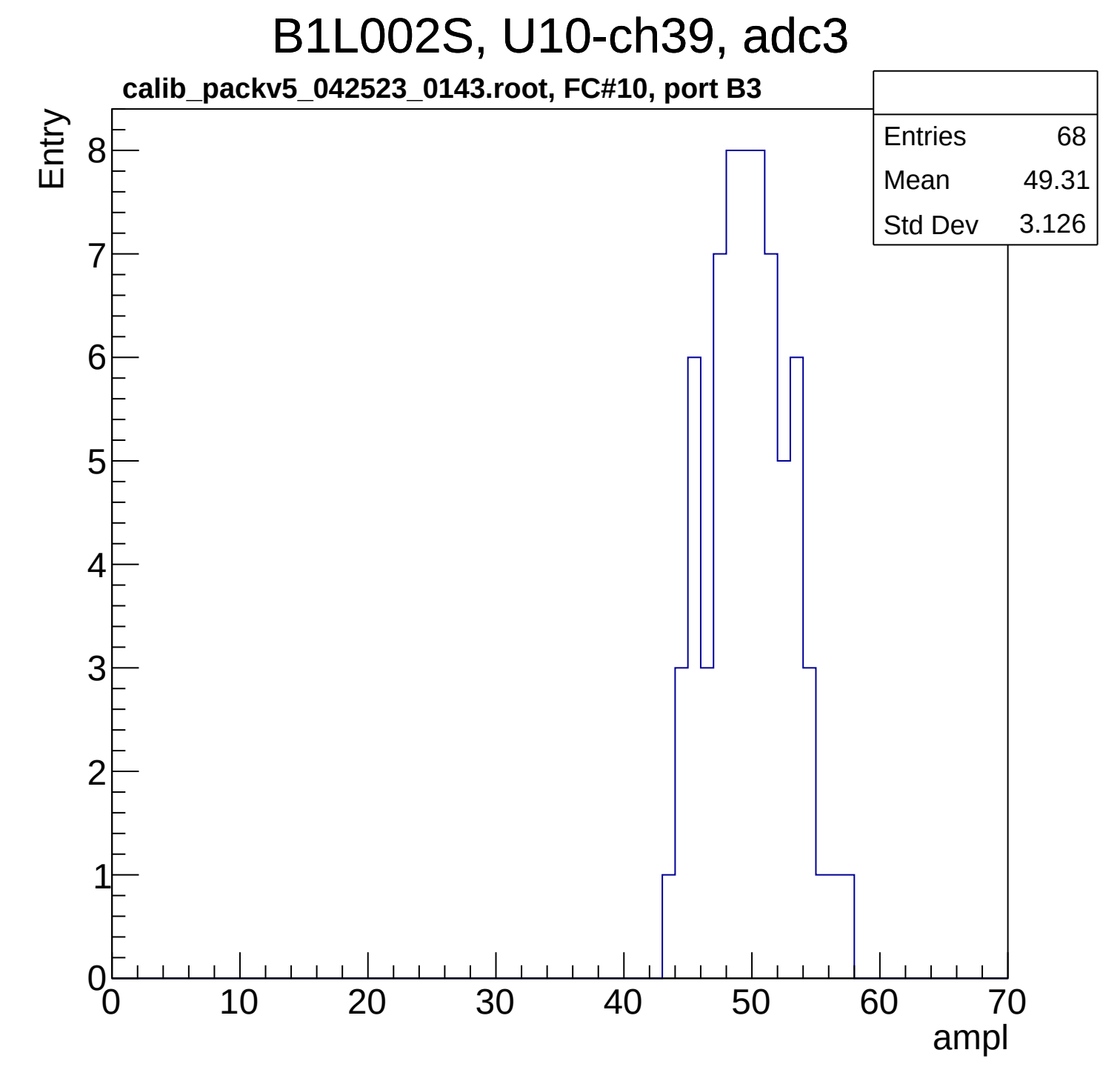
B1L002S, U10-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 49.31
Std Dev 3.126
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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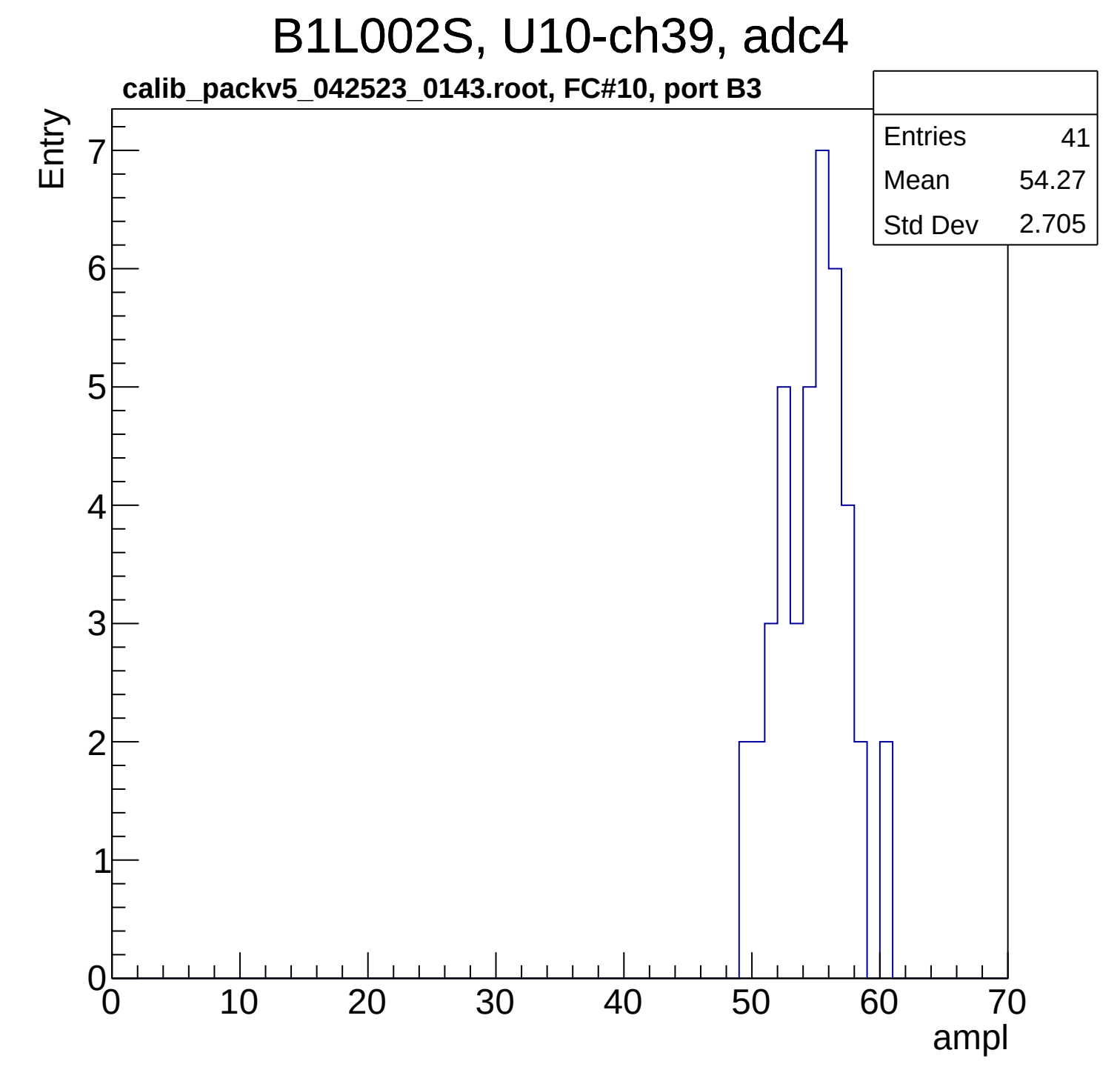
40
50
60
70


B1L002S, U10-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 54.27
Std Dev 2.705
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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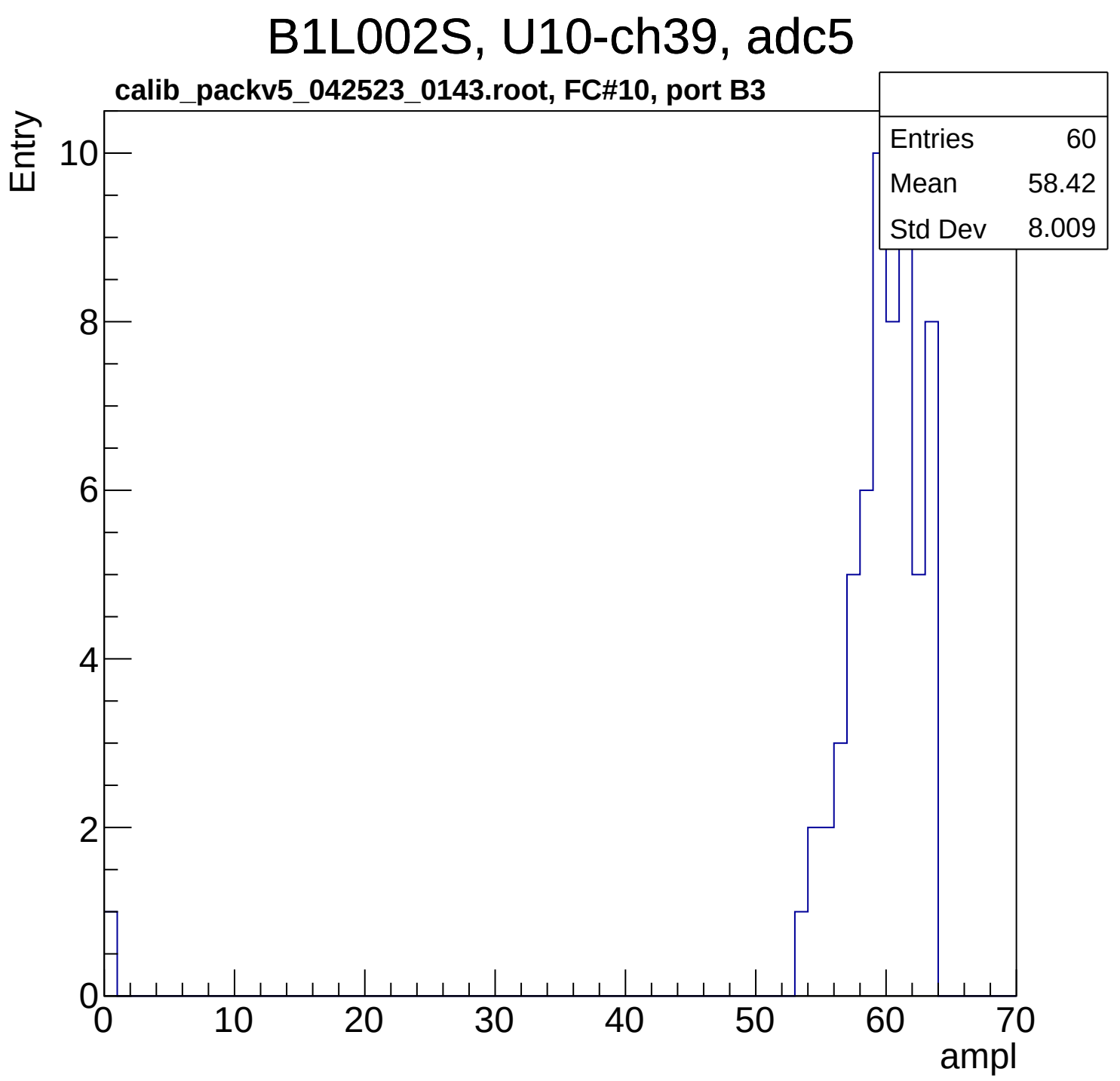
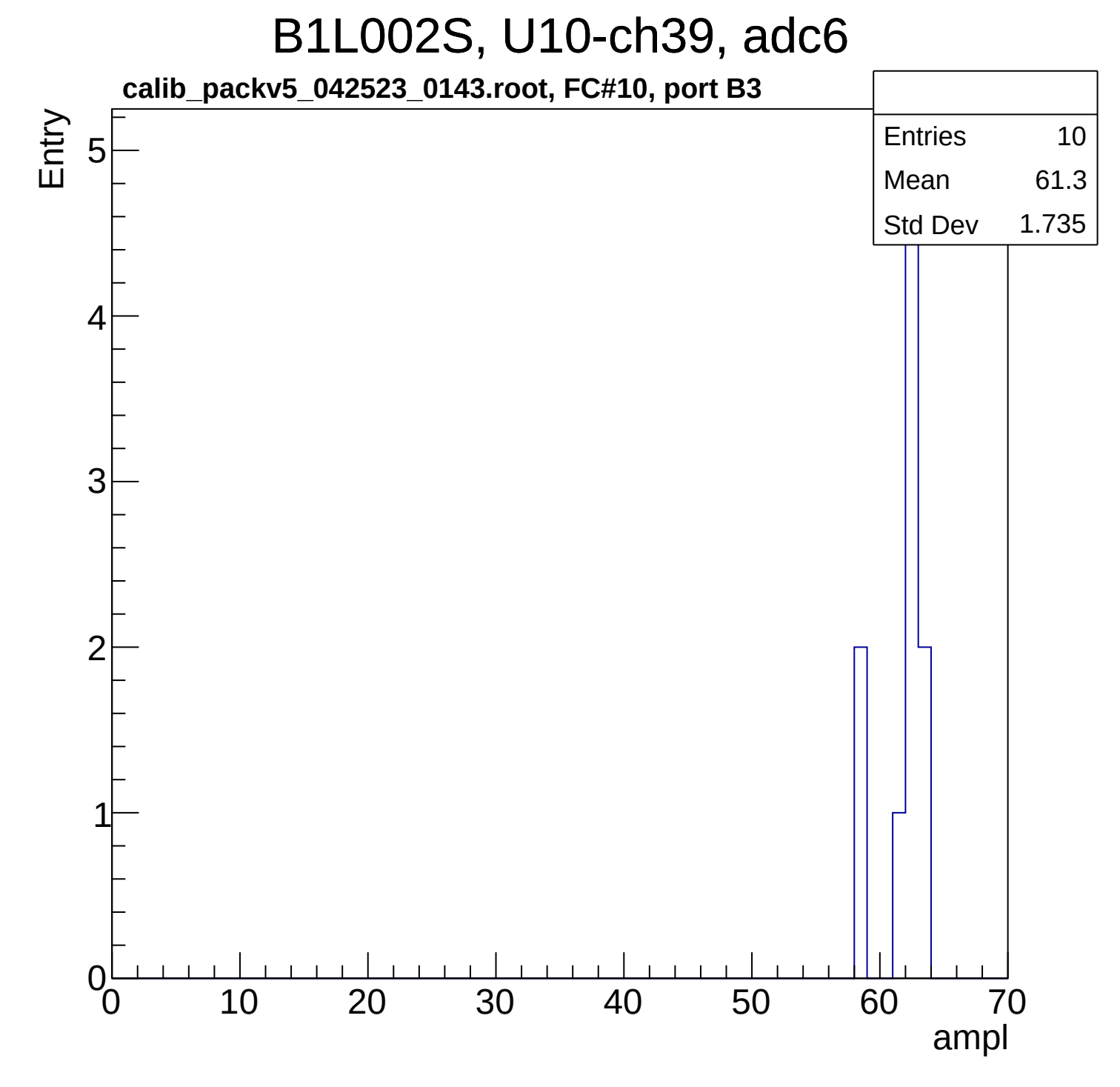
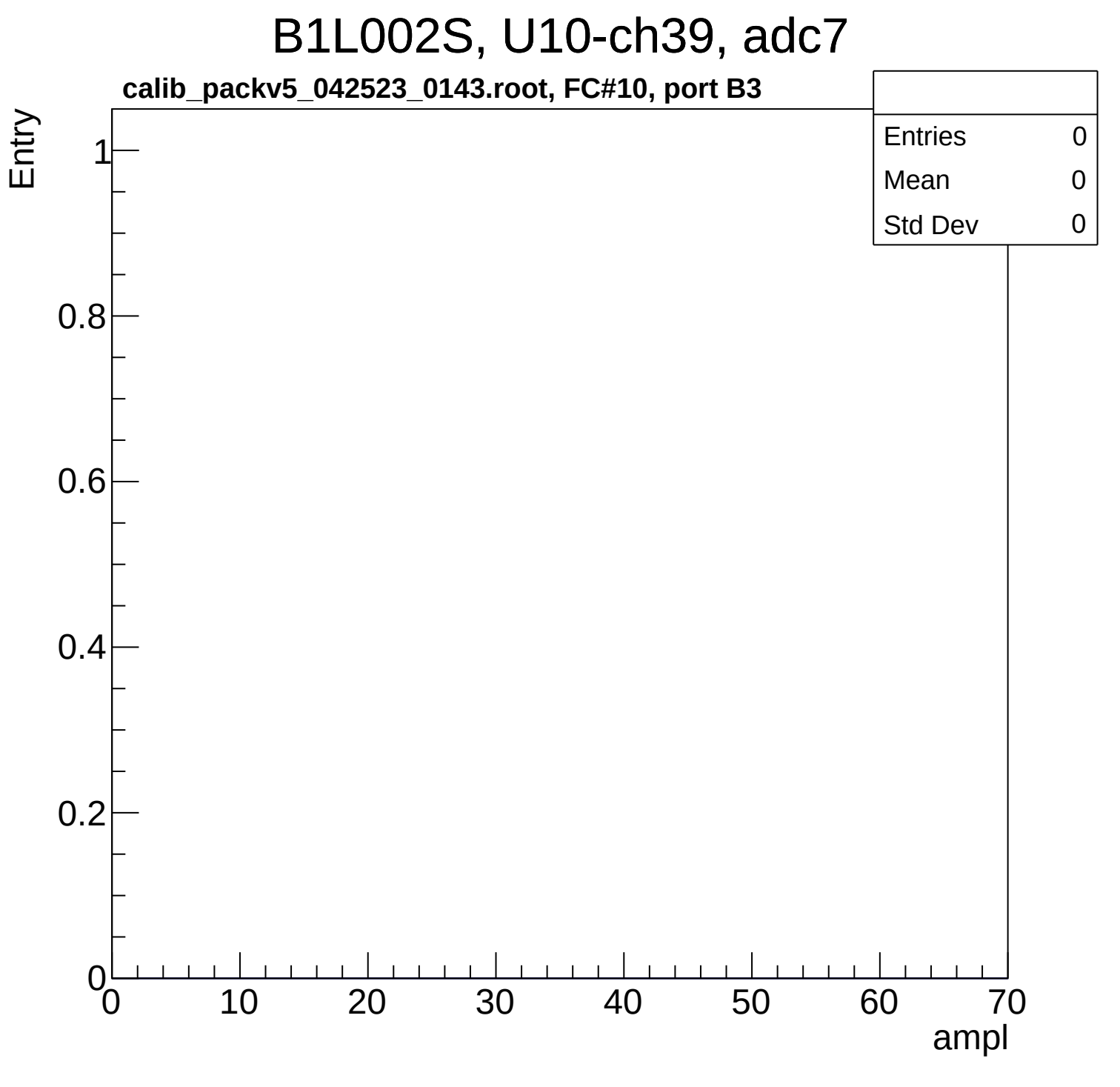
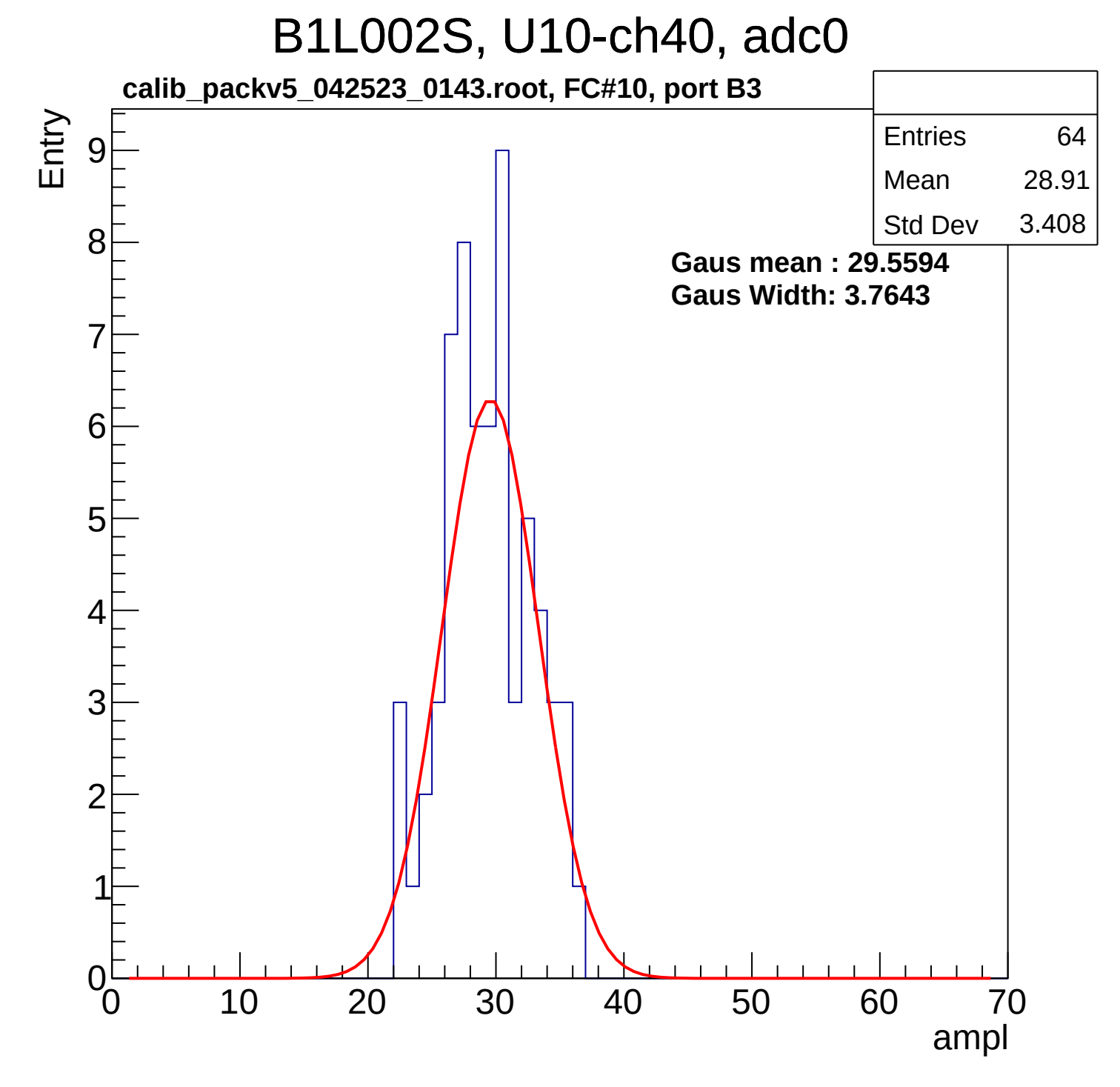
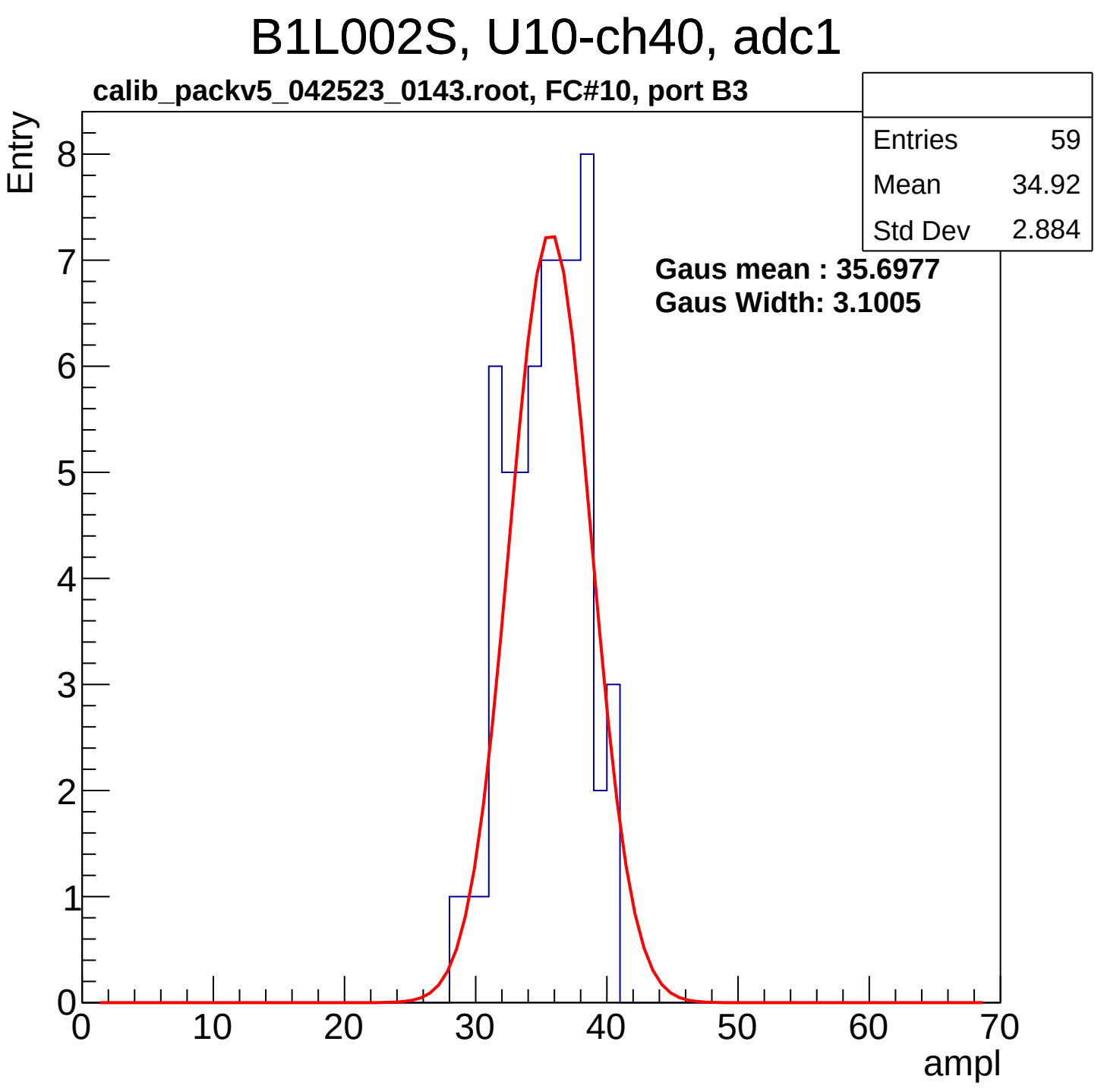
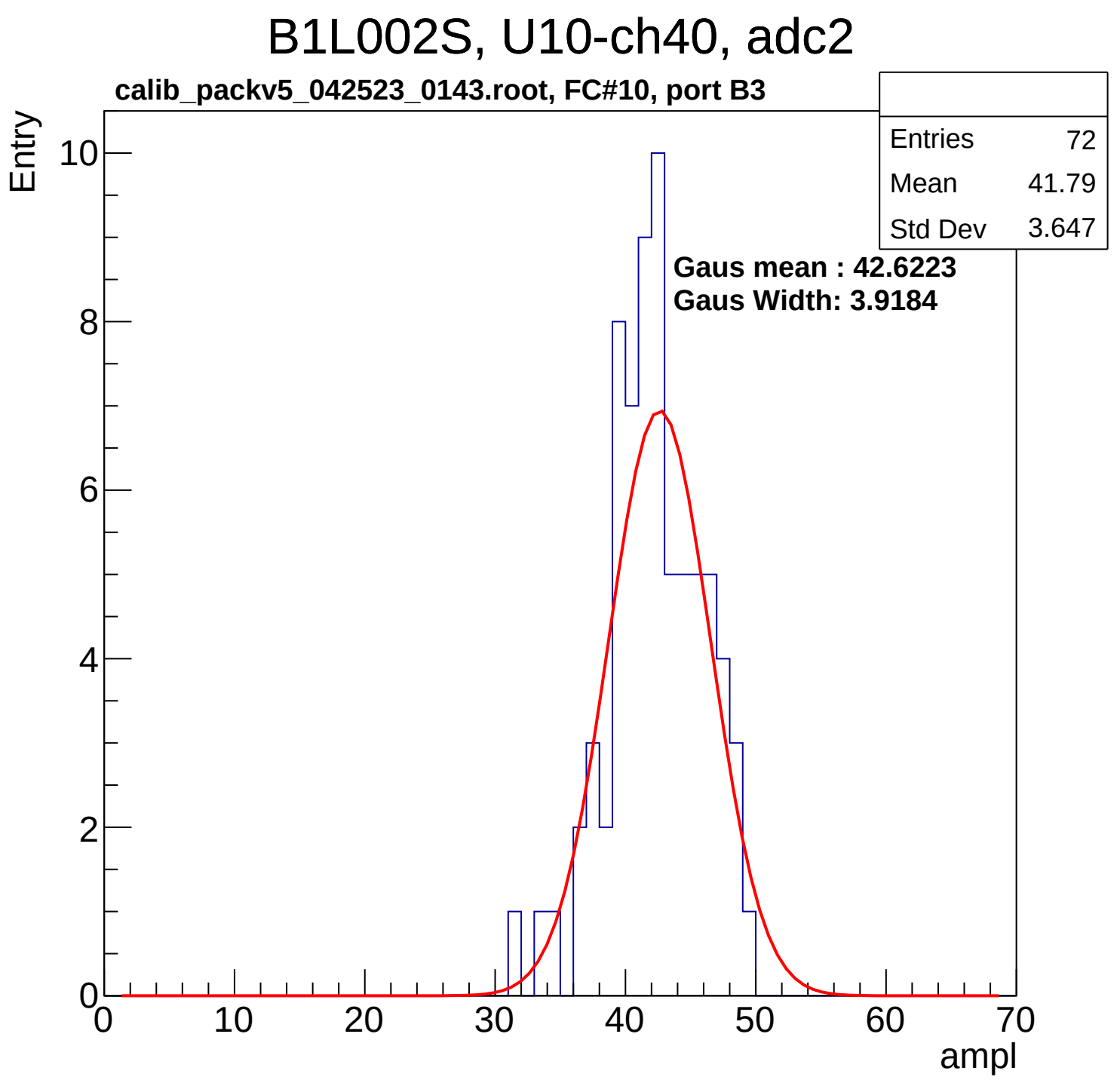

B1L002S, U10-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.42
Std Dev 8.009
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

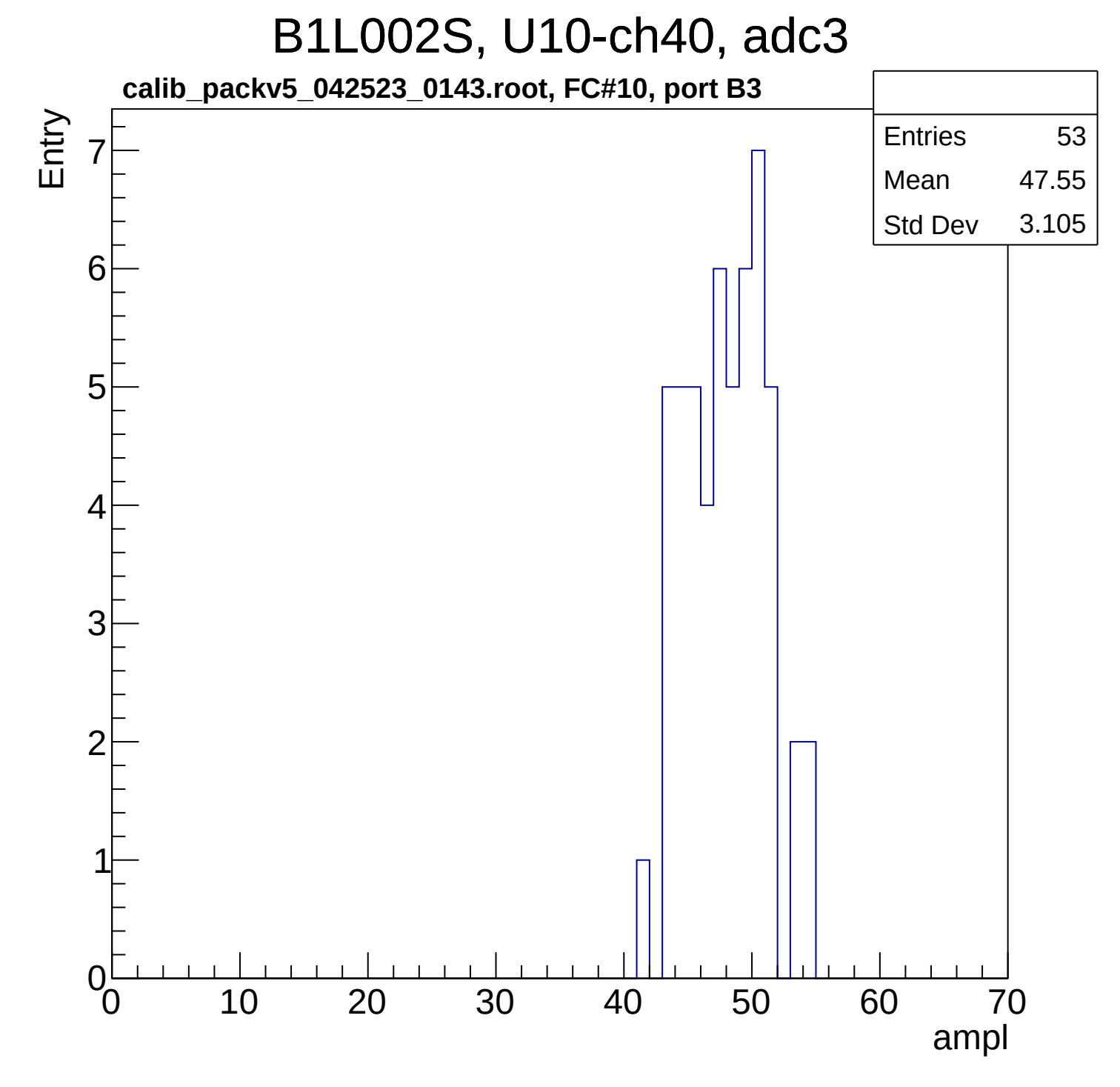

B1L002S, U10-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.3
Std Dev 1.735
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

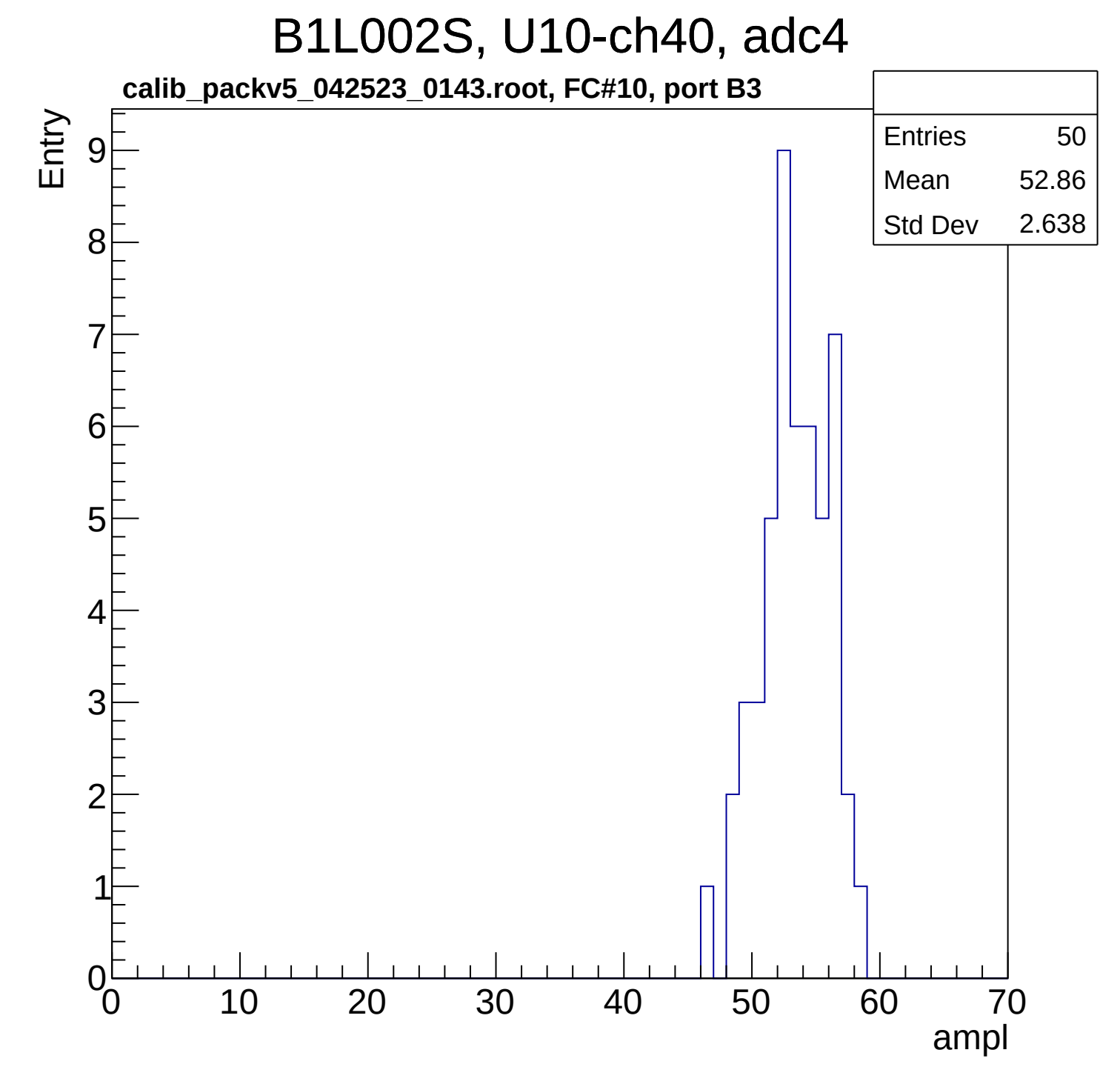

B1L002S, U10-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

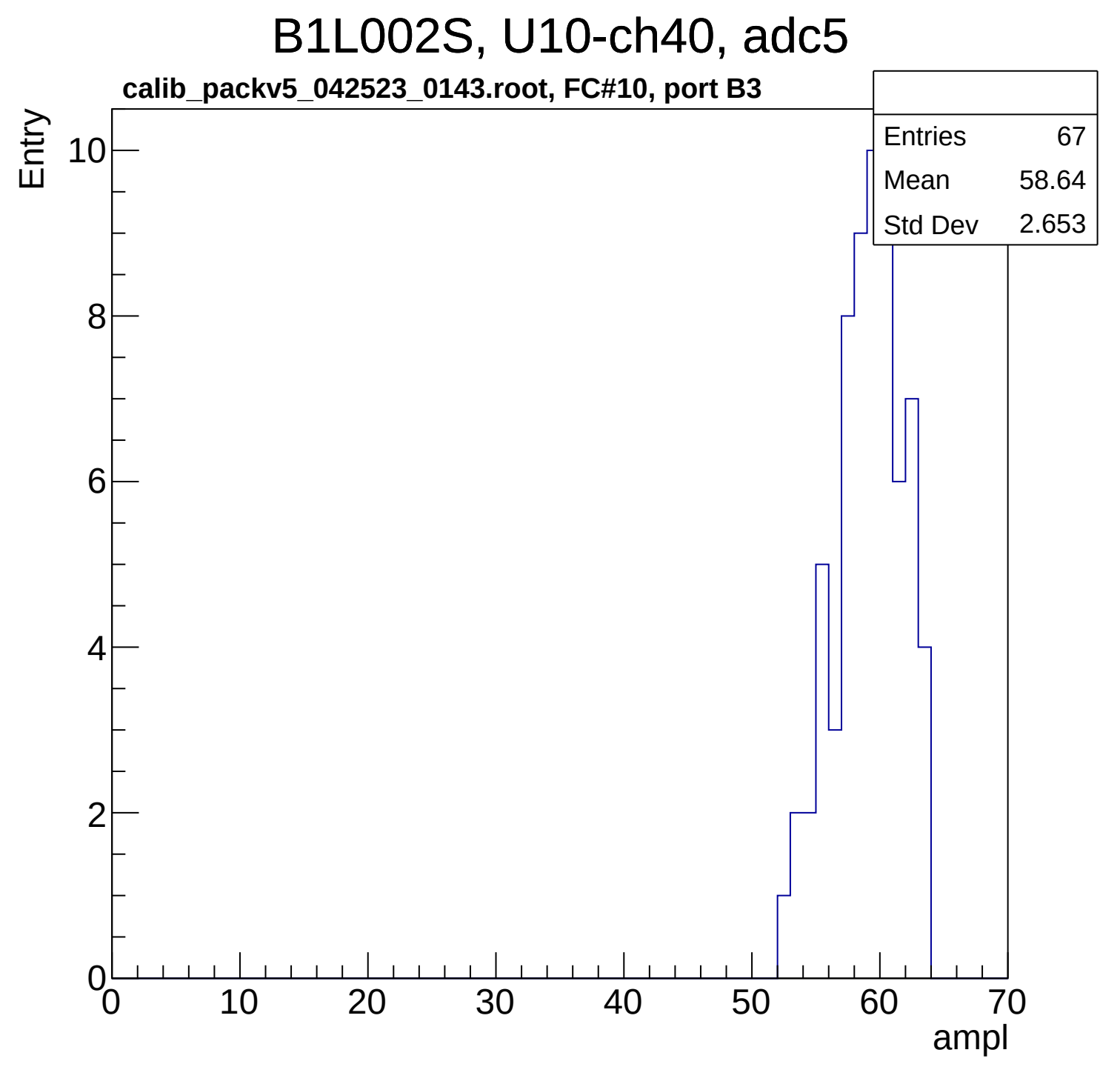

B1L002S, U10-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.91
Std Dev 3.408
Gaus mean : 29.5594
Gaus Width: 3.7643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

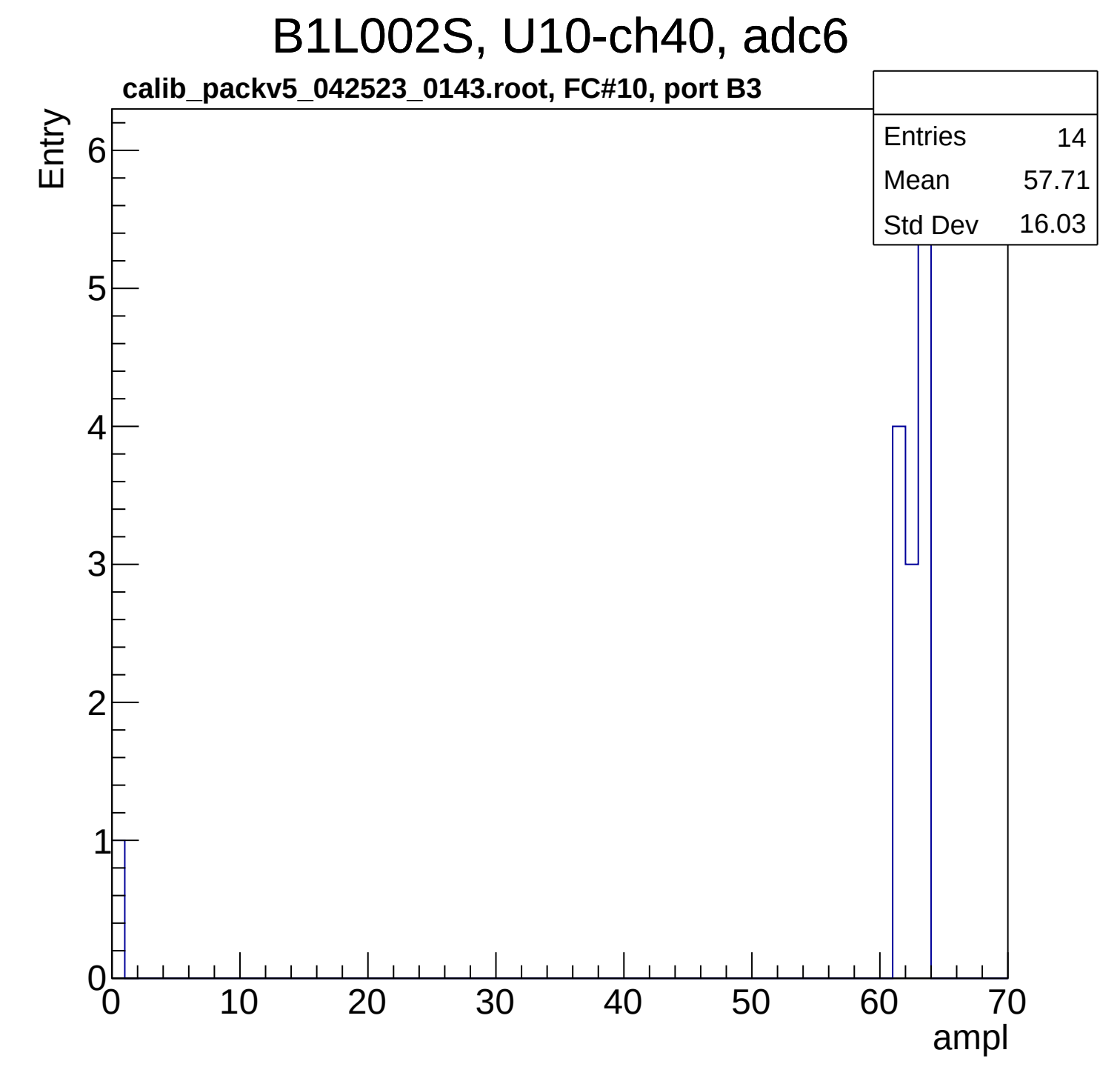

B1L002S, U10-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.92
Std Dev 2.884
Gaus mean : 35.6977
Gaus Width: 3.1005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

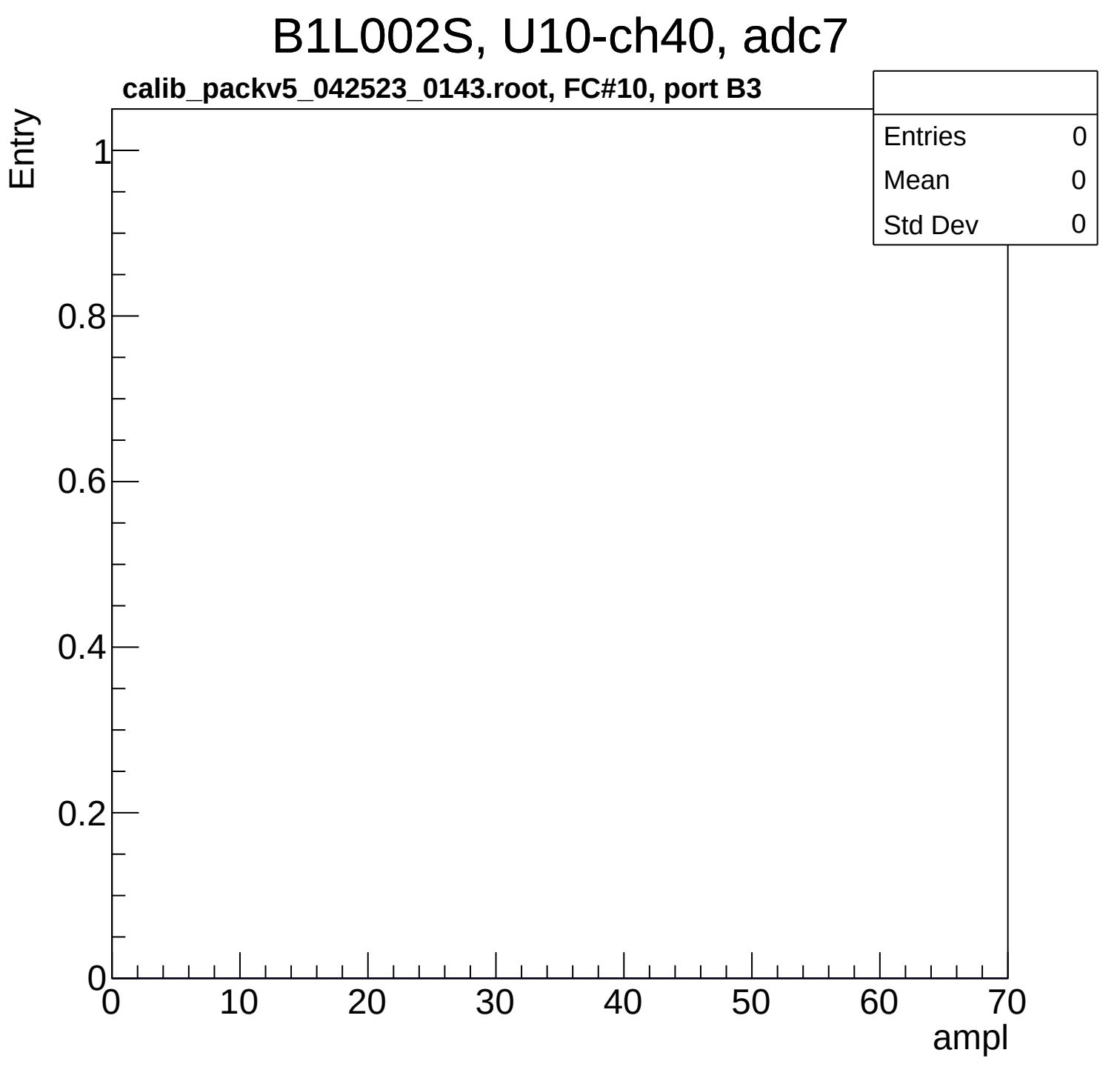

B1L002S, U10-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.79
Std Dev 3.647
Gaus mean : 42.6223
Gaus Width: 3.9184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

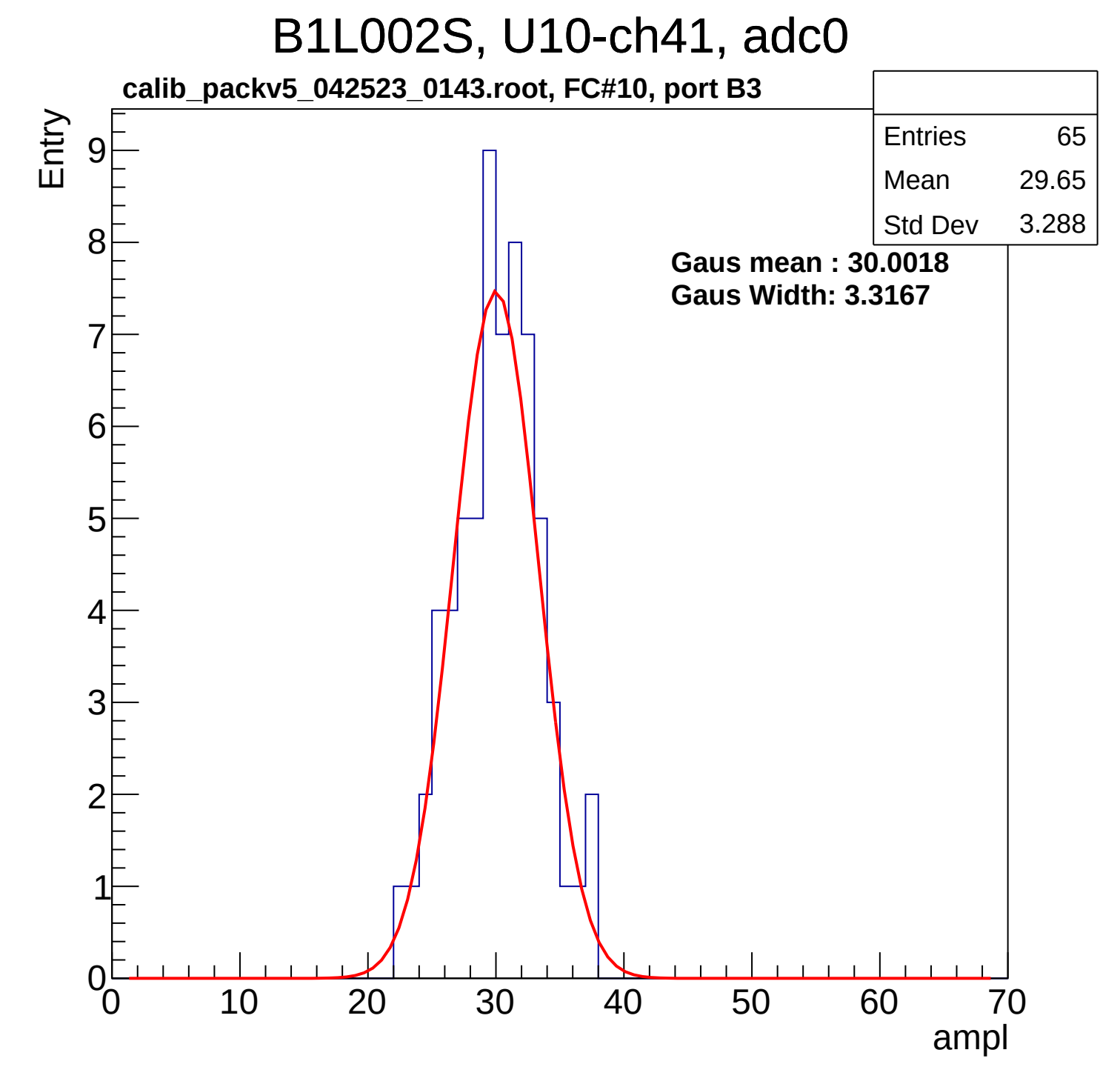

B1L002S, U10-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.55
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.86
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.64
Std Dev 2.653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.71
Std Dev 16.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.65
Std Dev 3.288
Gaus mean : 30.0018
Gaus Width: 3.3167
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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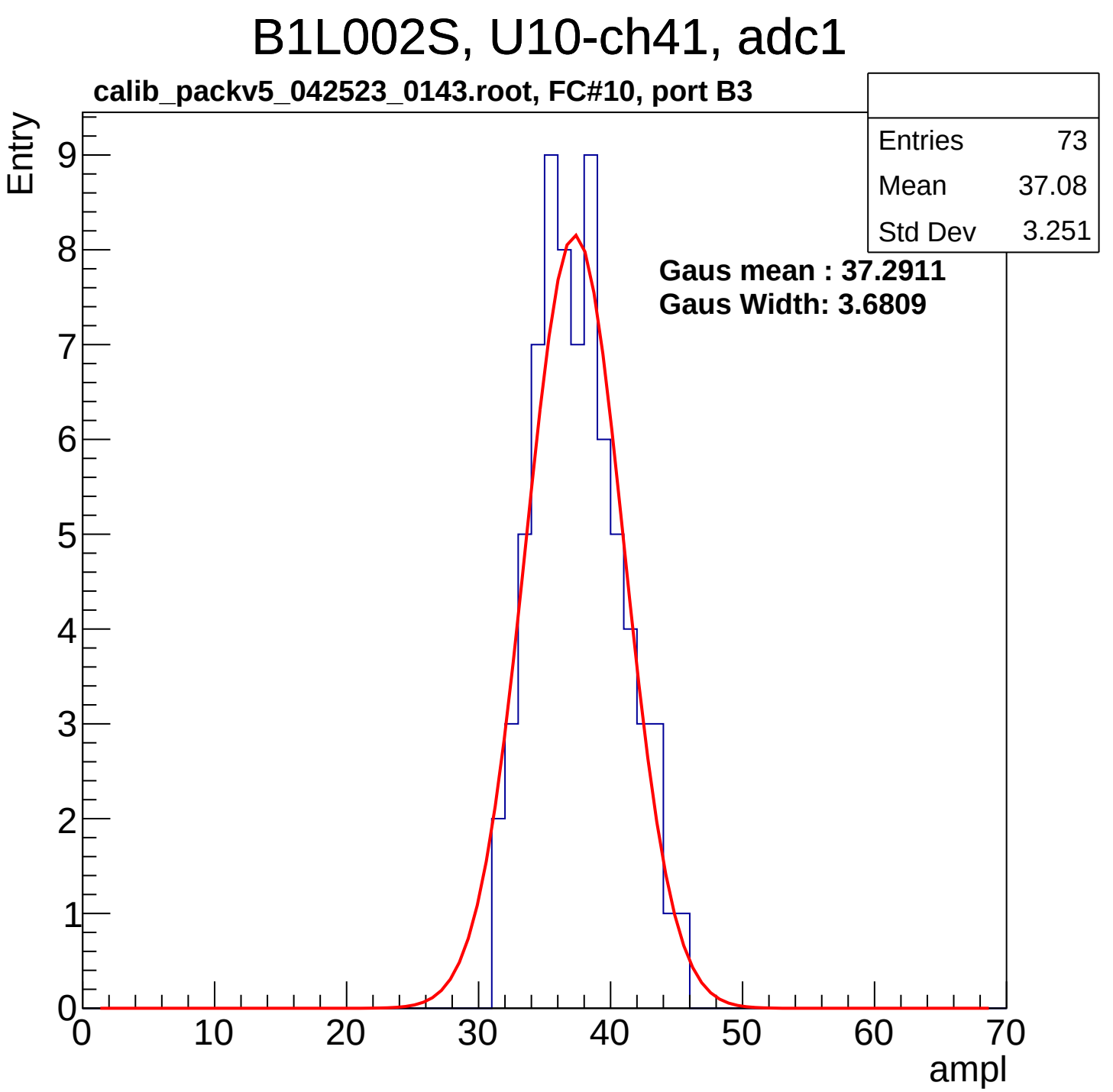
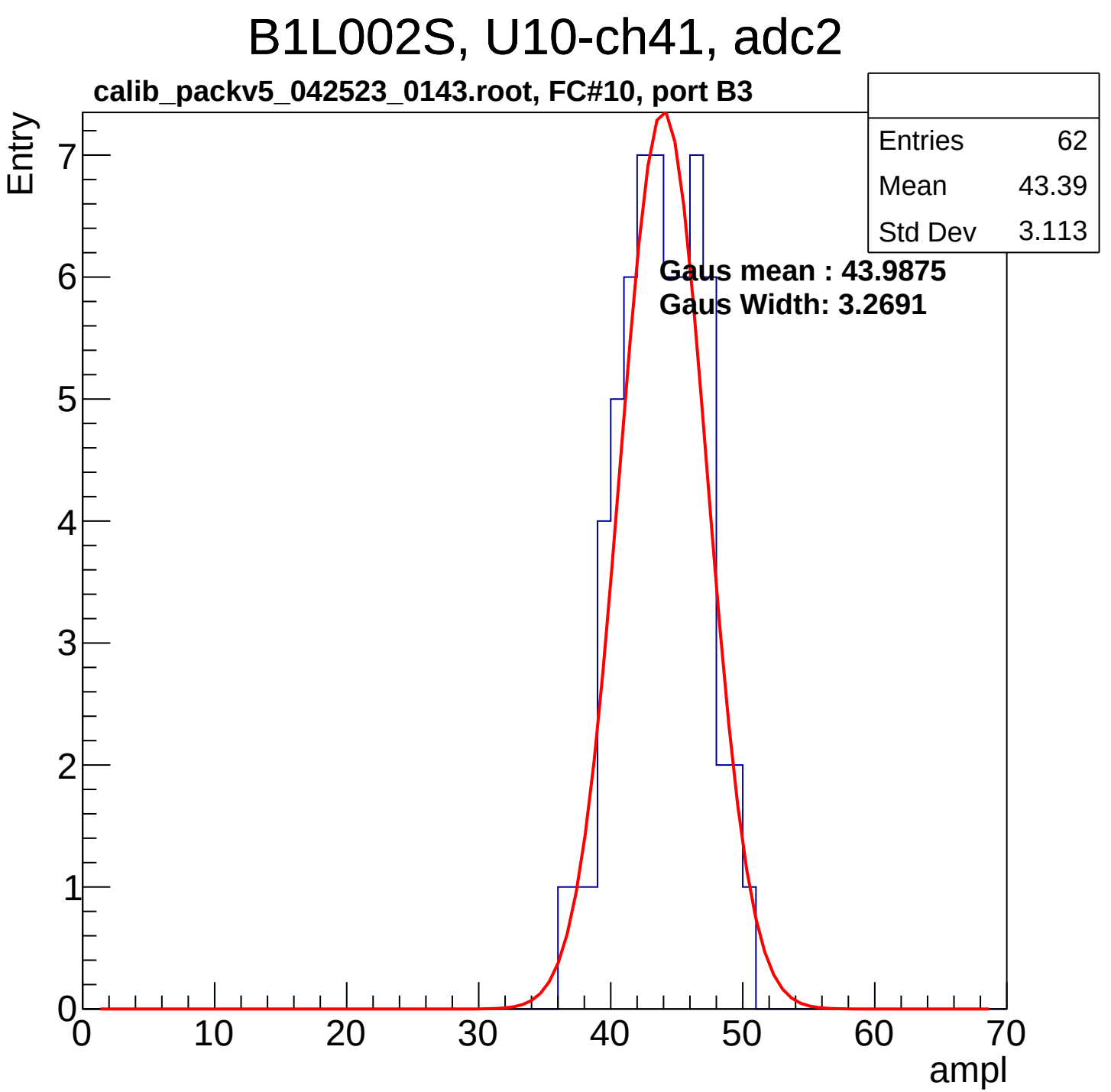
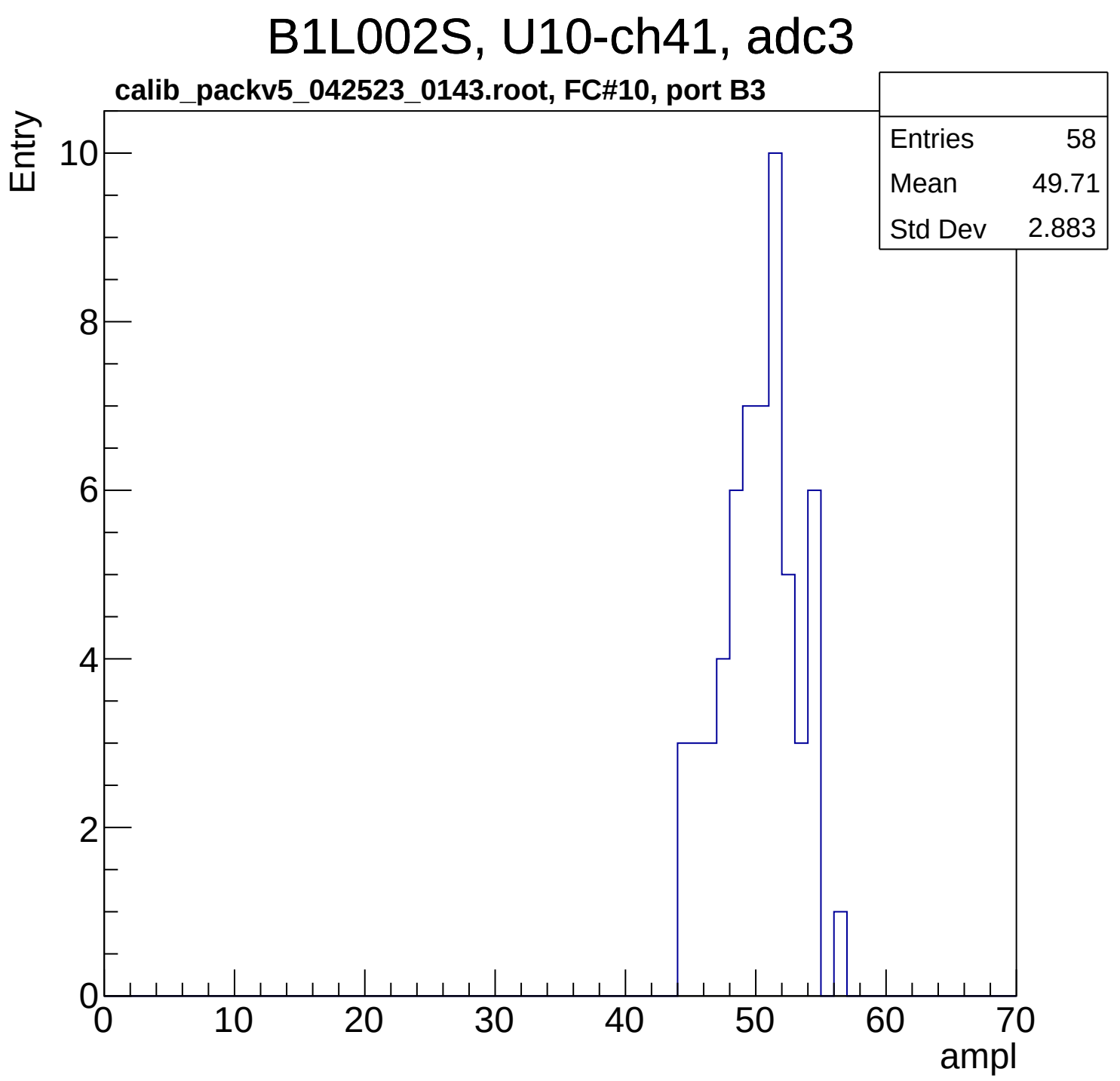
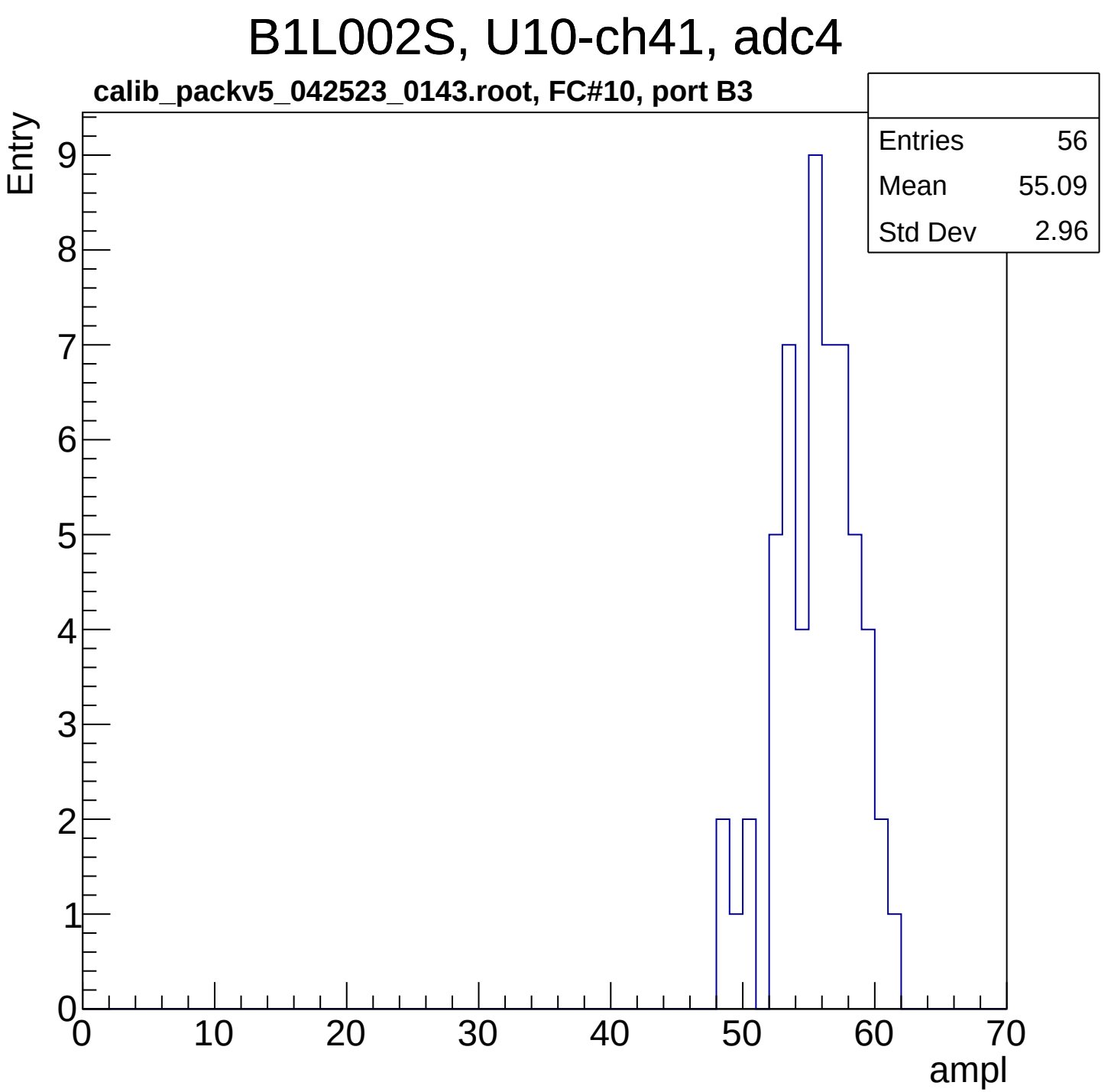

B1L002S, U10-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 37.08
Std Dev 3.251
Gaus mean : 37.2911
Gaus Width: 3.6809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 43.39
Std Dev 3.113
Gaus mean : 43.9875
Gaus Width: 3.2691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 49.71
Std Dev 2.883
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.09
Std Dev 2.96
Entry
ampl
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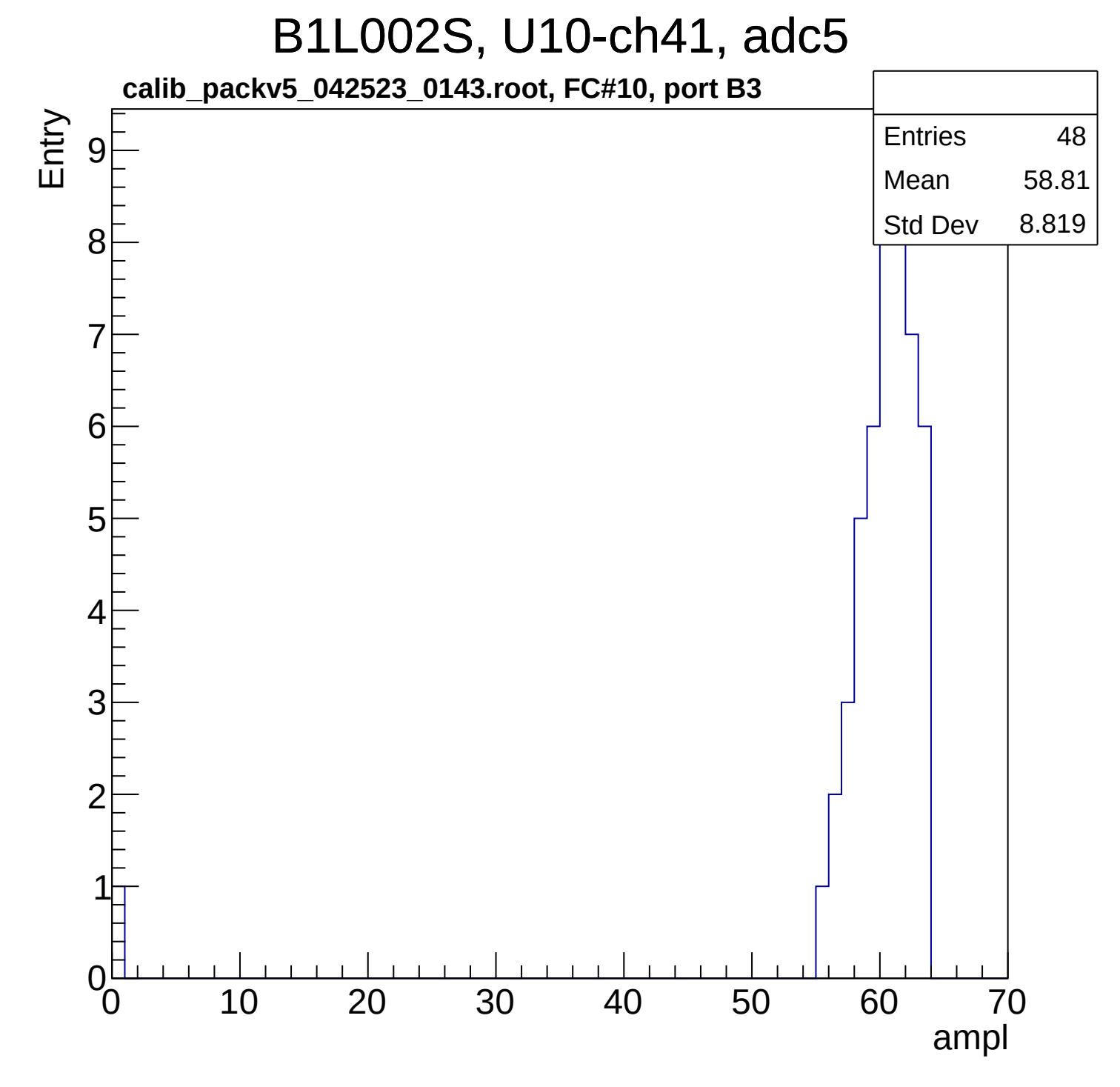
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.81
Std Dev 8.819
Entry
ampl
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3
4
5
6
7
8
9
0
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40
50
60
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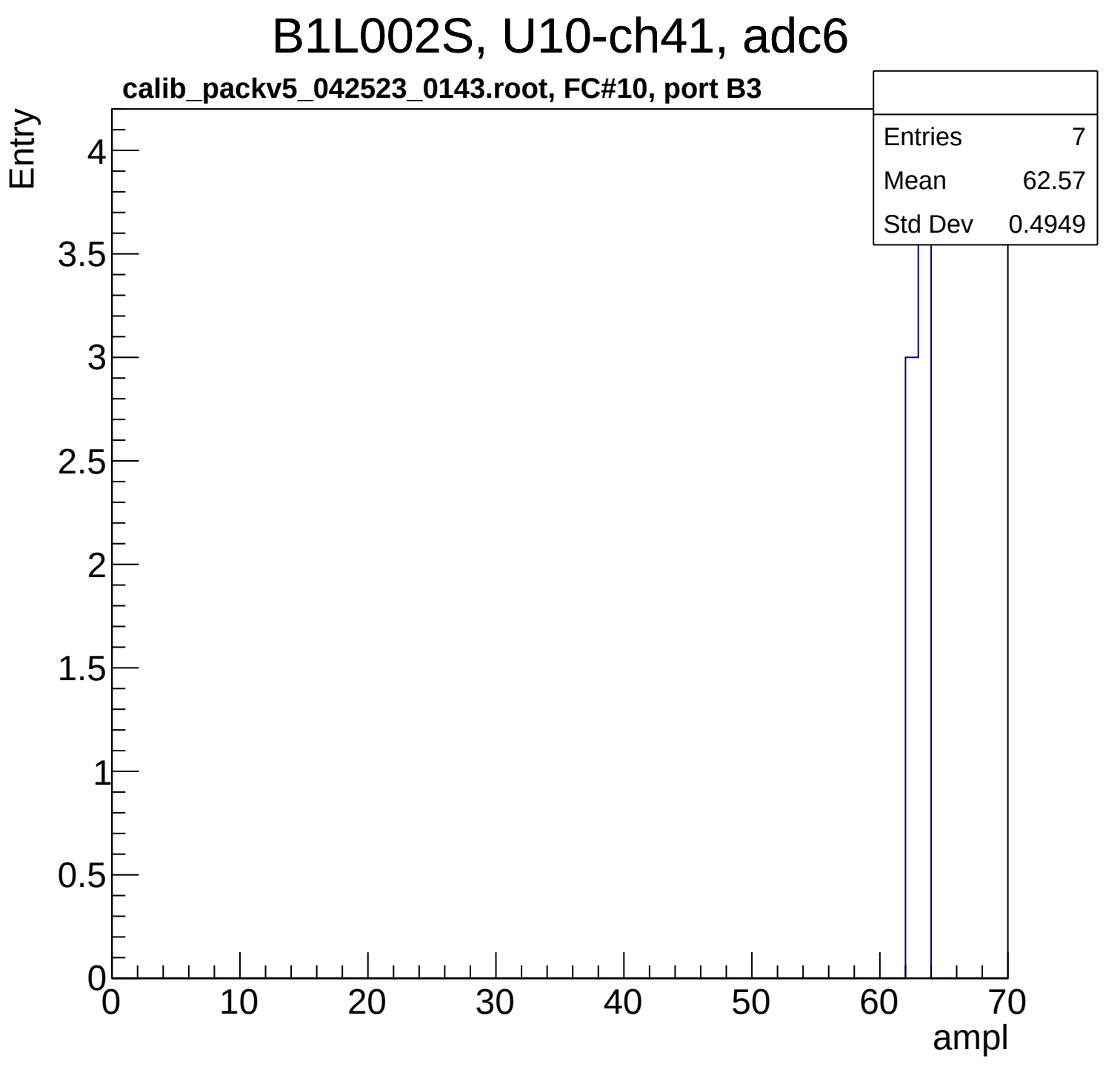
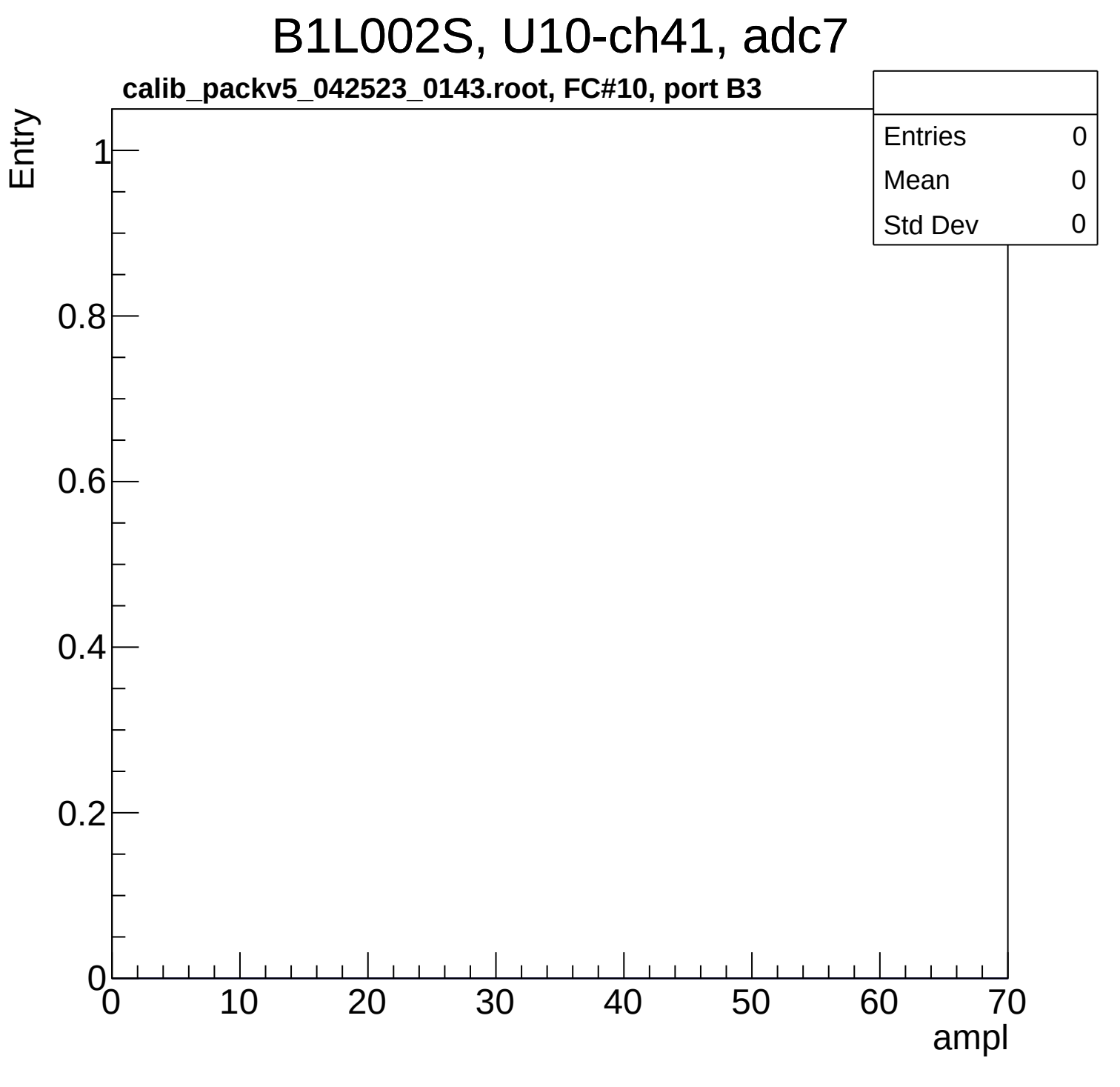
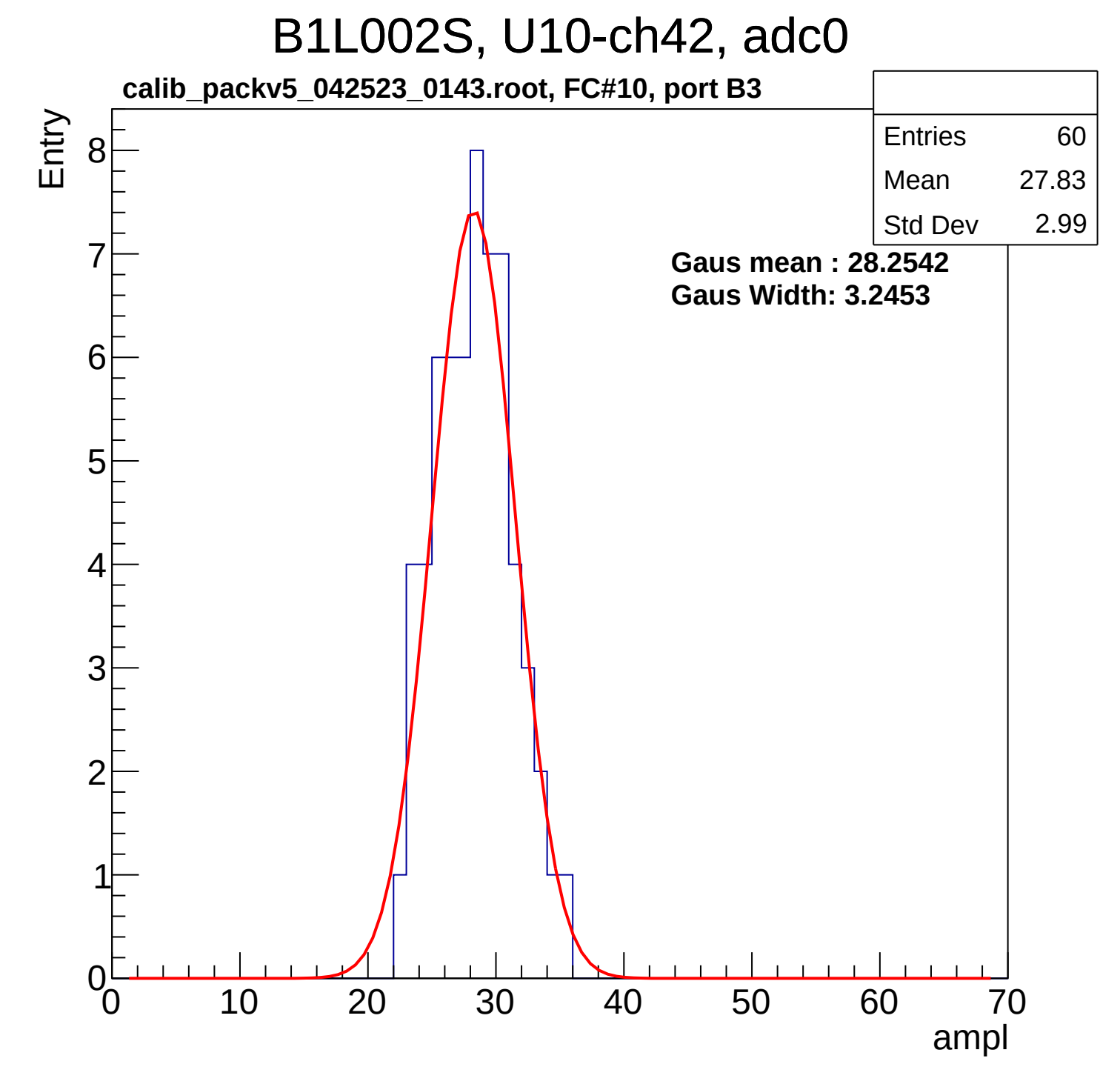
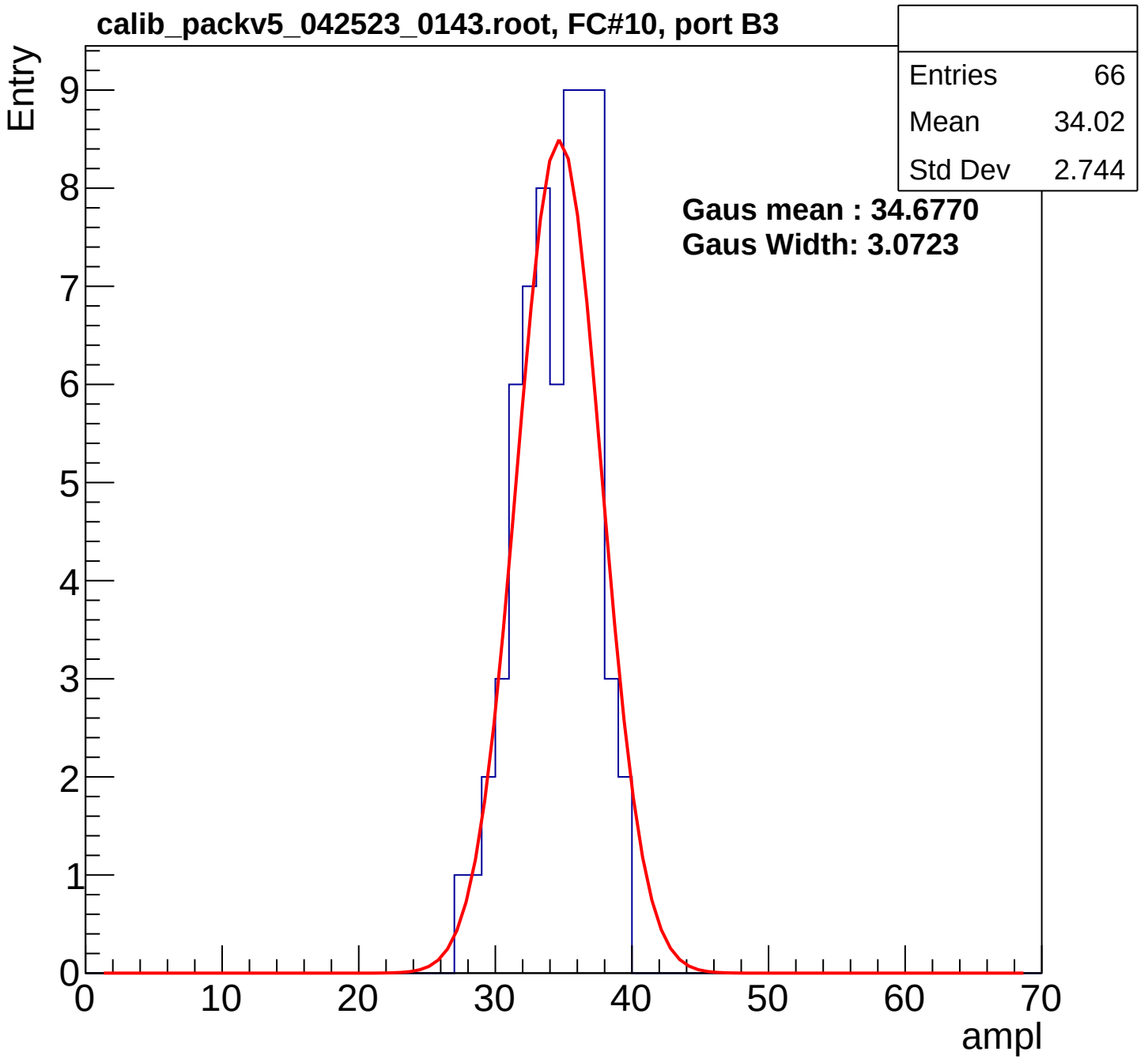

B1L002S, U10-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.83
Std Dev 2.99
Gaus mean : 28.2542
Gaus Width: 3.2453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.02
Std Dev 2.744
Gaus mean : 34.6770
Gaus Width: 3.0723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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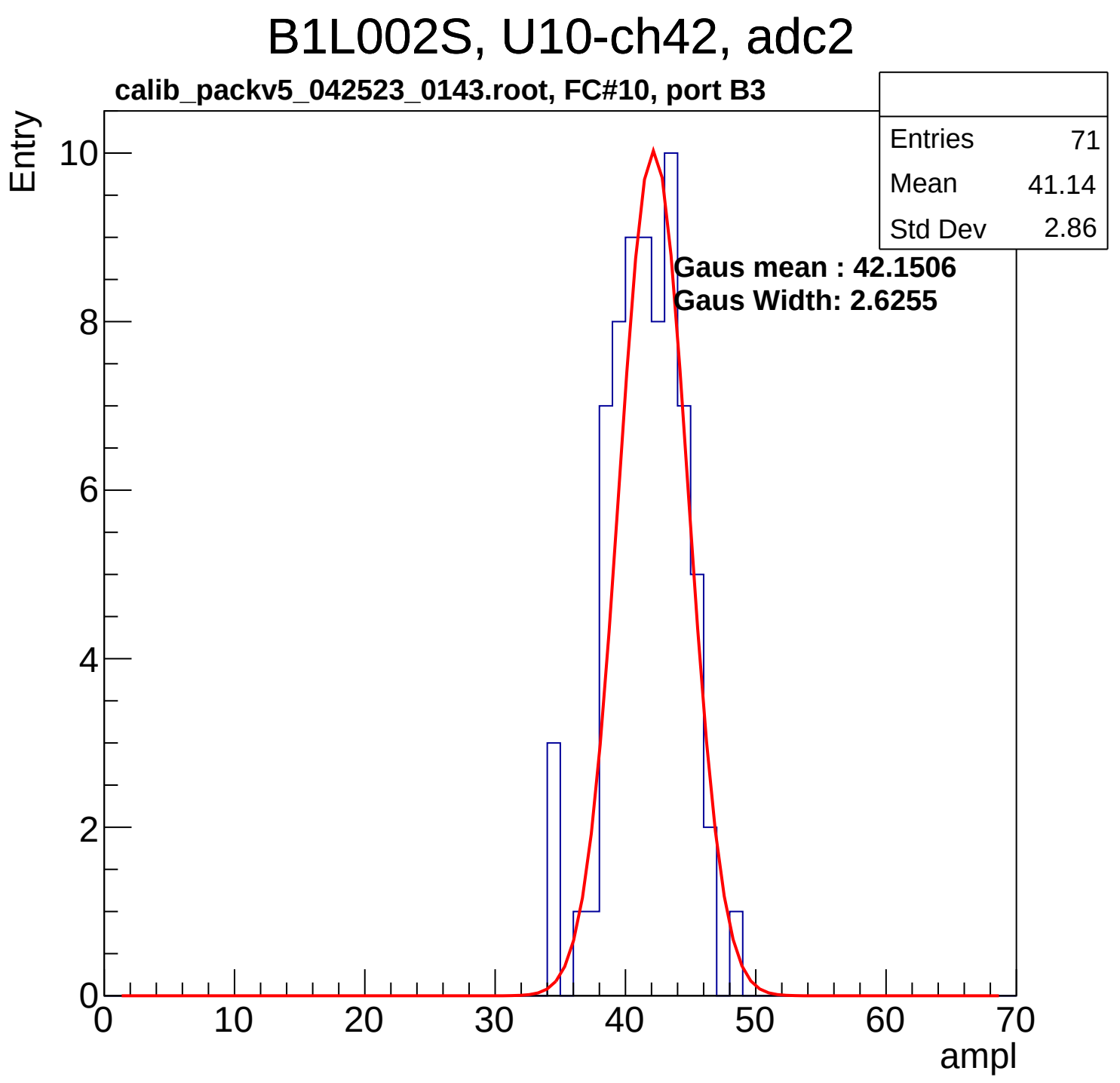
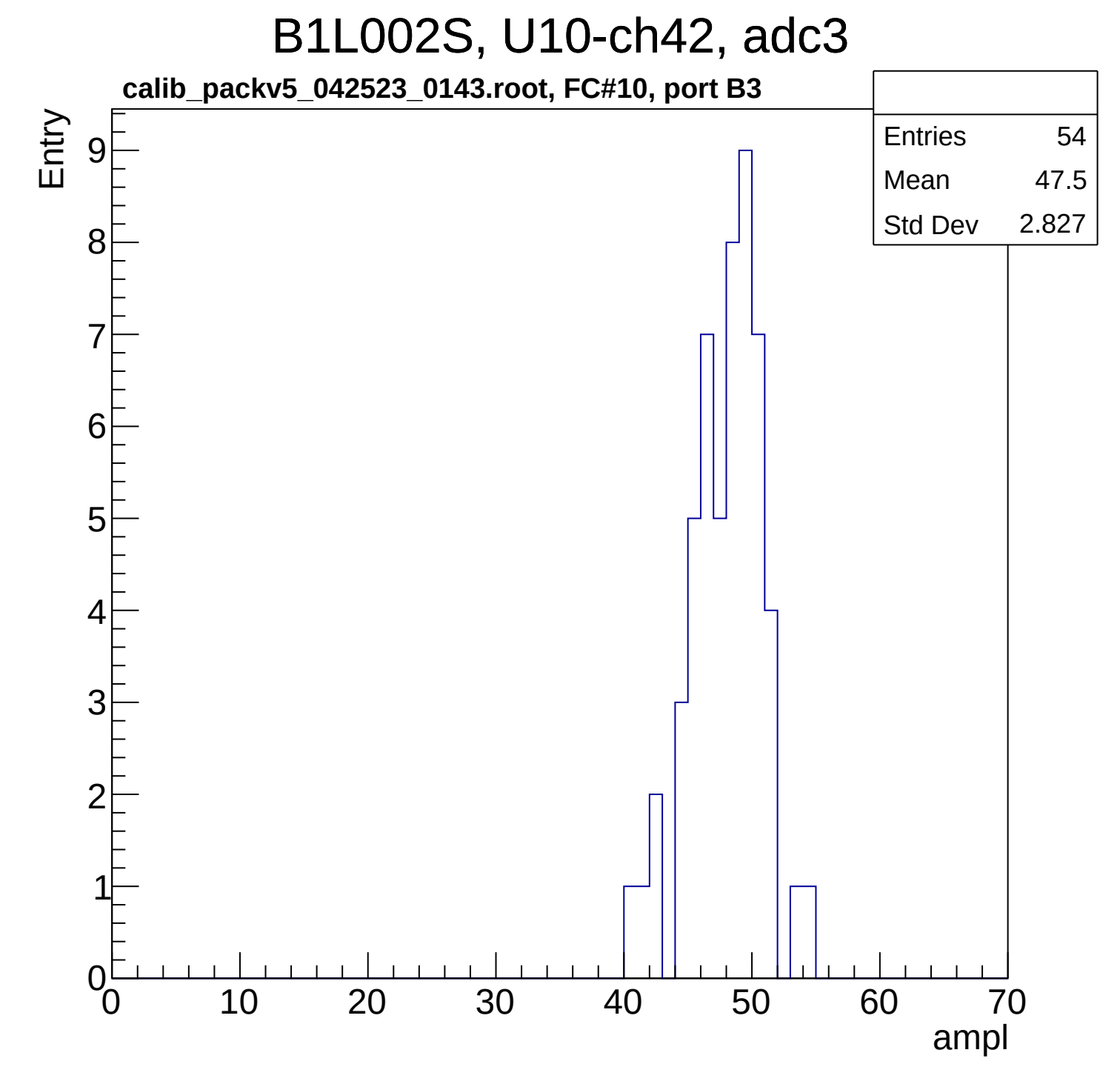

B1L002S, U10-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.14
Std Dev 2.86
Gaus mean : 42.1506
Gaus Width: 2.6255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.5
Std Dev 2.827
Entry
ampl
0
1
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0
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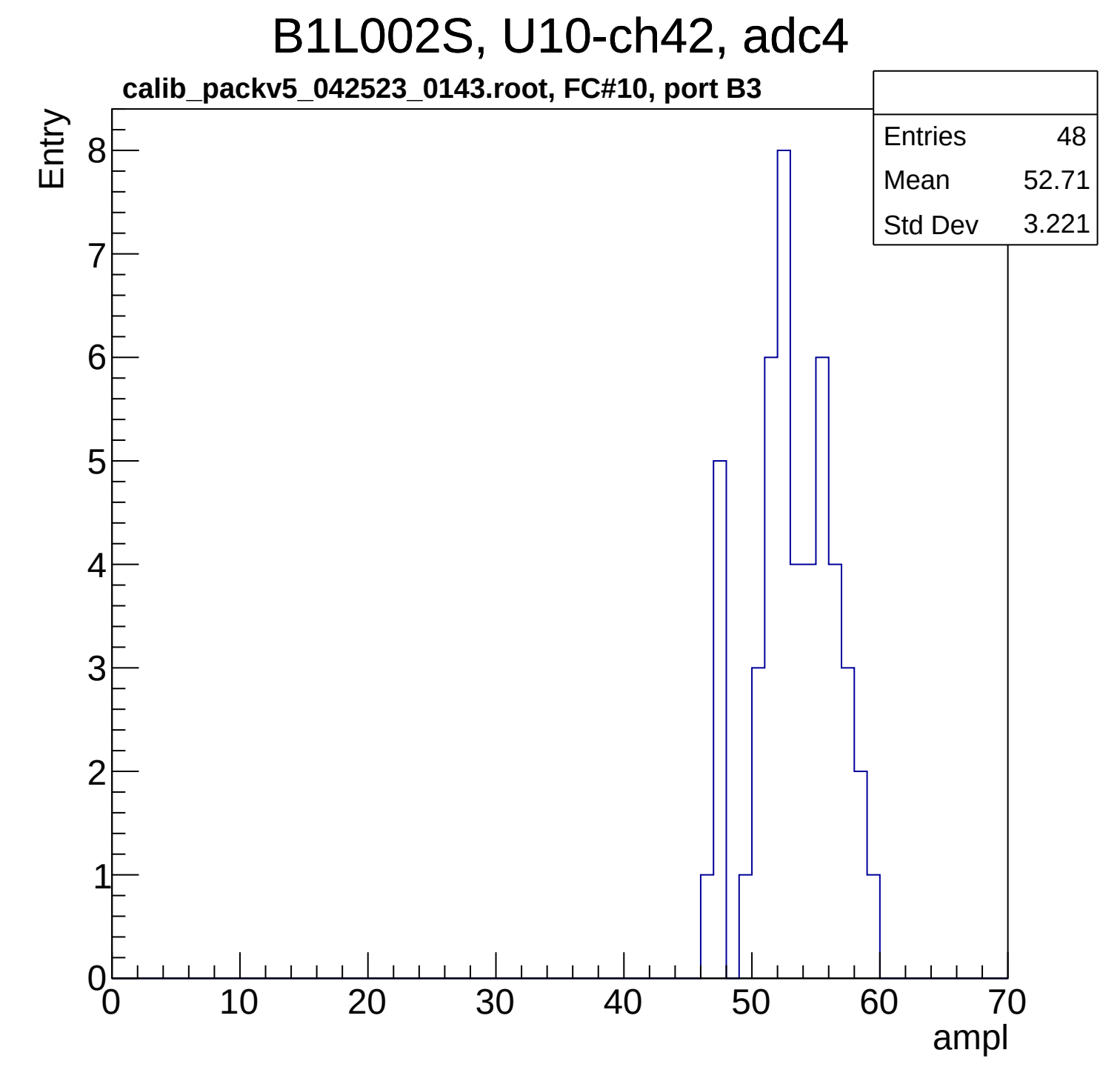
B1L002S, U10-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.71
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
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0
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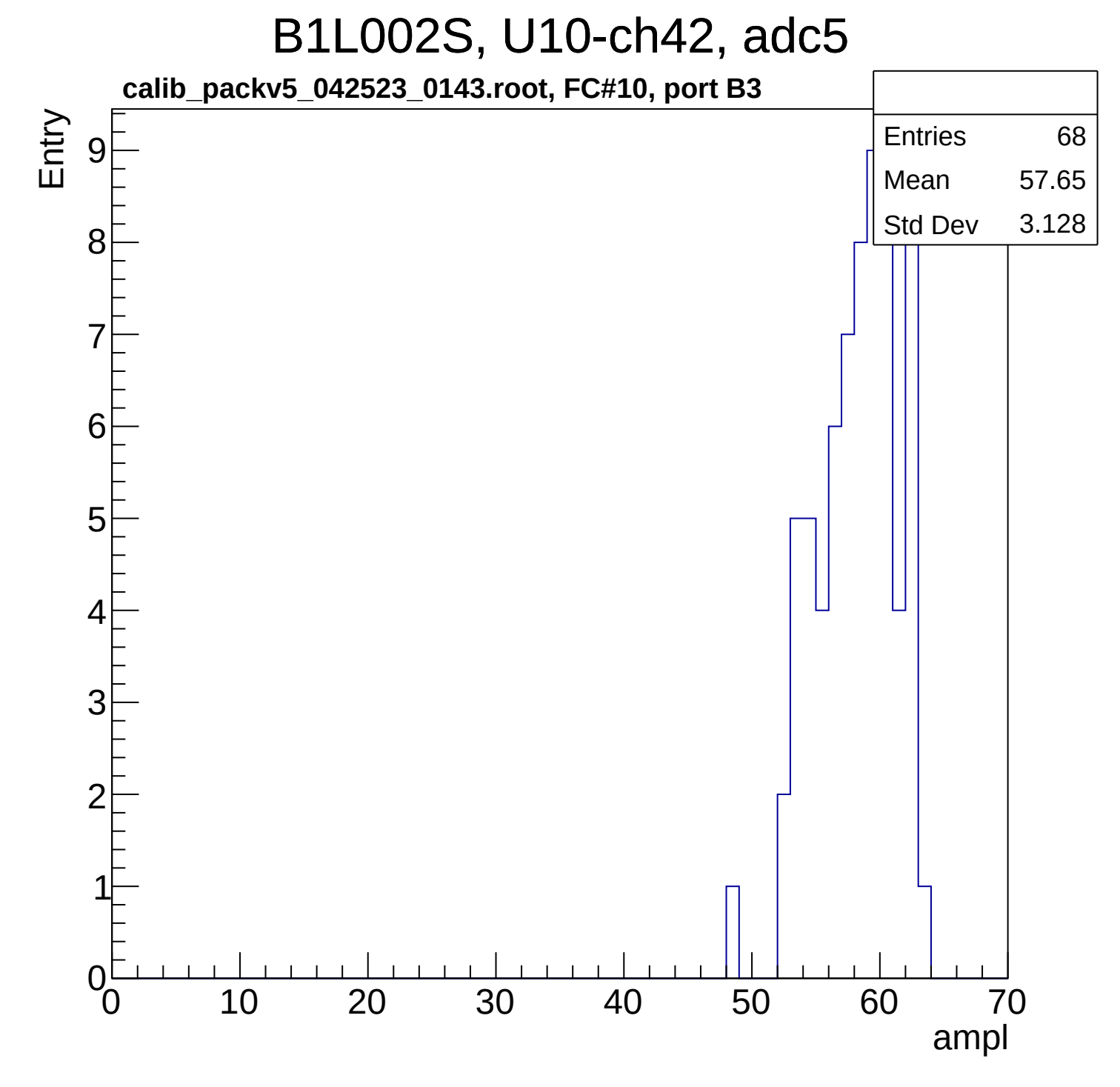
B1L002S, U10-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.65
Std Dev 3.128
Entry
ampl
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2
3
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5
6
7
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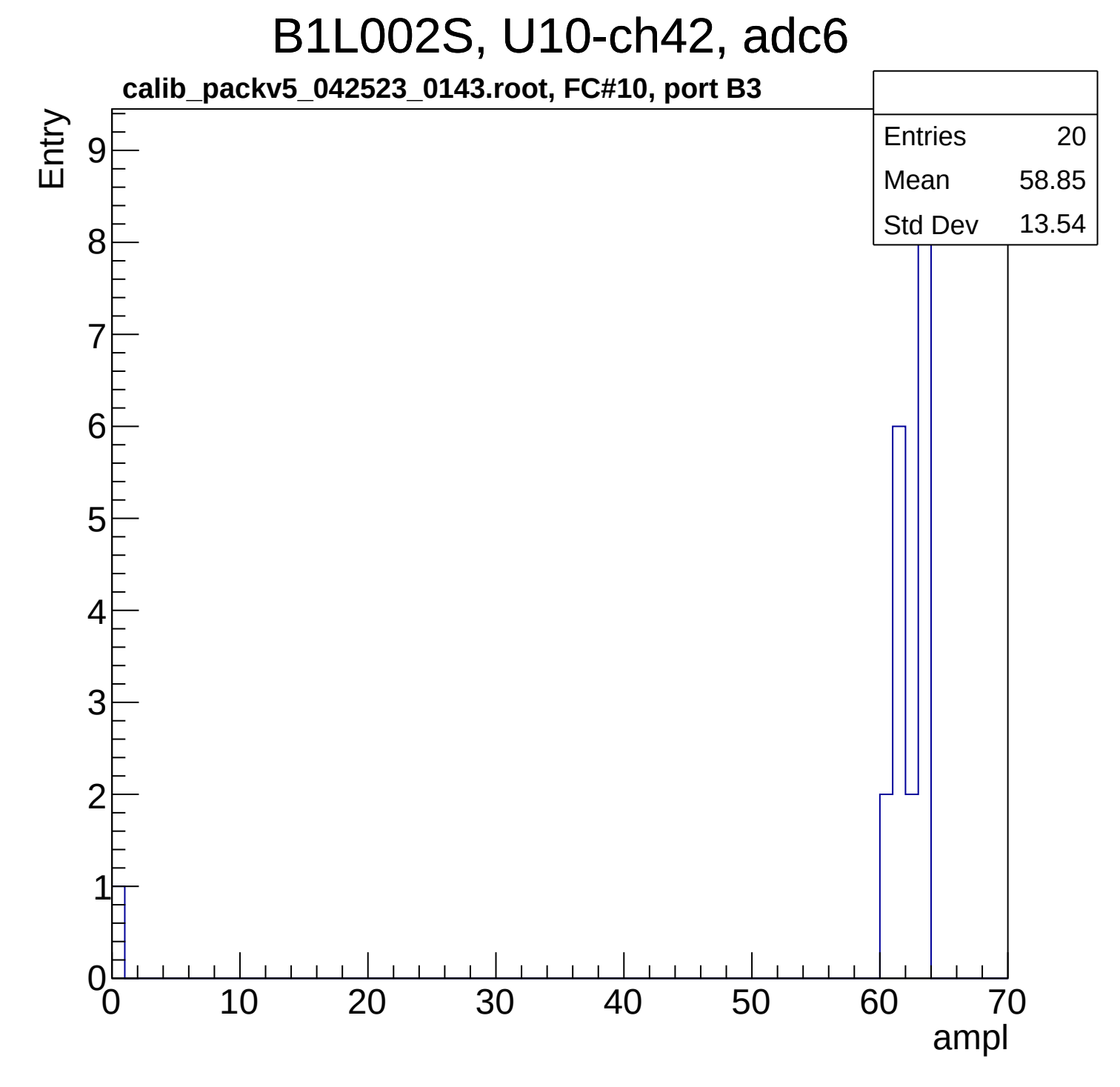
B1L002S, U10-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.85
Std Dev 13.54
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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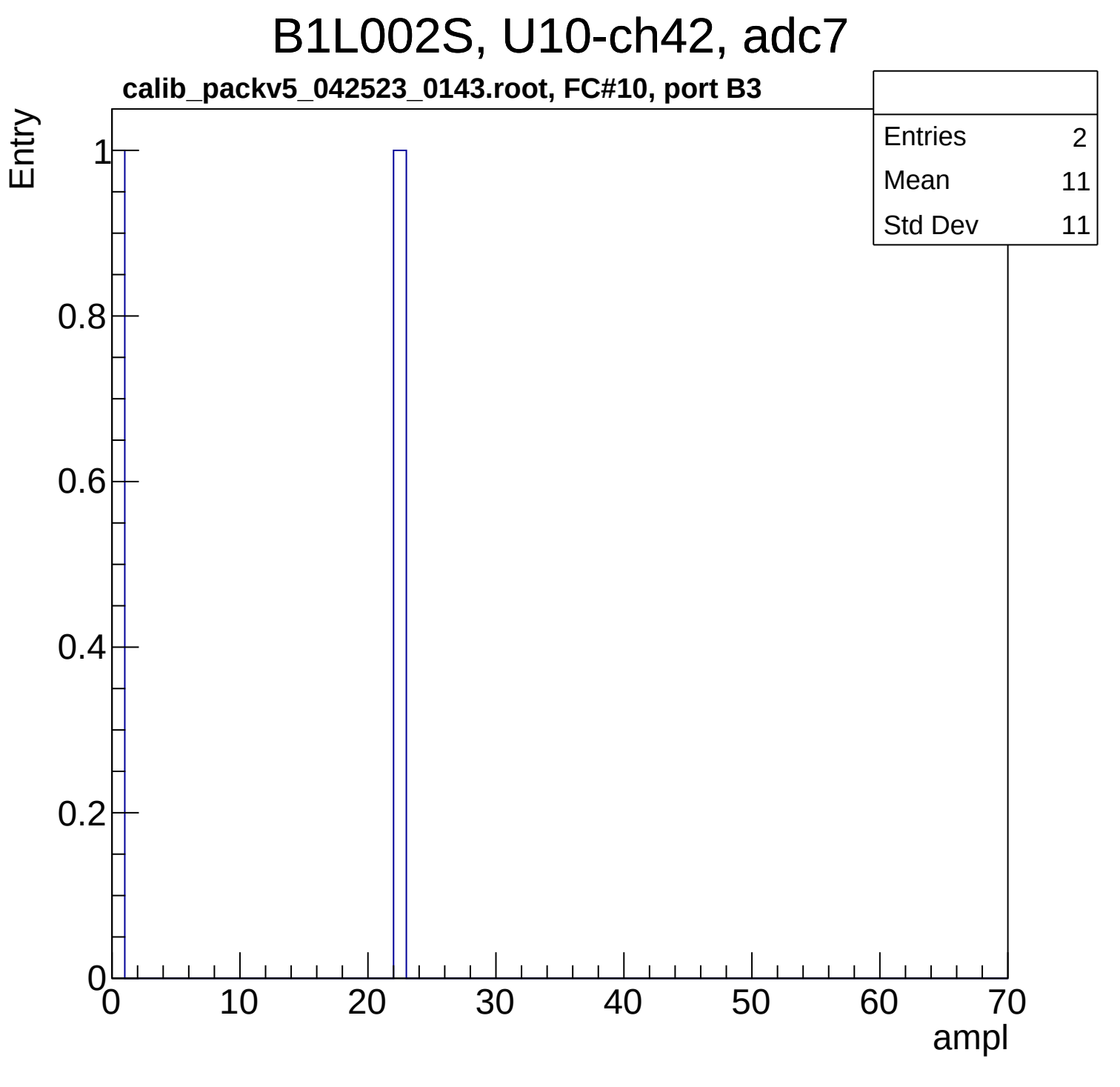
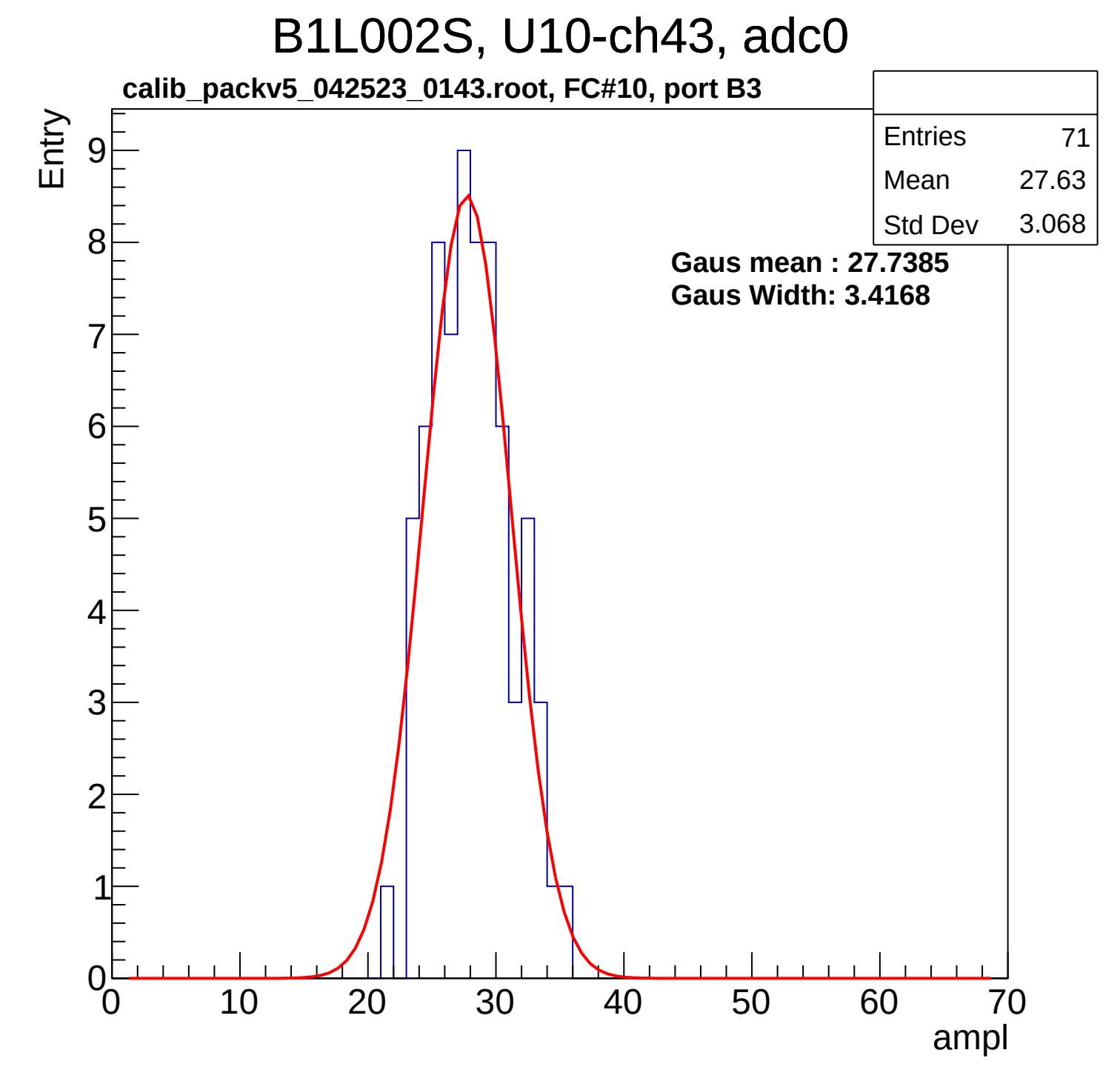

B1L002S, U10-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.63
Std Dev 3.068
Gaus mean : 27.7385
Gaus Width: 3.4168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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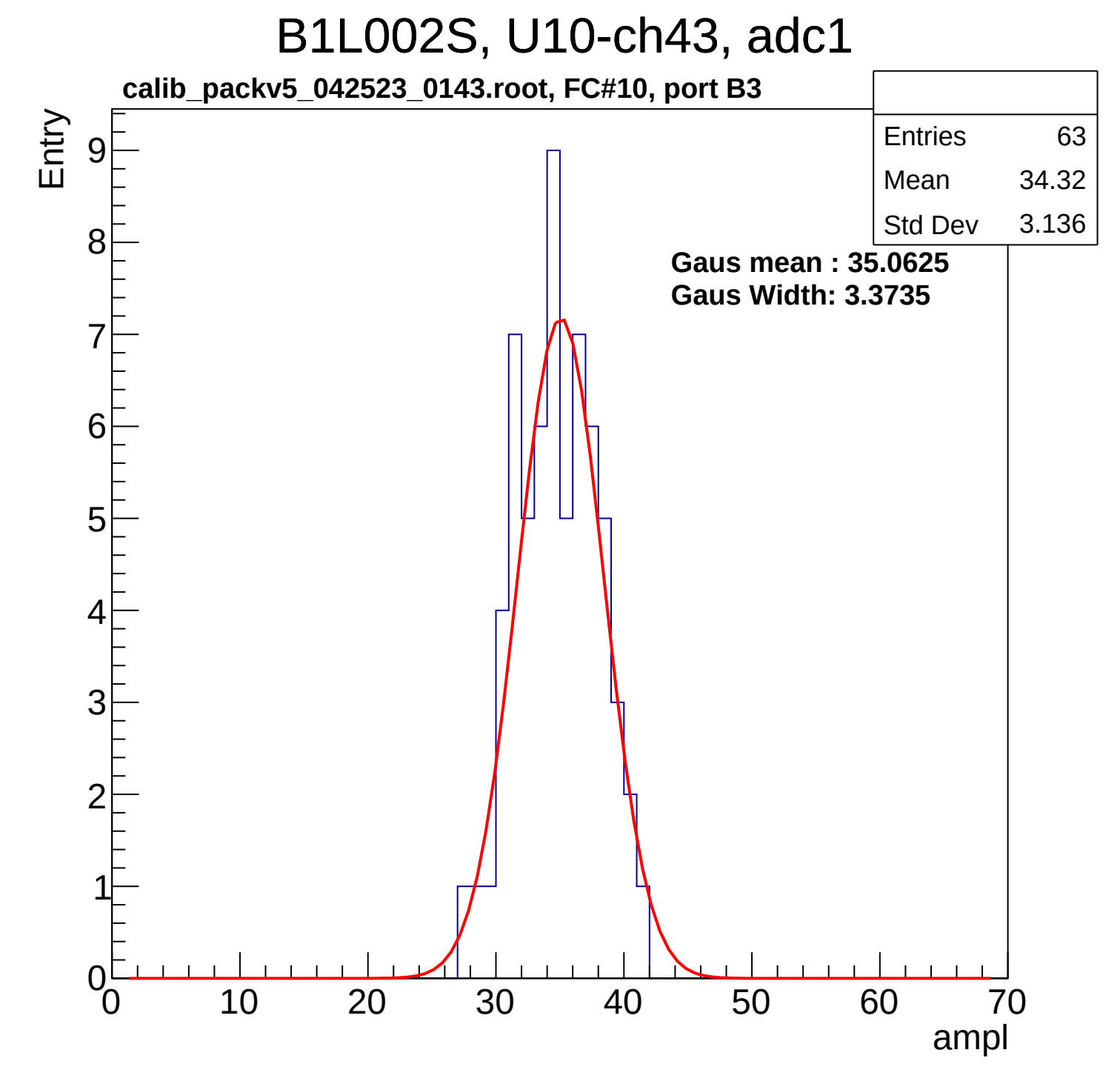
B1L002S, U10-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.32
Std Dev 3.136
Gaus mean : 35.0625
Gaus Width: 3.3735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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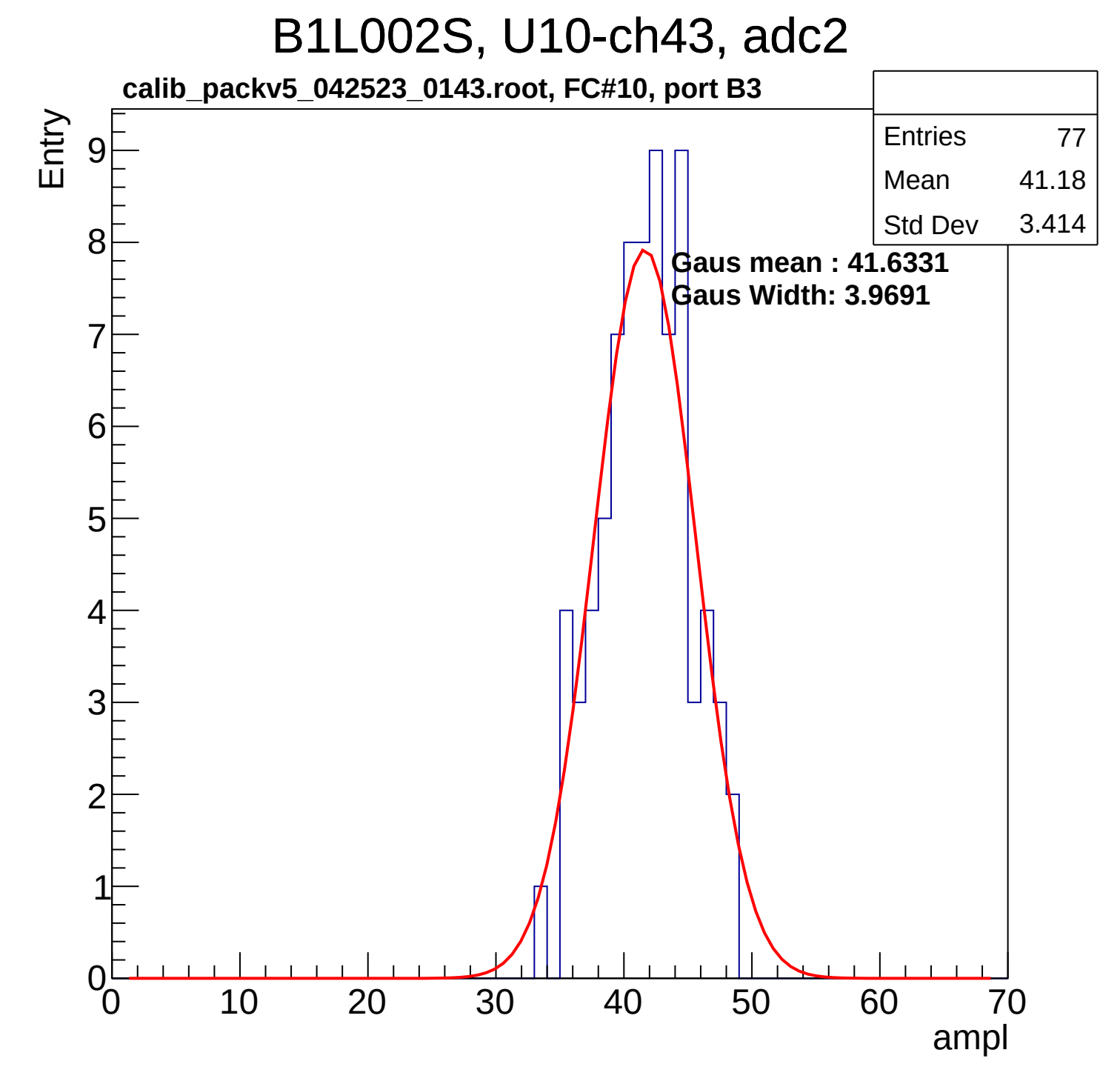
B1L002S, U10-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41.18
Std Dev 3.414
Gaus mean : 41.6331
Gaus Width: 3.9691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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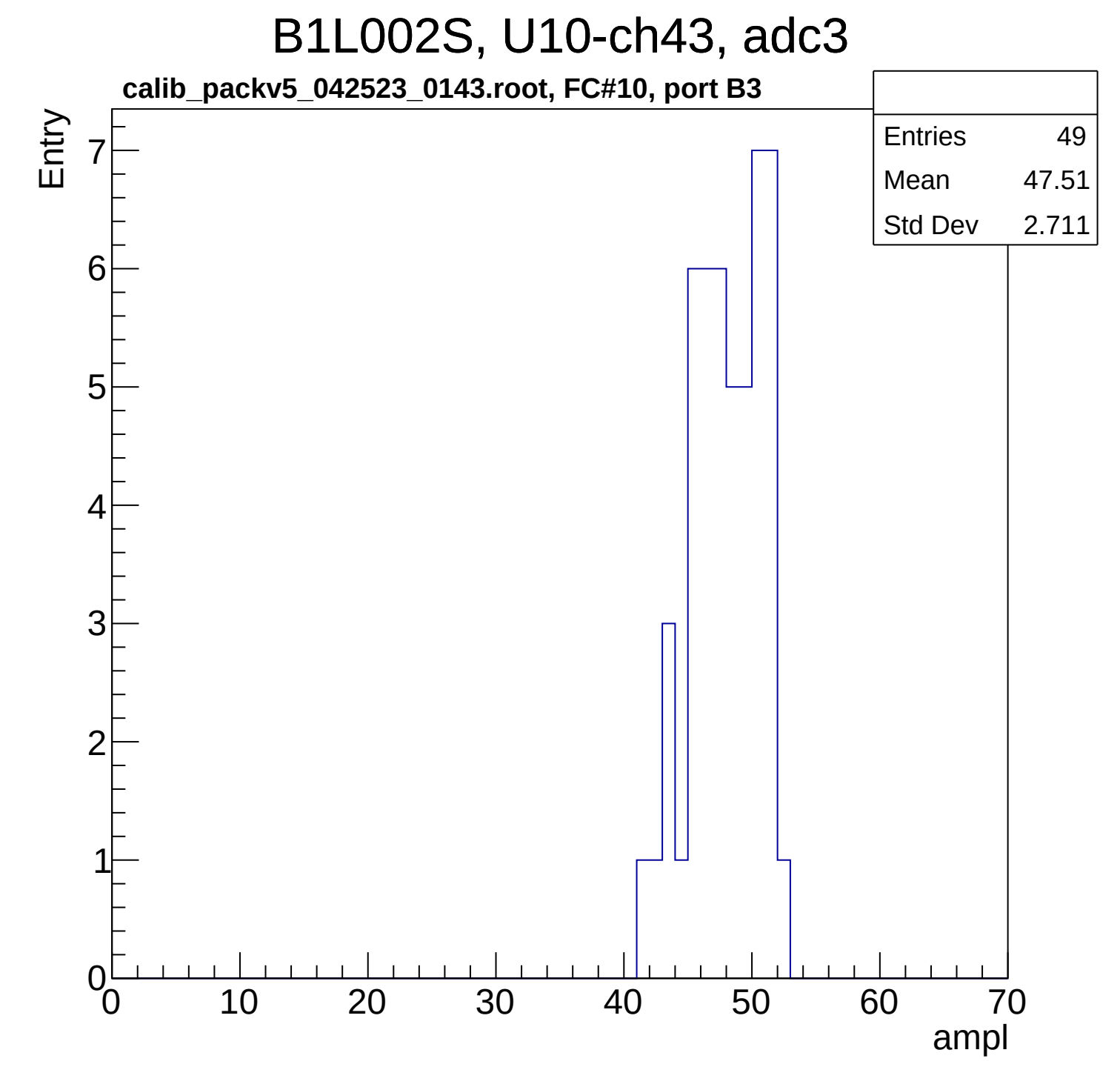
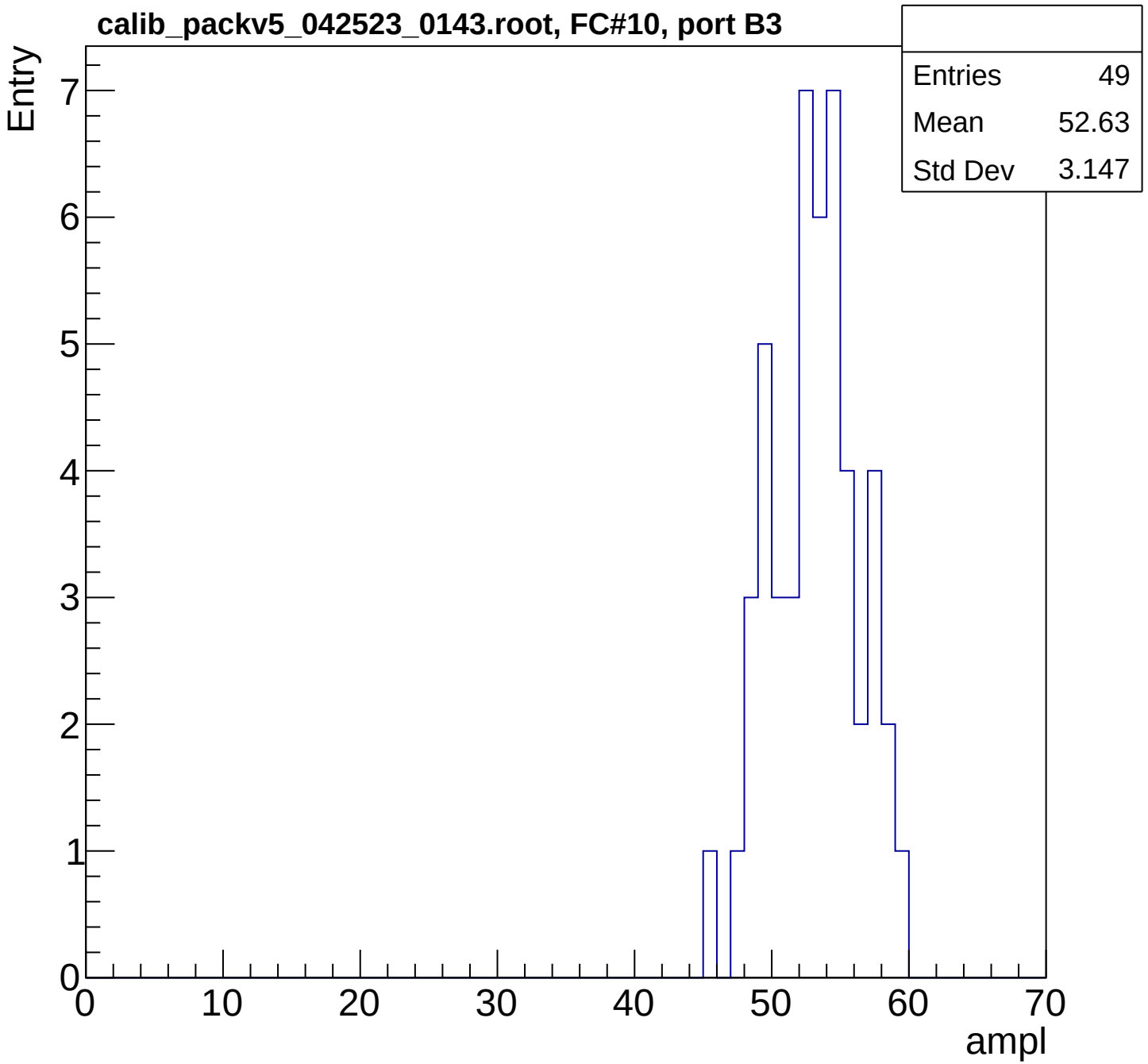
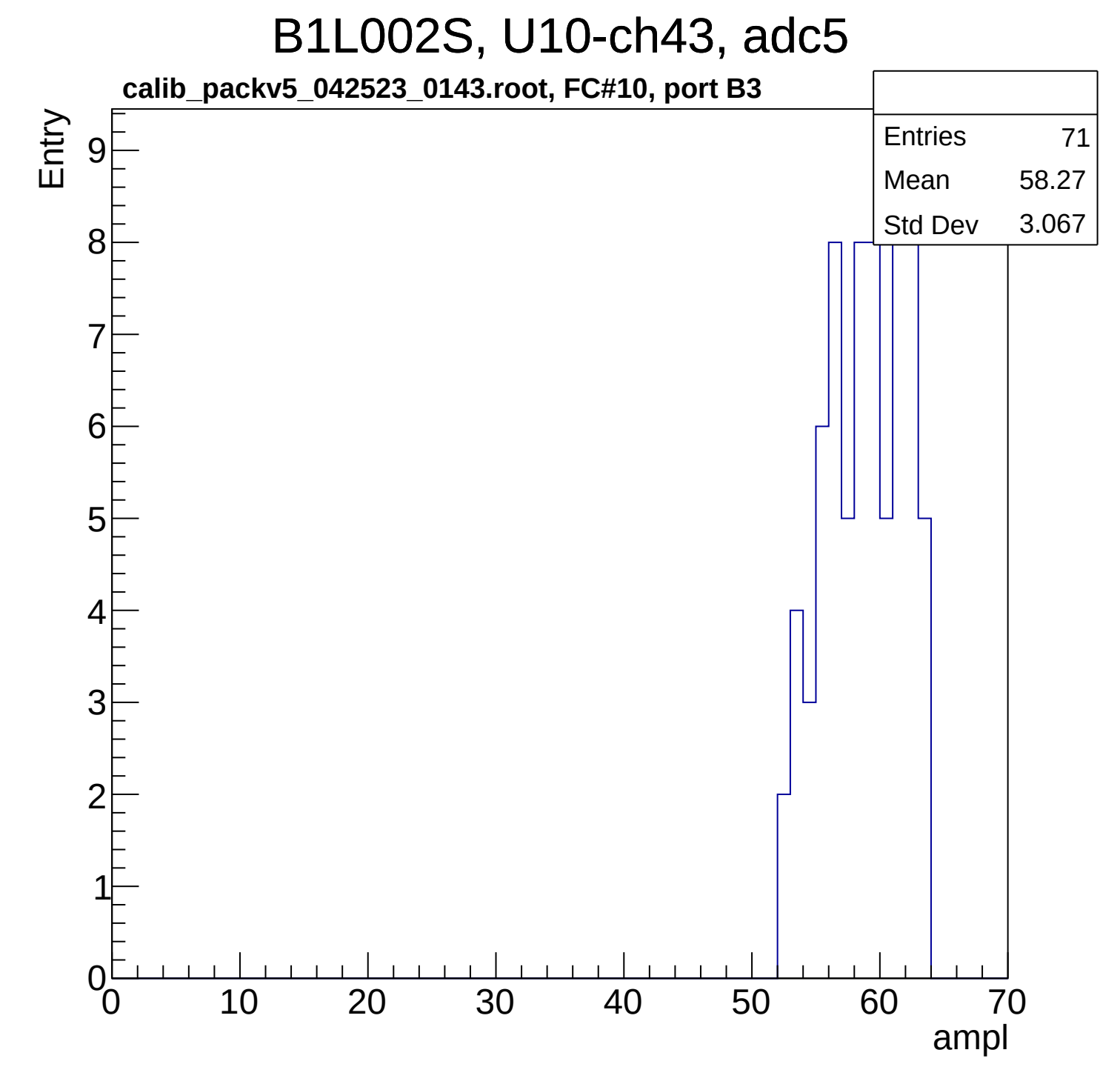
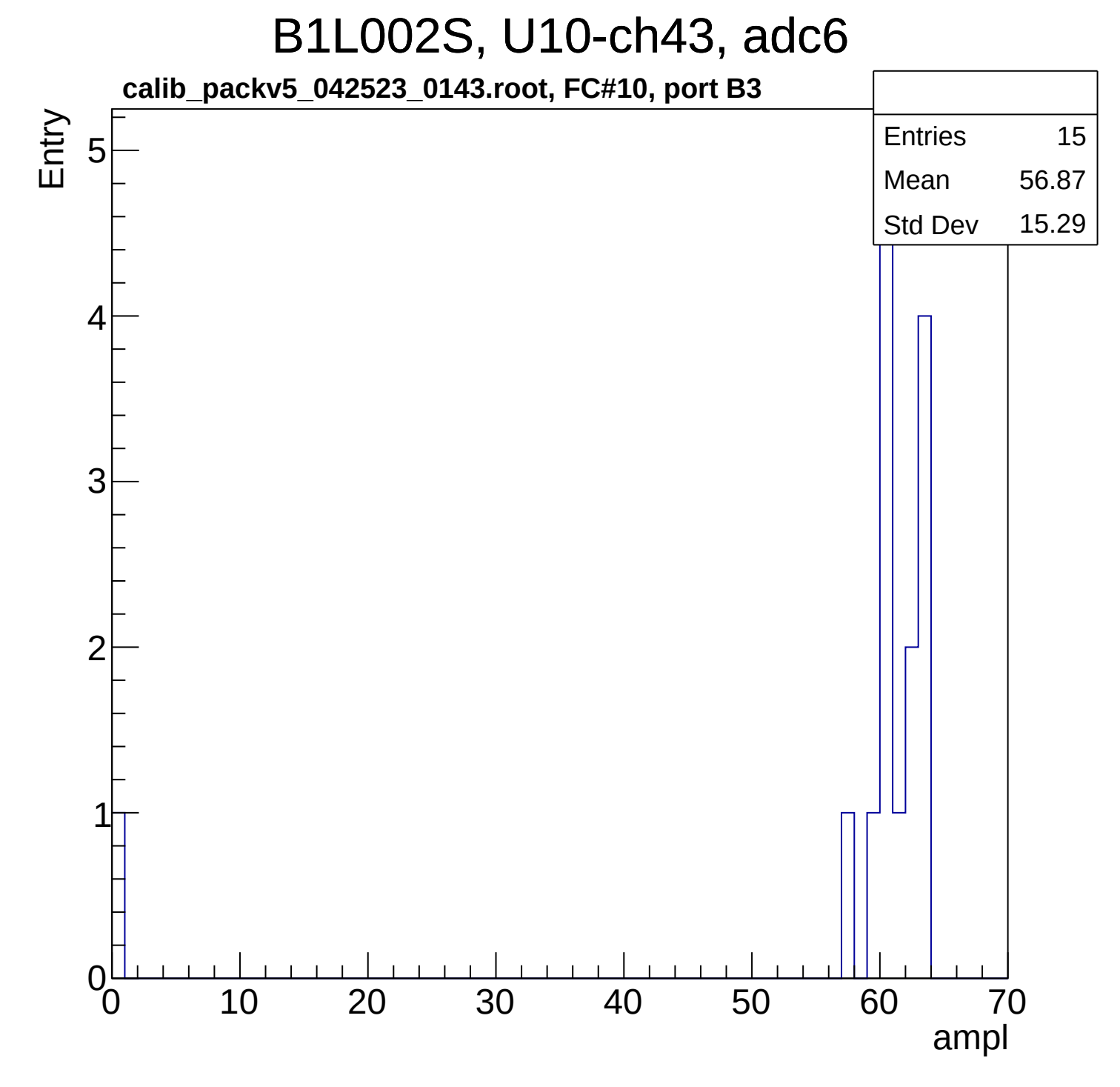
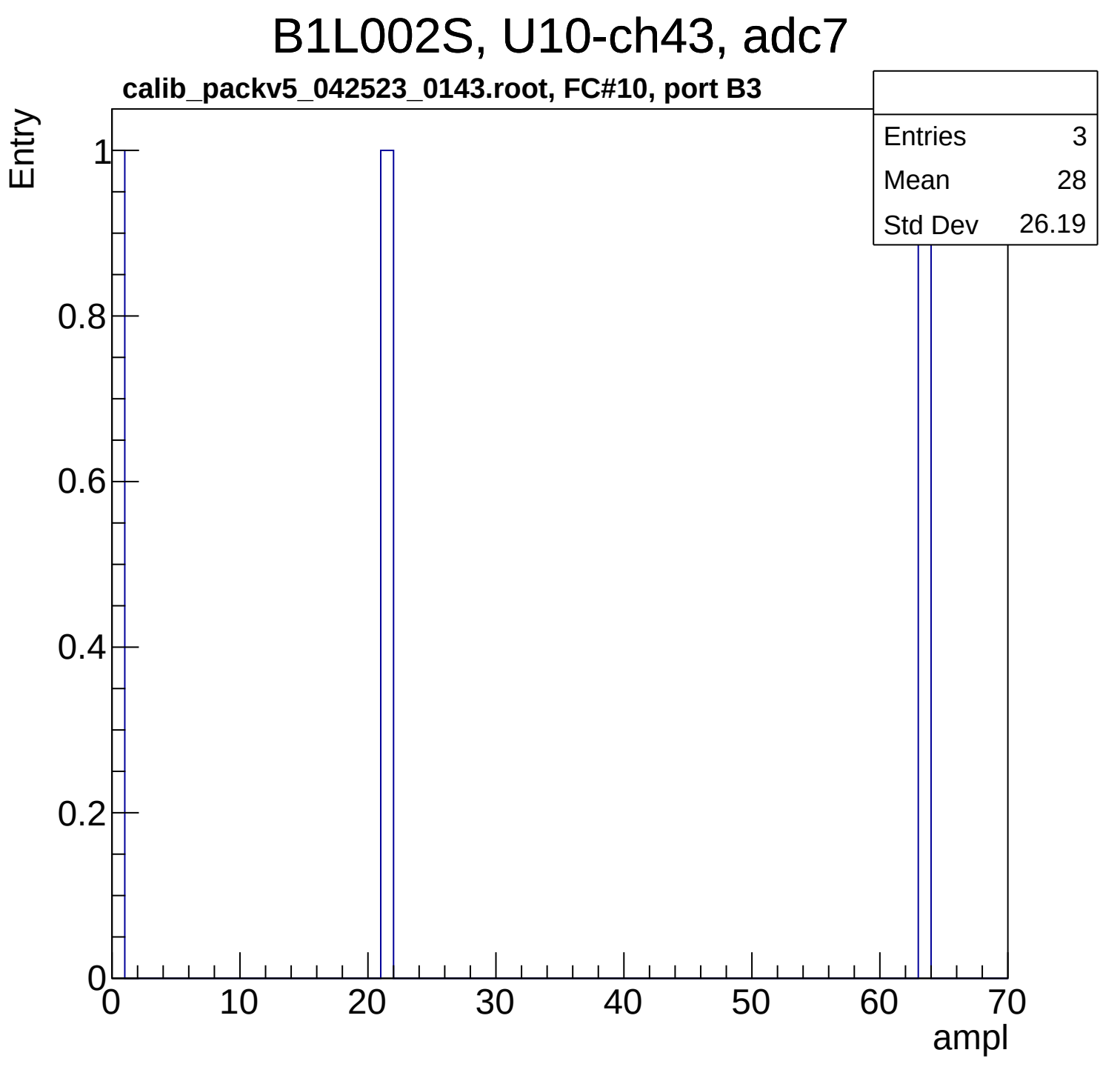

B1L002S, U10-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 47.51
Std Dev 2.711
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.63
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 58.27
Std Dev 3.067
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 56.87
Std Dev 15.29
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
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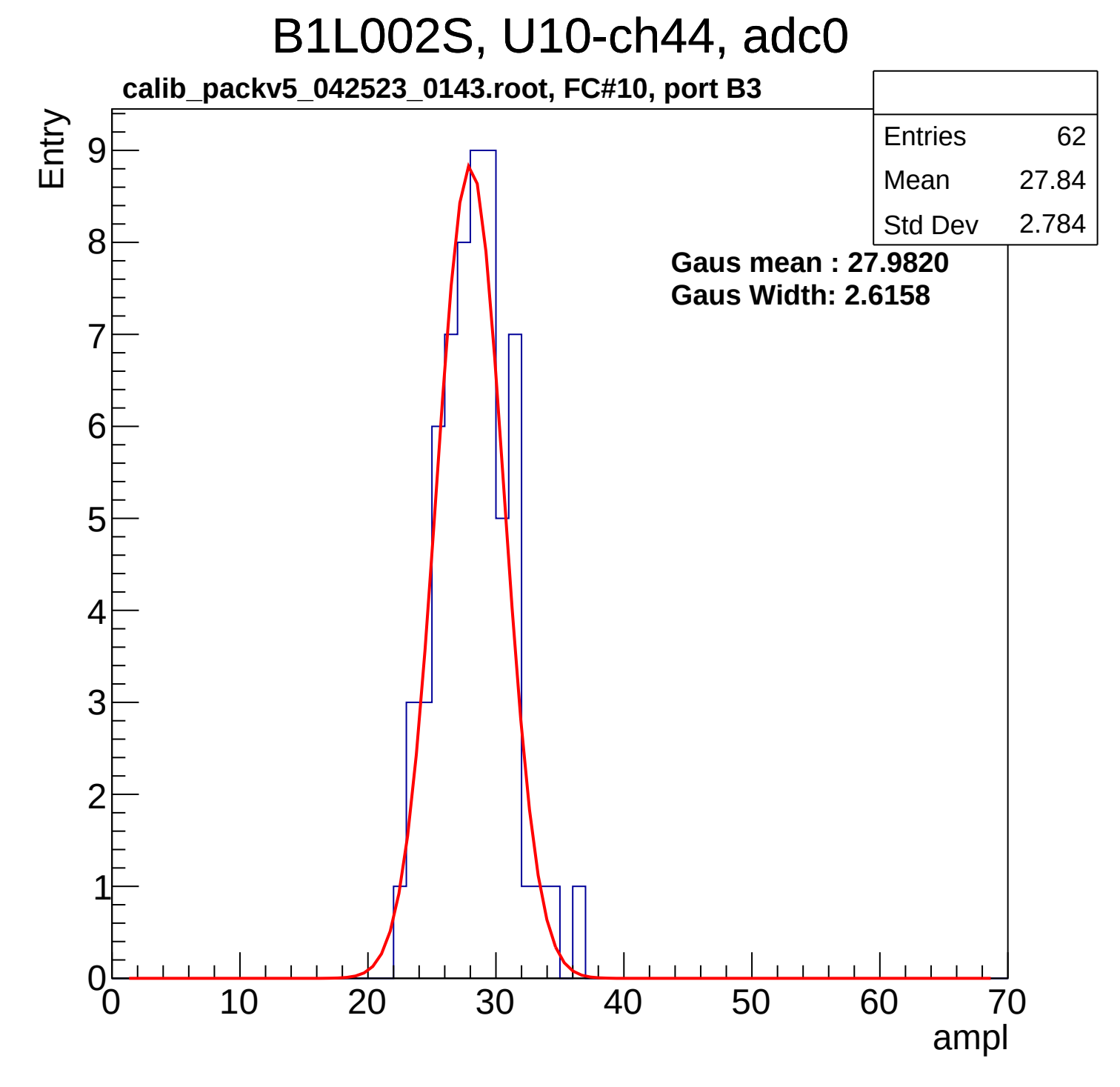
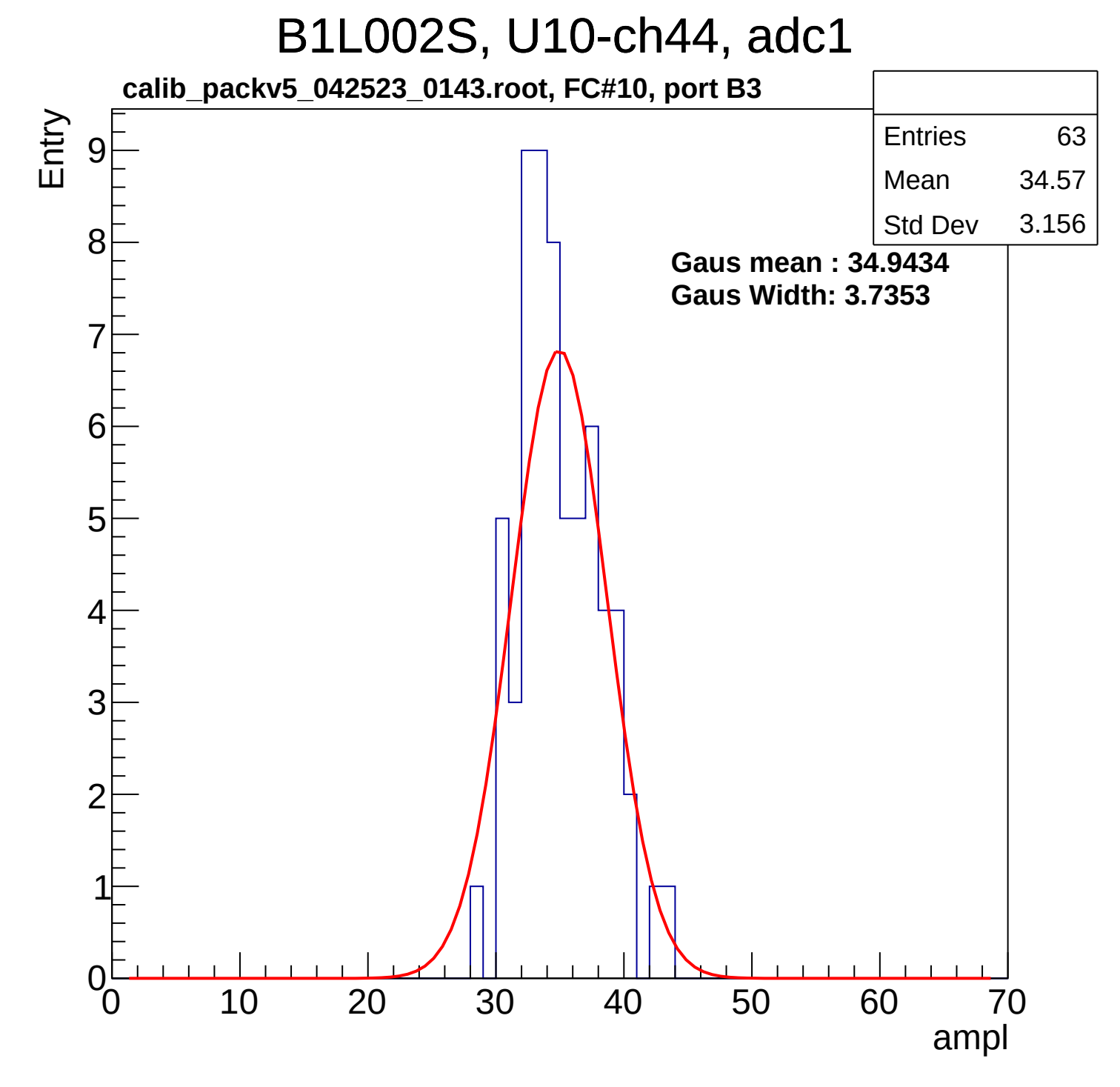
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U10-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.84
Std Dev 2.784
Gaus mean : 27.9820
Gaus Width: 2.6158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.57
Std Dev 3.156
Gaus mean : 34.9434
Gaus Width: 3.7353
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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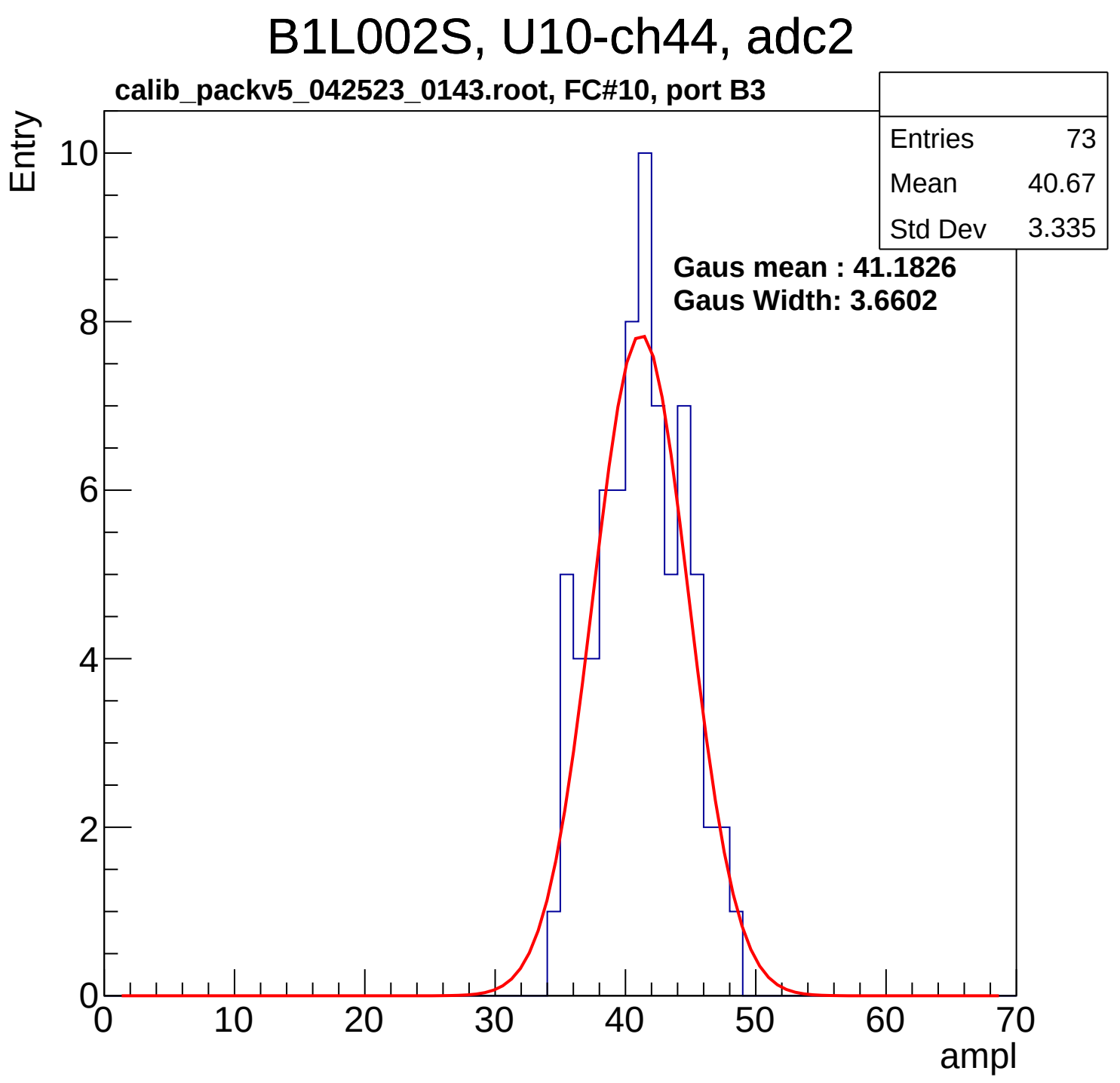
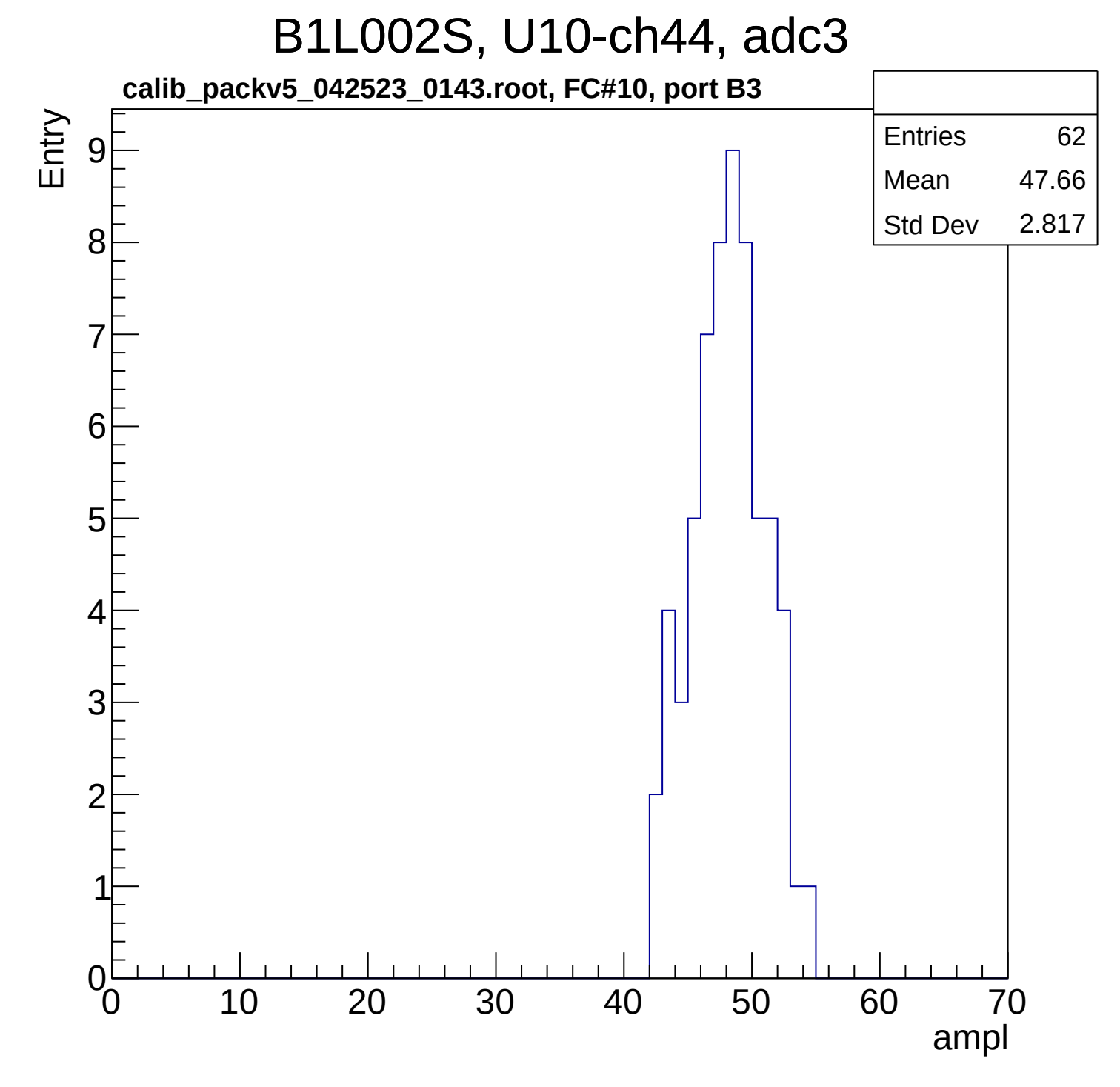

B1L002S, U10-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.67
Std Dev 3.335
Gaus mean : 41.1826
Gaus Width: 3.6602
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.66
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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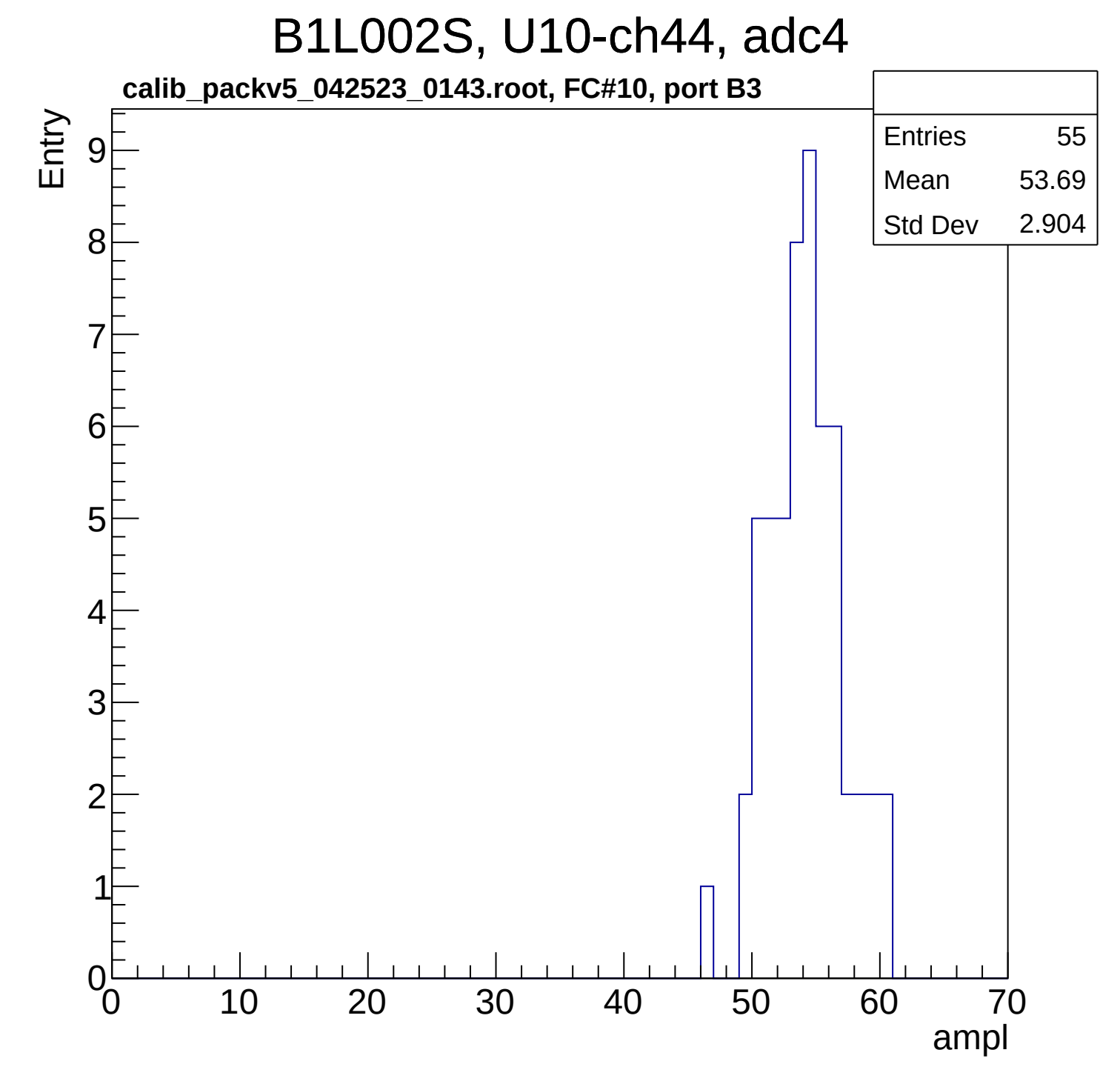
B1L002S, U10-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.69
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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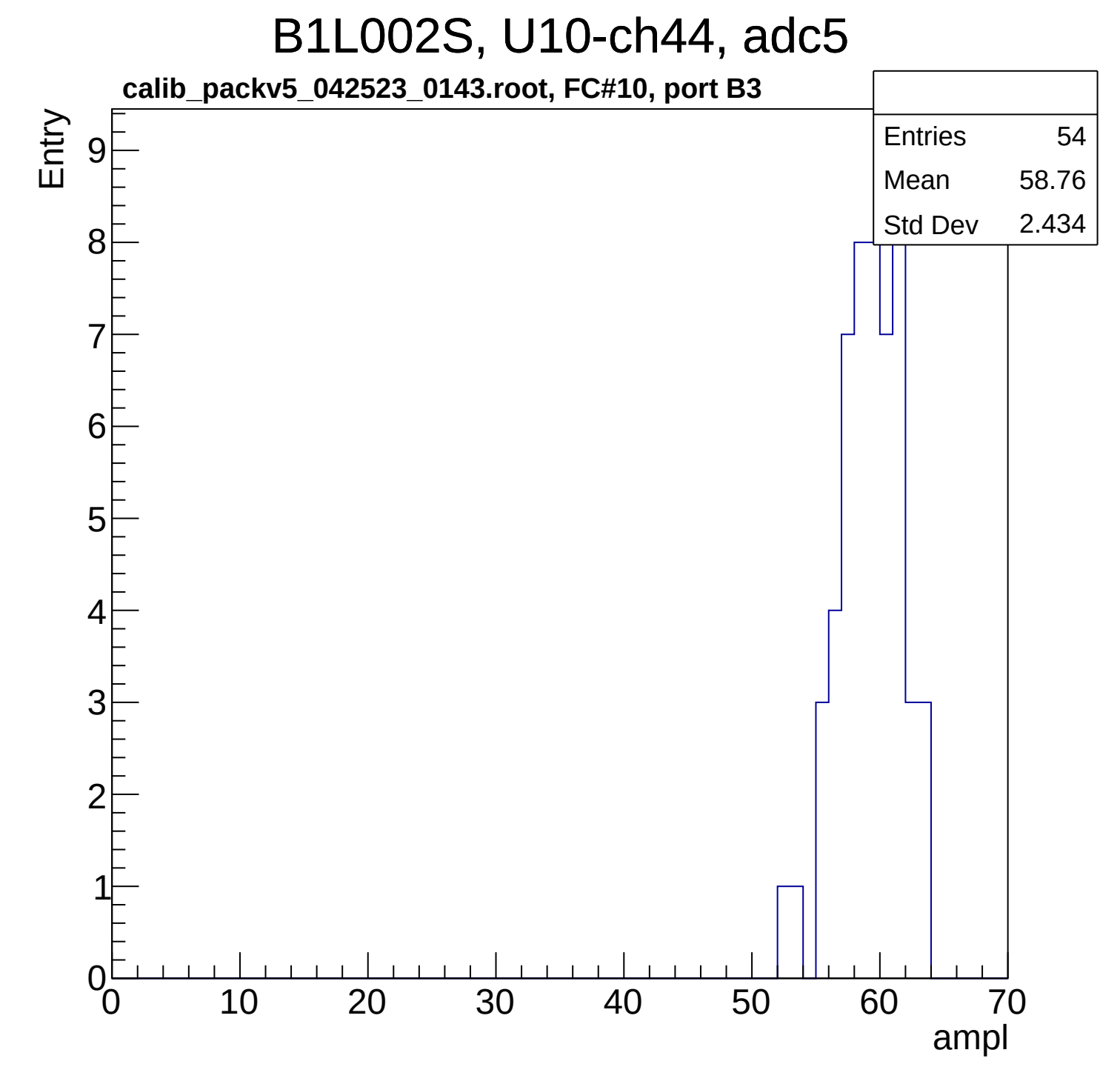
B1L002S, U10-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.76
Std Dev 2.434
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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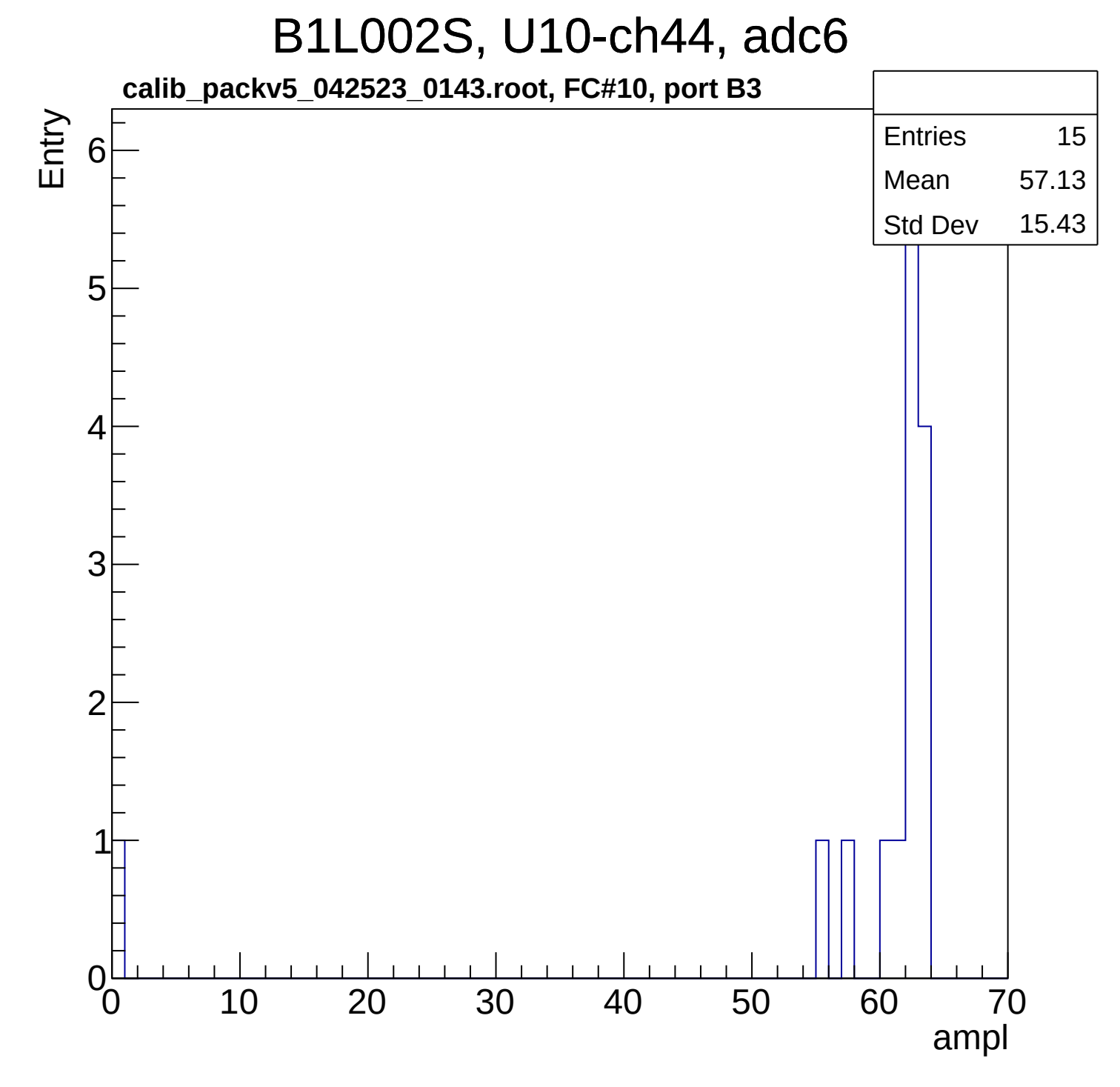
B1L002S, U10-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.13
Std Dev 15.43
Entry
0
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2
3
4
5
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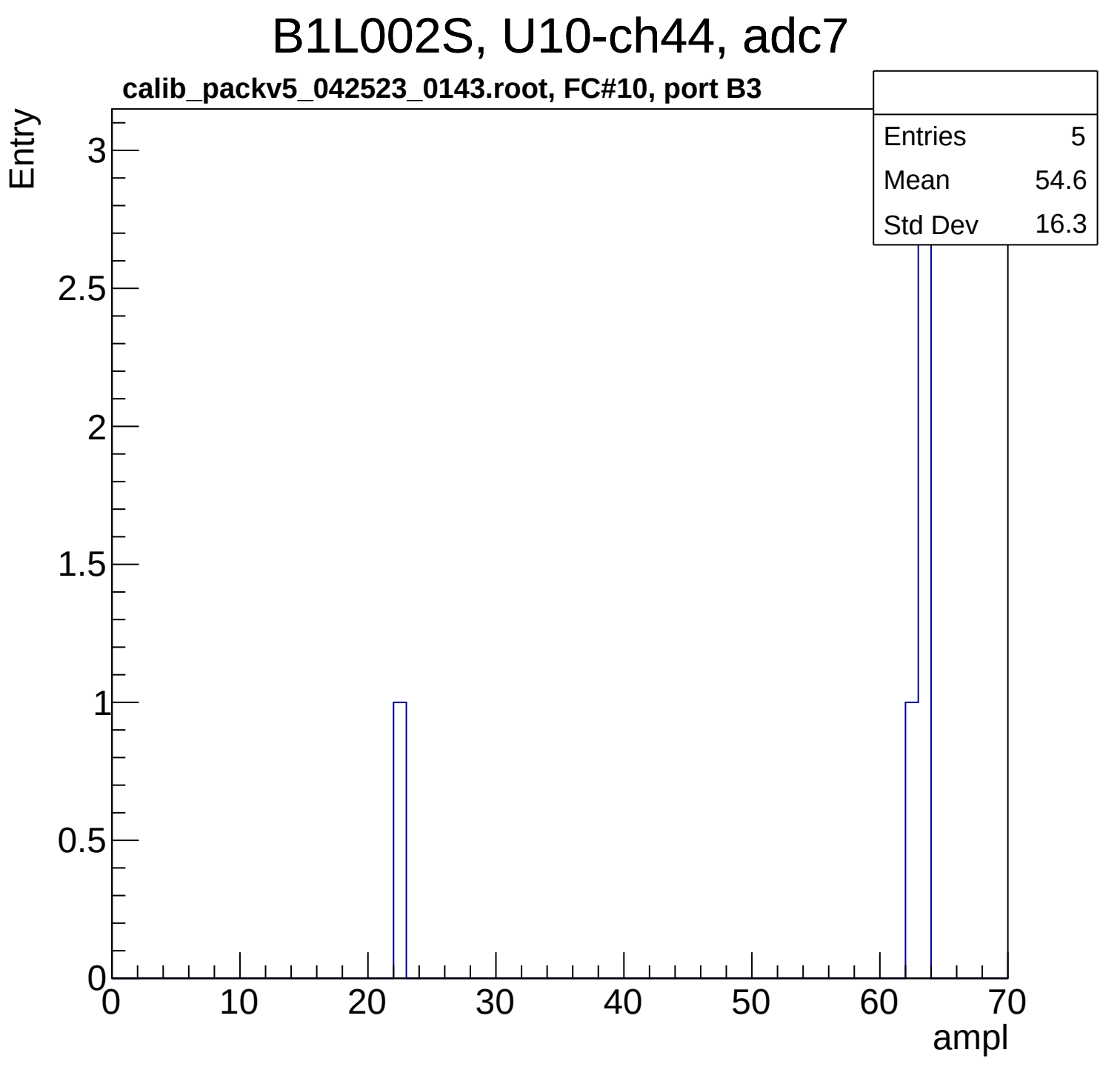
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 54.6
Std Dev 16.3
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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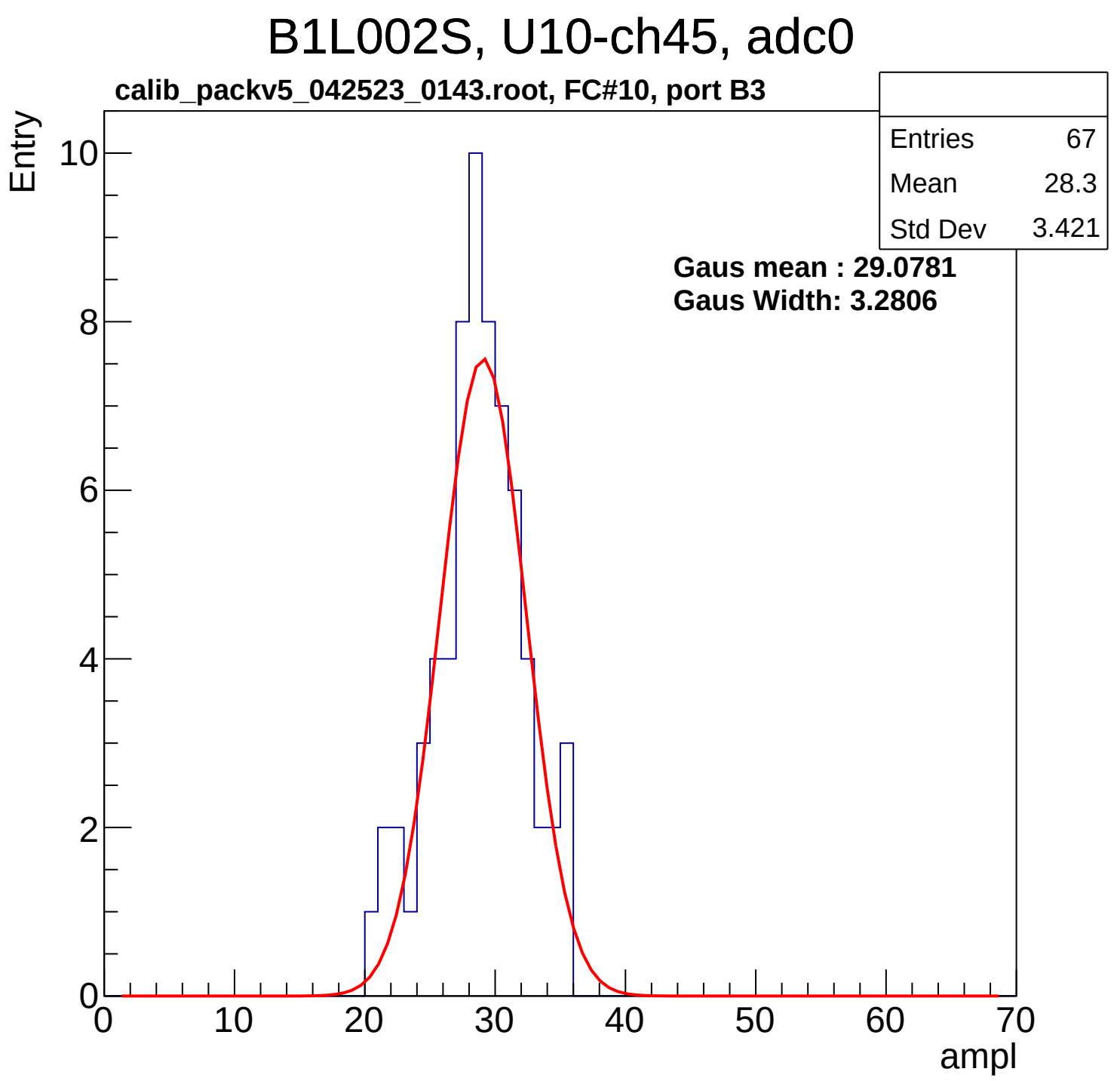
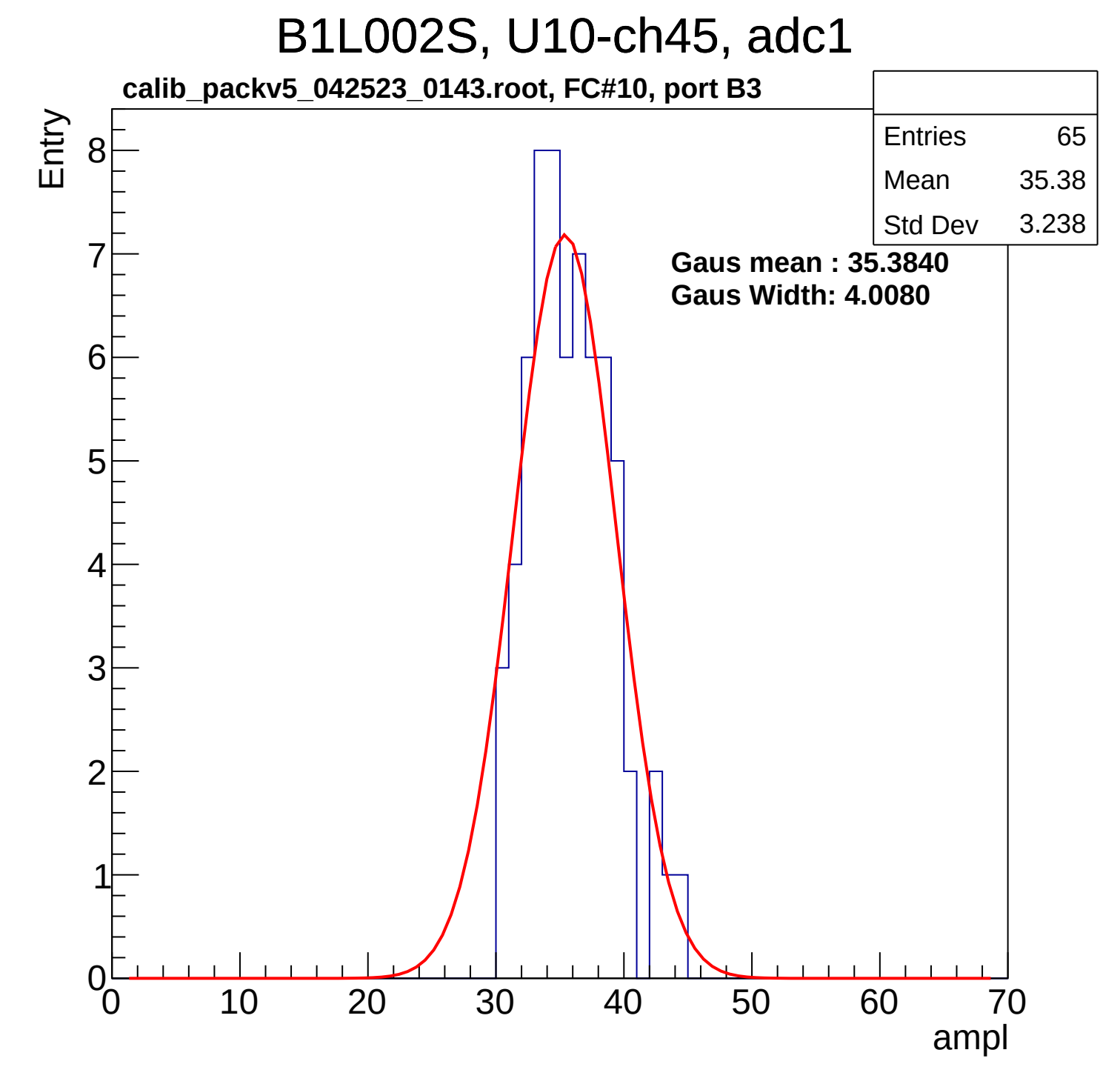
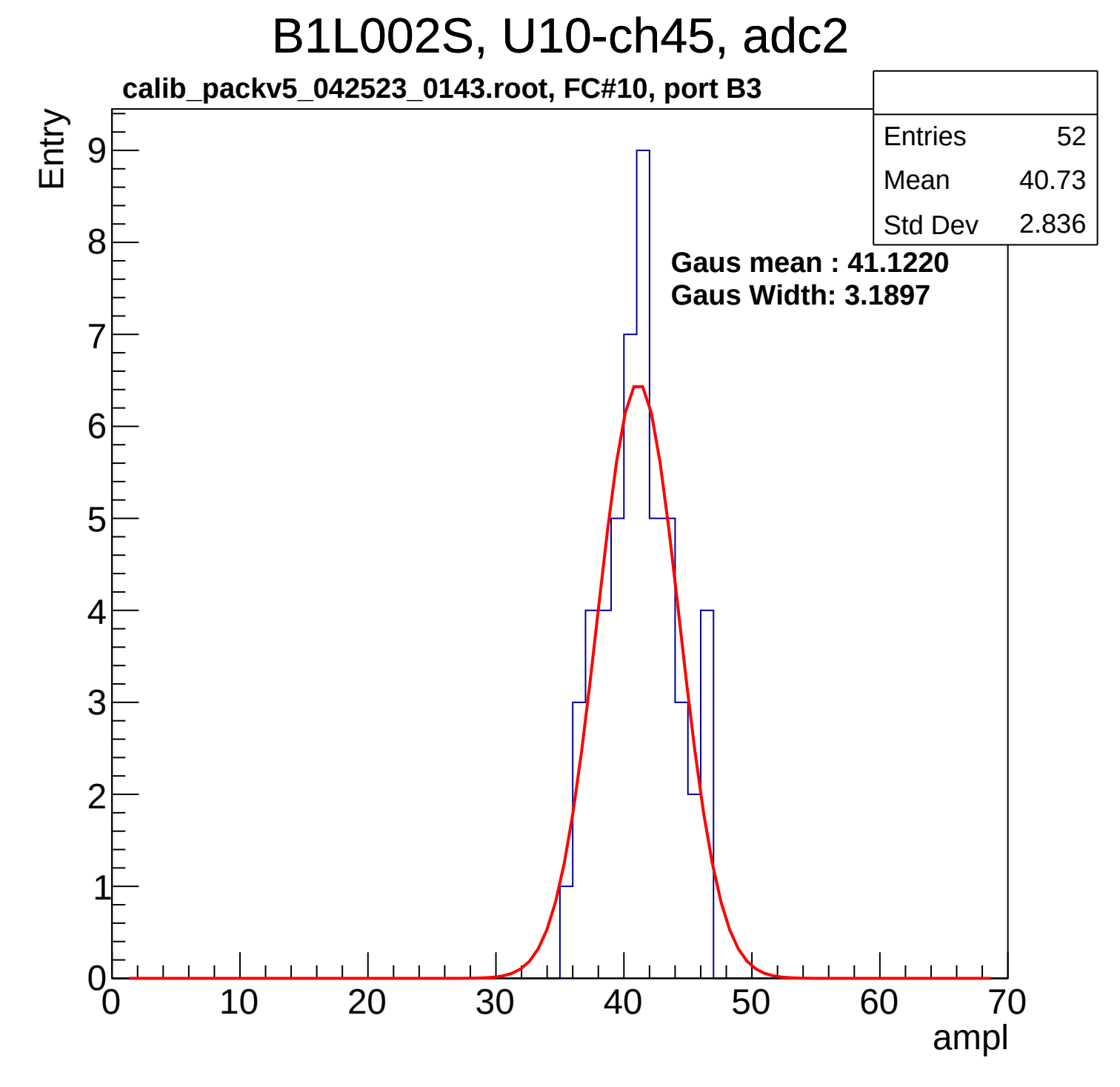
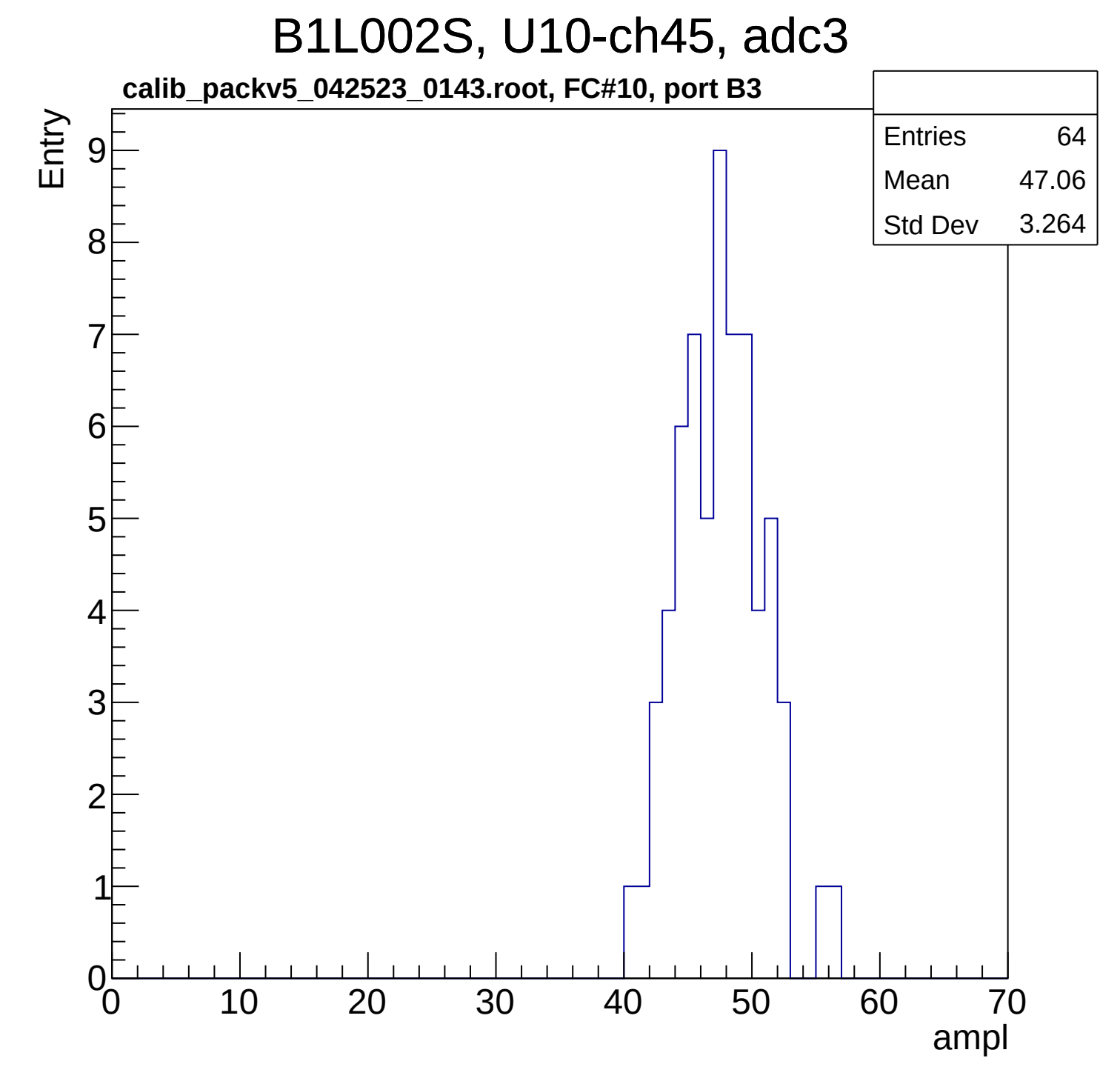
60
70


B1L002S, U10-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.3
Std Dev 3.421
Gaus mean : 29.0781
Gaus Width: 3.2806
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.38
Std Dev 3.238
Gaus mean : 35.3840
Gaus Width: 4.0080
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.73
Std Dev 2.836
Gaus mean : 41.1220
Gaus Width: 3.1897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.06
Std Dev 3.264
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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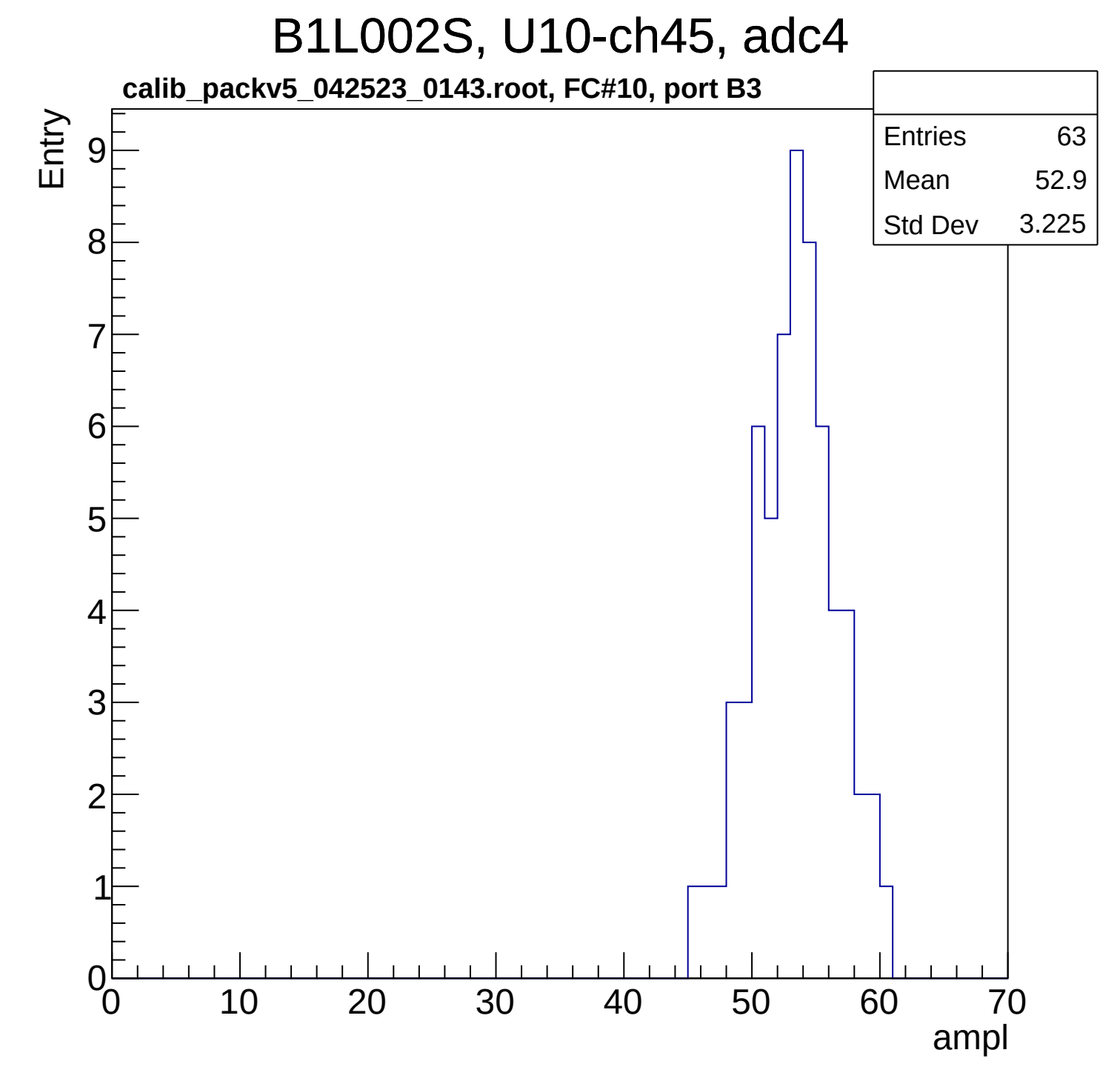
B1L002S, U10-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.9
Std Dev 3.225
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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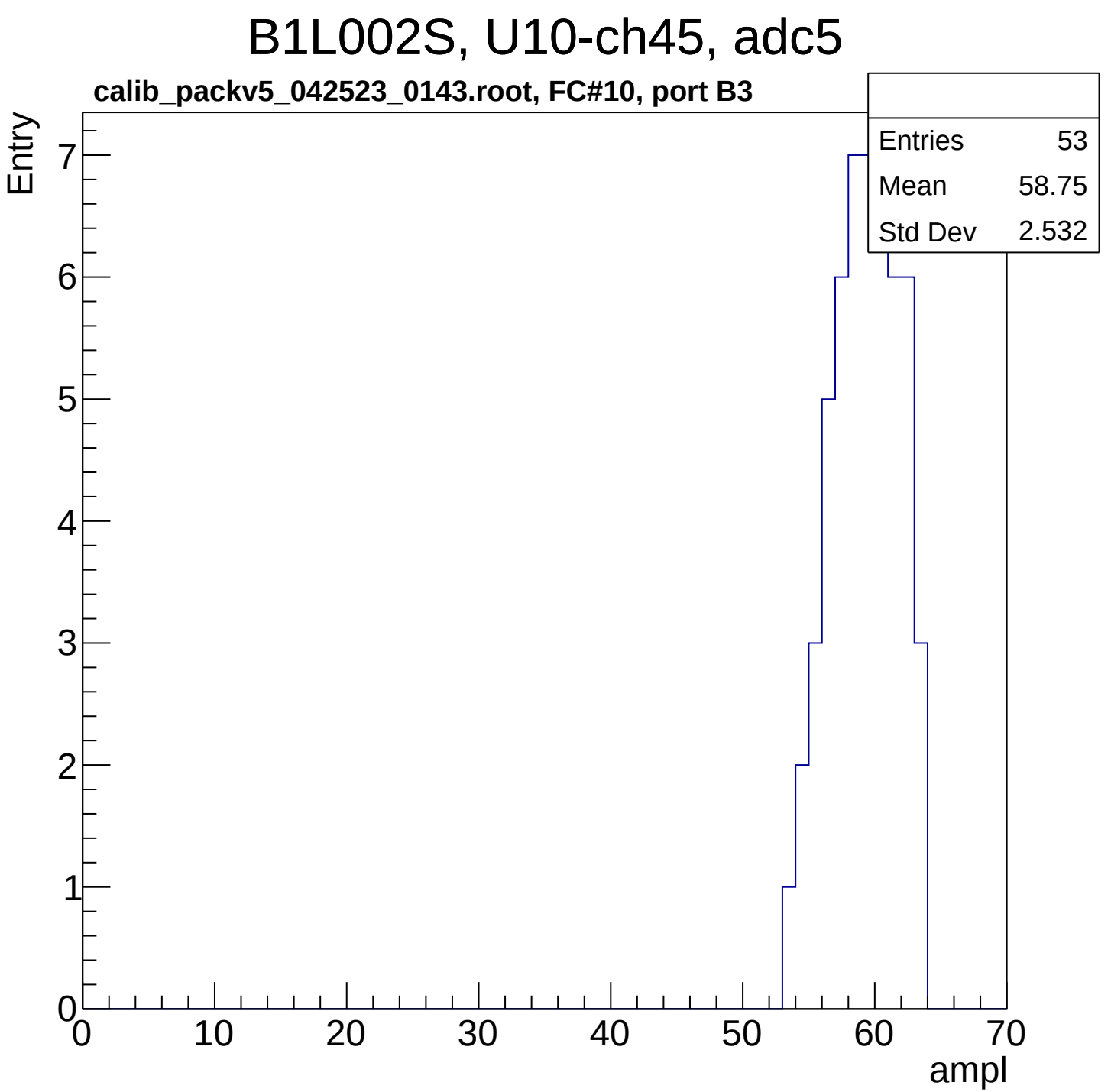
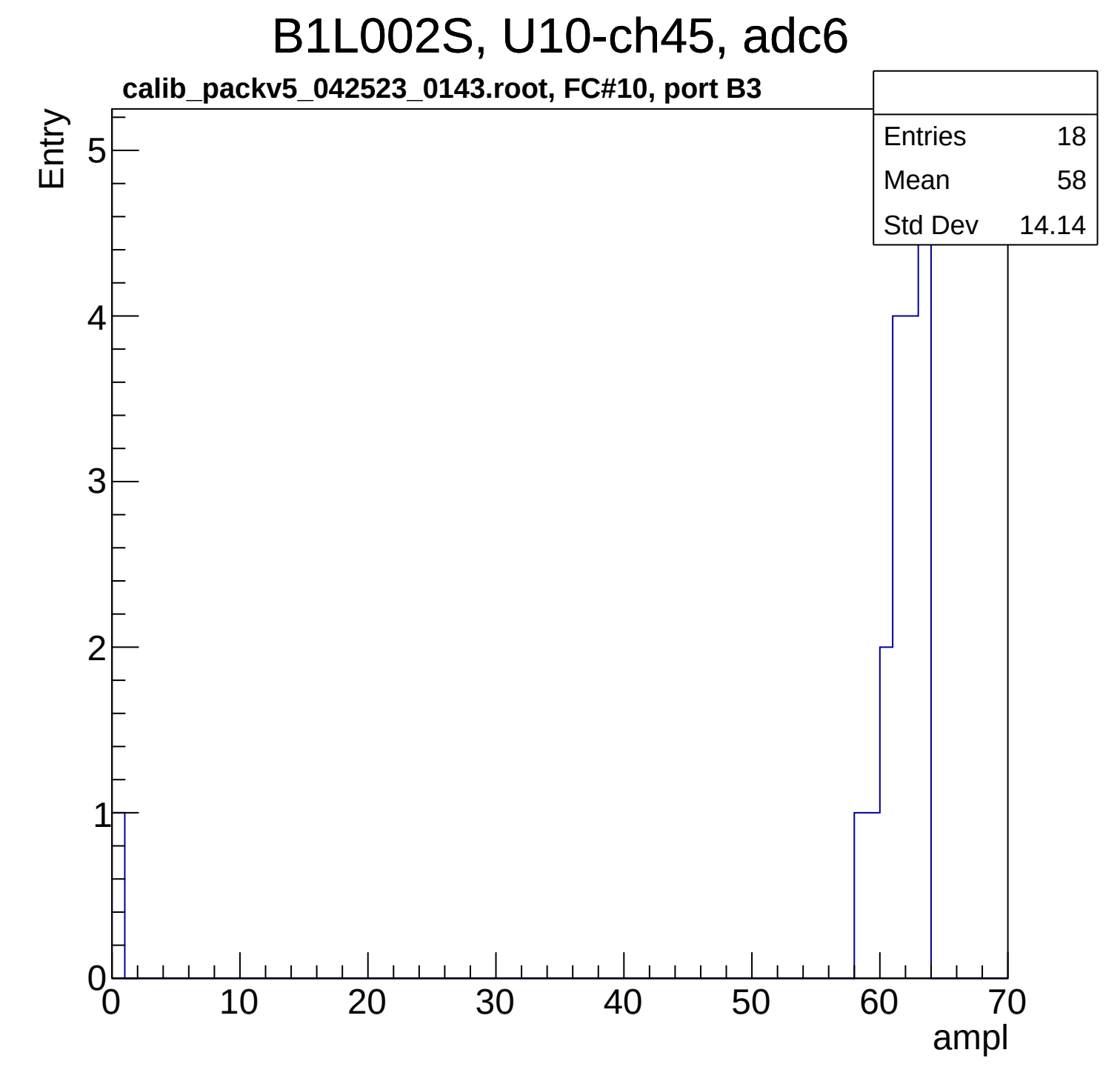

B1L002S, U10-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.75
Std Dev 2.532
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58
Std Dev 14.14
Entry
0
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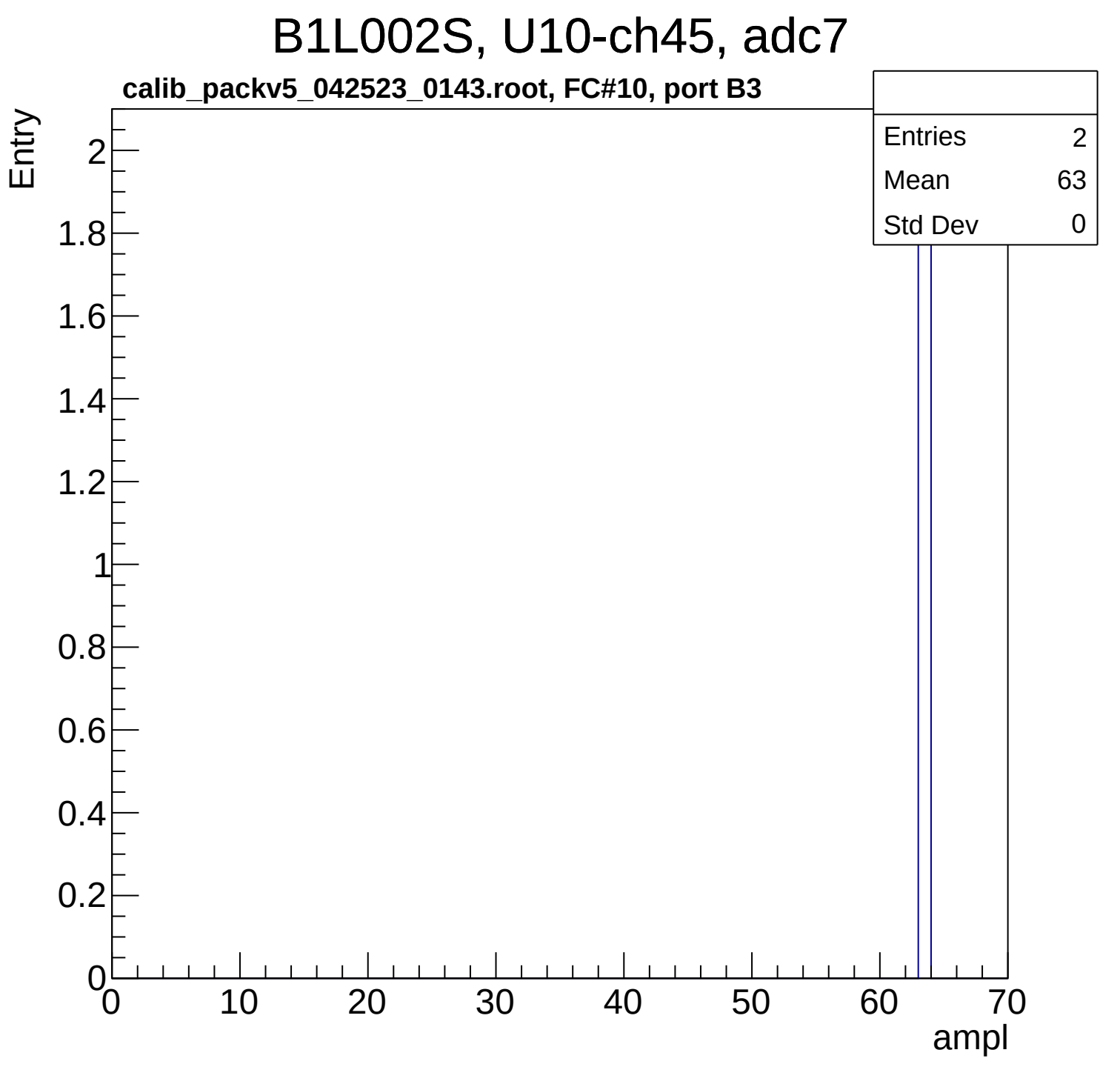
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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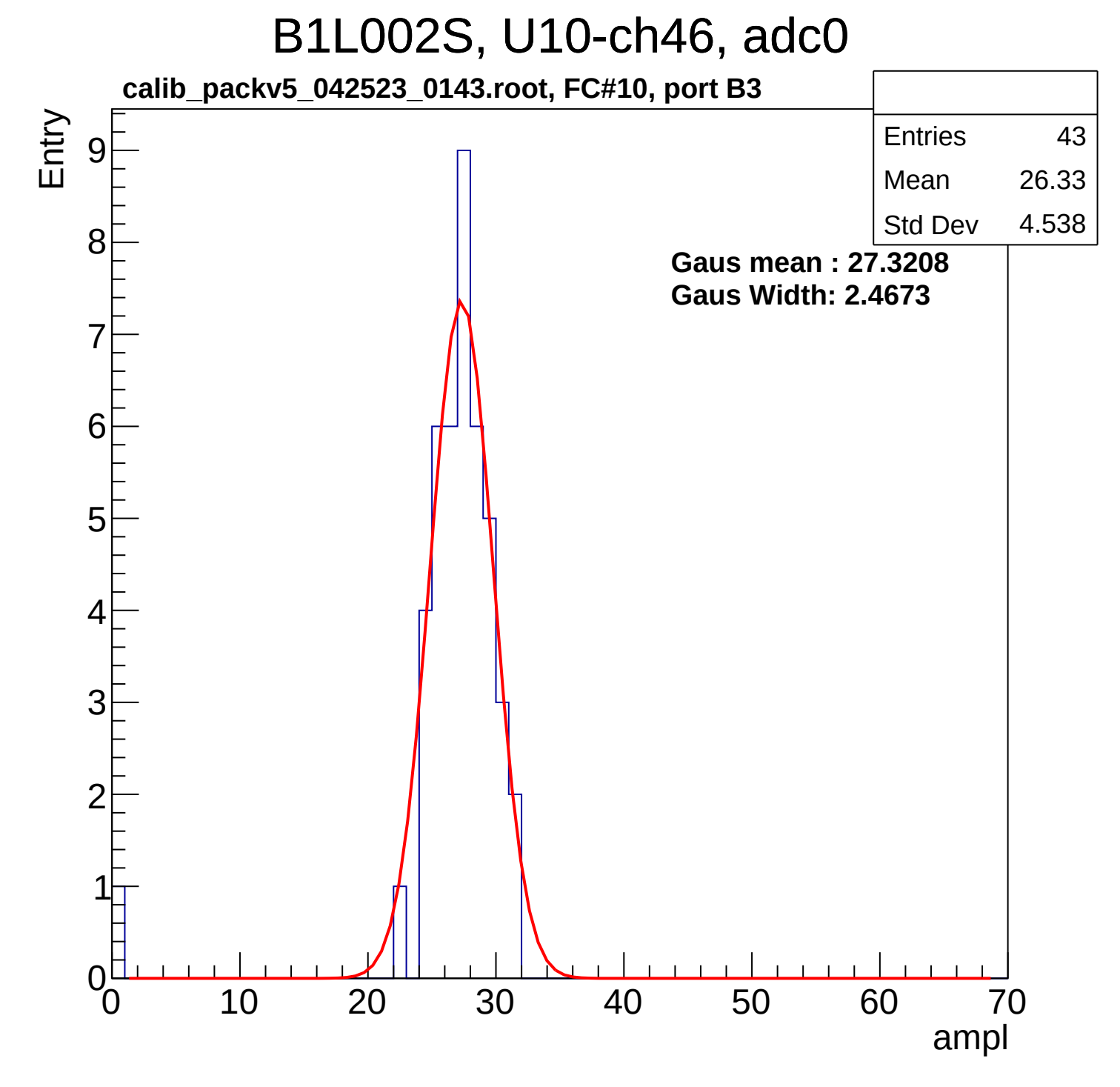
B1L002S, U10-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 26.33
Std Dev 4.538
Gaus mean : 27.3208
Gaus Width: 2.4673
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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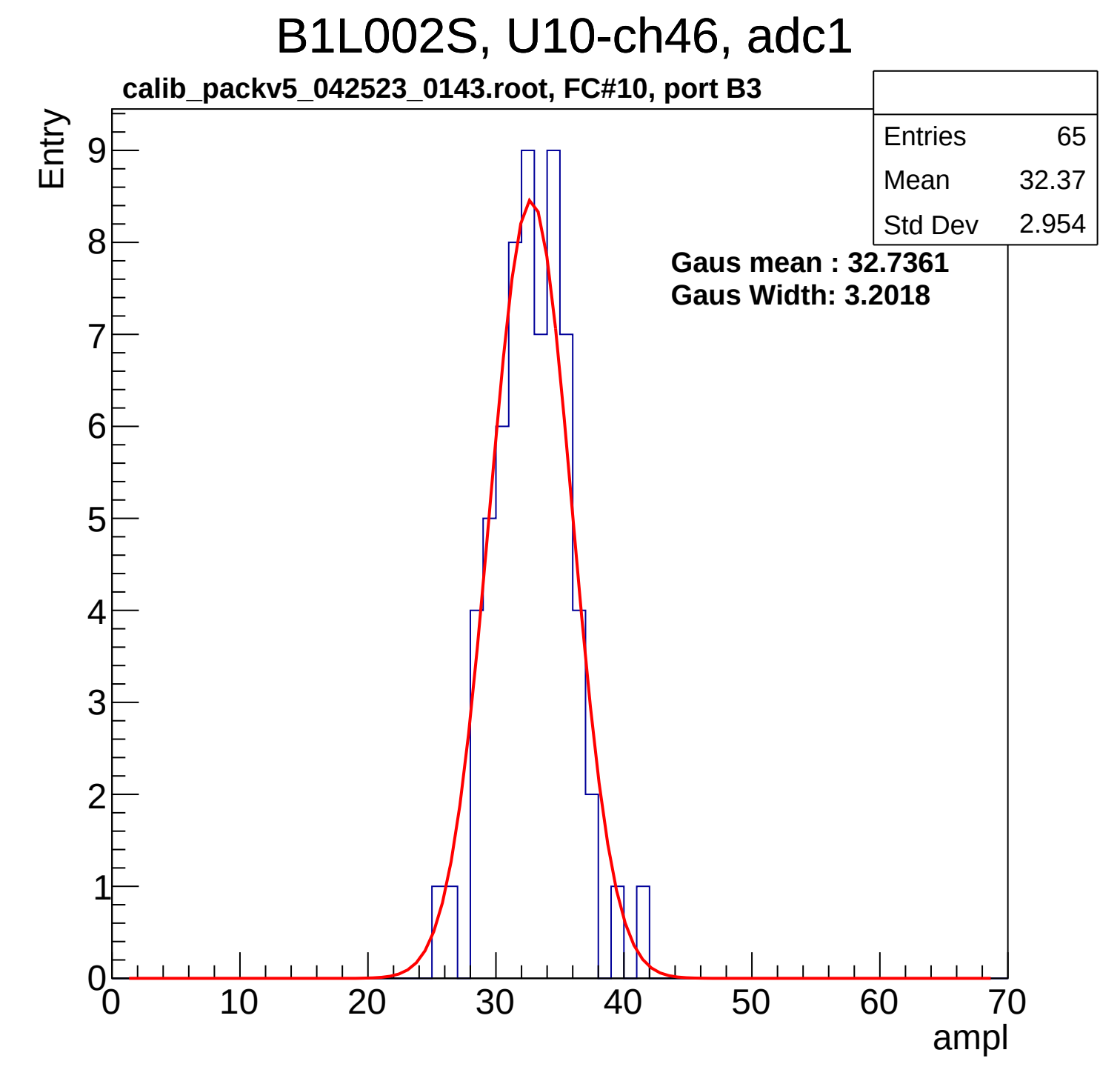
B1L002S, U10-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 32.37
Std Dev 2.954
Gaus mean : 32.7361
Gaus Width: 3.2018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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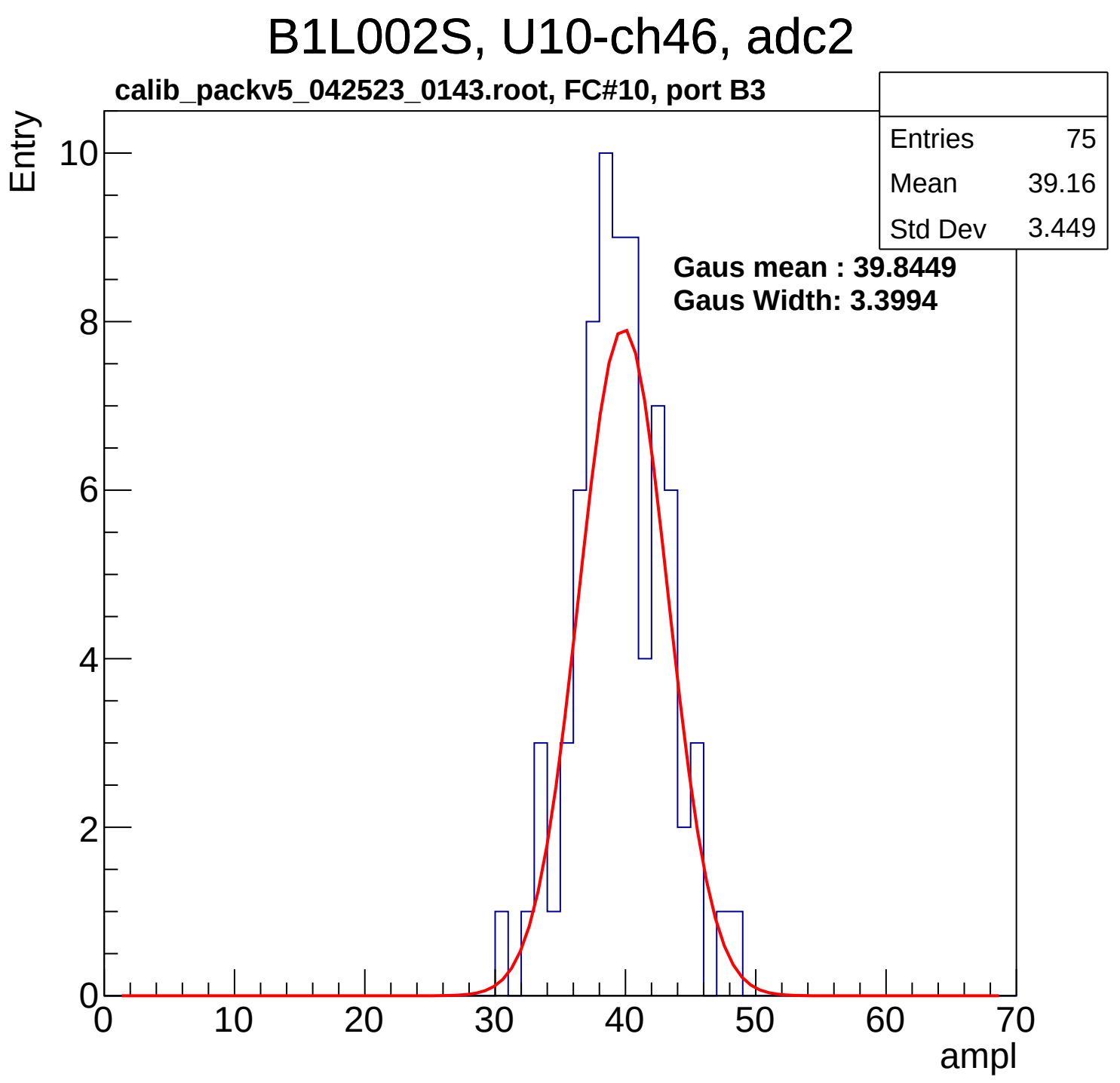
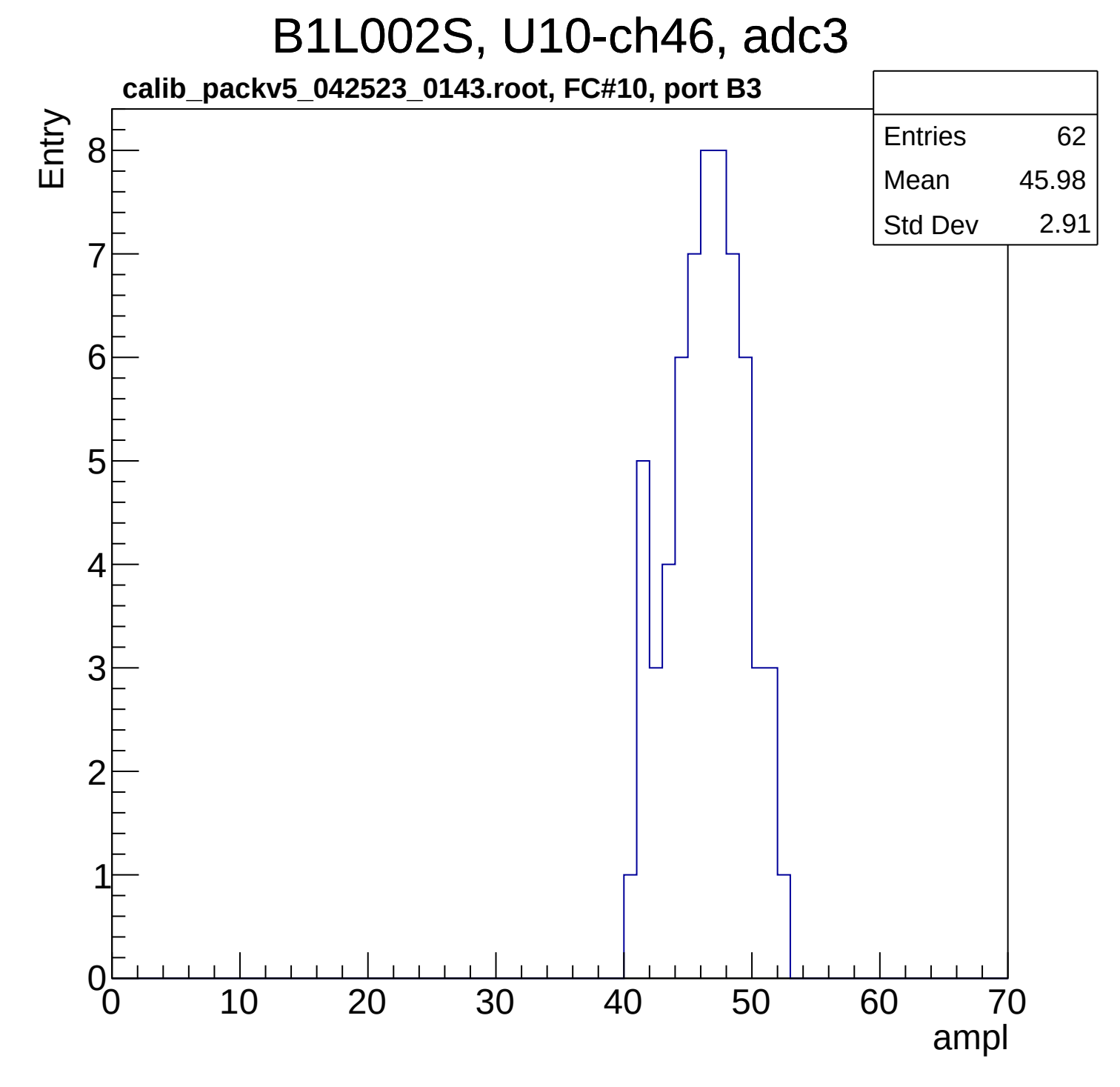

B1L002S, U10-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 39.16
Std Dev 3.449
Gaus mean : 39.8449
Gaus Width: 3.3994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 45.98
Std Dev 2.91
Entry
ampl
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1
2
3
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5
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7
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0
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60
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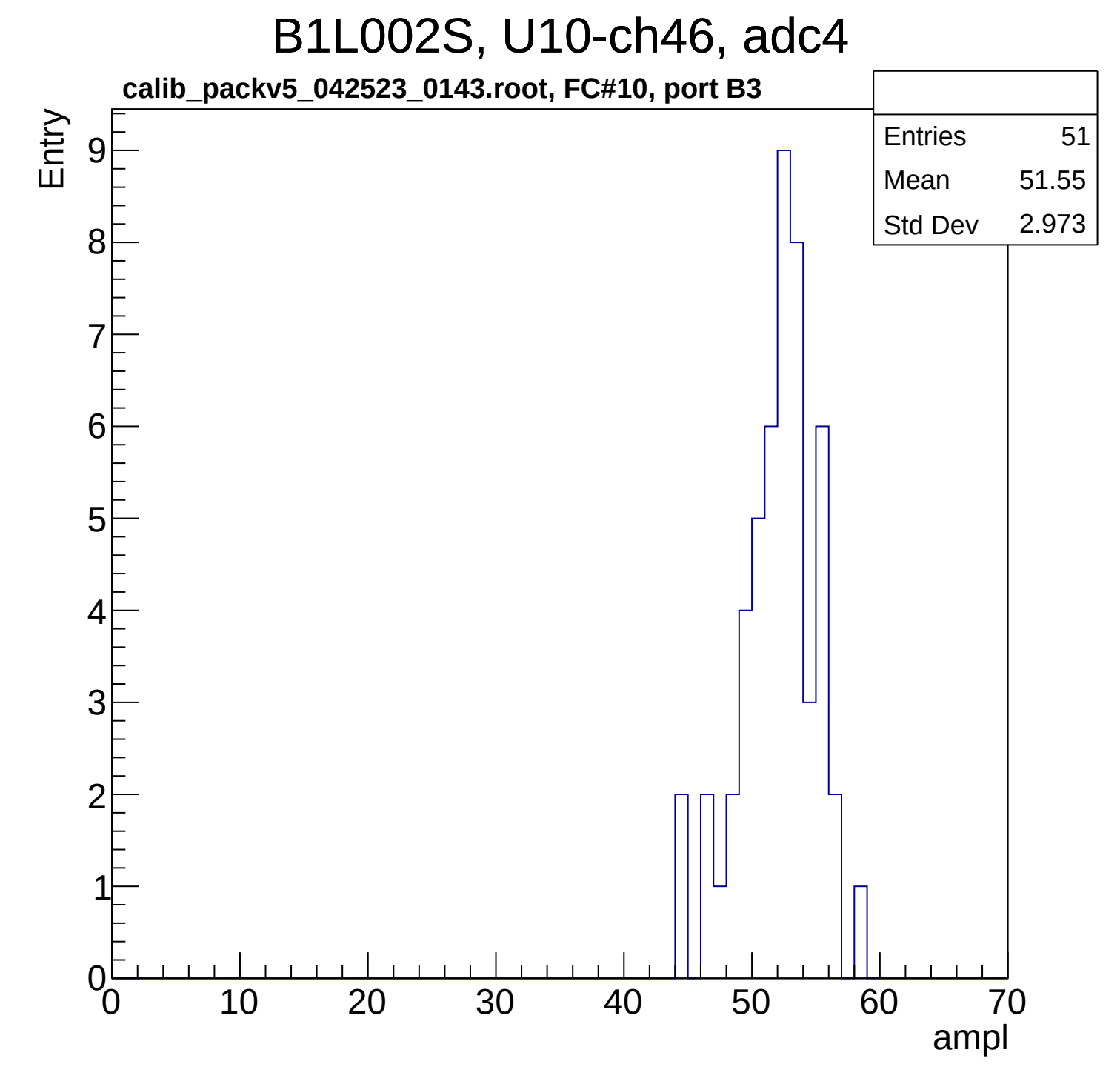
B1L002S, U10-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.55
Std Dev 2.973
Entry
ampl
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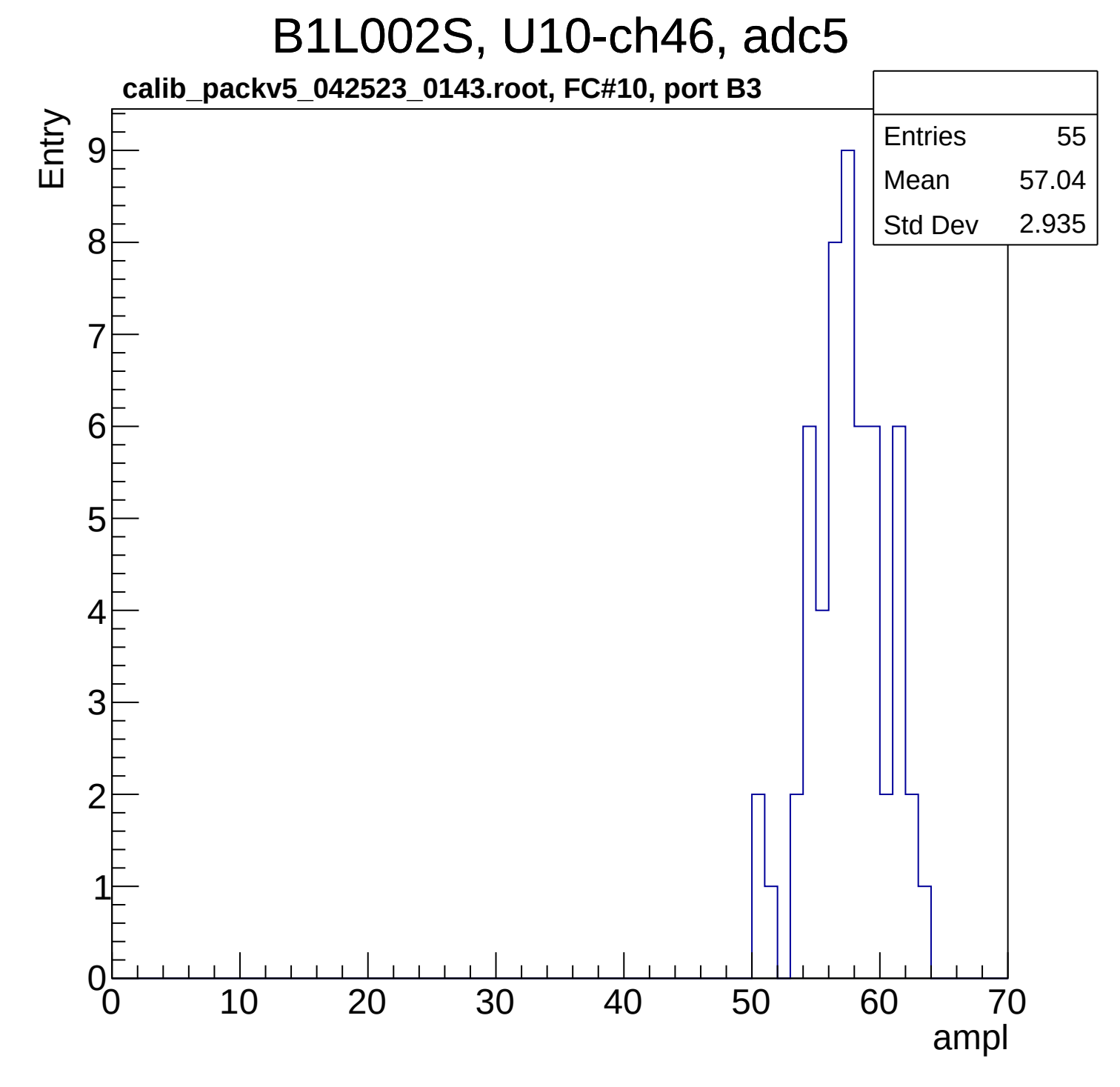
B1L002S, U10-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.04
Std Dev 2.935
Entry
ampl
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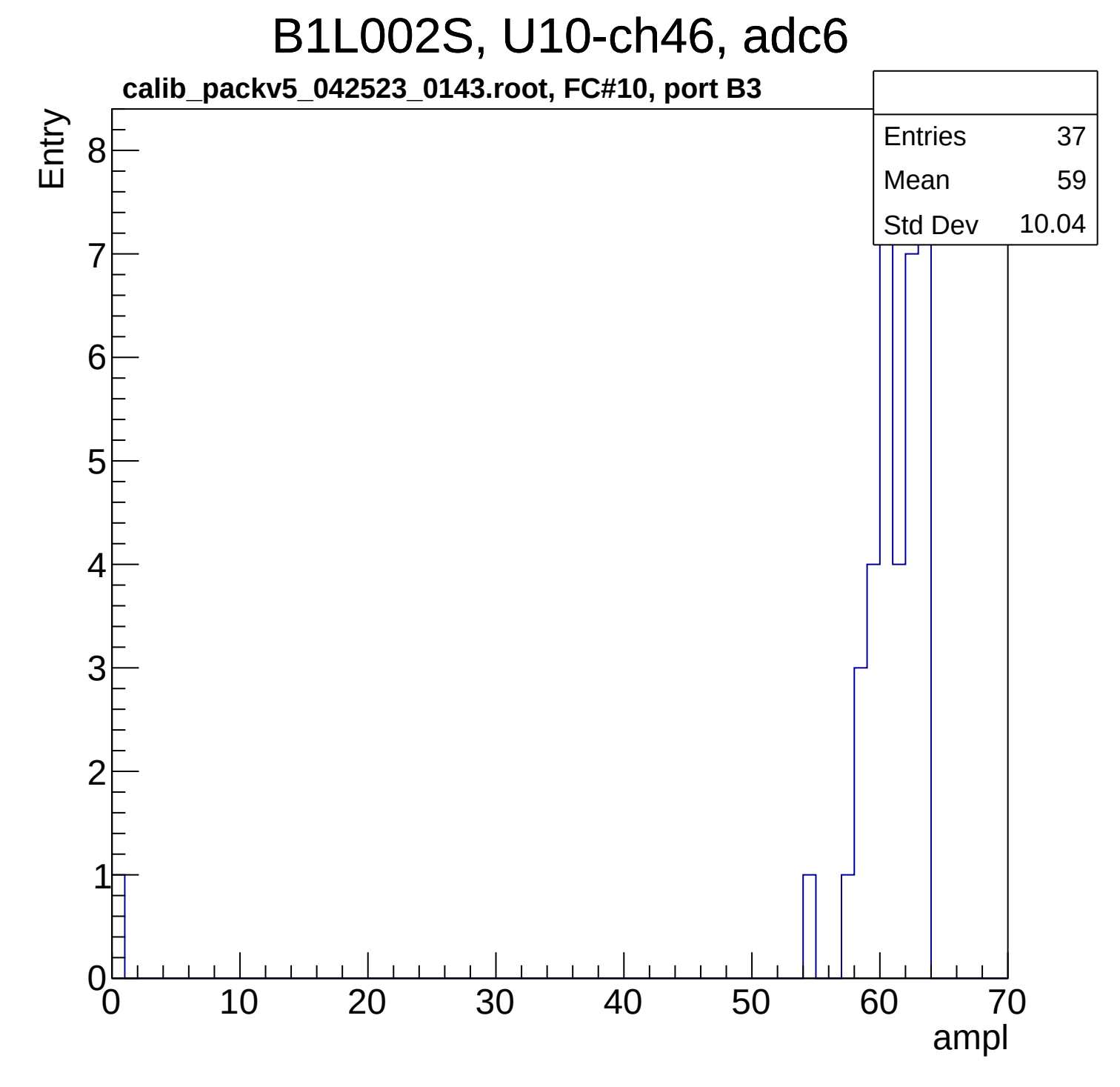
B1L002S, U10-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 59
Std Dev 10.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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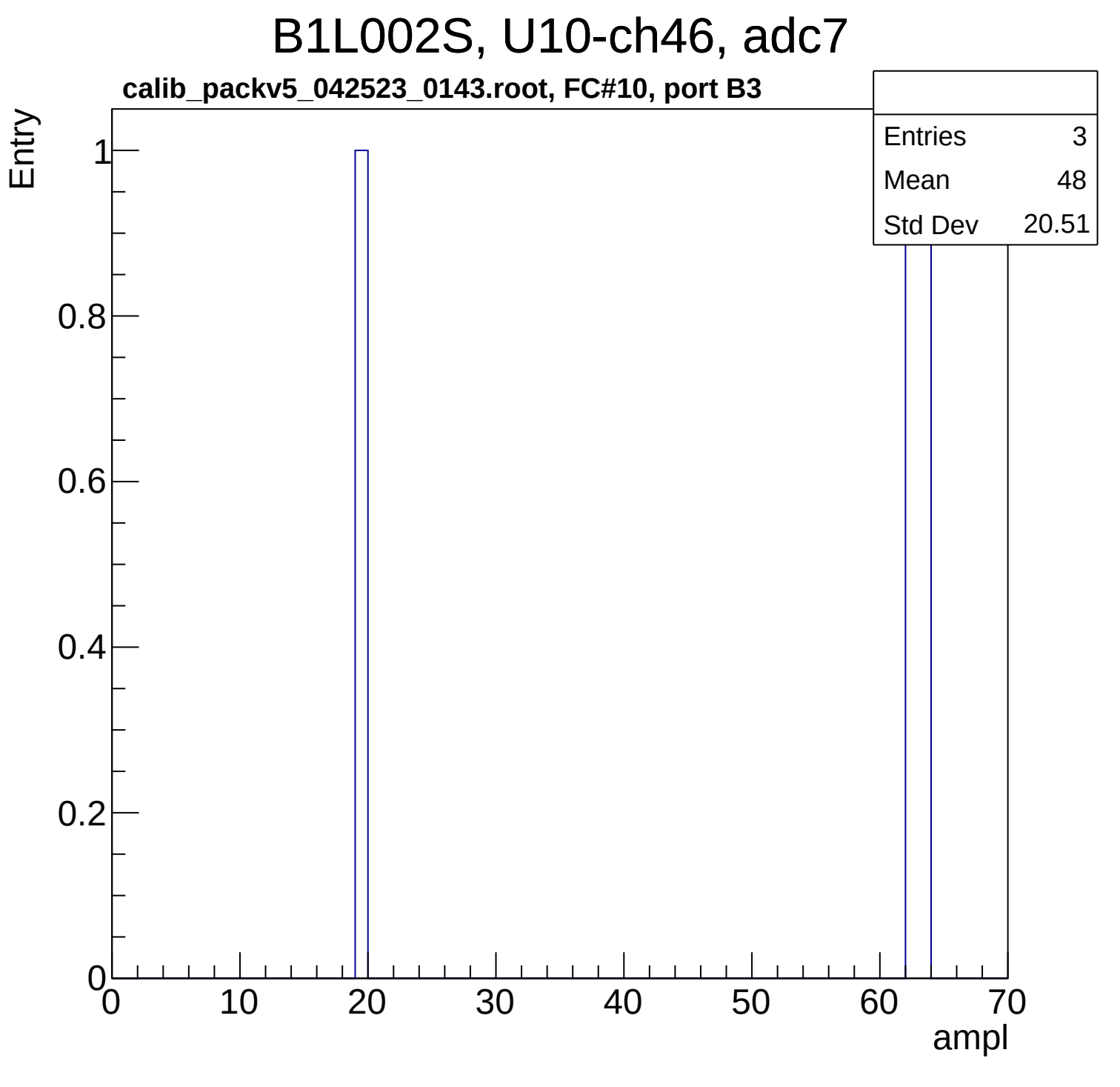
B1L002S, U10-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48
Std Dev 20.51
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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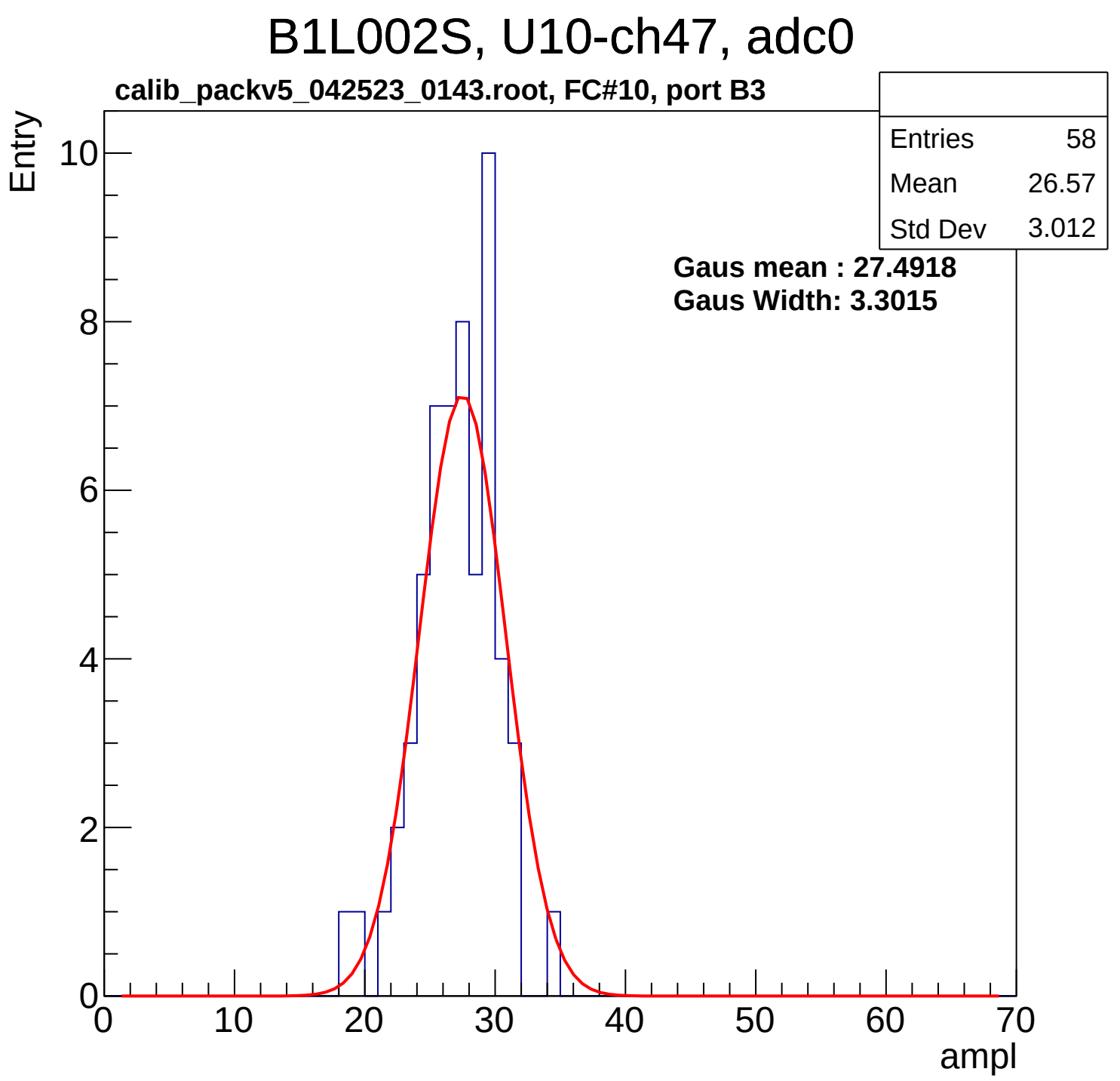
B1L002S, U10-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.57
Std Dev 3.012
Gaus mean : 27.4918
Gaus Width: 3.3015
Entry
ampl
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4
6
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40
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60
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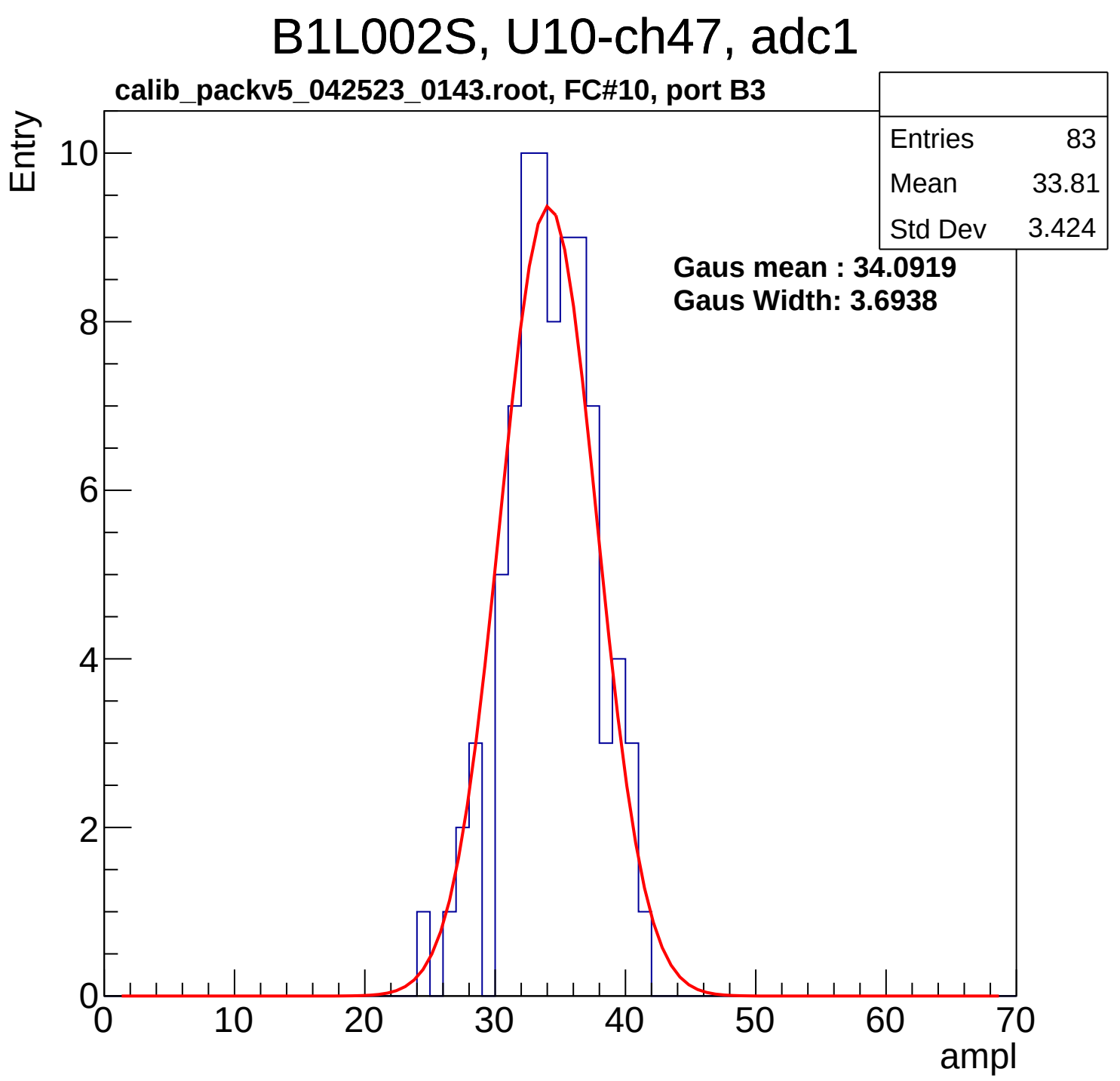
B1L002S, U10-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 33.81
Std Dev 3.424
Gaus mean : 34.0919
Gaus Width: 3.6938
Entry
ampl
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2
4
6
8
10
0
10
20
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40
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60
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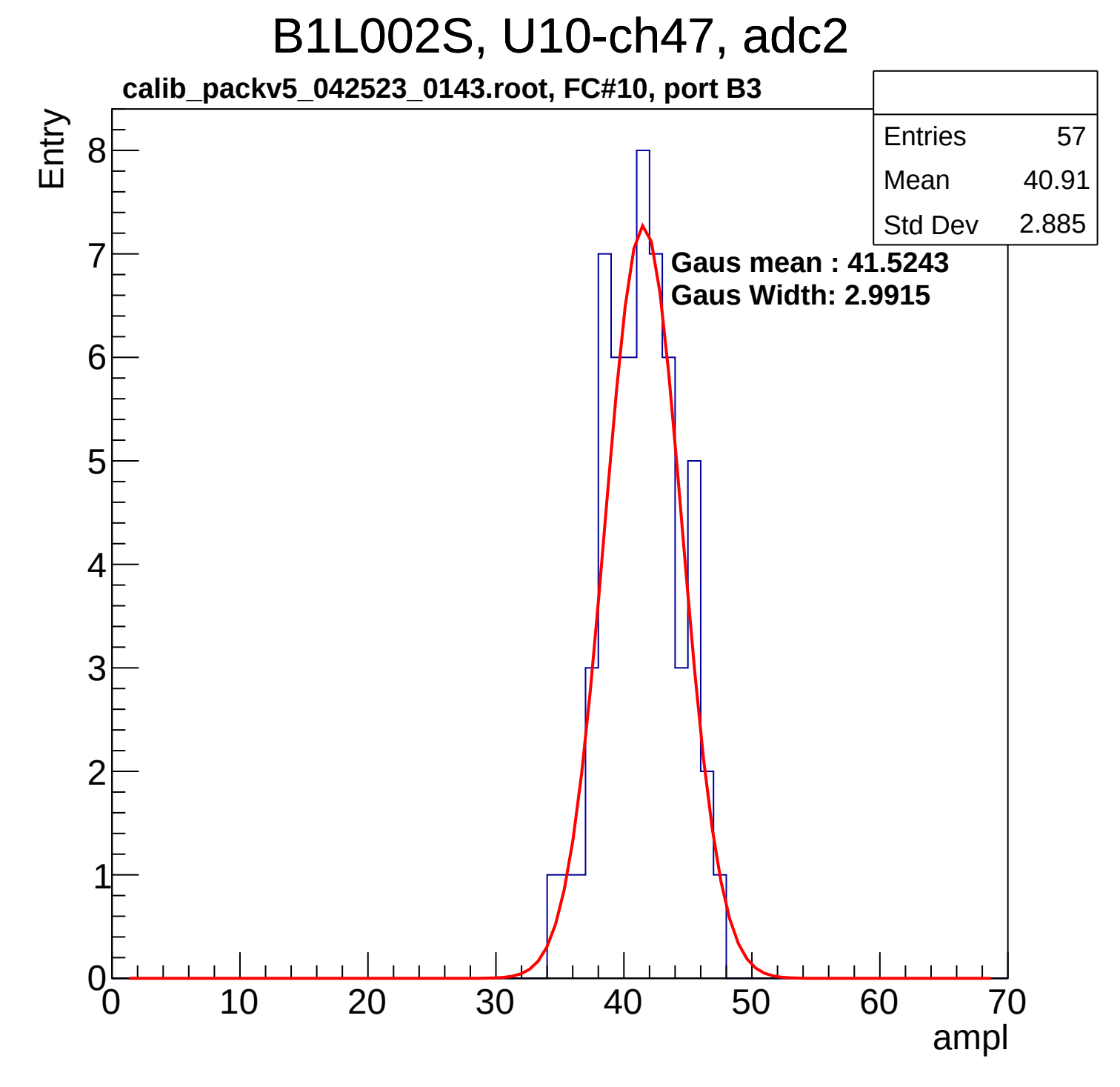
B1L002S, U10-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.91
Std Dev 2.885
Gaus mean : 41.5243
Gaus Width: 2.9915
Entry
ampl
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1
2
3
4
5
6
7
8
0
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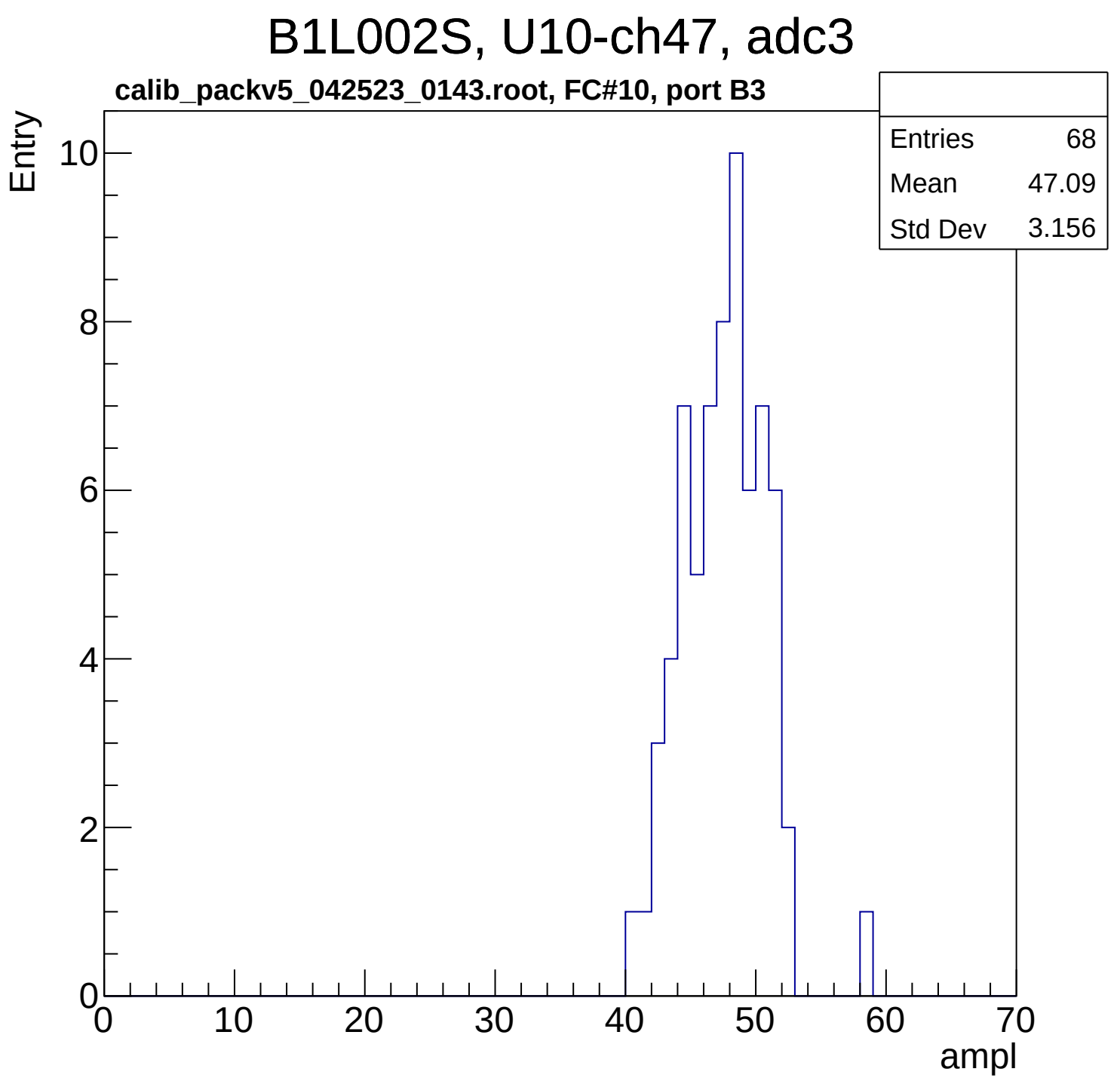
B1L002S, U10-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.09
Std Dev 3.156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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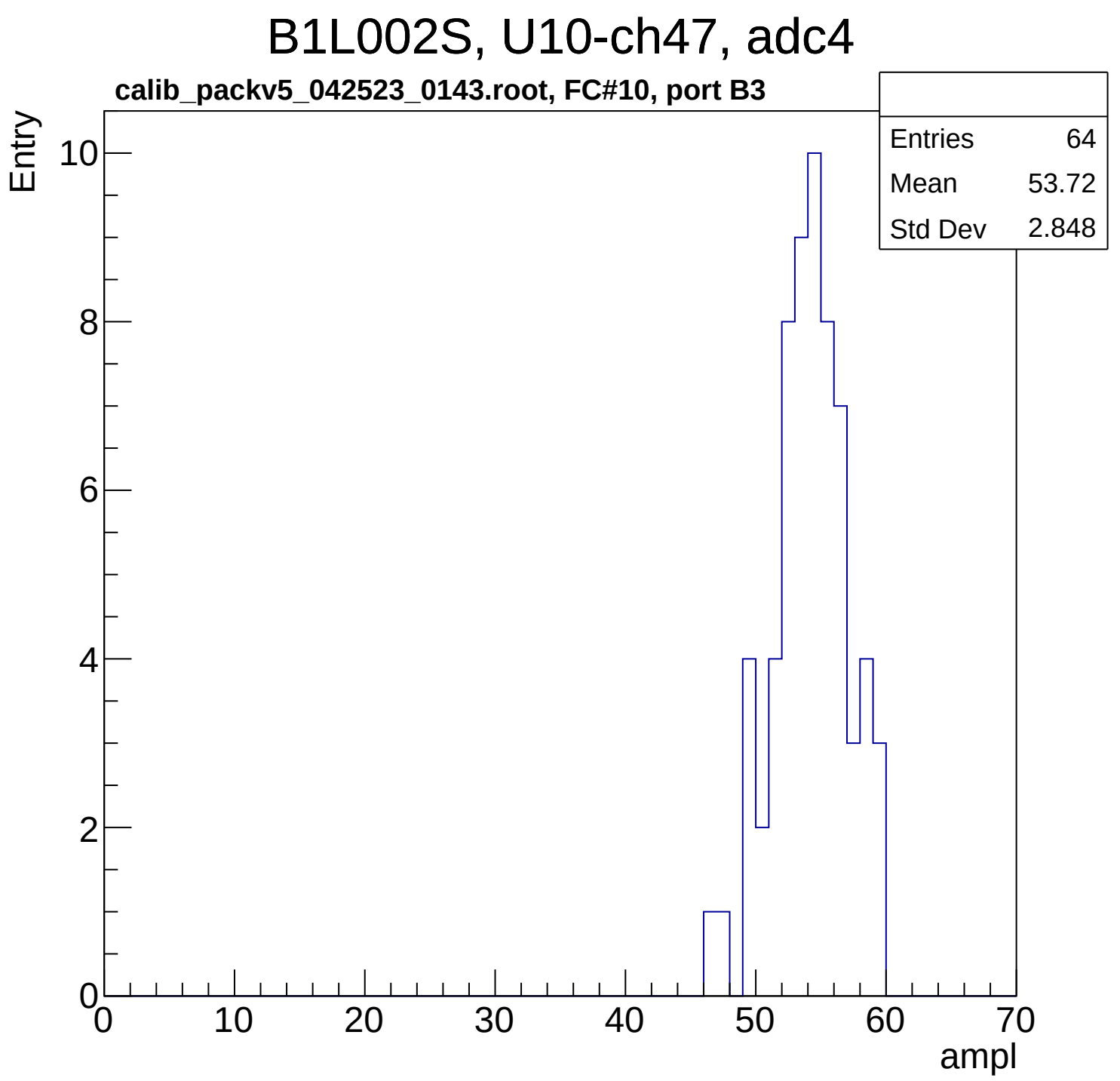
B1L002S, U10-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.72
Std Dev 2.848
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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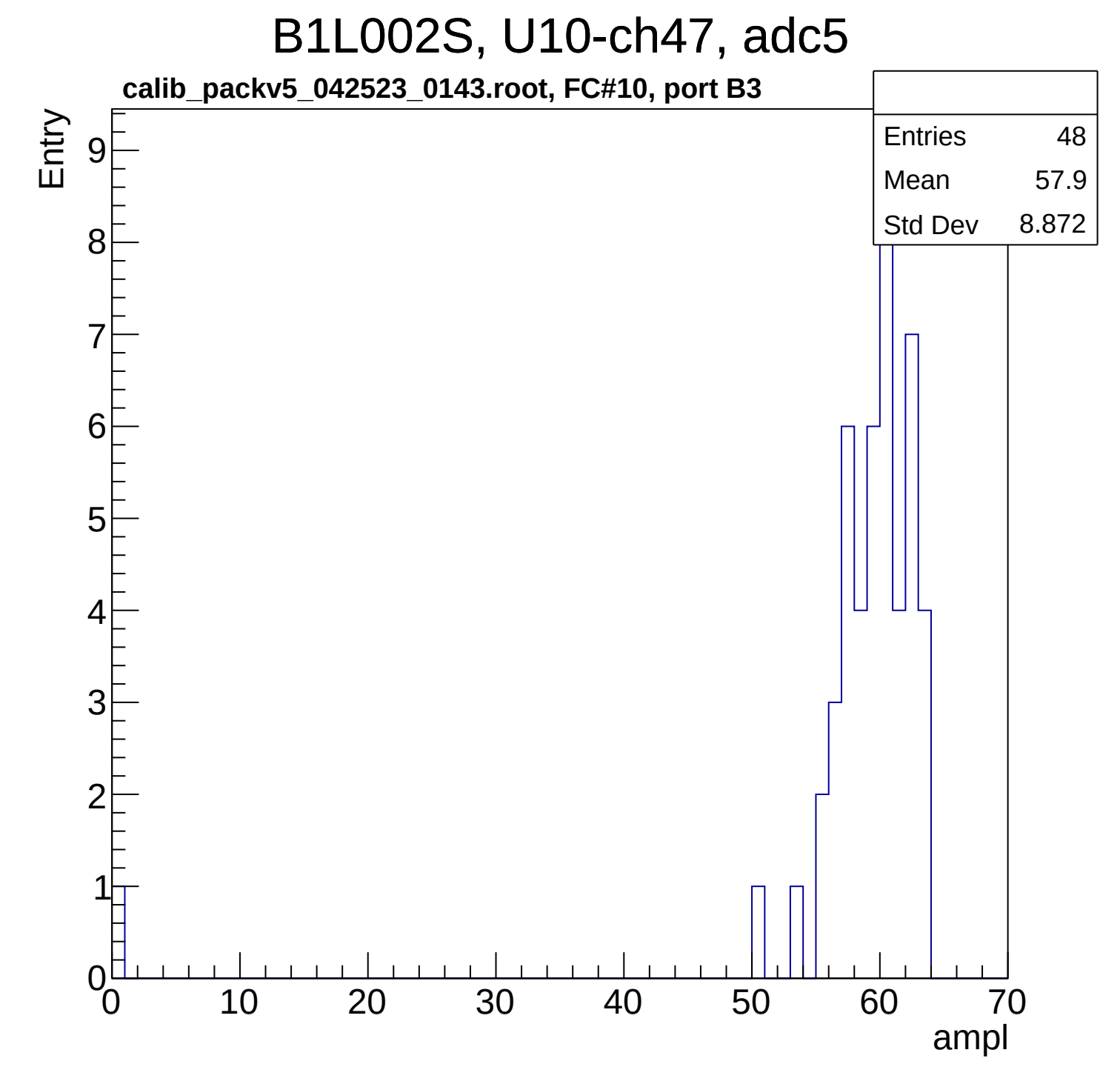
B1L002S, U10-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.9
Std Dev 8.872
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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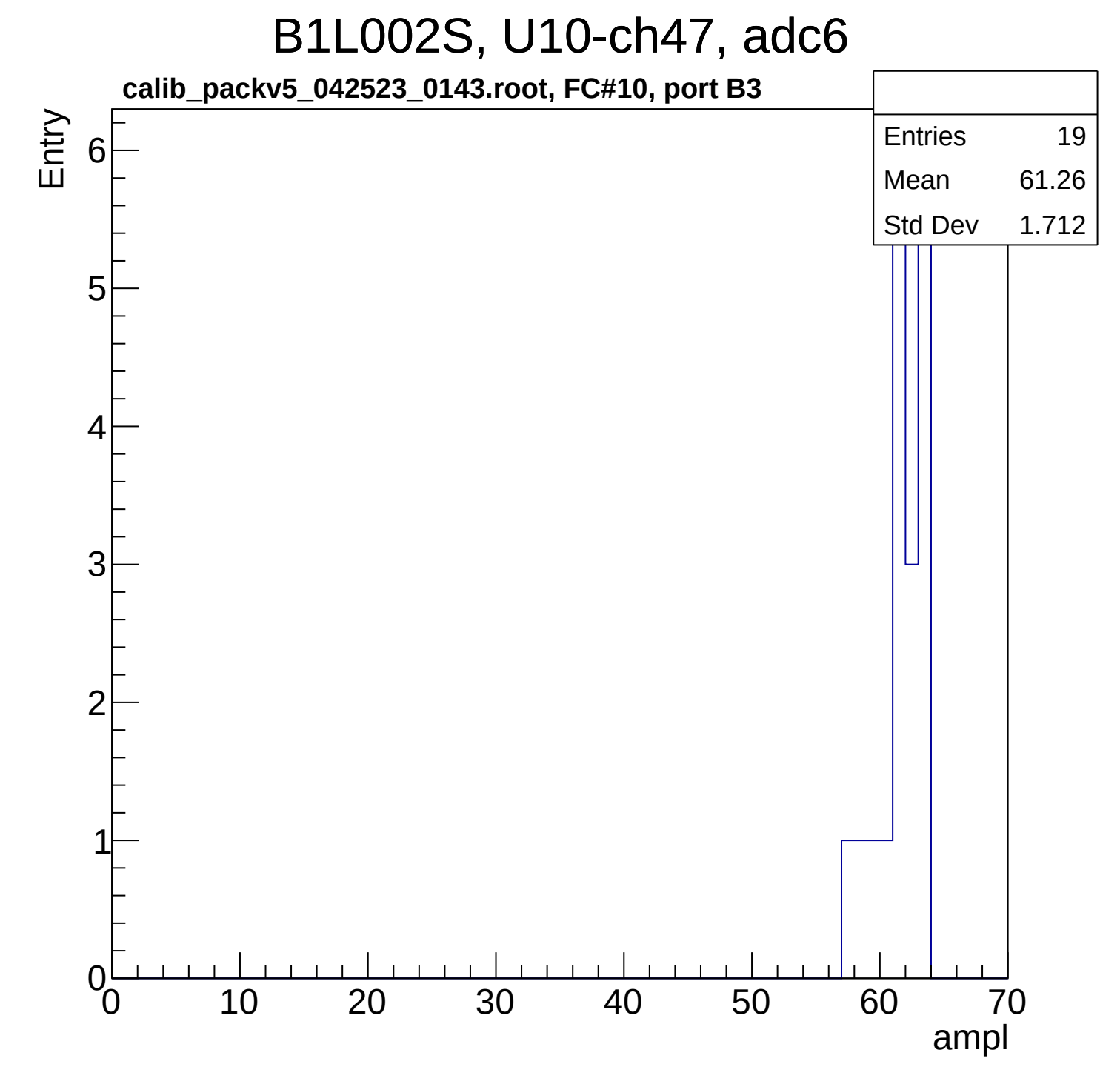
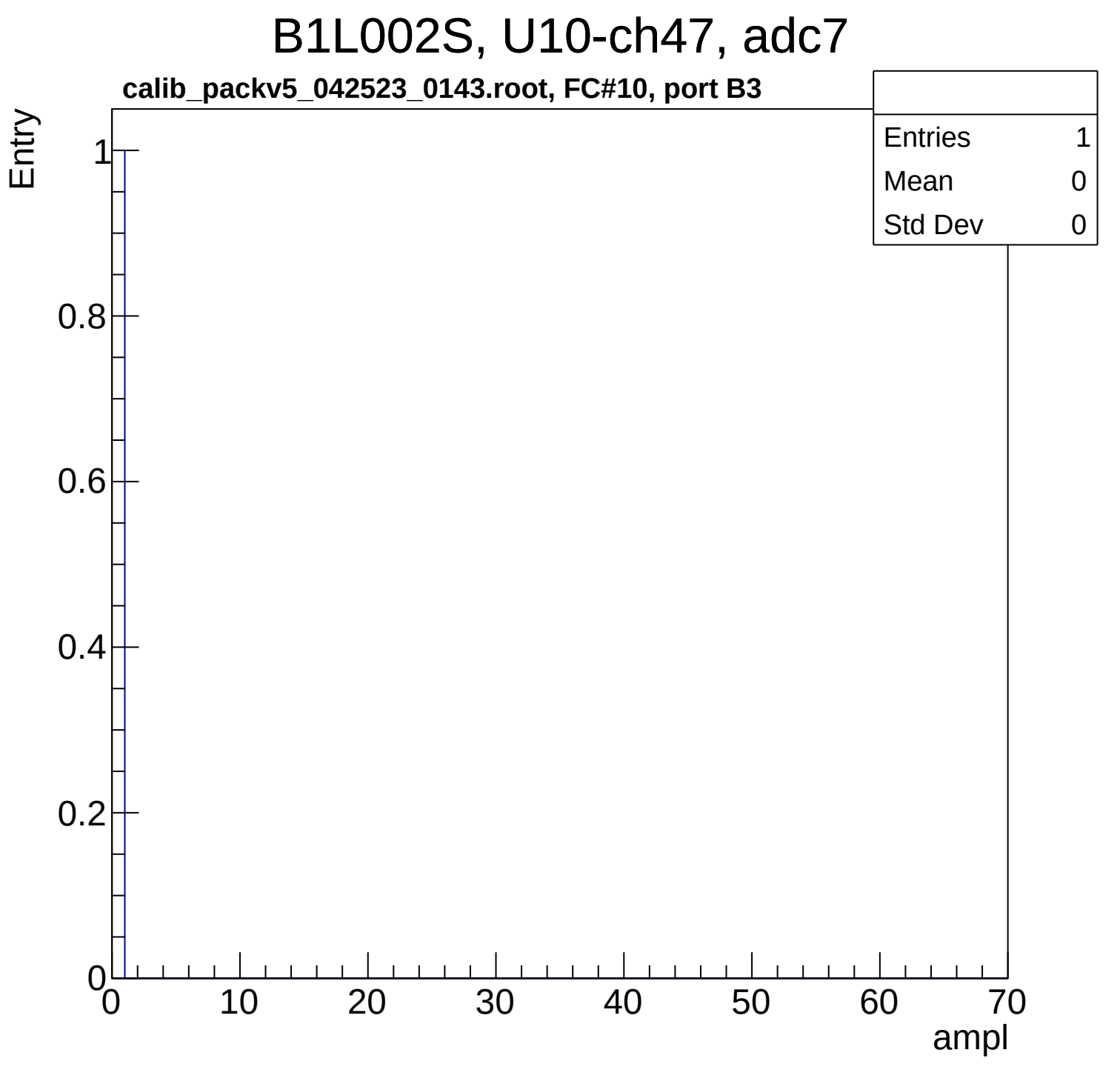
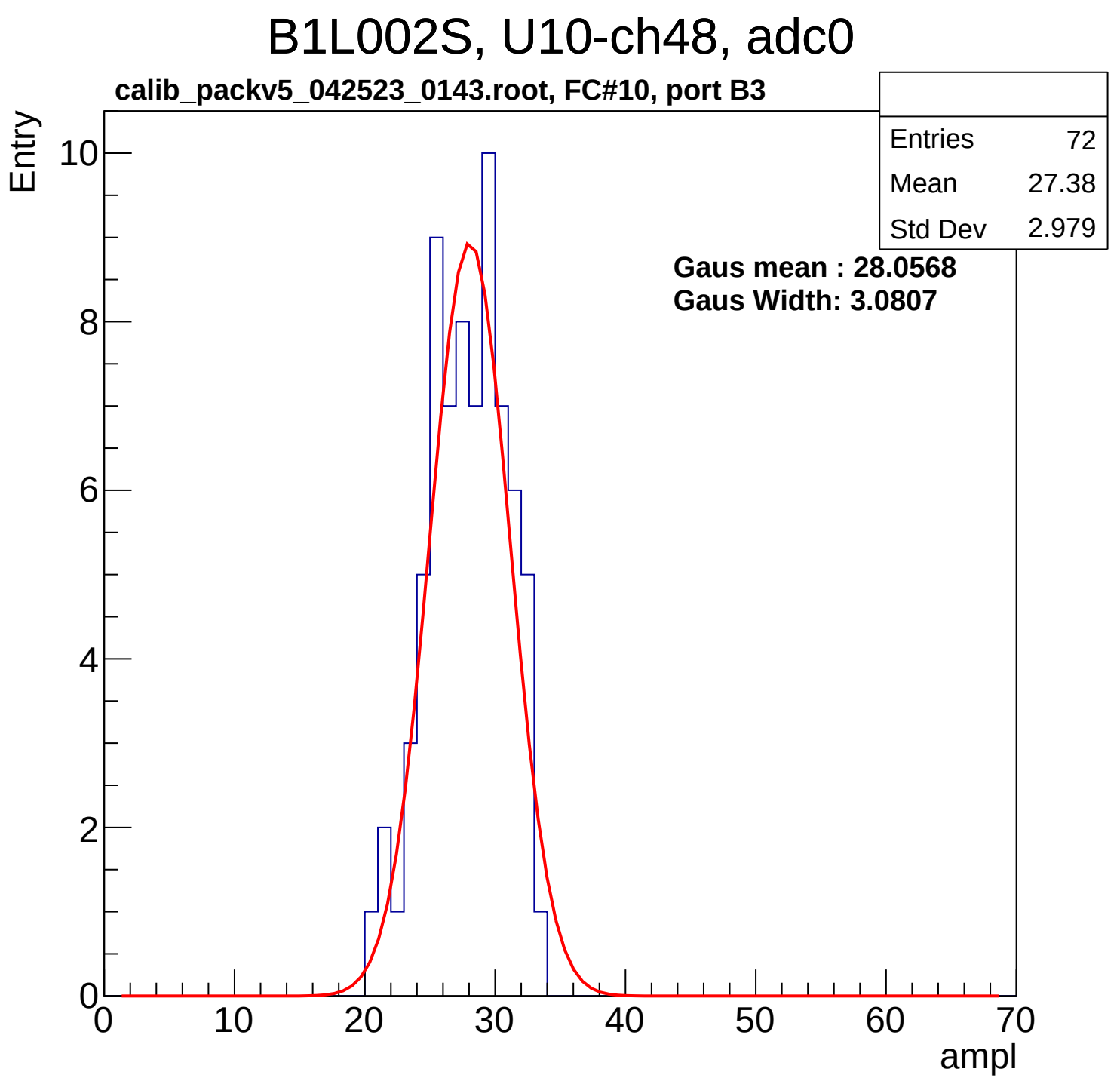

B1L002S, U10-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.26
Std Dev 1.712
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.38
Std Dev 2.979
Gaus mean : 28.0568
Gaus Width: 3.0807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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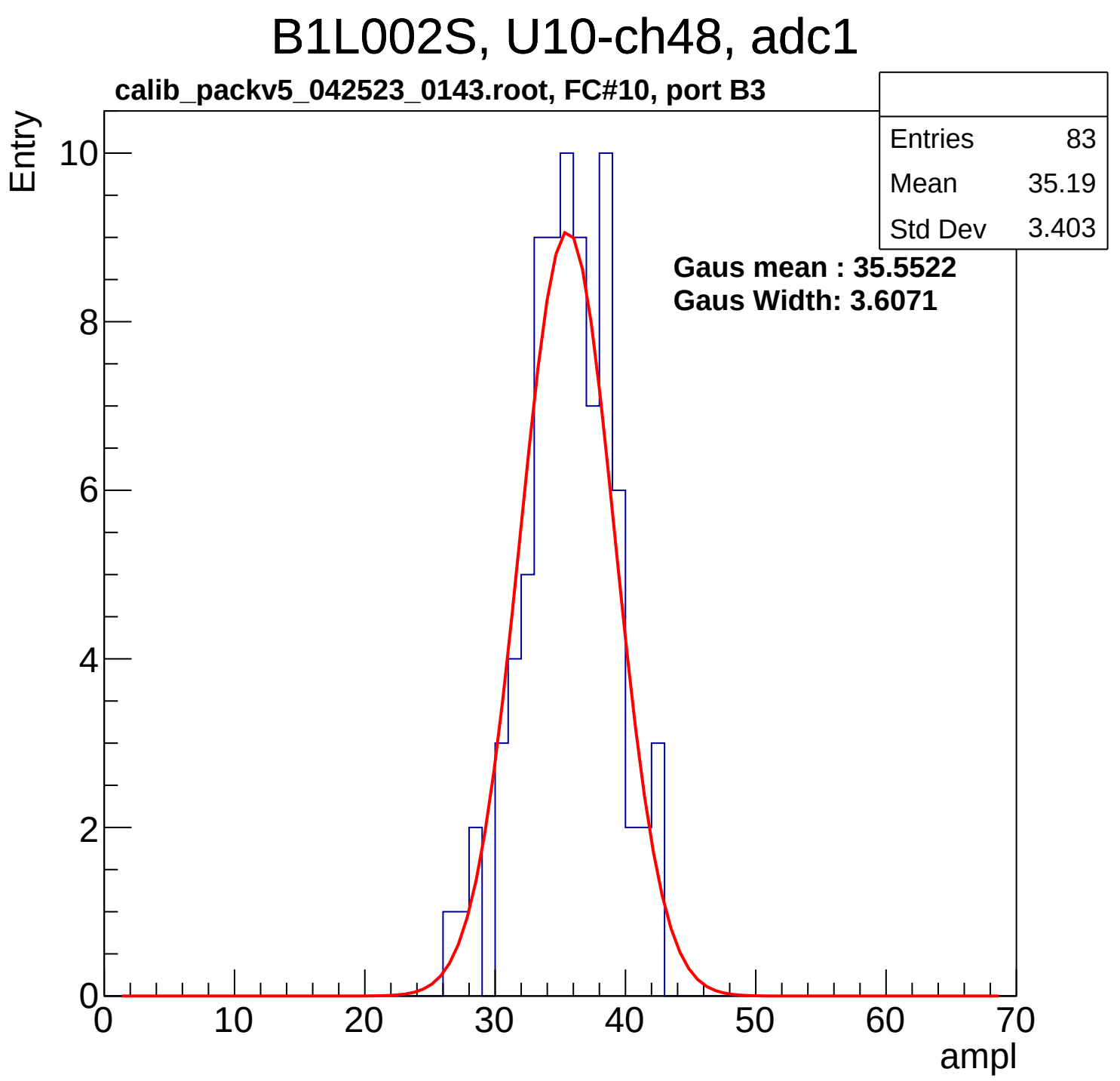
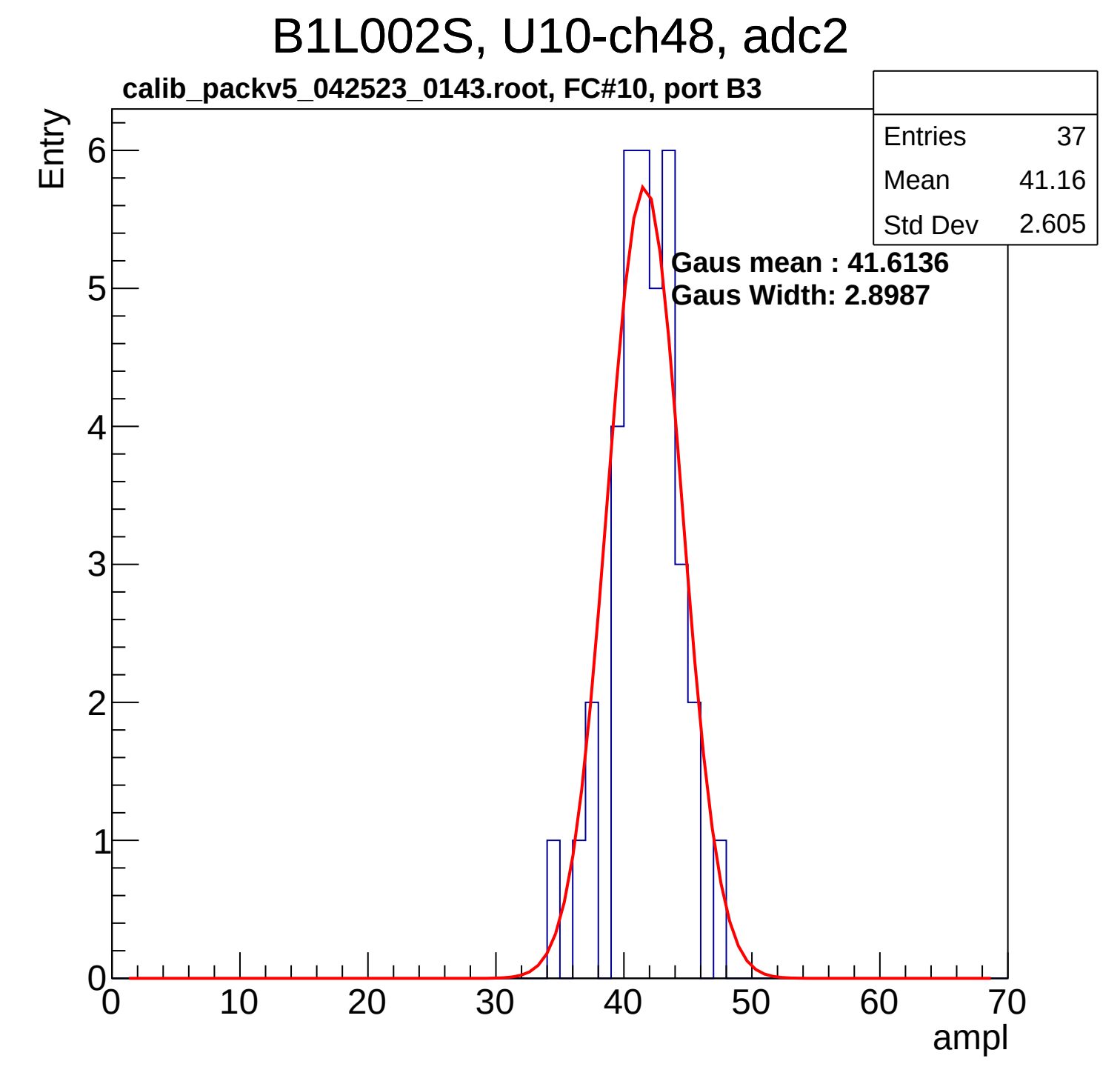
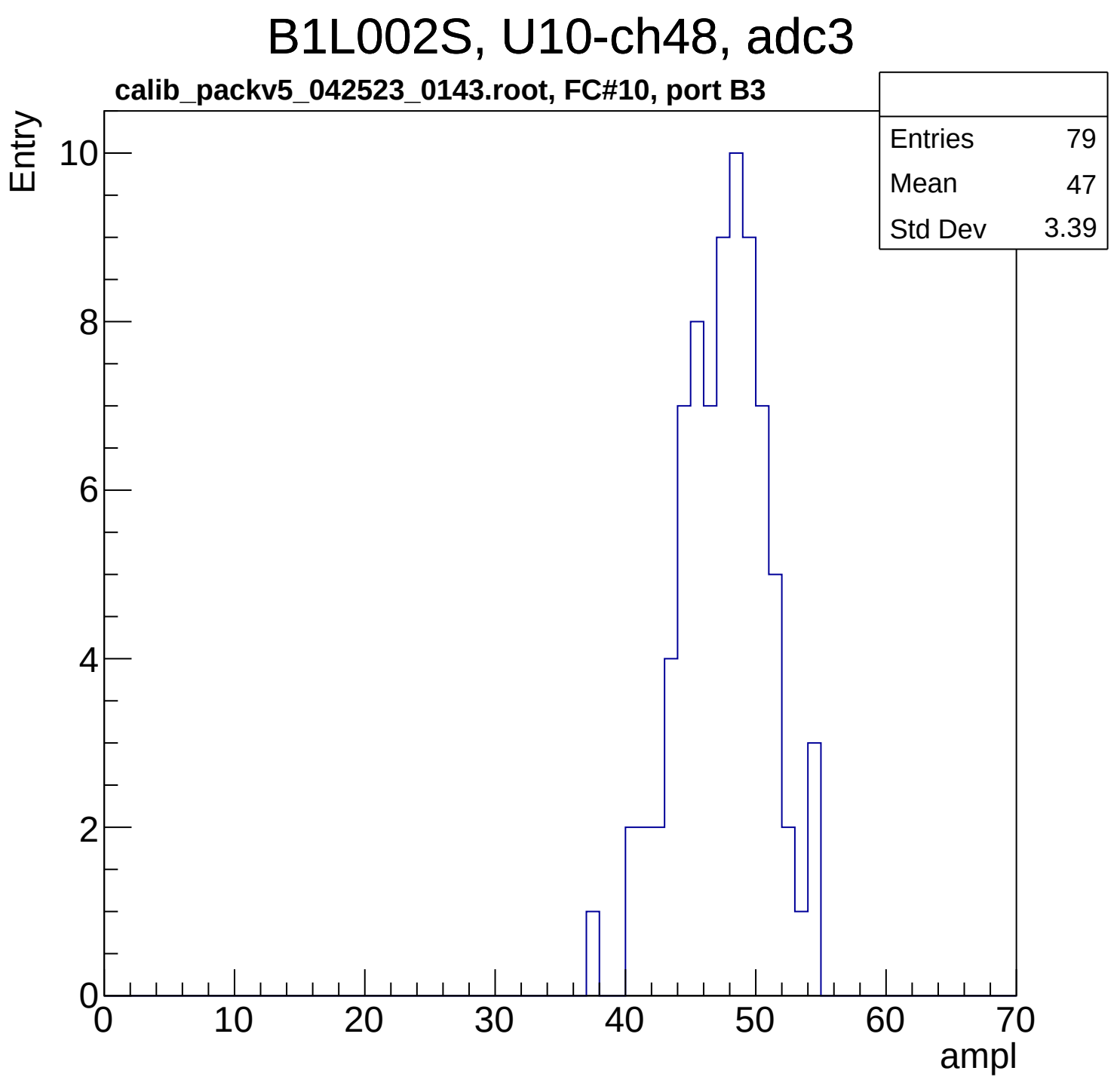
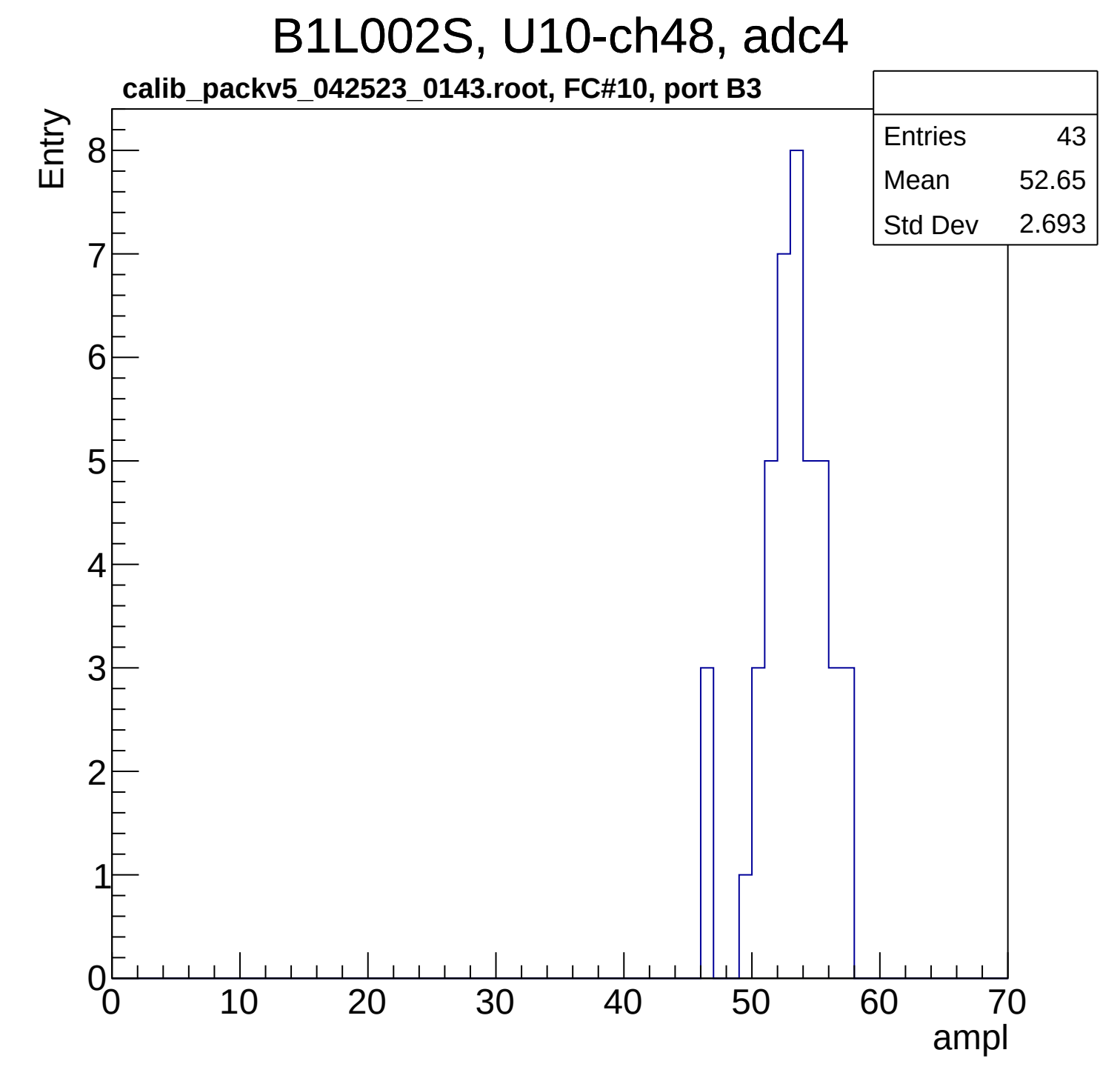

B1L002S, U10-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 35.19
Std Dev 3.403
Gaus mean : 35.5522
Gaus Width: 3.6071
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 41.16
Std Dev 2.605
Gaus mean : 41.6136
Gaus Width: 2.8987
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U10-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 47
Std Dev 3.39
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 52.65
Std Dev 2.693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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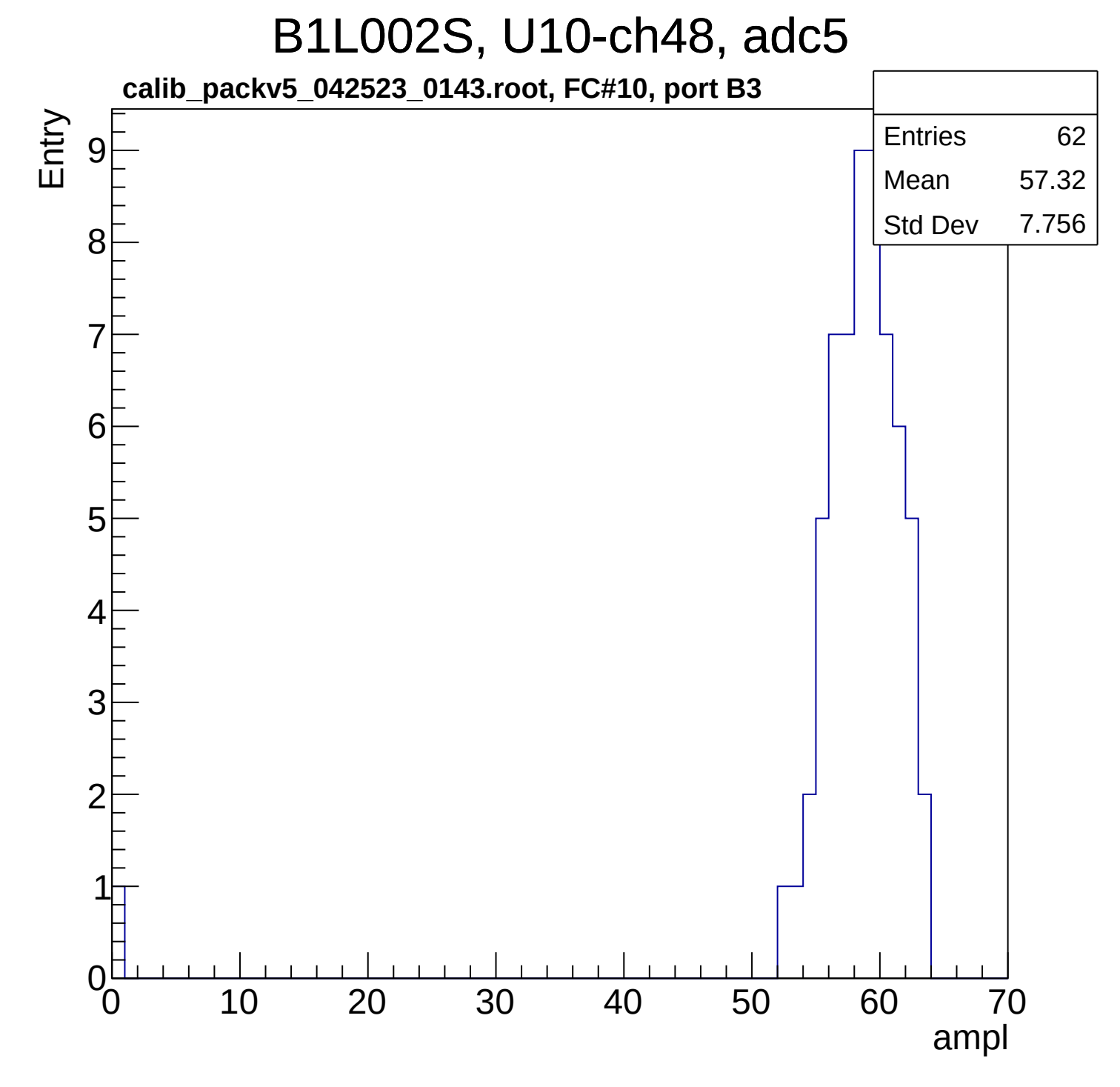
B1L002S, U10-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.32
Std Dev 7.756
Entry
ampl
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4
5
6
7
8
9
0
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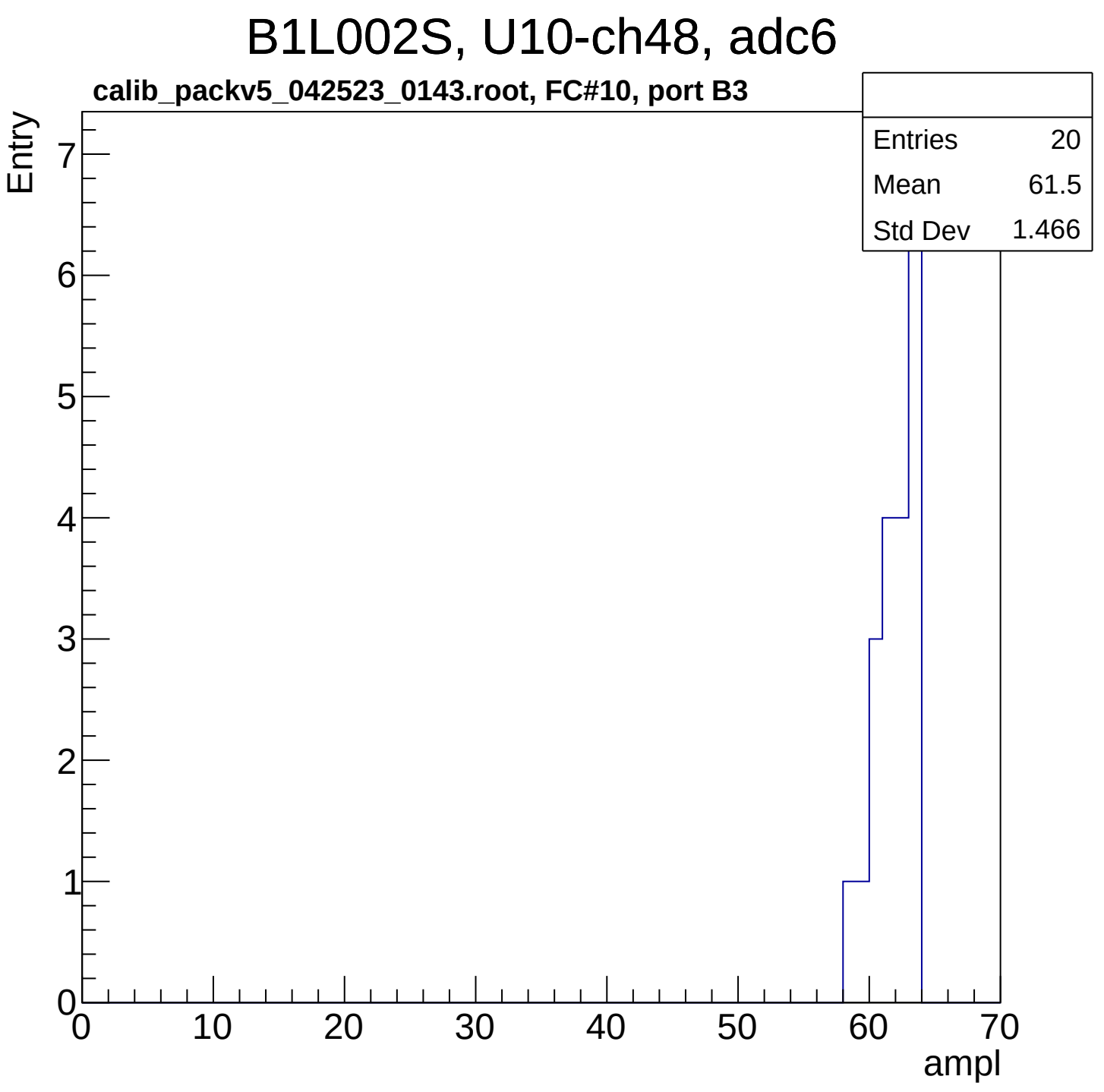
B1L002S, U10-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.5
Std Dev 1.466
Entry
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1
2
3
4
5
6
7
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10
20
30
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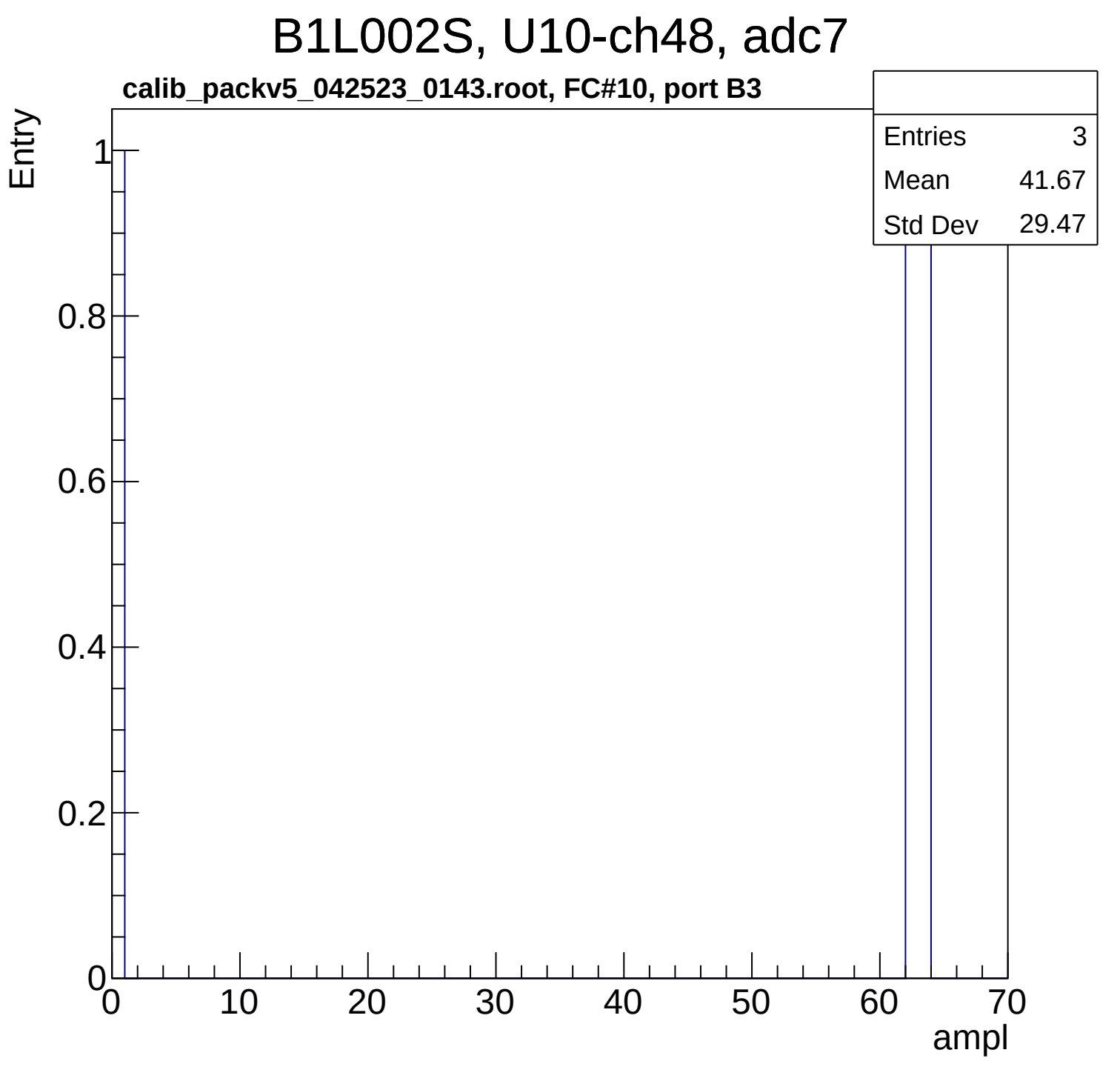
50
60
70
ampl


B1L002S, U10-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
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0.6
0.4
0.2
0
0
10
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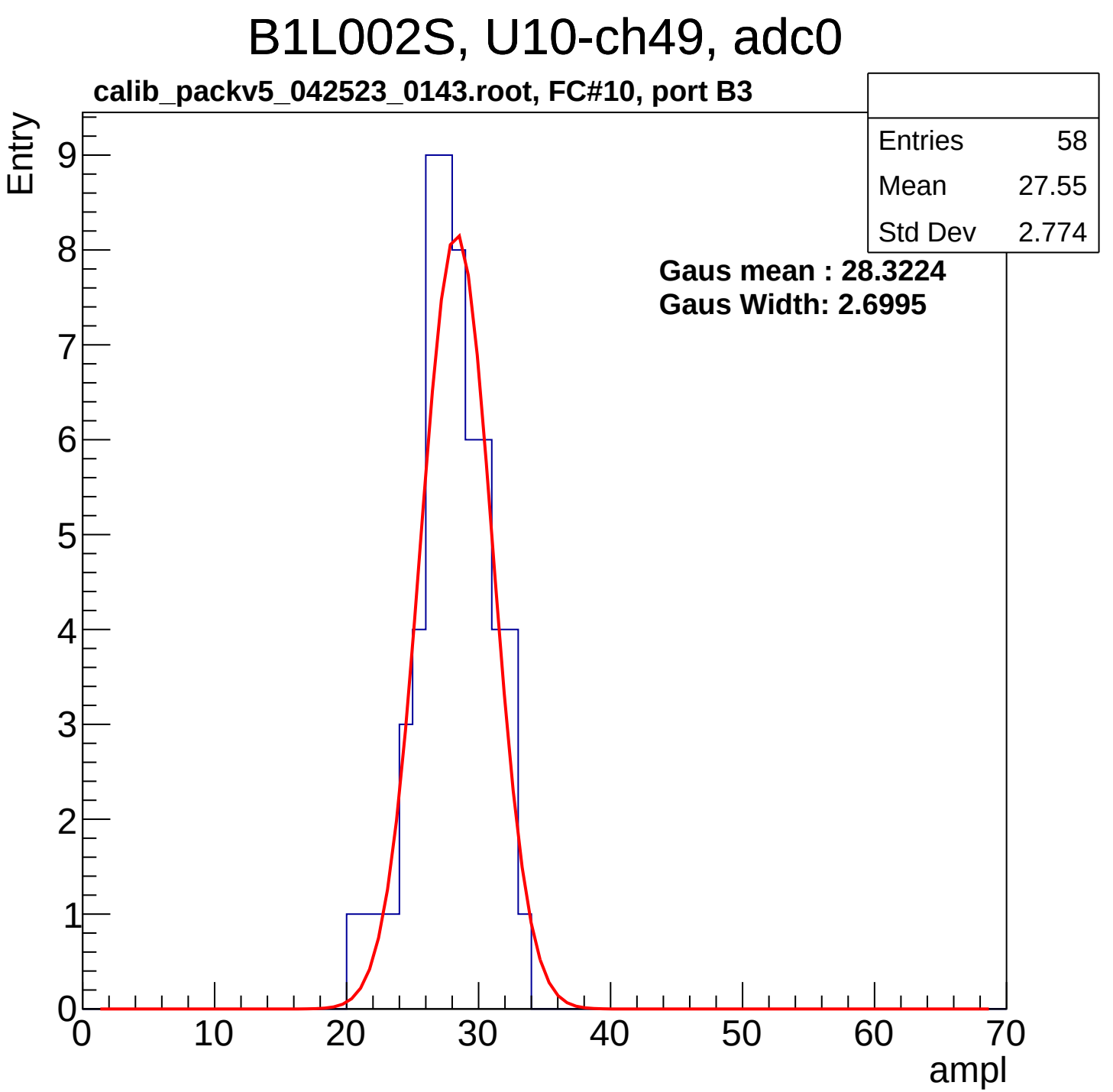
50
60
70
ampl


B1L002S, U10-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.55
Std Dev 2.774
Gaus mean : 28.3224
Gaus Width: 2.6995
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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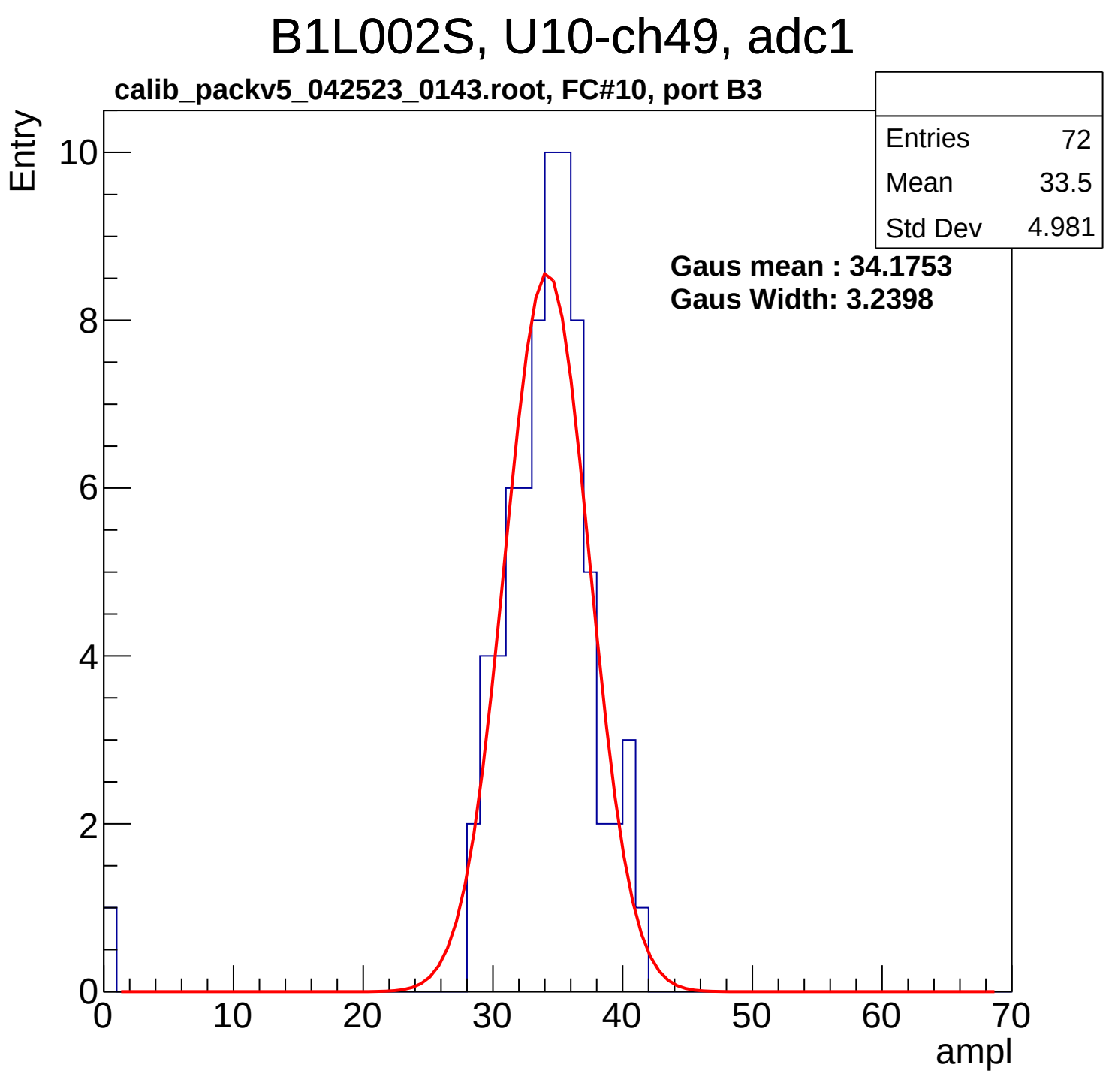
B1L002S, U10-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.5
Std Dev 4.981
Gaus mean : 34.1753
Gaus Width: 3.2398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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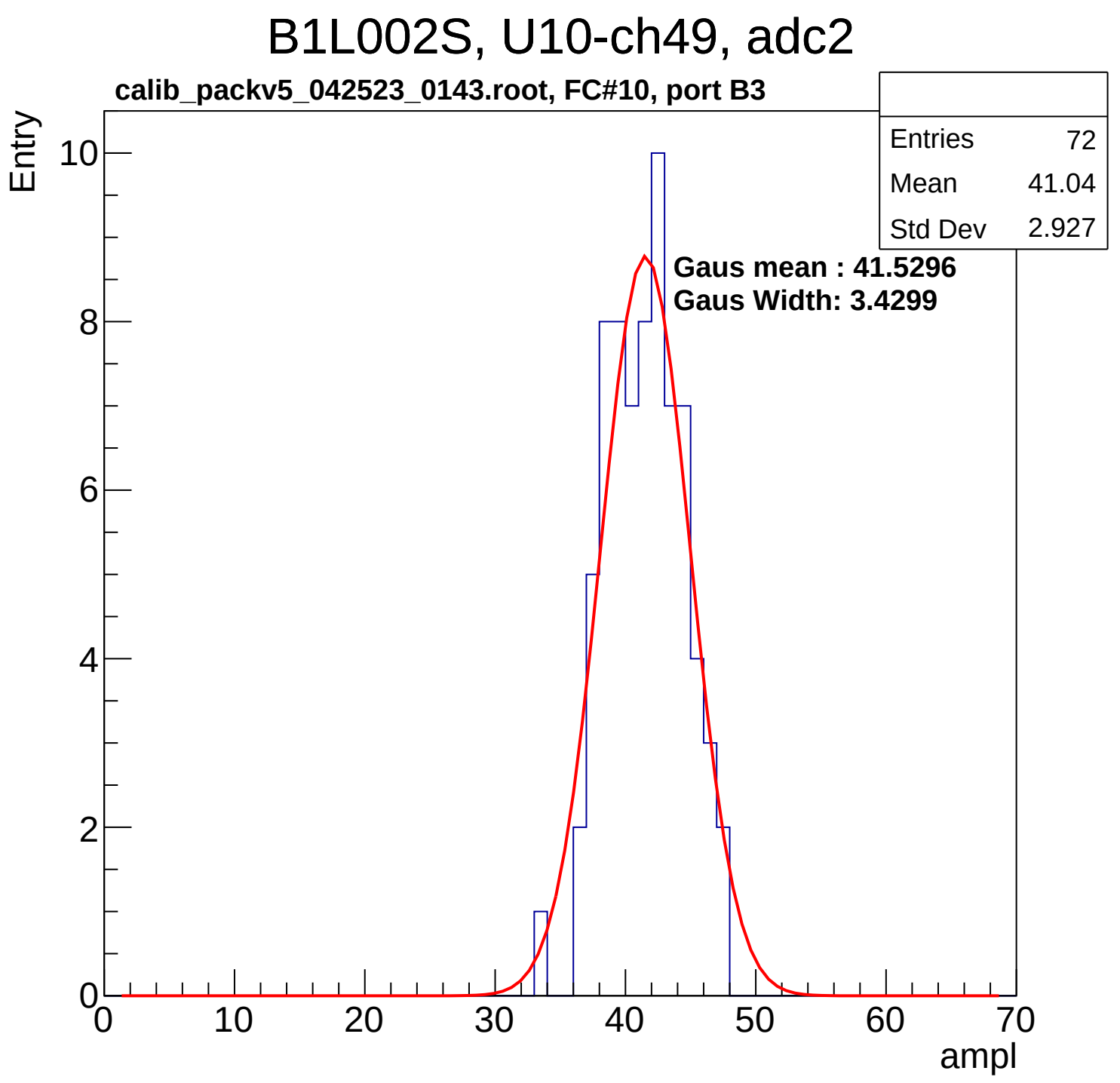
B1L002S, U10-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.04
Std Dev 2.927
Gaus mean : 41.5296
Gaus Width: 3.4299
Entry
ampl
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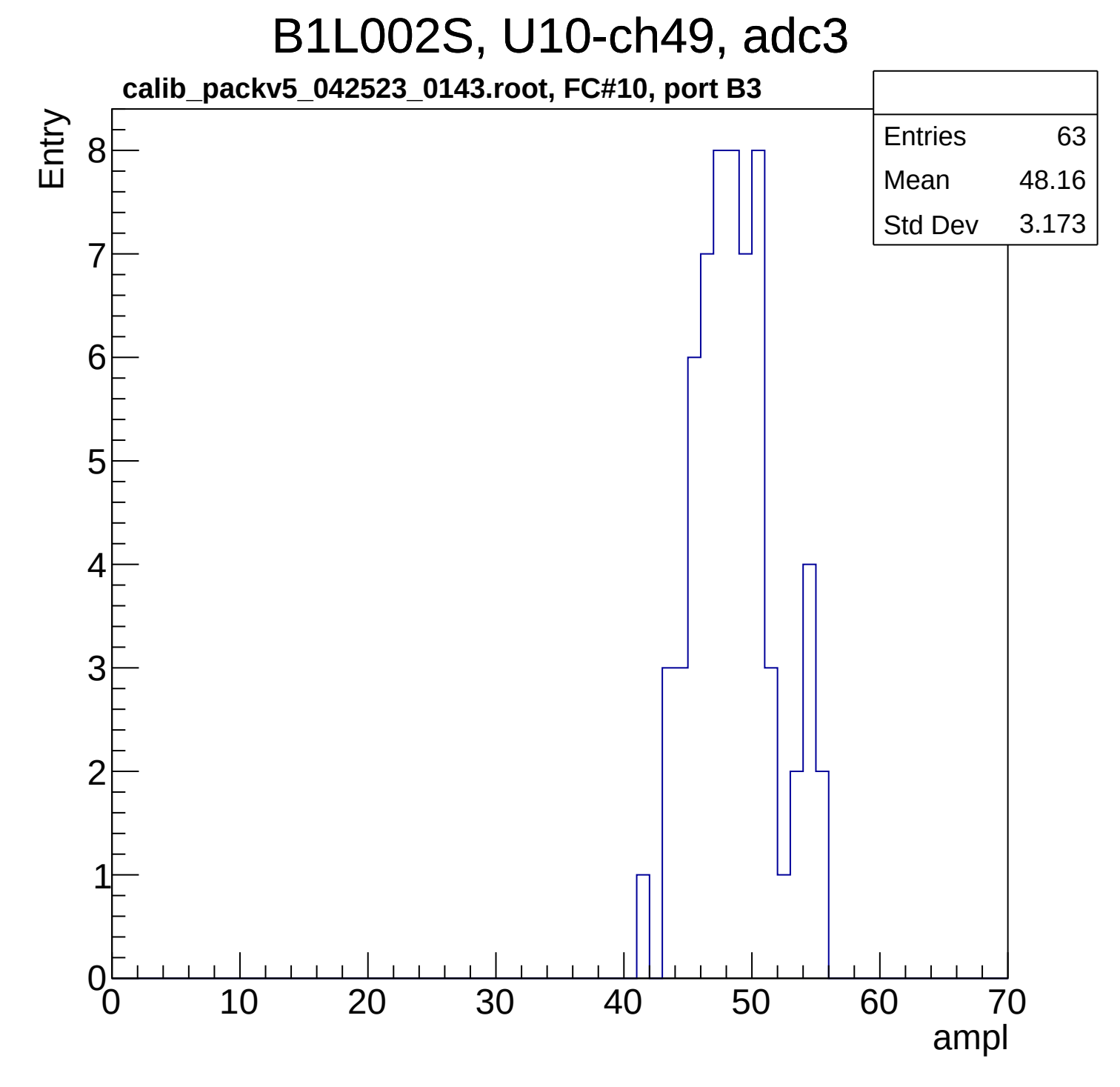
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.16
Std Dev 3.173
Entry
ampl
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3
4
5
6
7
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0
10
20
30
40
50
60
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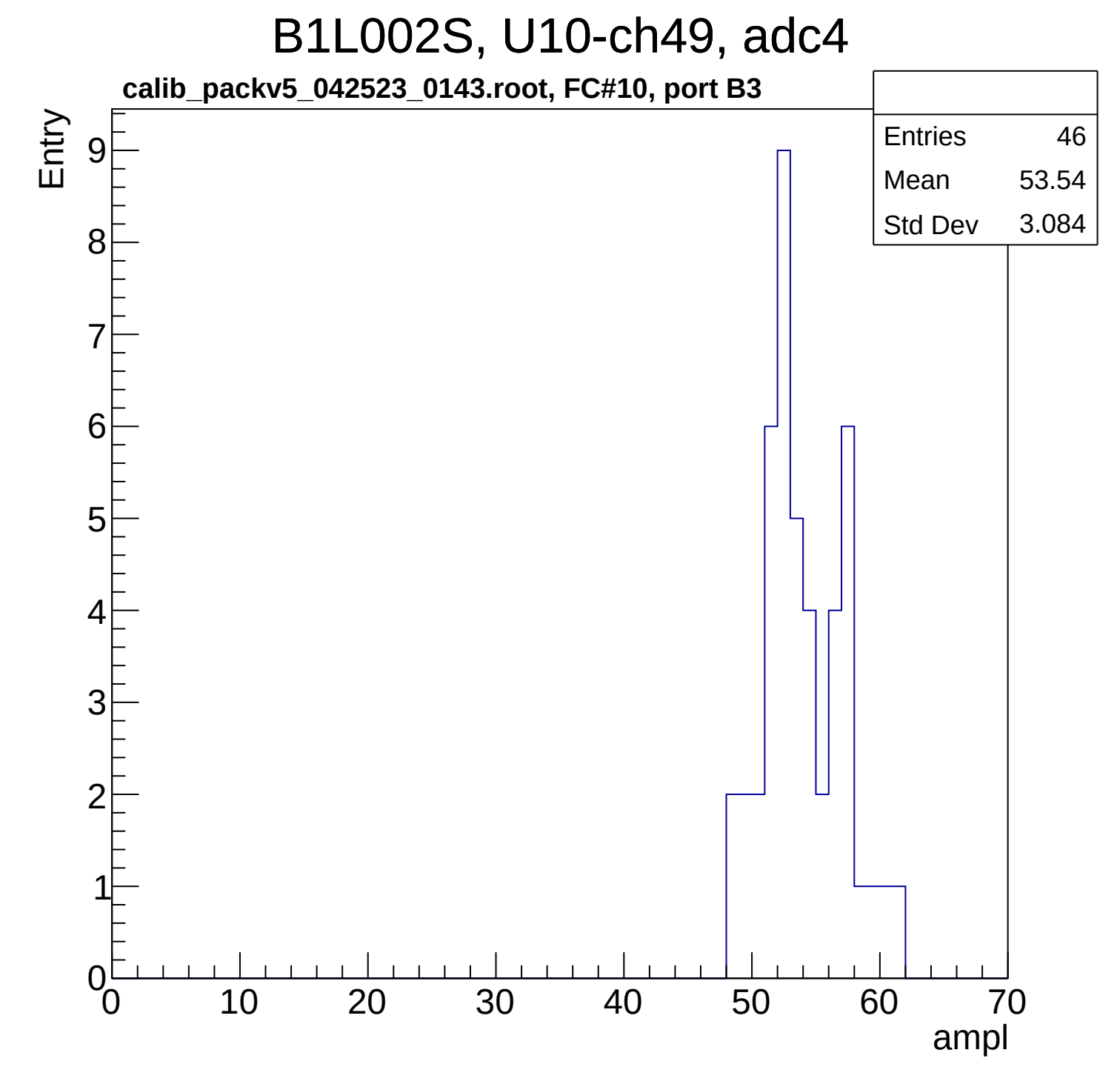
B1L002S, U10-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 53.54
Std Dev 3.084
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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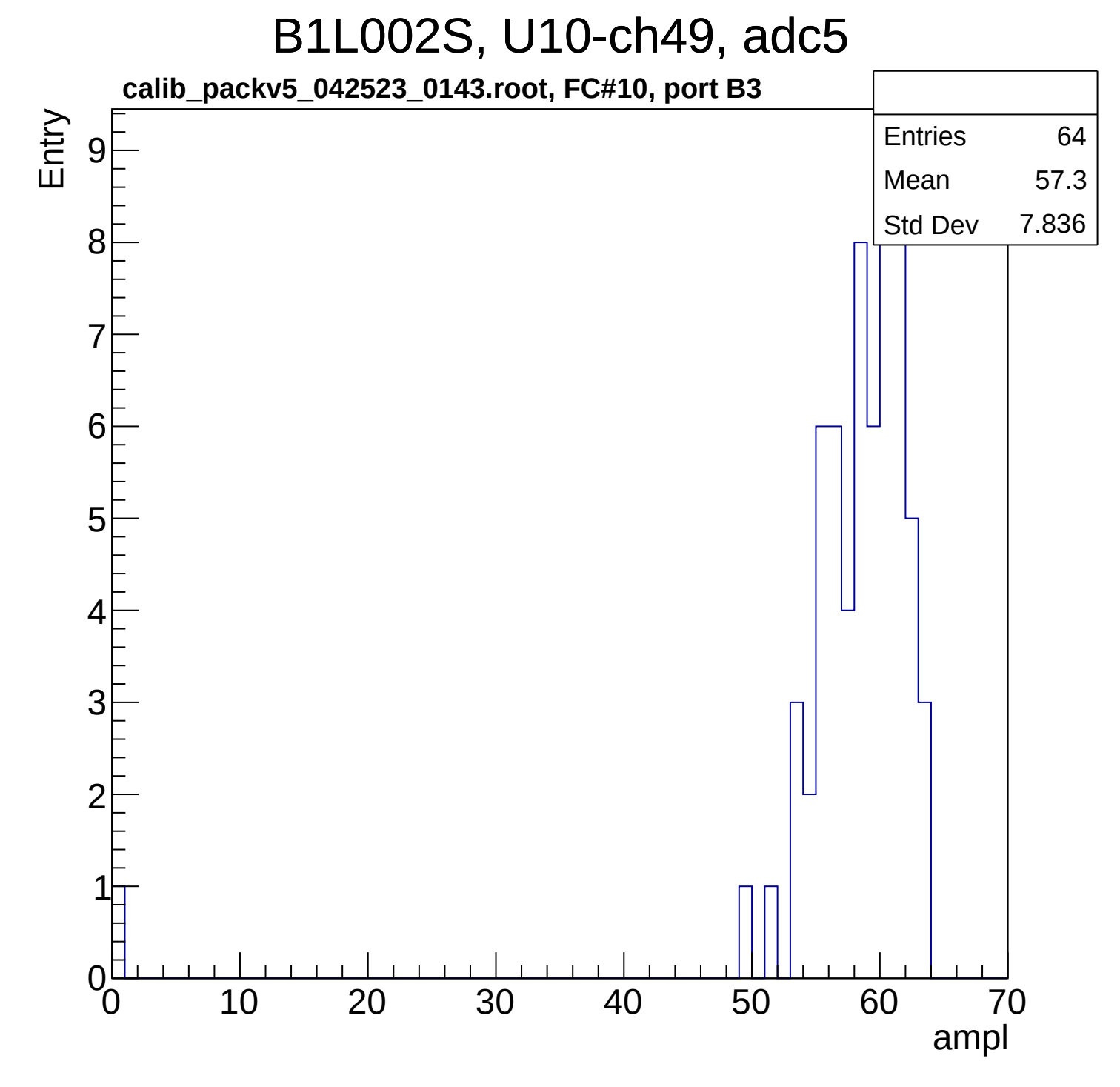
B1L002S, U10-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.3
Std Dev 7.836
Entry
ampl
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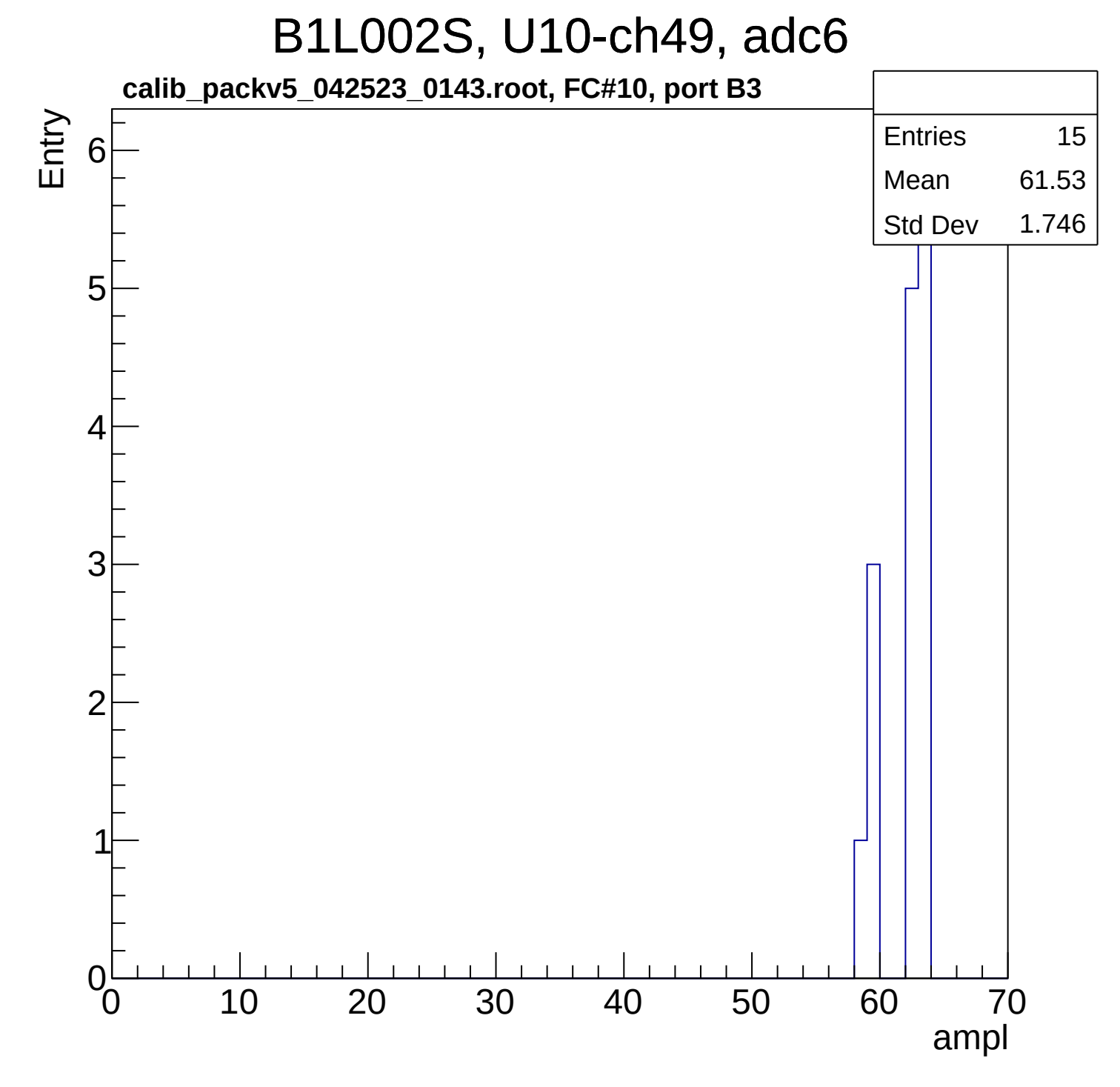
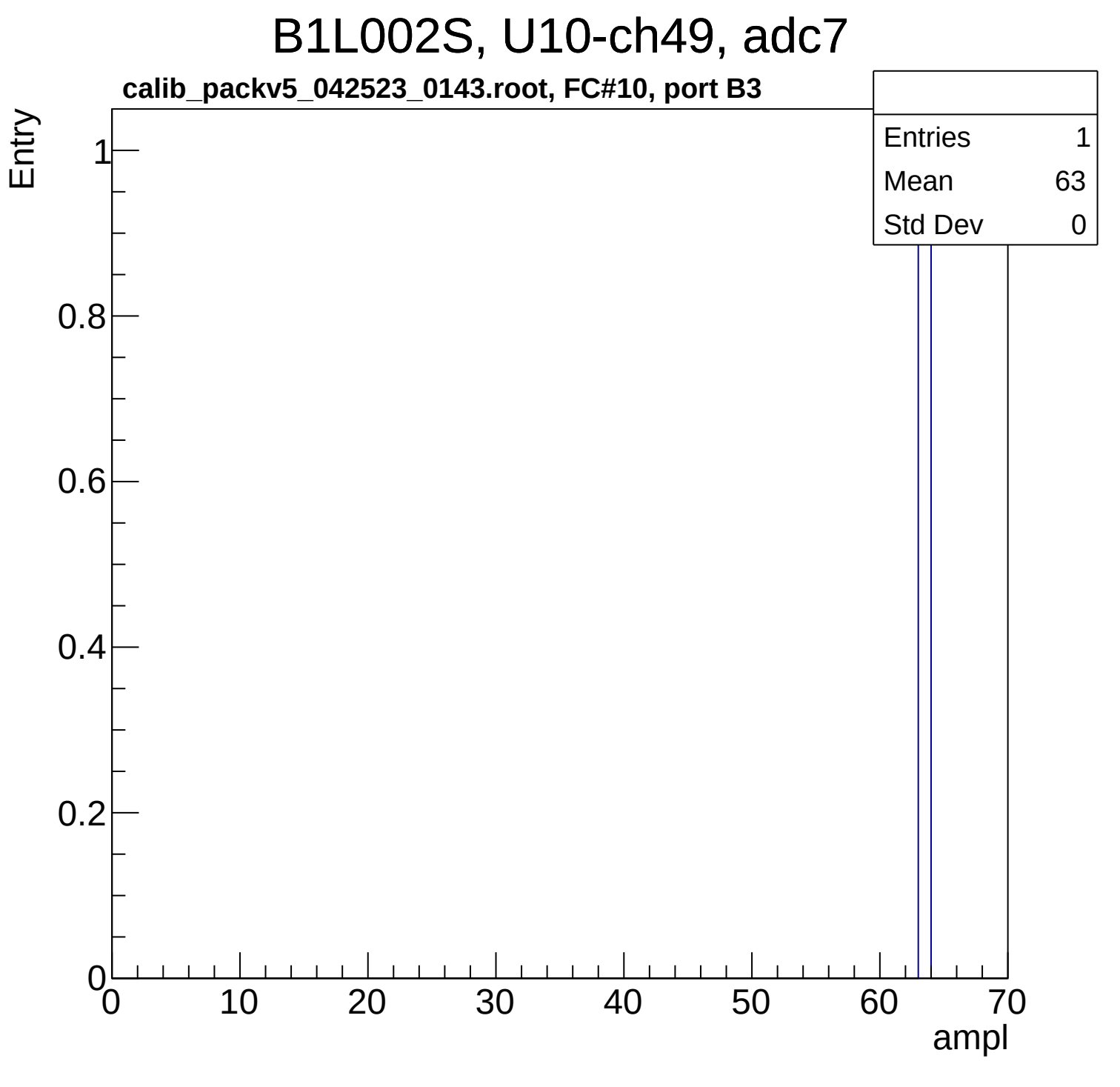
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.53
Std Dev 1.746
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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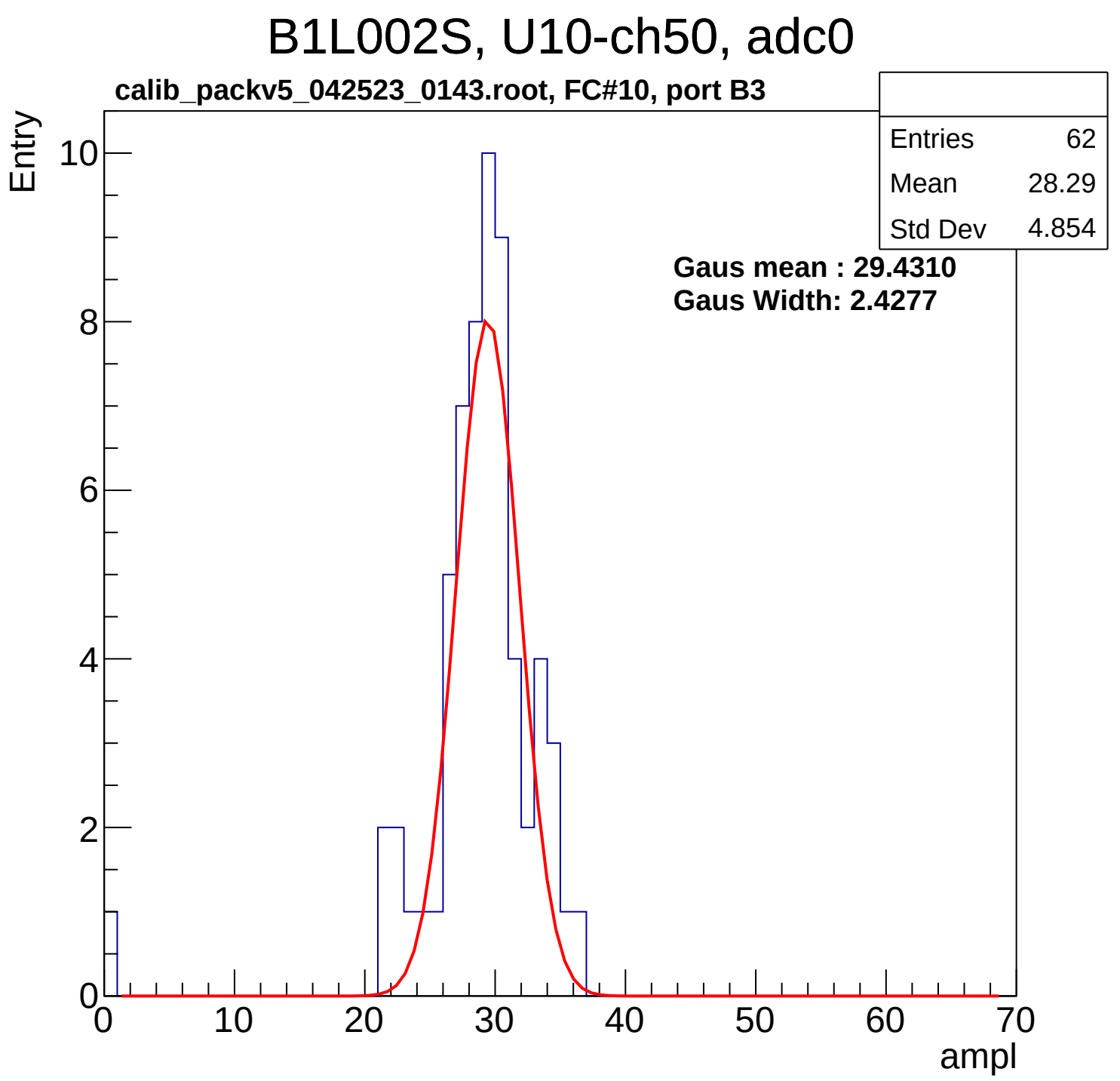
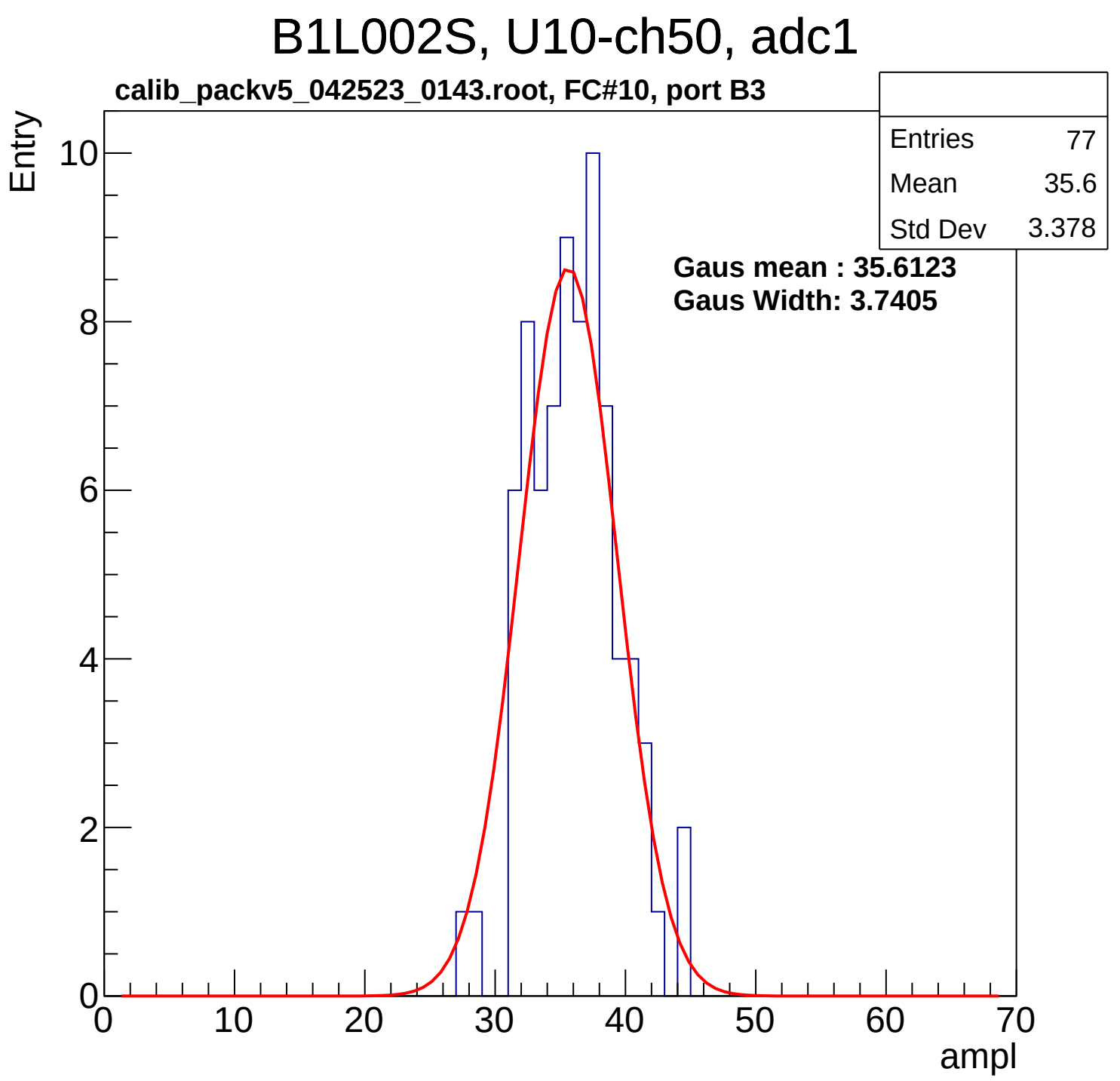
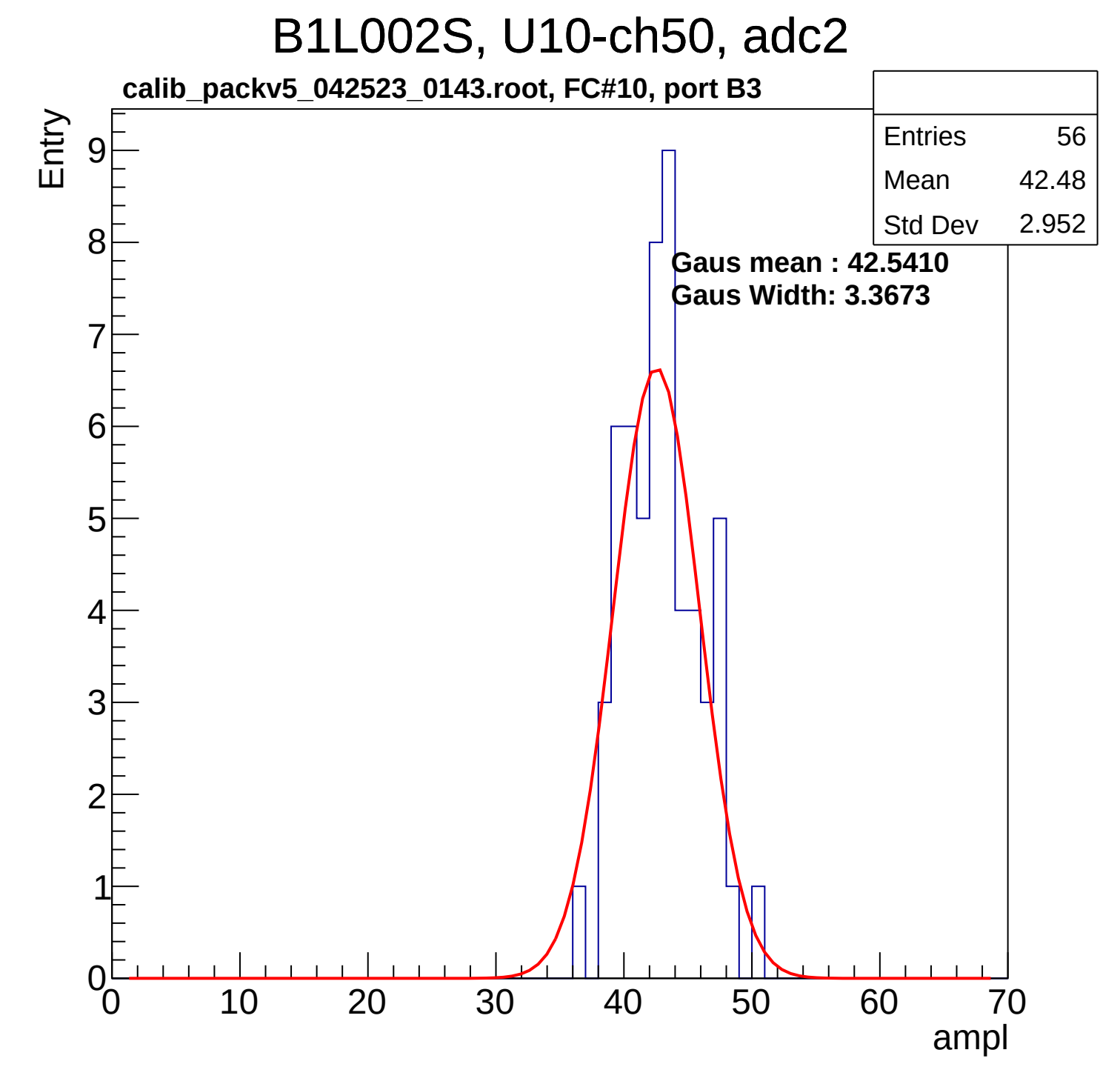
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.29
Std Dev 4.854
Gaus mean : 29.4310
Gaus Width: 2.4277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.6
Std Dev 3.378
Gaus mean : 35.6123
Gaus Width: 3.7405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.48
Std Dev 2.952
Gaus mean : 42.5410
Gaus Width: 3.3673
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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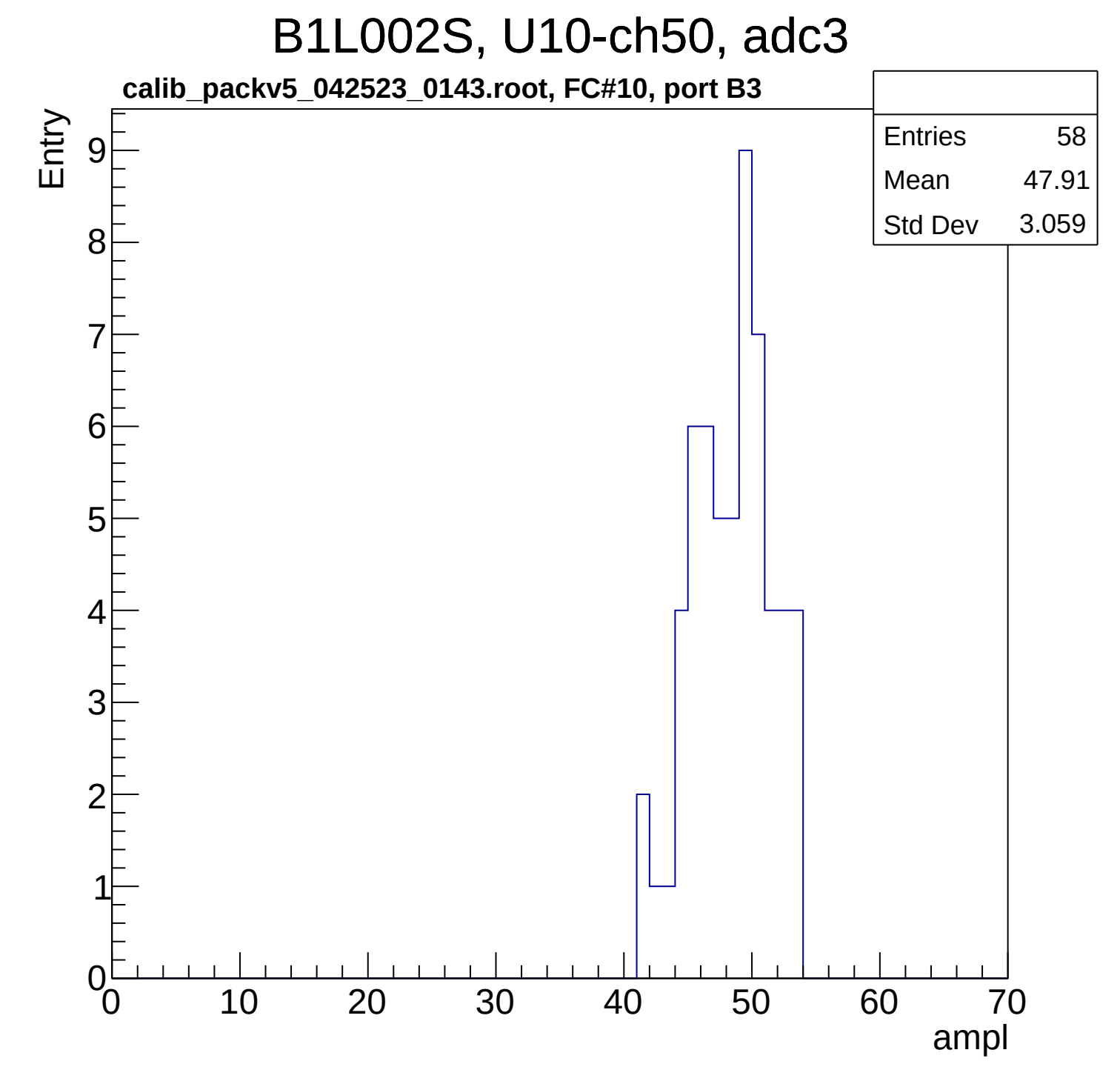
B1L002S, U10-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.91
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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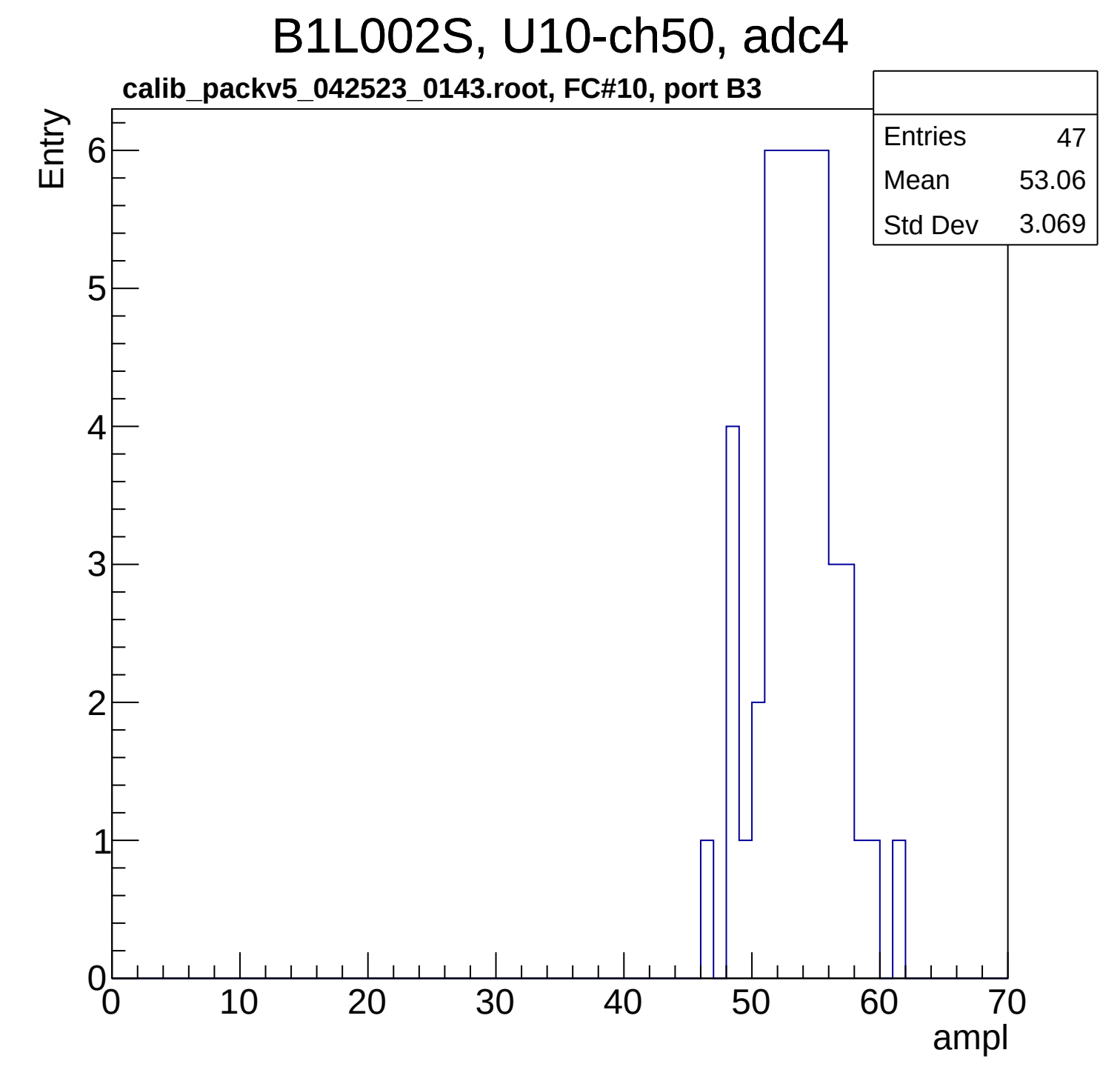
B1L002S, U10-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.06
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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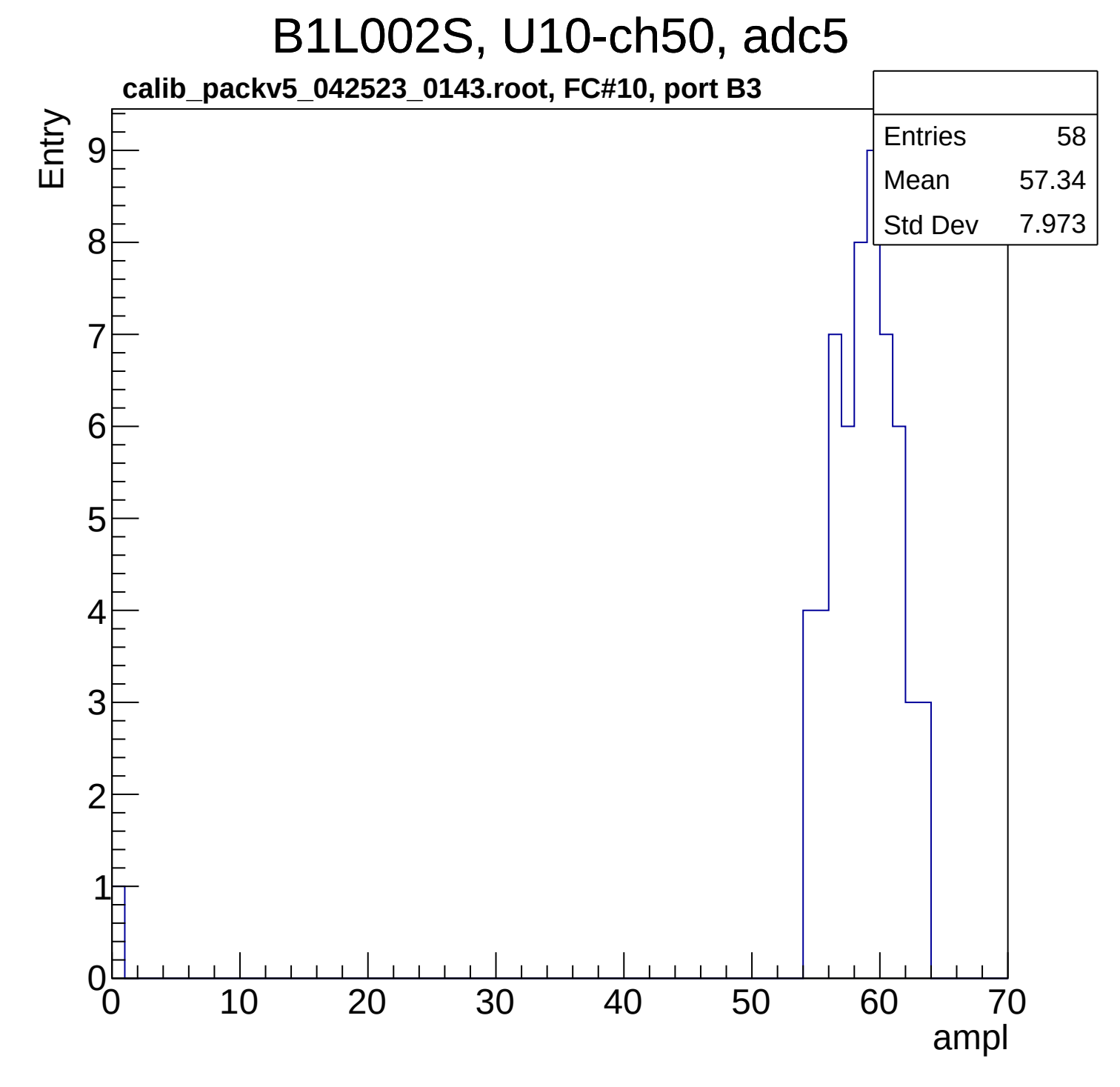
B1L002S, U10-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.34
Std Dev 7.973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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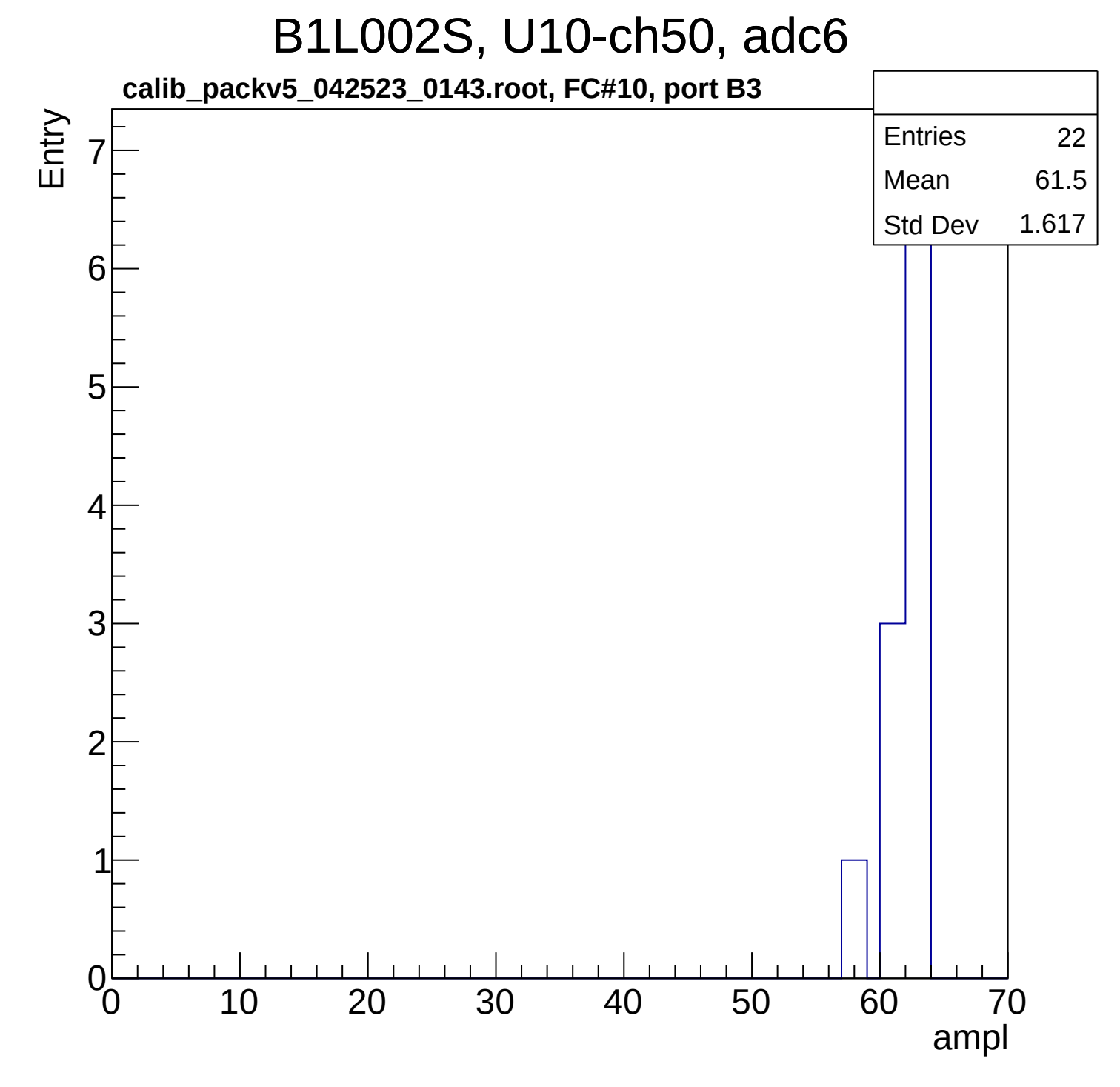
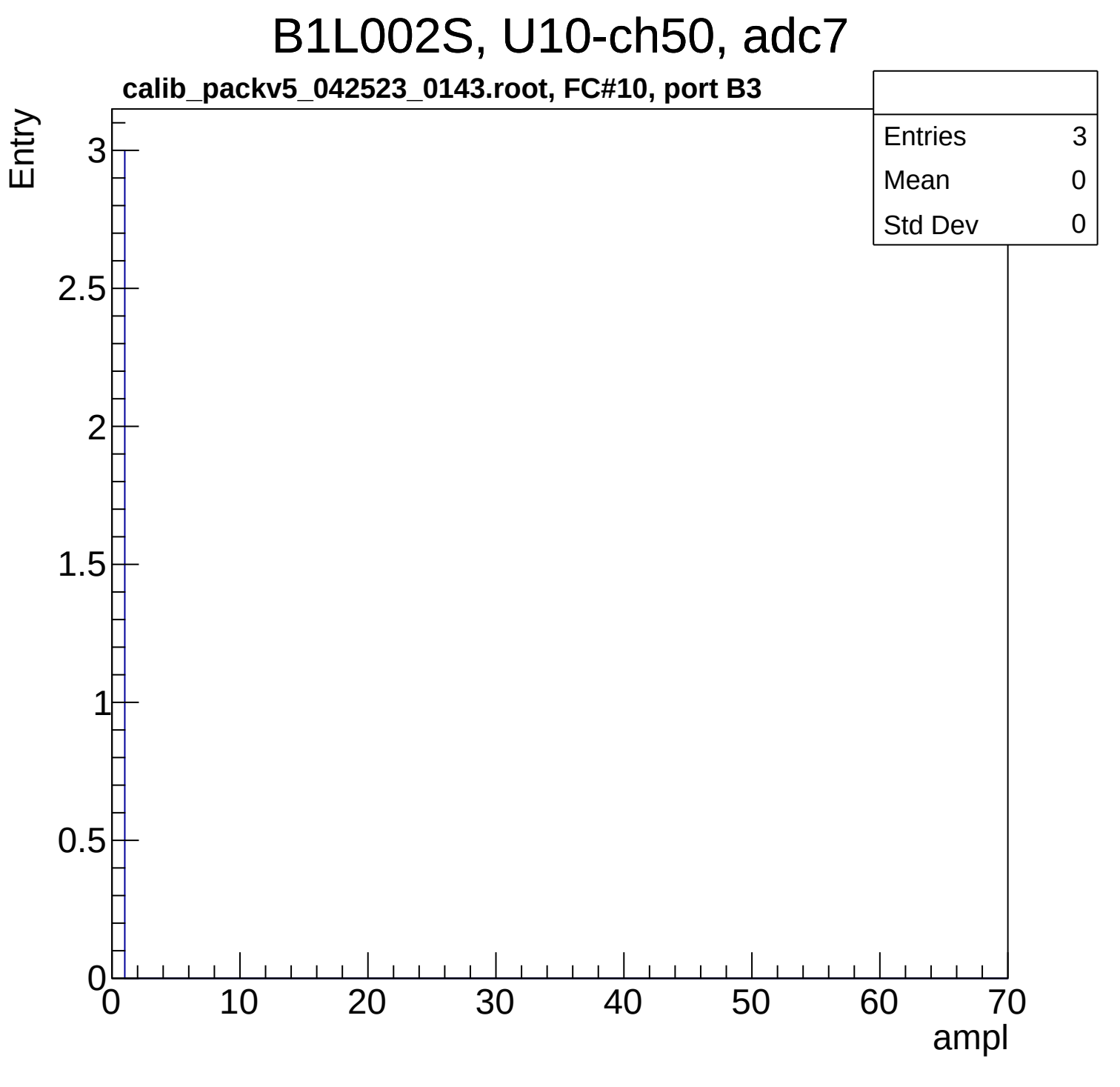
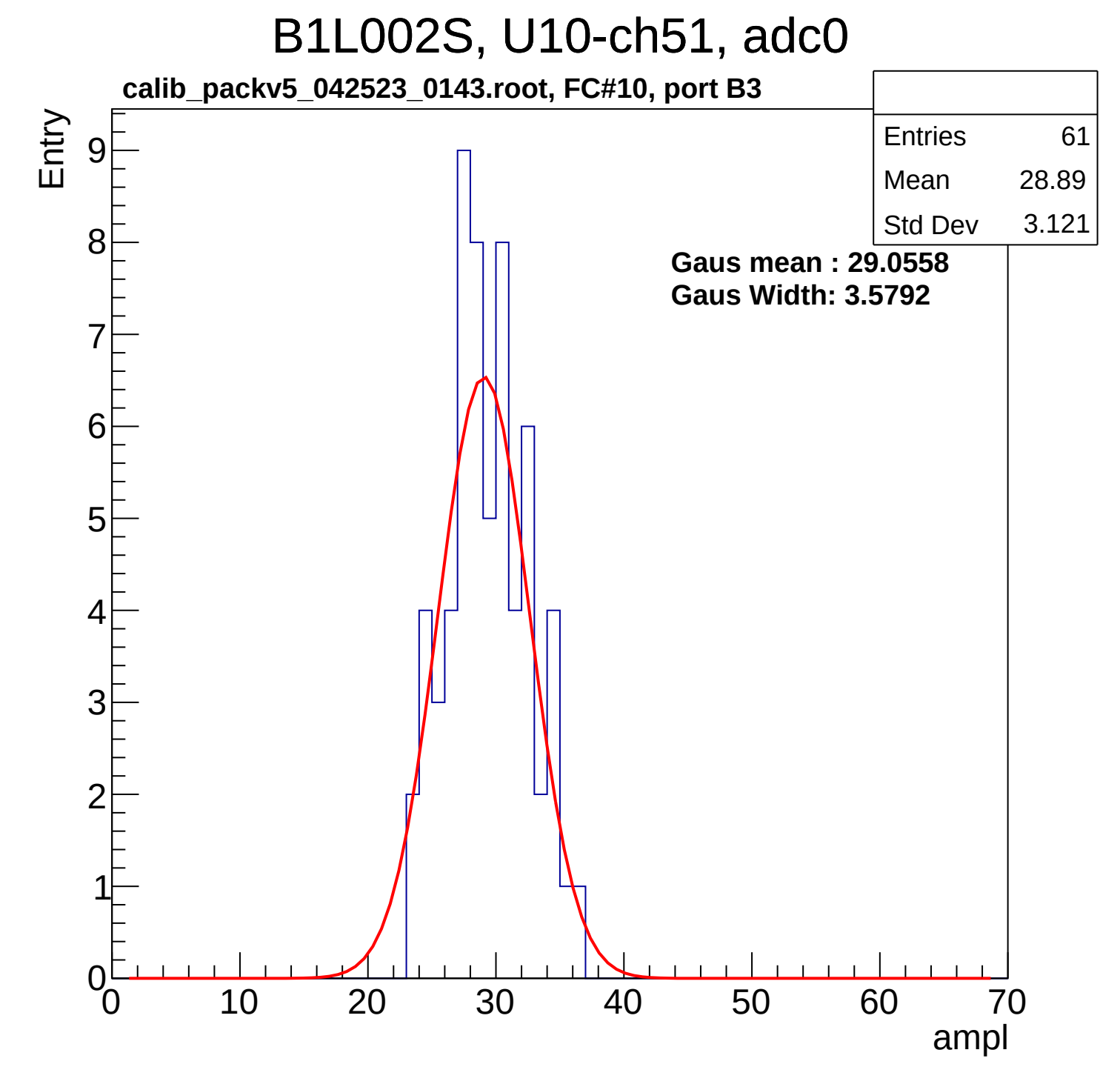
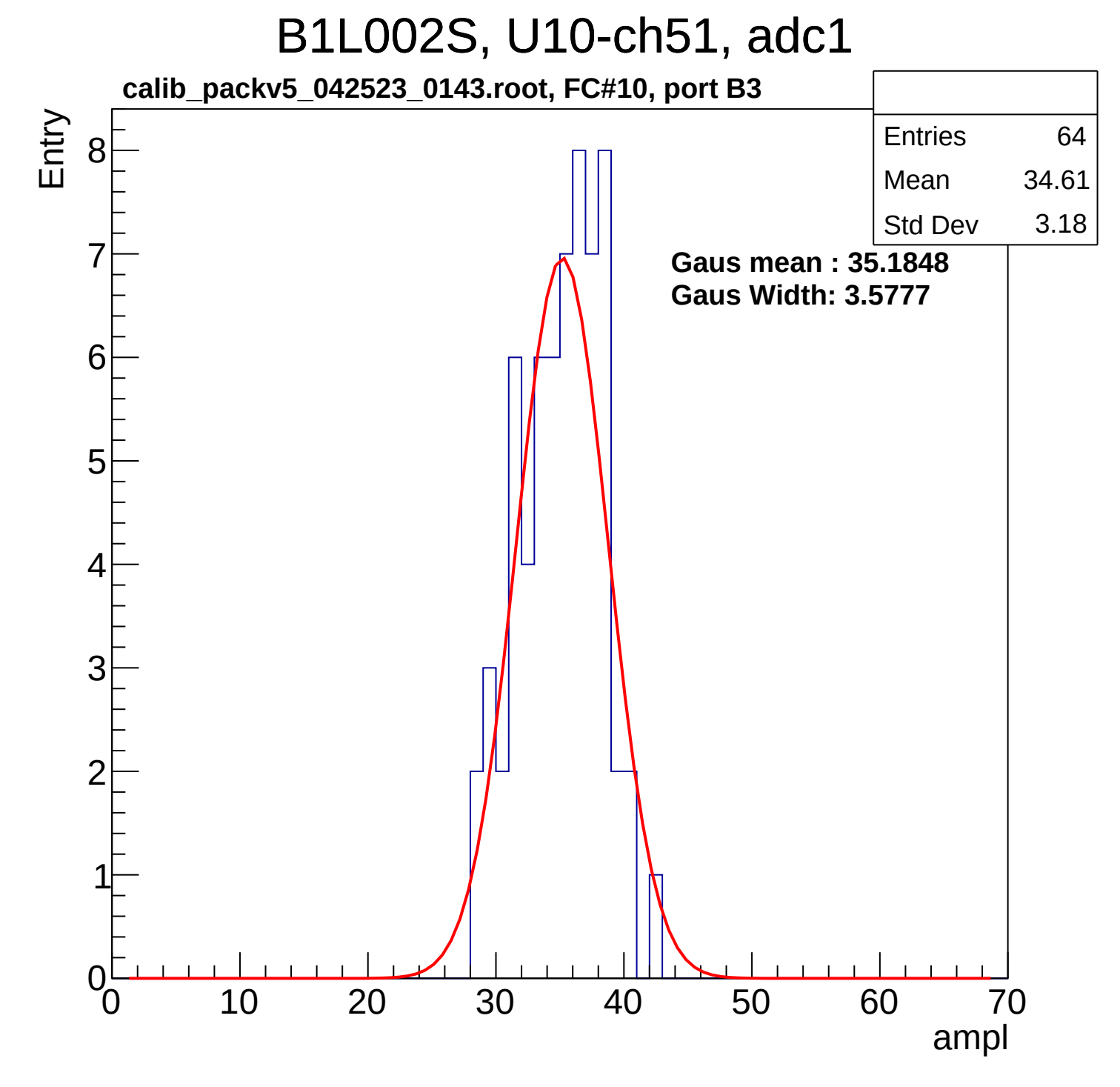
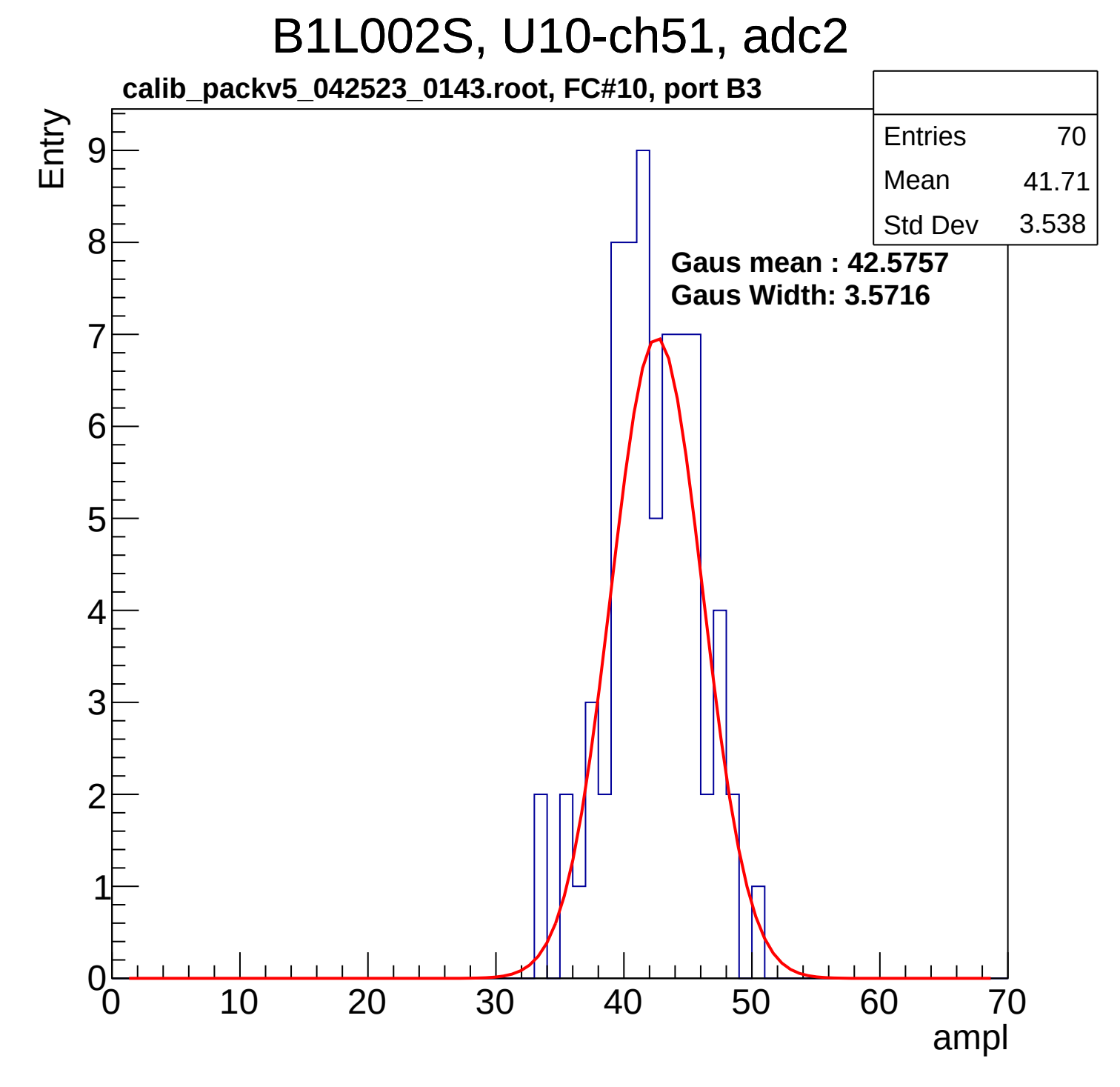
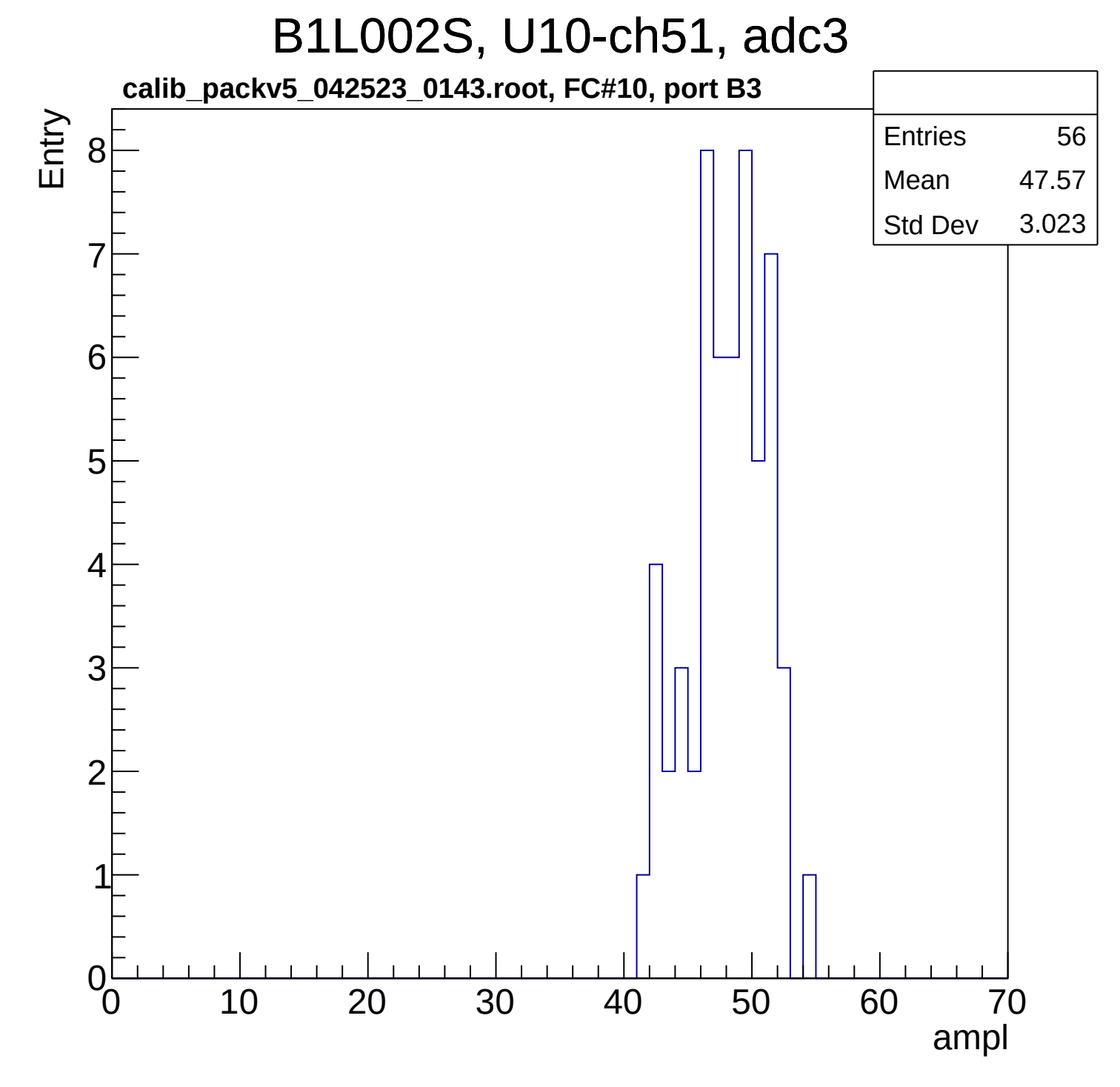

B1L002S, U10-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.5
Std Dev 1.617
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

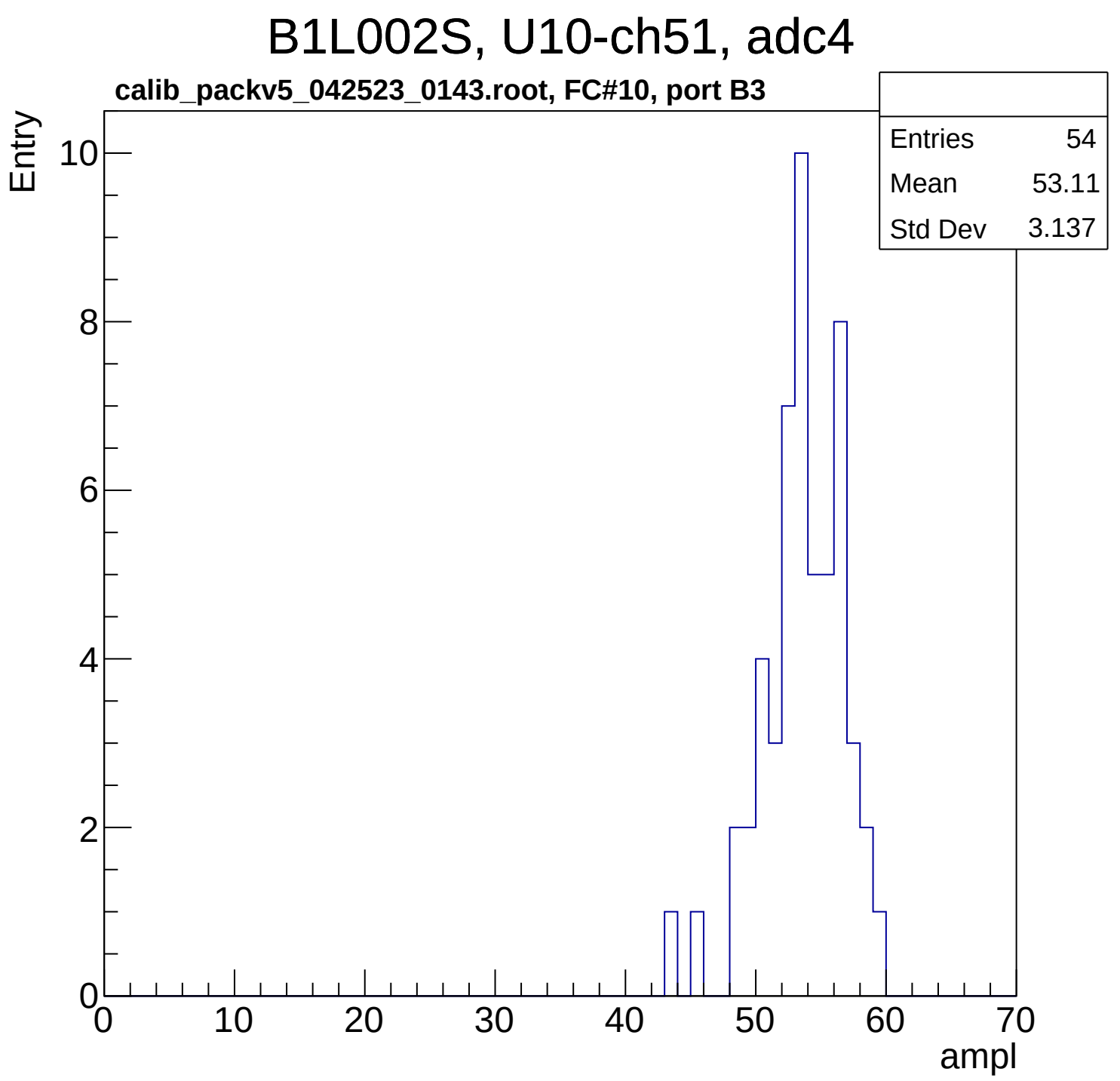

B1L002S, U10-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

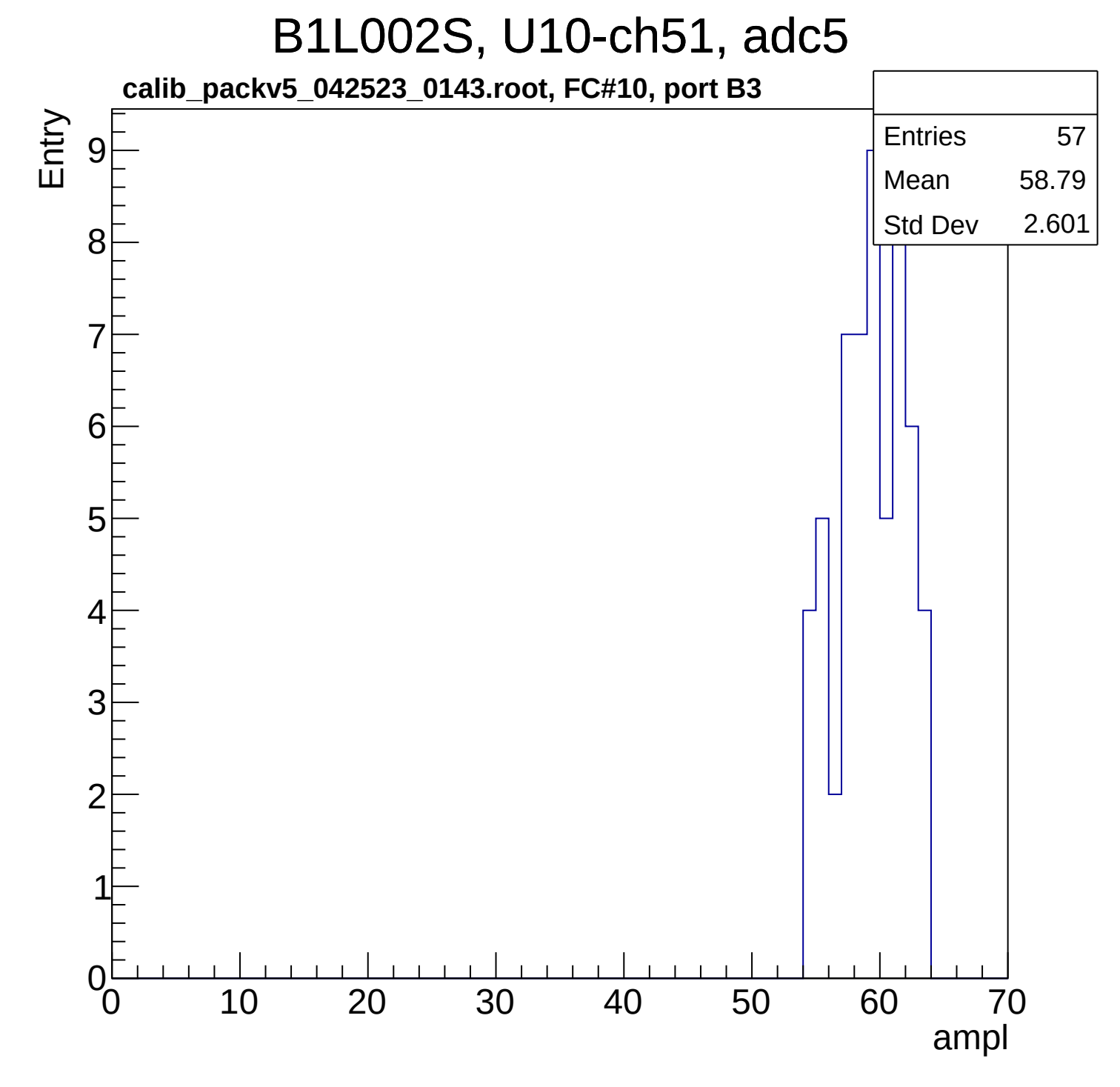

B1L002S, U10-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.89
Std Dev 3.121
Gaus mean : 29.0558
Gaus Width: 3.5792
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

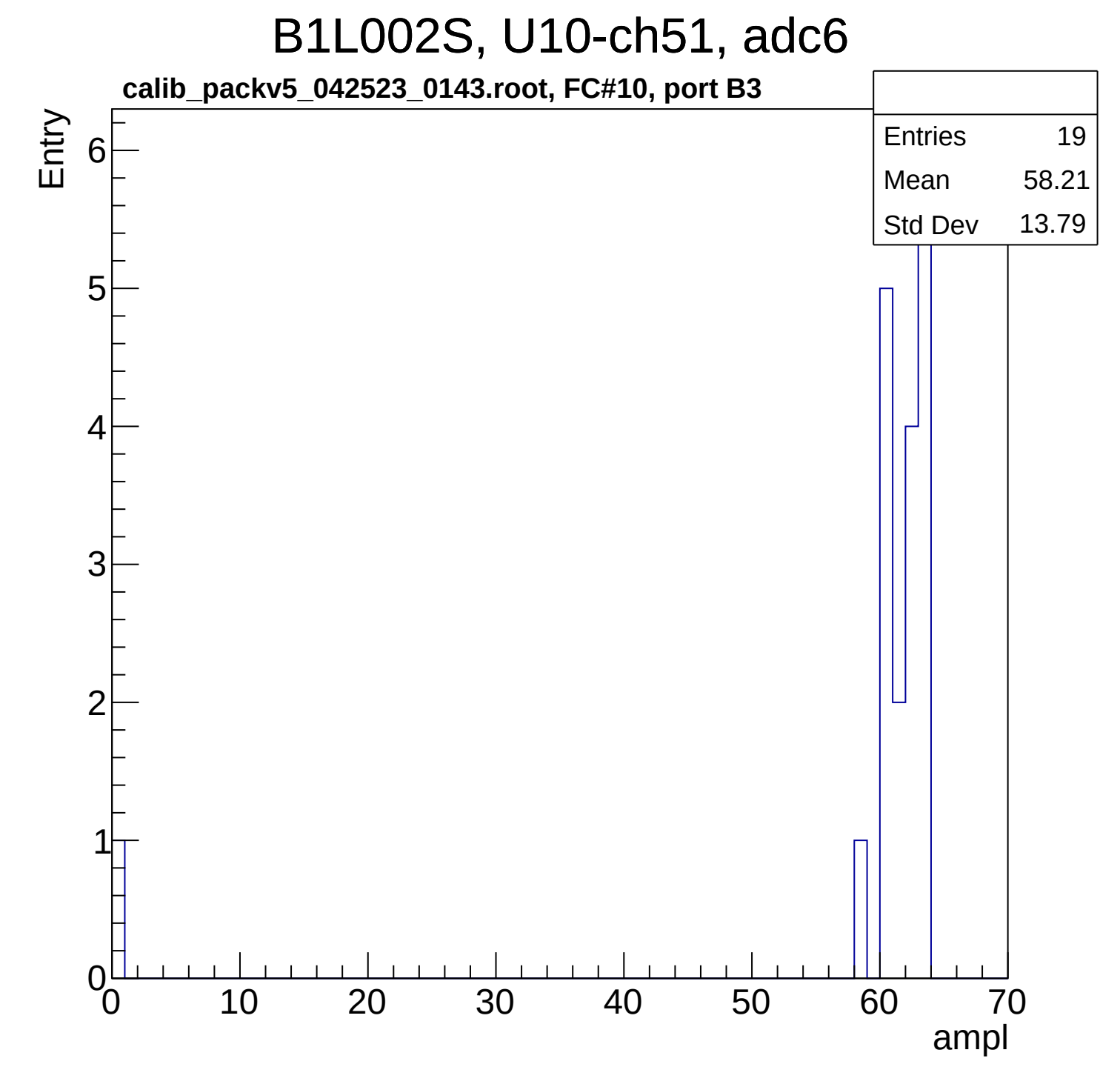

B1L002S, U10-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.61
Std Dev 3.18
Gaus mean : 35.1848
Gaus Width: 3.5777
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

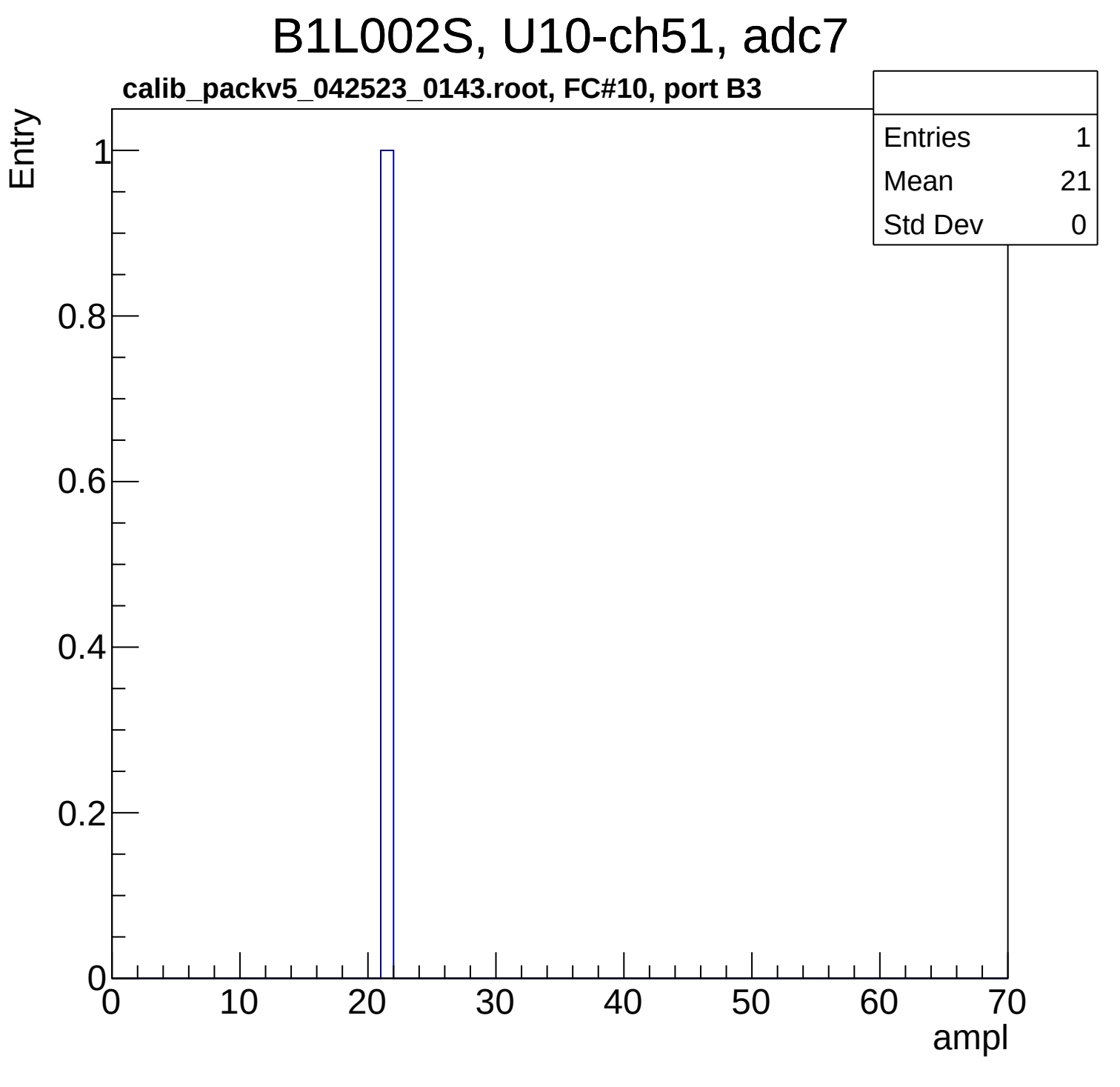

B1L002S, U10-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.71
Std Dev 3.538
Gaus mean : 42.5757
Gaus Width: 3.5716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

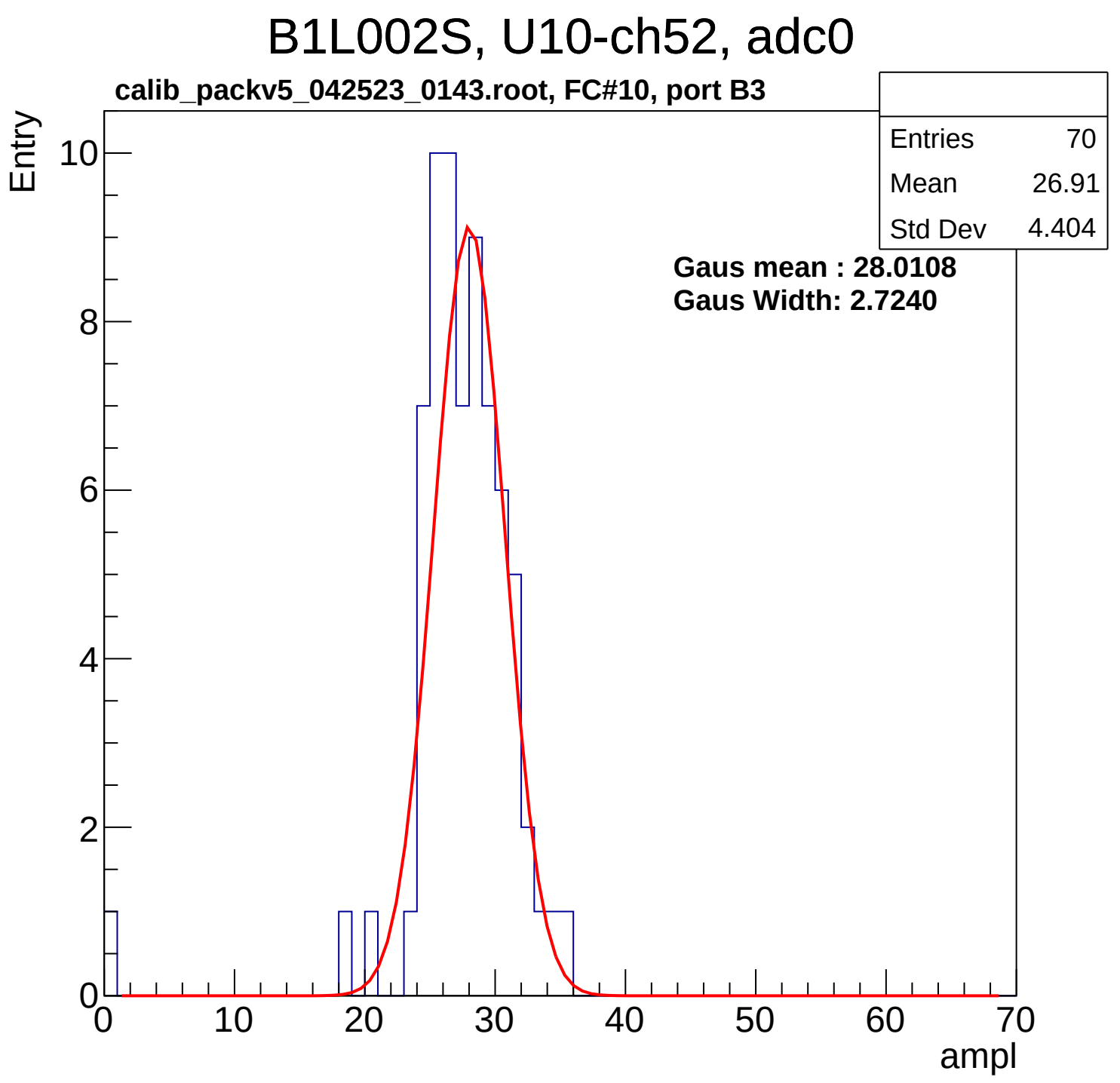

B1L002S, U10-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.57
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

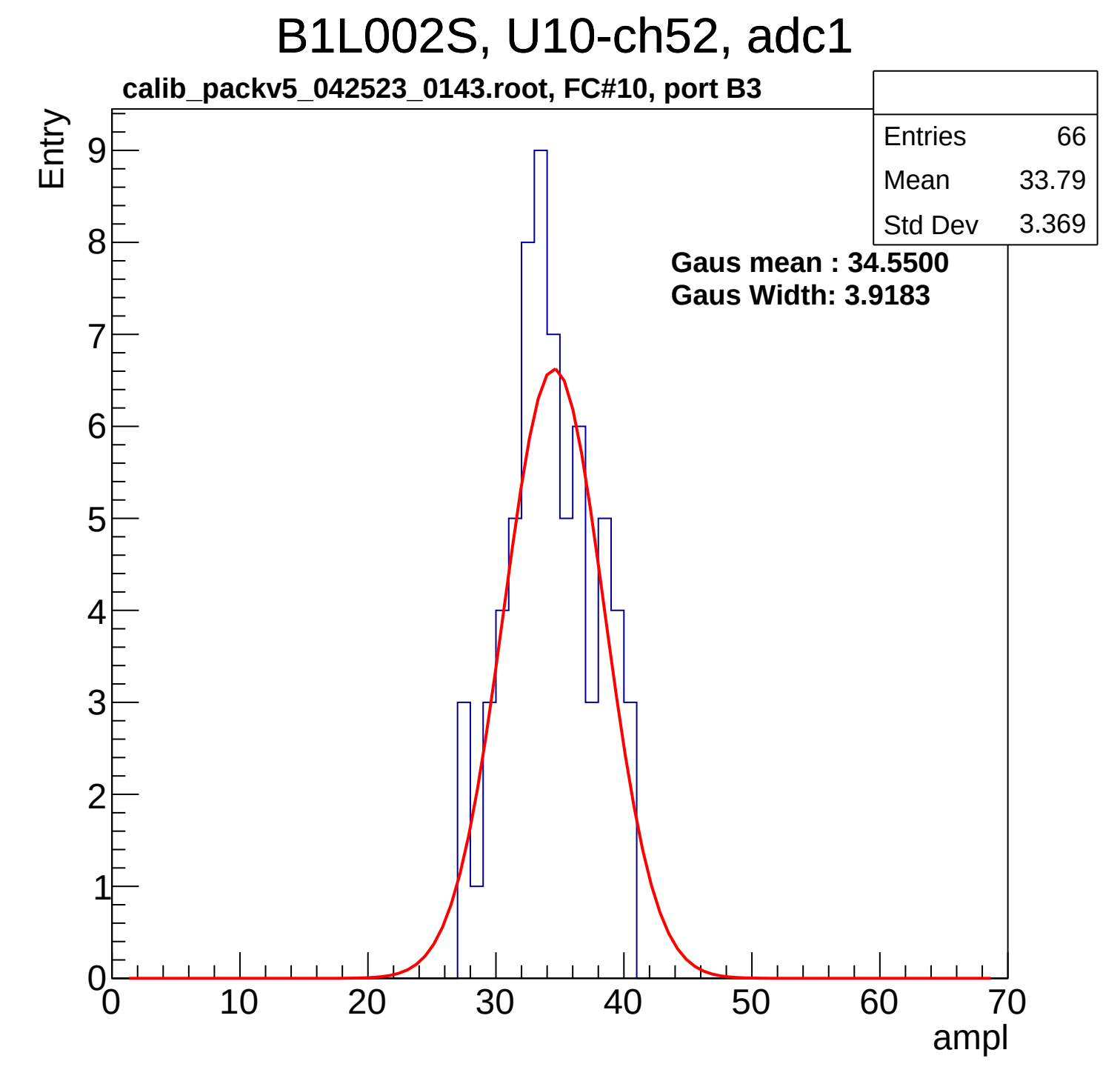

B1L002S, U10-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.11
Std Dev 3.137
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

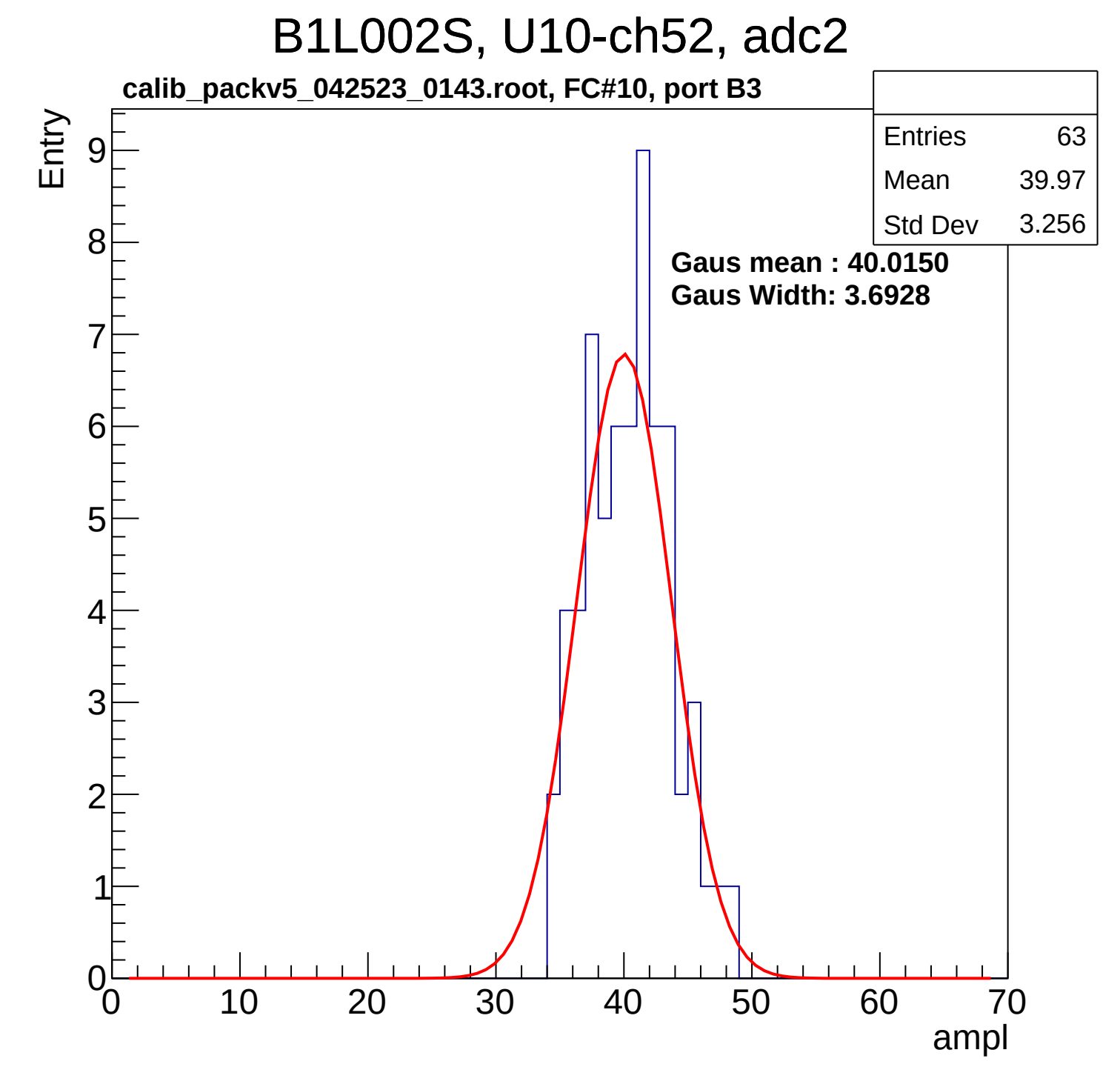

B1L002S, U10-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.79
Std Dev 2.601
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

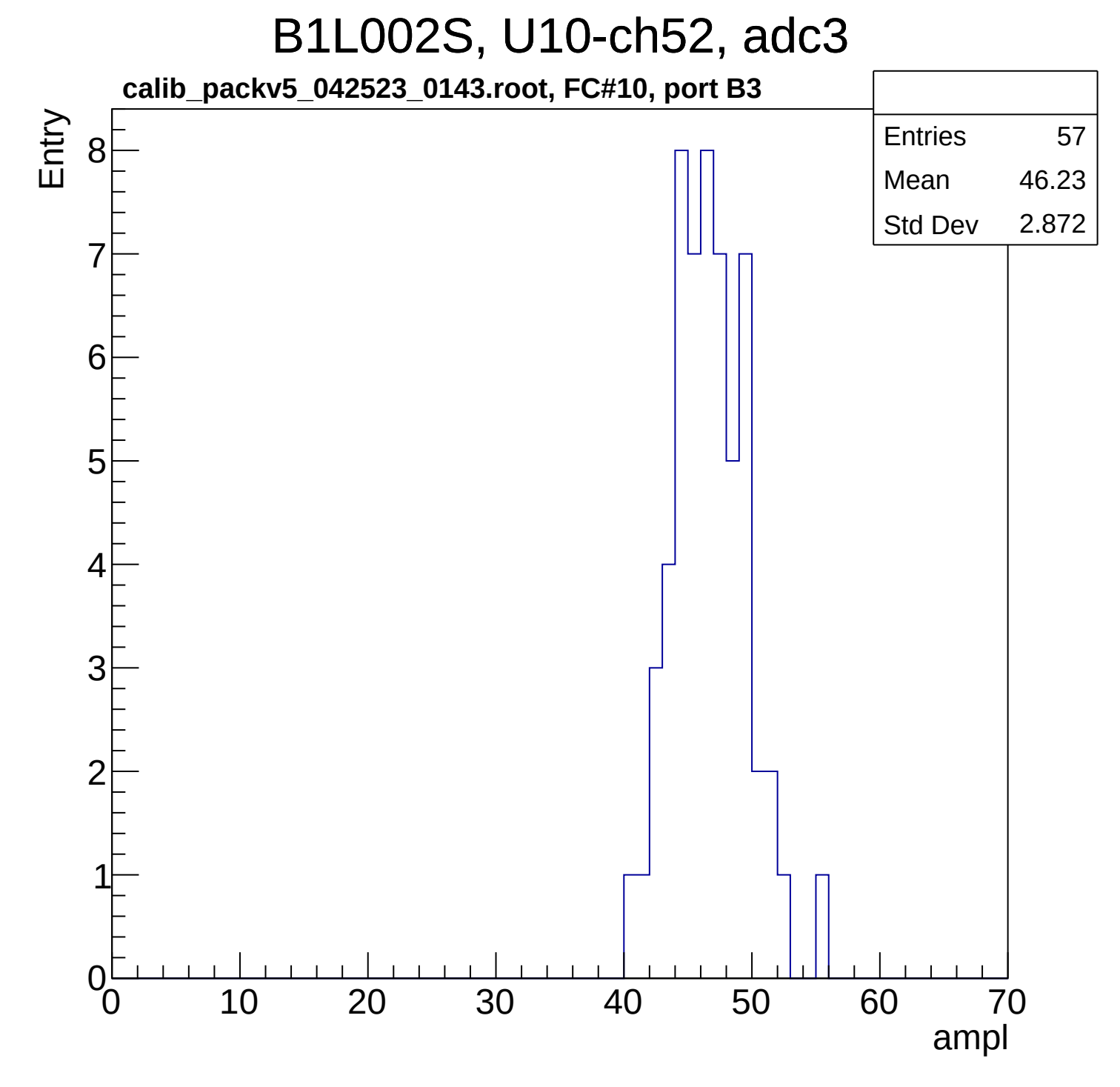

B1L002S, U10-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.21
Std Dev 13.79
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

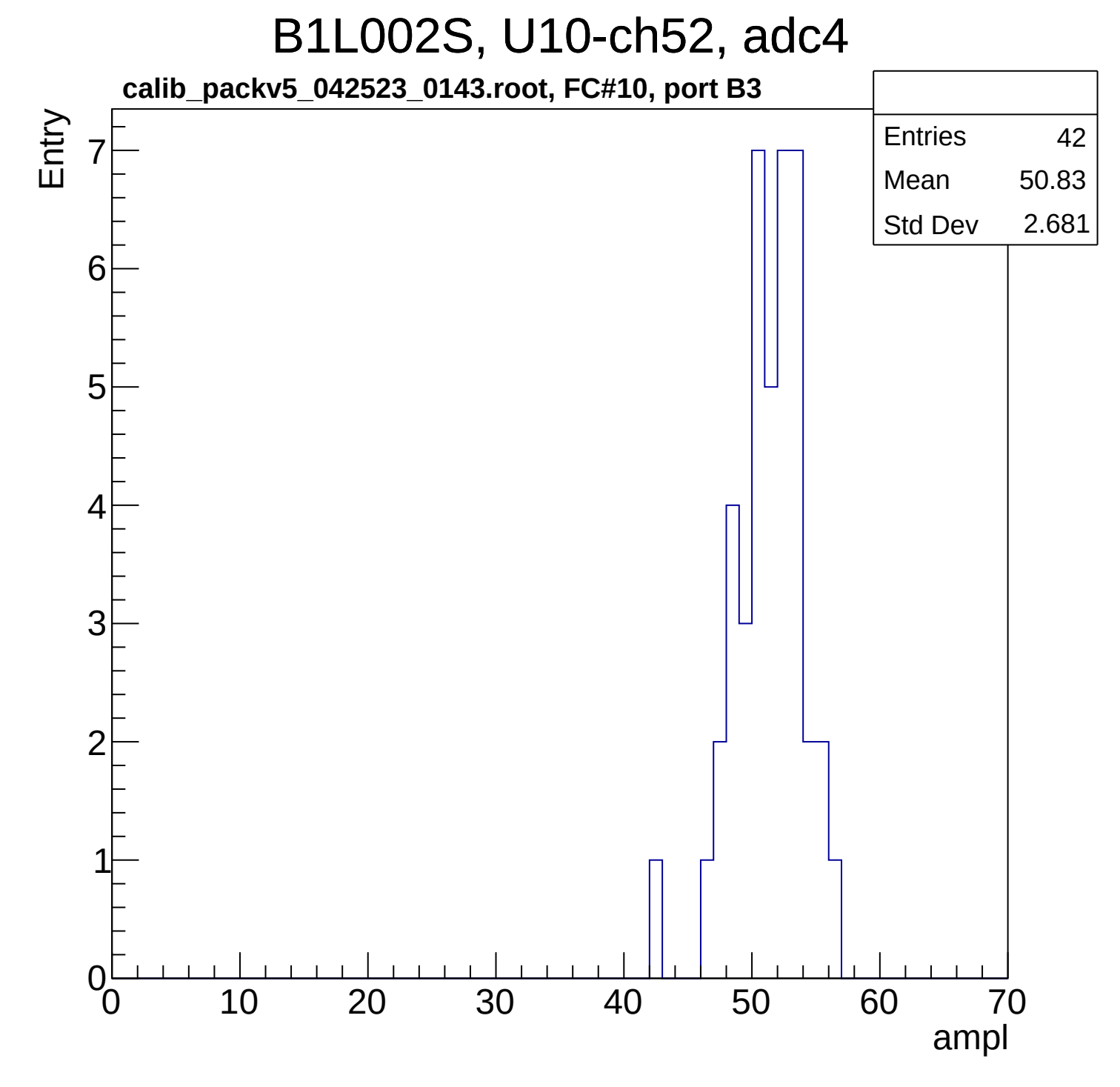

B1L002S, U10-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

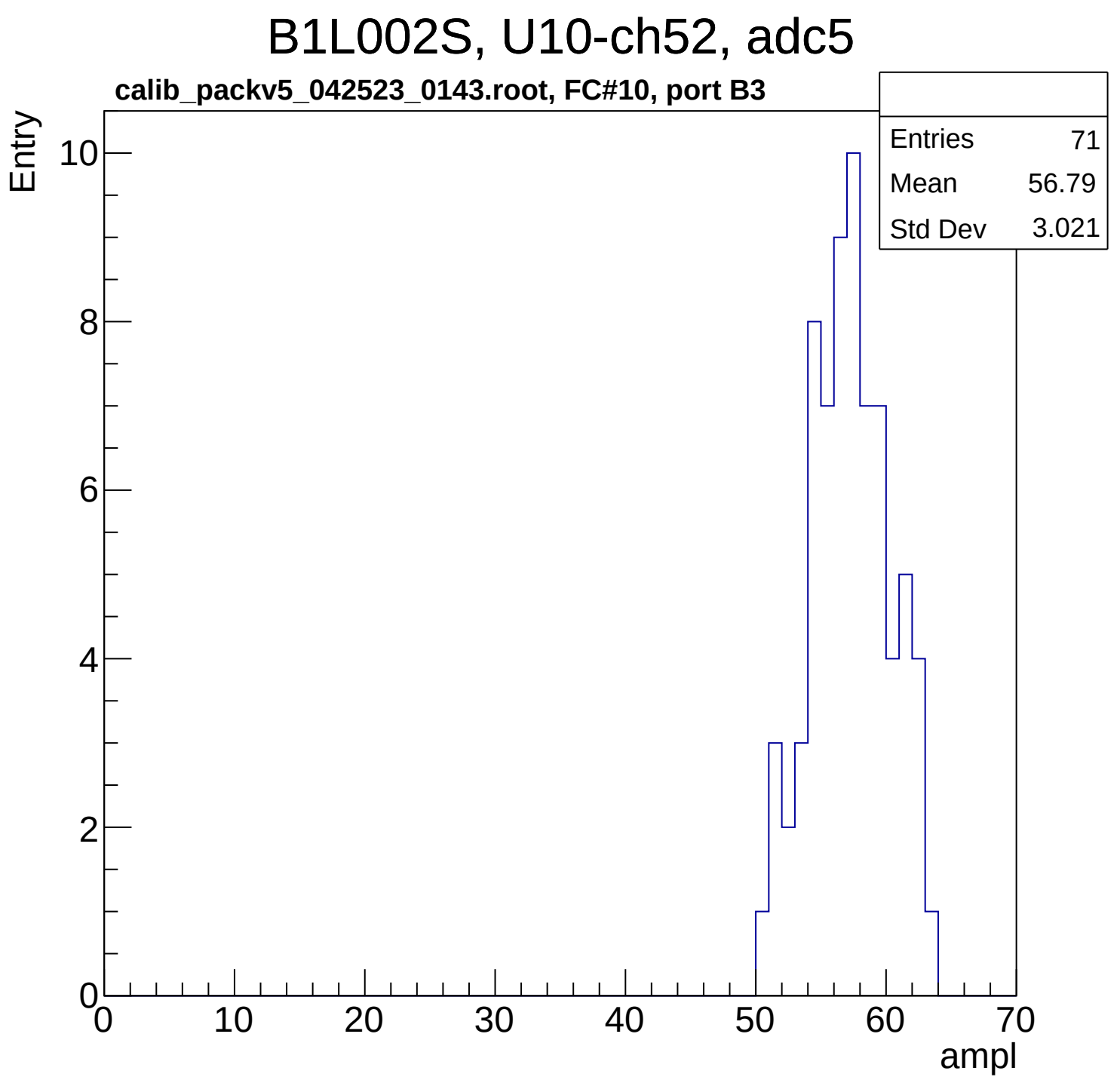

B1L002S, U10-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.91
Std Dev 4.404
Gaus mean : 28.0108
Gaus Width: 2.7240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

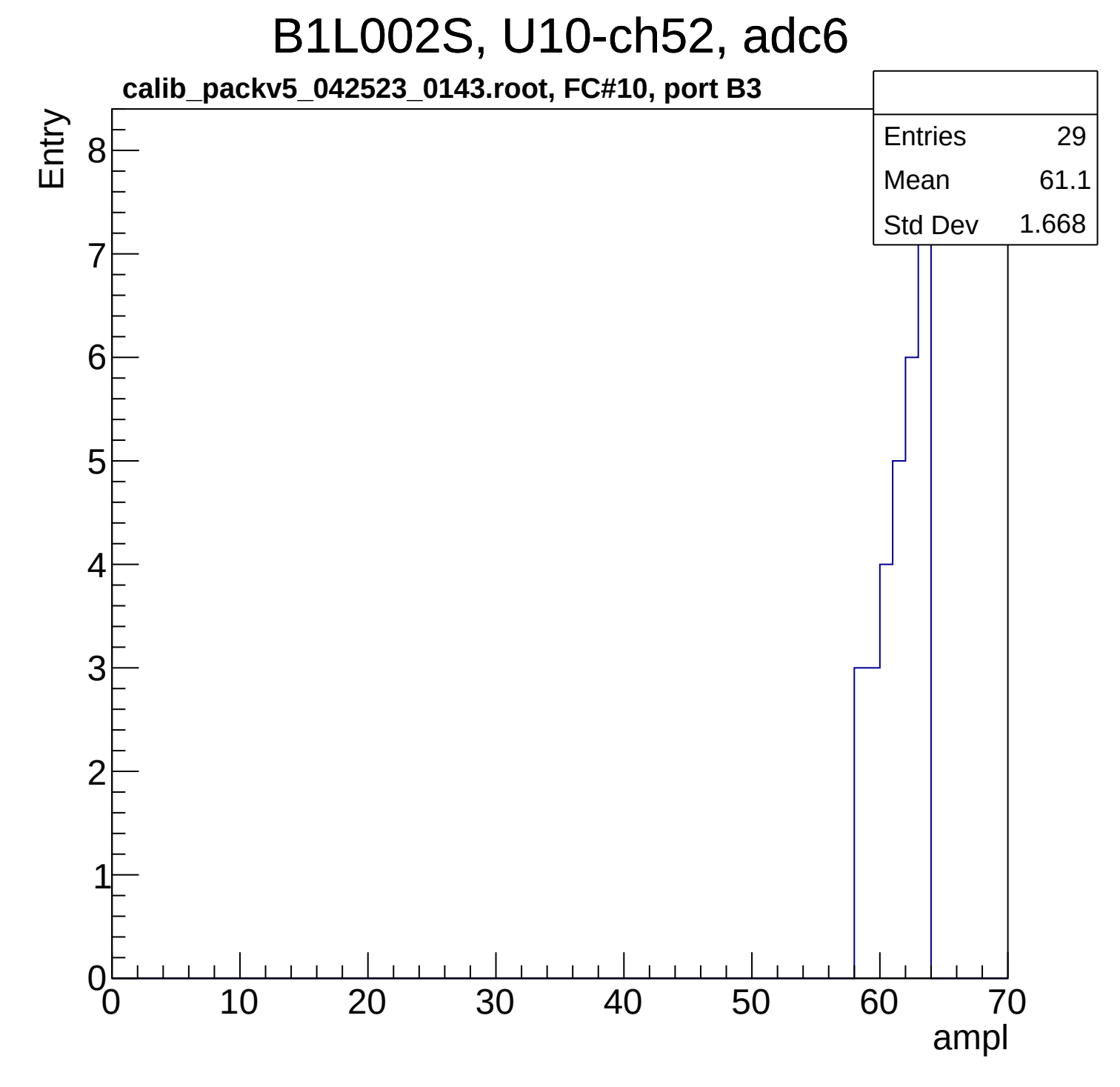

B1L002S, U10-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.79
Std Dev 3.369
Gaus mean : 34.5500
Gaus Width: 3.9183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.97
Std Dev 3.256
Gaus mean : 40.0150
Gaus Width: 3.6928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.23
Std Dev 2.872
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 50.83
Std Dev 2.681
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 56.79
Std Dev 3.021
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 61.1
Std Dev 1.668
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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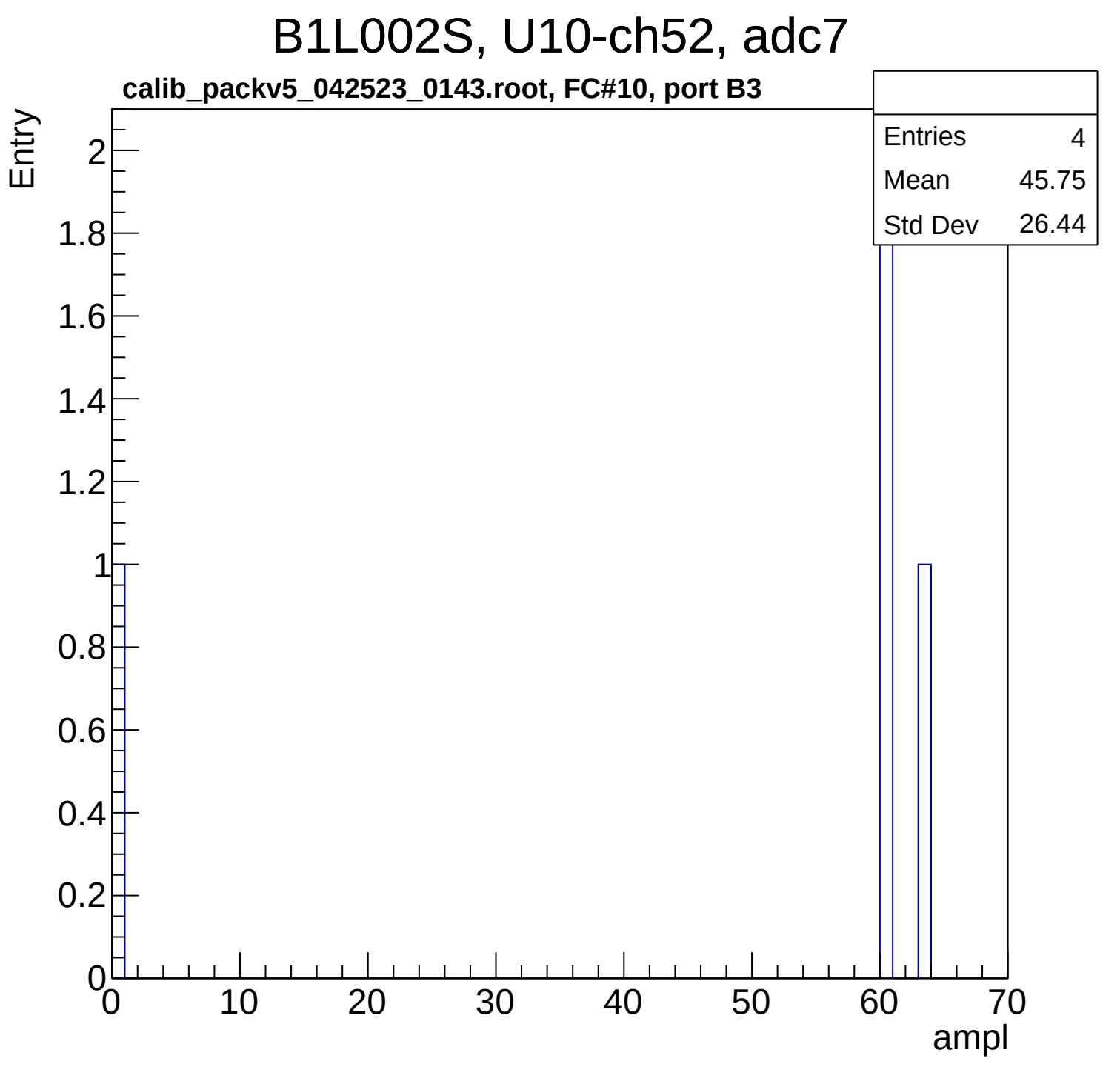
B1L002S, U10-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 45.75
Std Dev 26.44
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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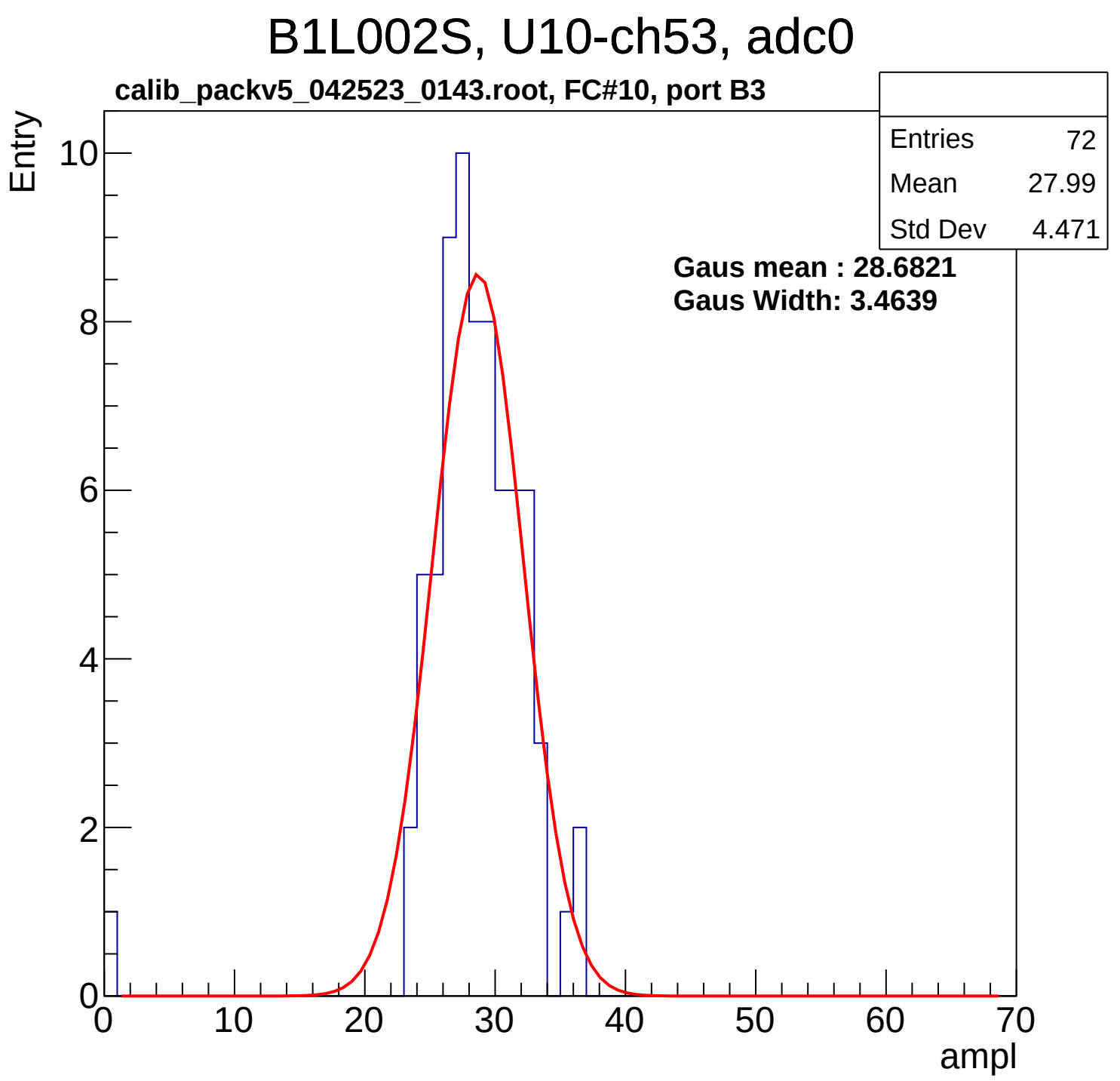
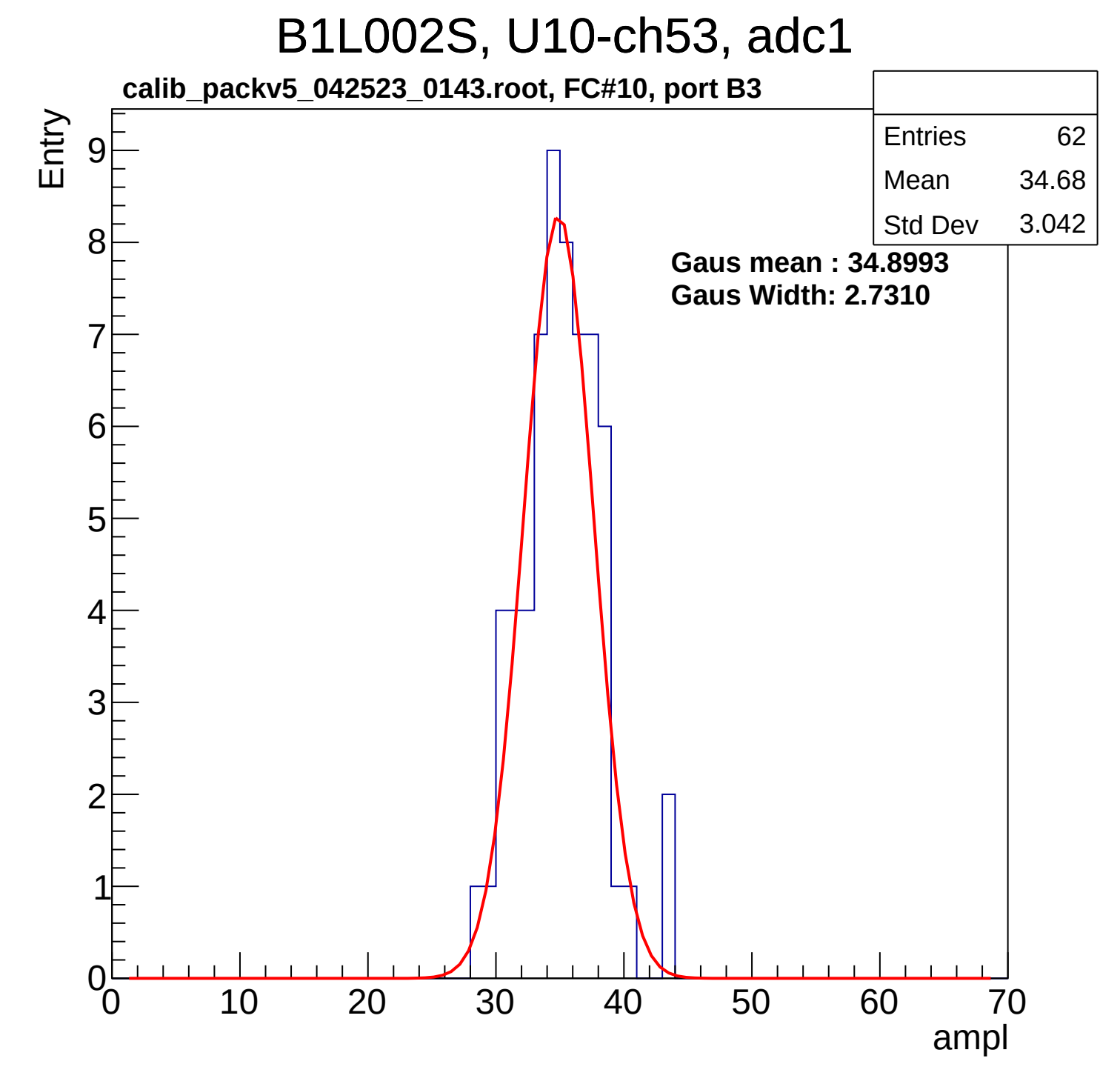
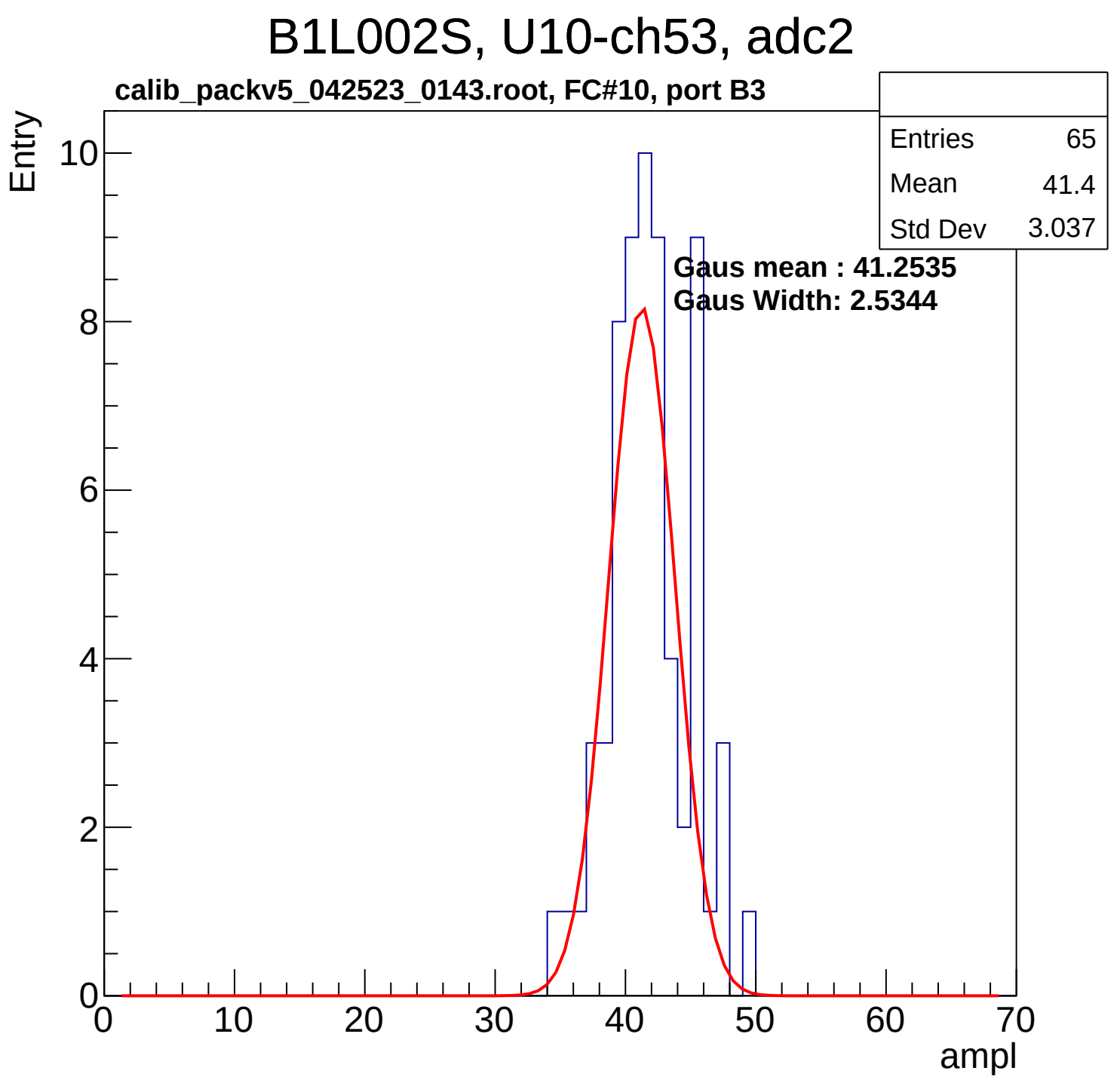
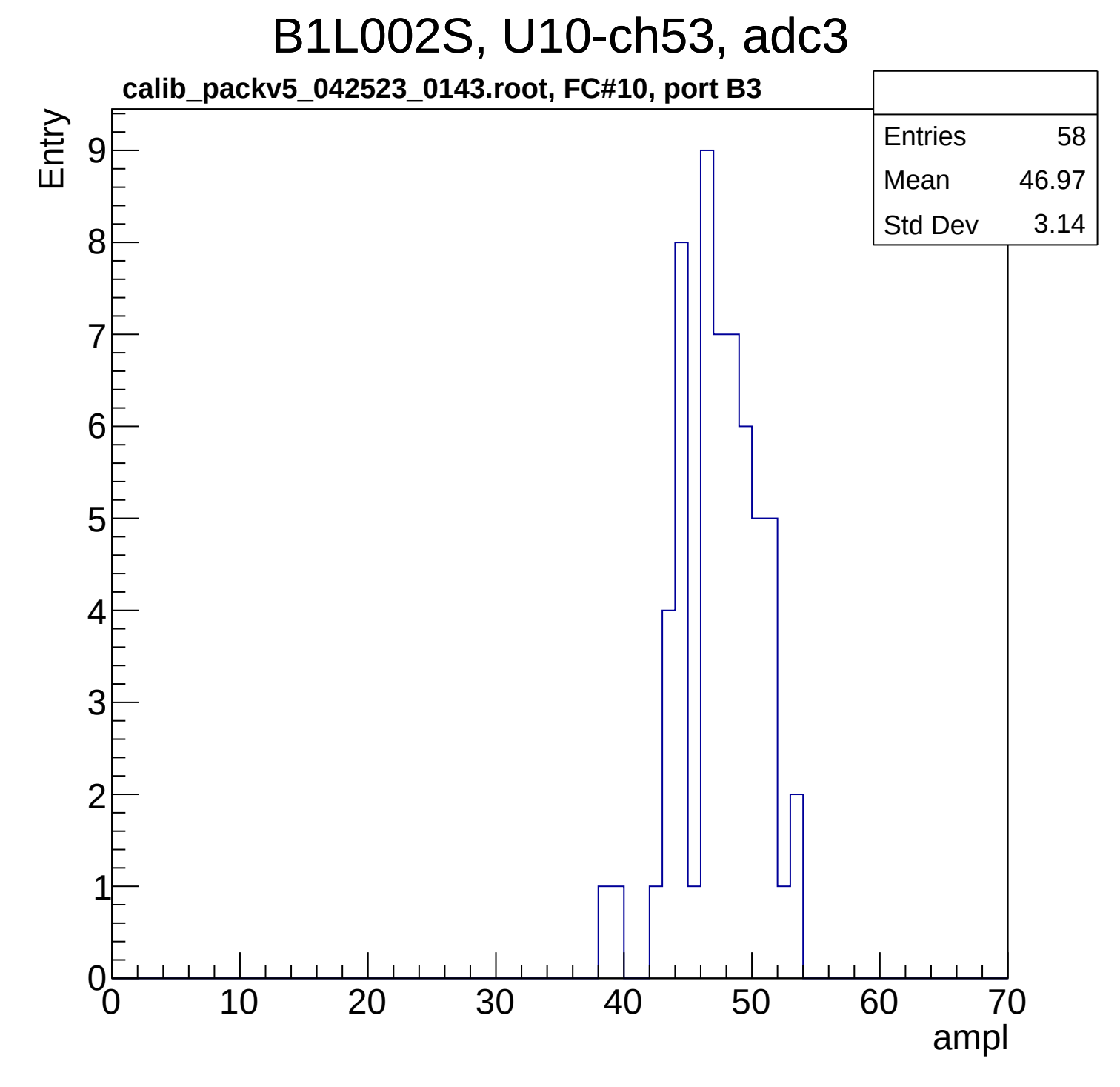
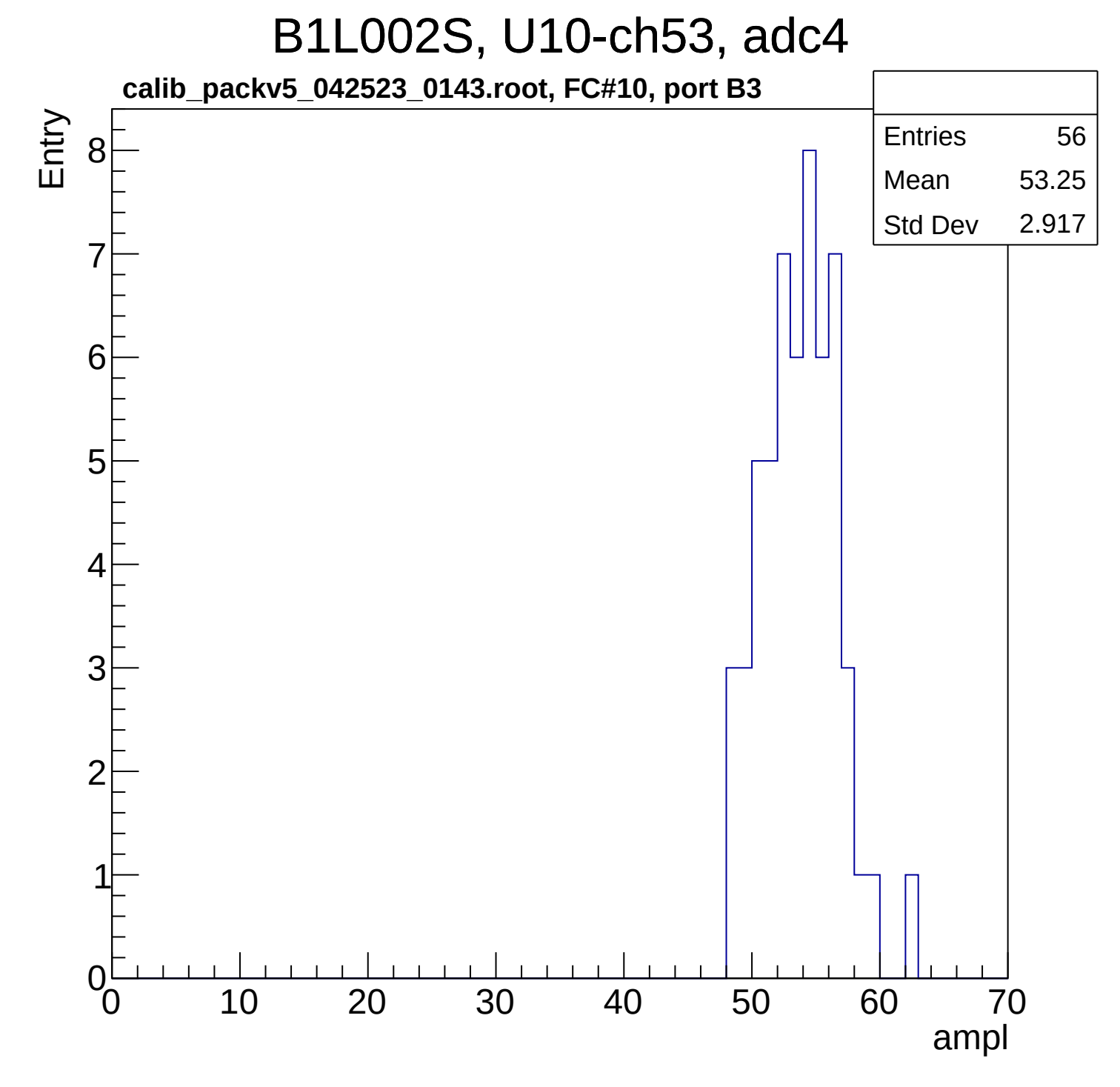
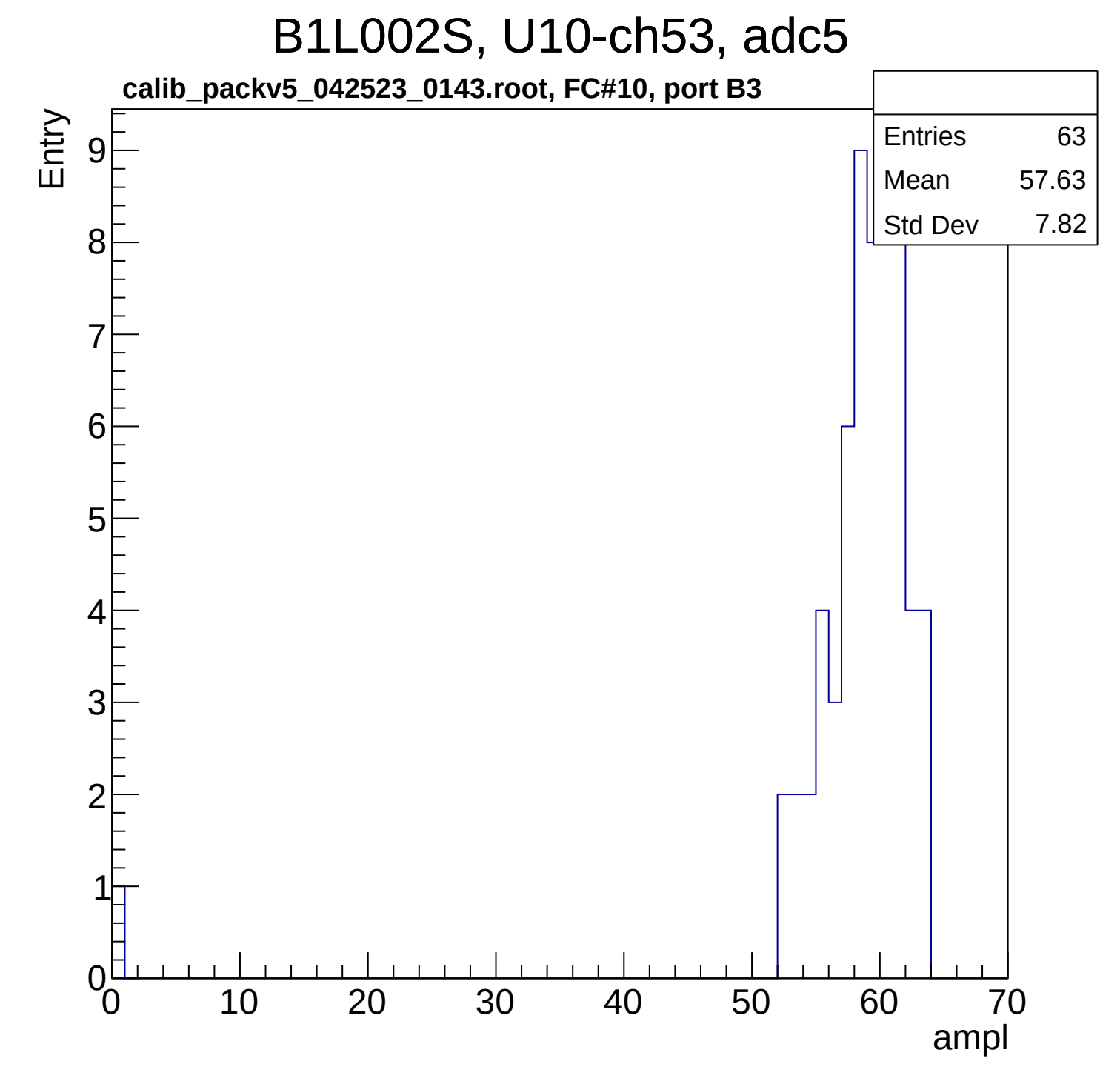

B1L002S, U10-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.99
Std Dev 4.471
Gaus mean : 28.6821
Gaus Width: 3.4639
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

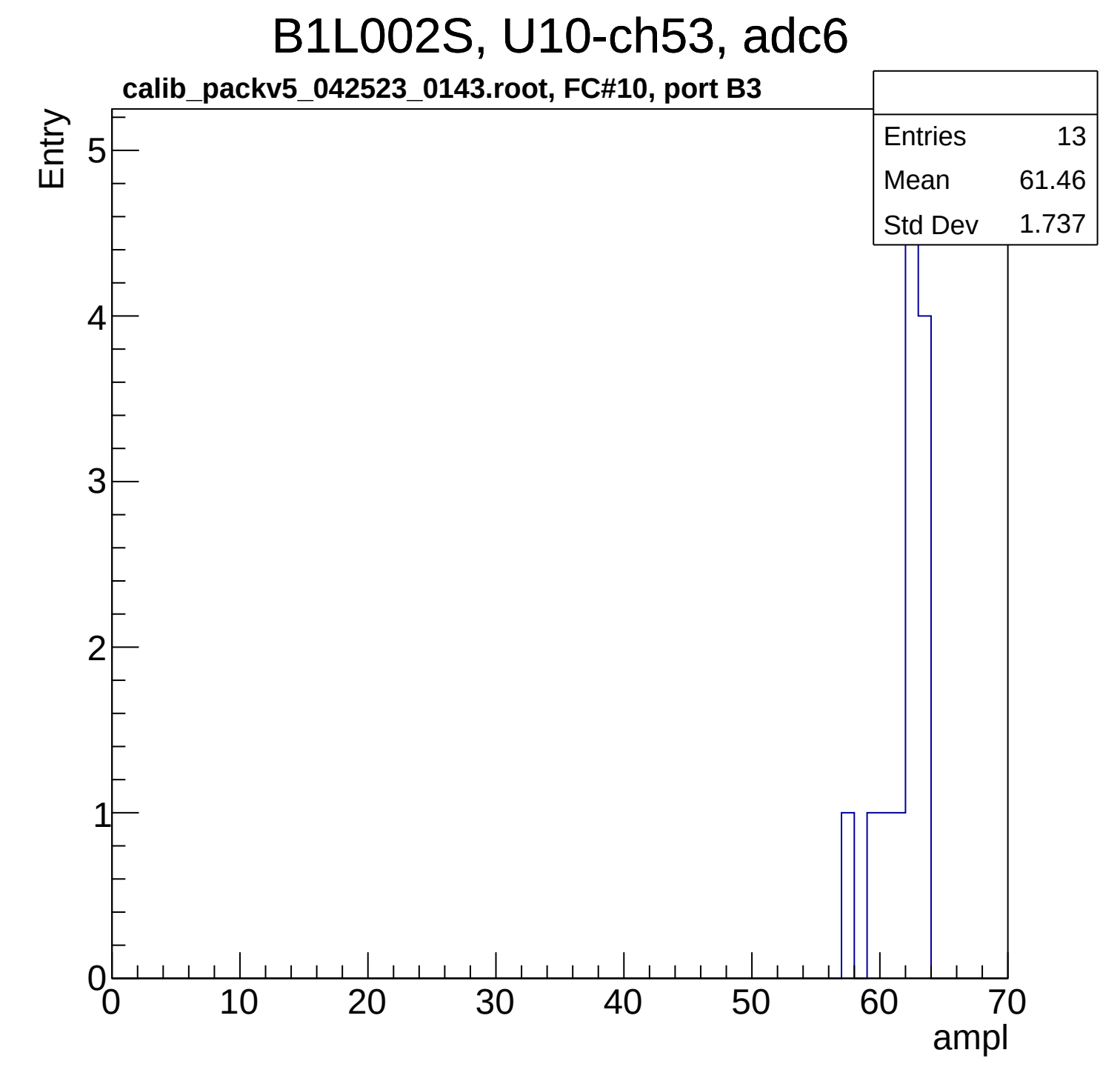

B1L002S, U10-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.68
Std Dev 3.042
Gaus mean : 34.8993
Gaus Width: 2.7310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

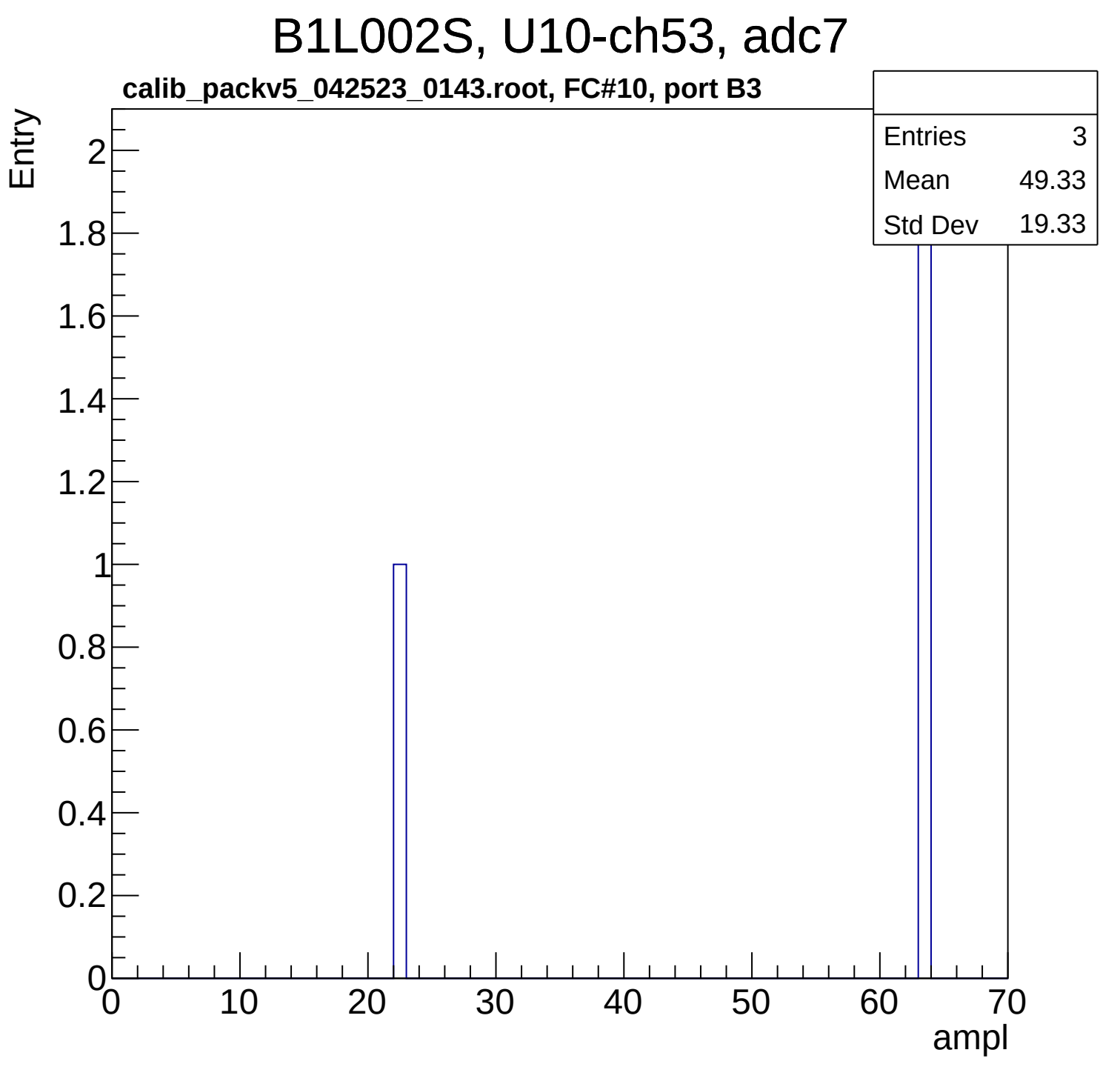

B1L002S, U10-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.4
Std Dev 3.037
Gaus mean : 41.2535
Gaus Width: 2.5344
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

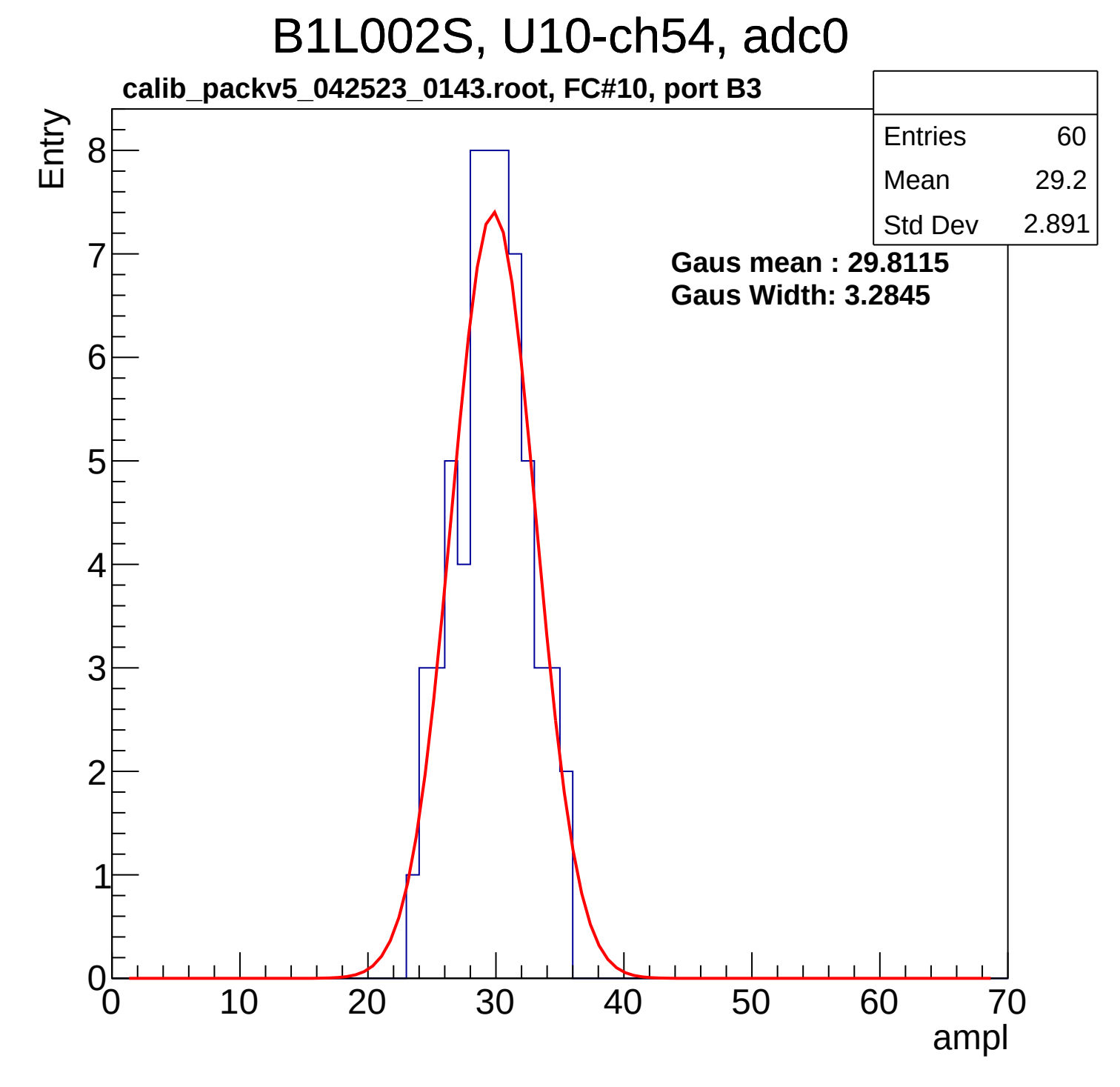

B1L002S, U10-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.97
Std Dev 3.14
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

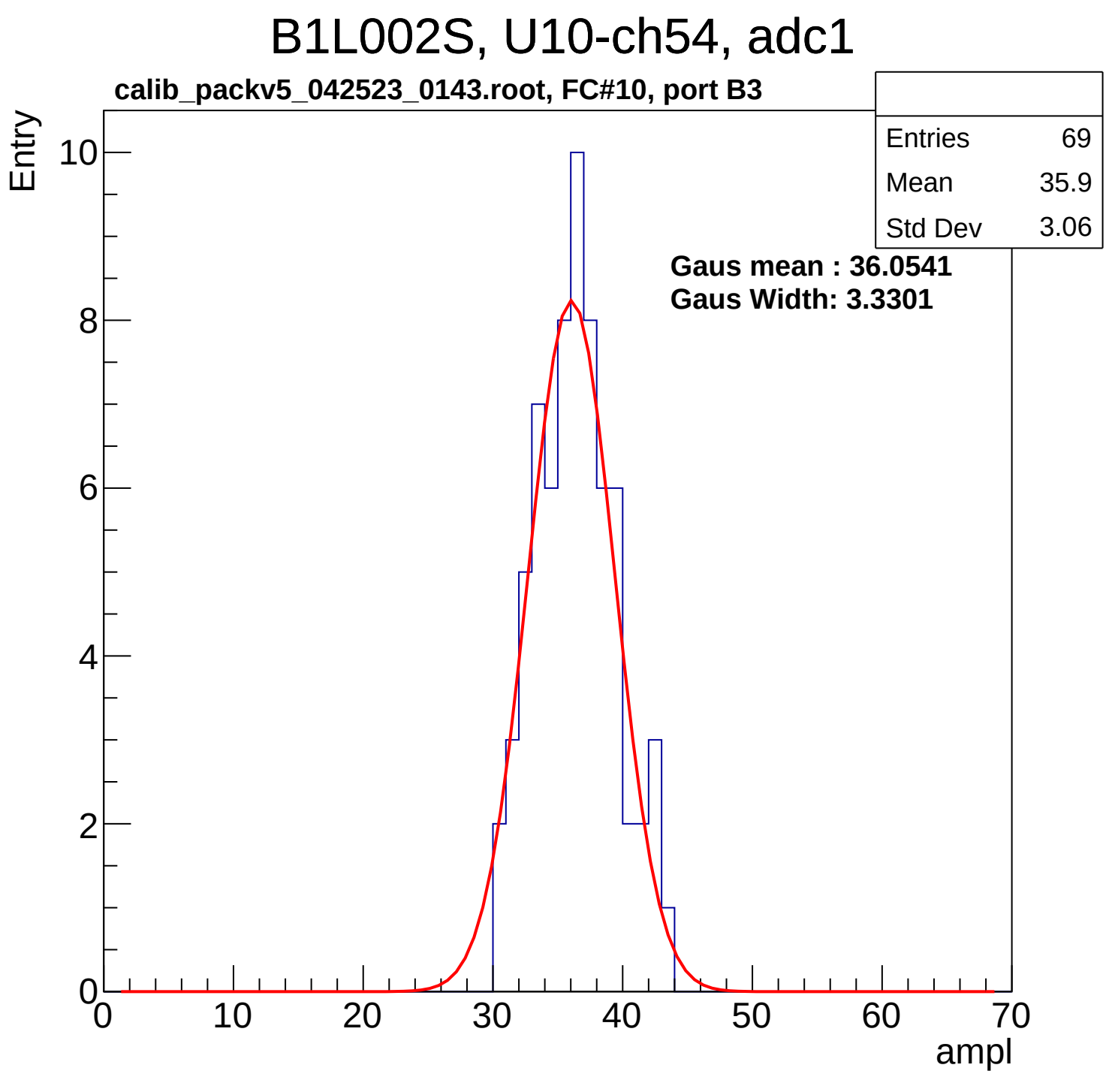

B1L002S, U10-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.25
Std Dev 2.917
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

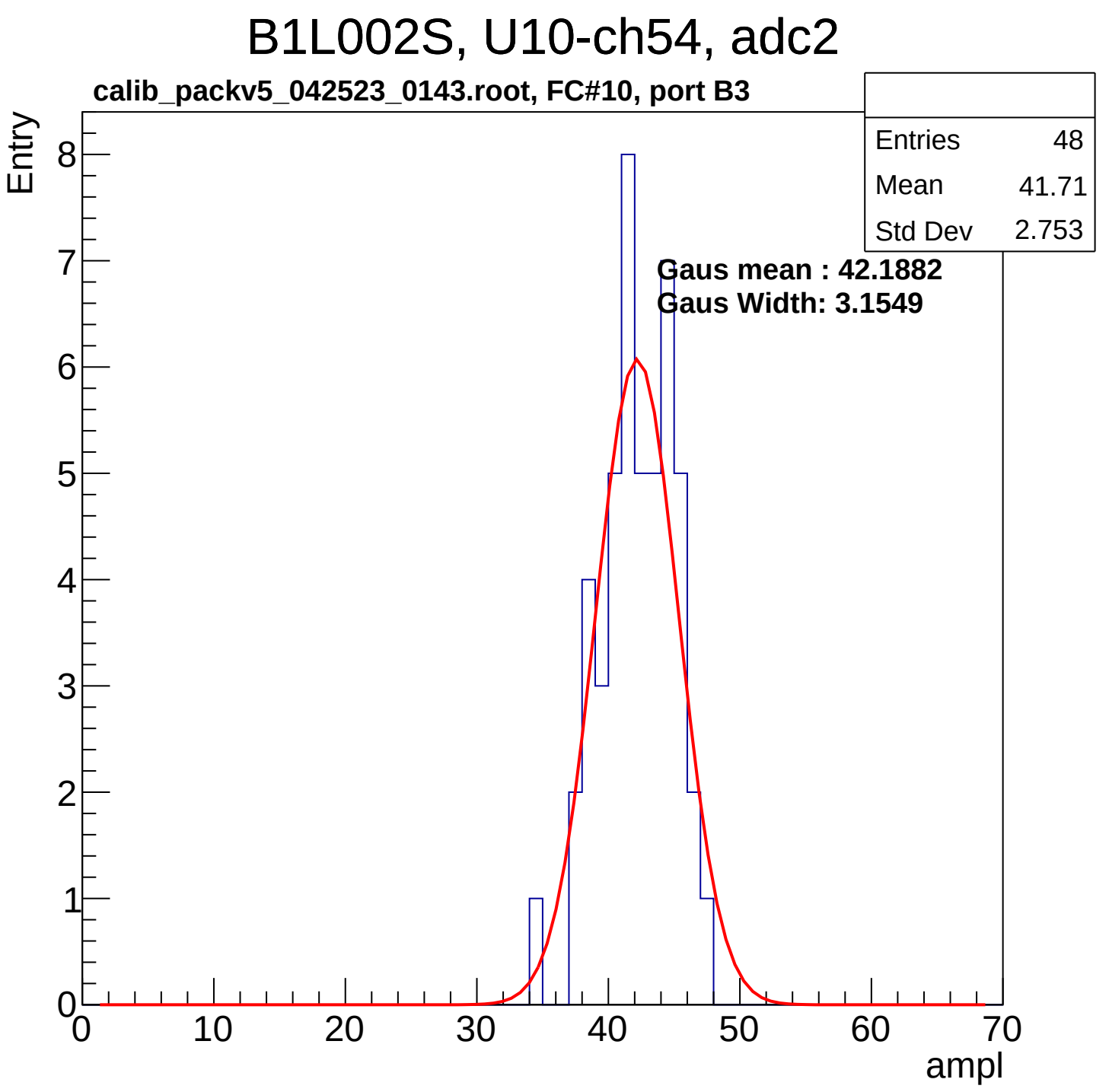

B1L002S, U10-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.63
Std Dev 7.82
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.46
Std Dev 1.737
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 49.33
Std Dev 19.33
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.2
Std Dev 2.891
Gaus mean : 29.8115
Gaus Width: 3.2845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.9
Std Dev 3.06
Gaus mean : 36.0541
Gaus Width: 3.3301
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.71
Std Dev 2.753
Gaus mean : 42.1882
Gaus Width: 3.1549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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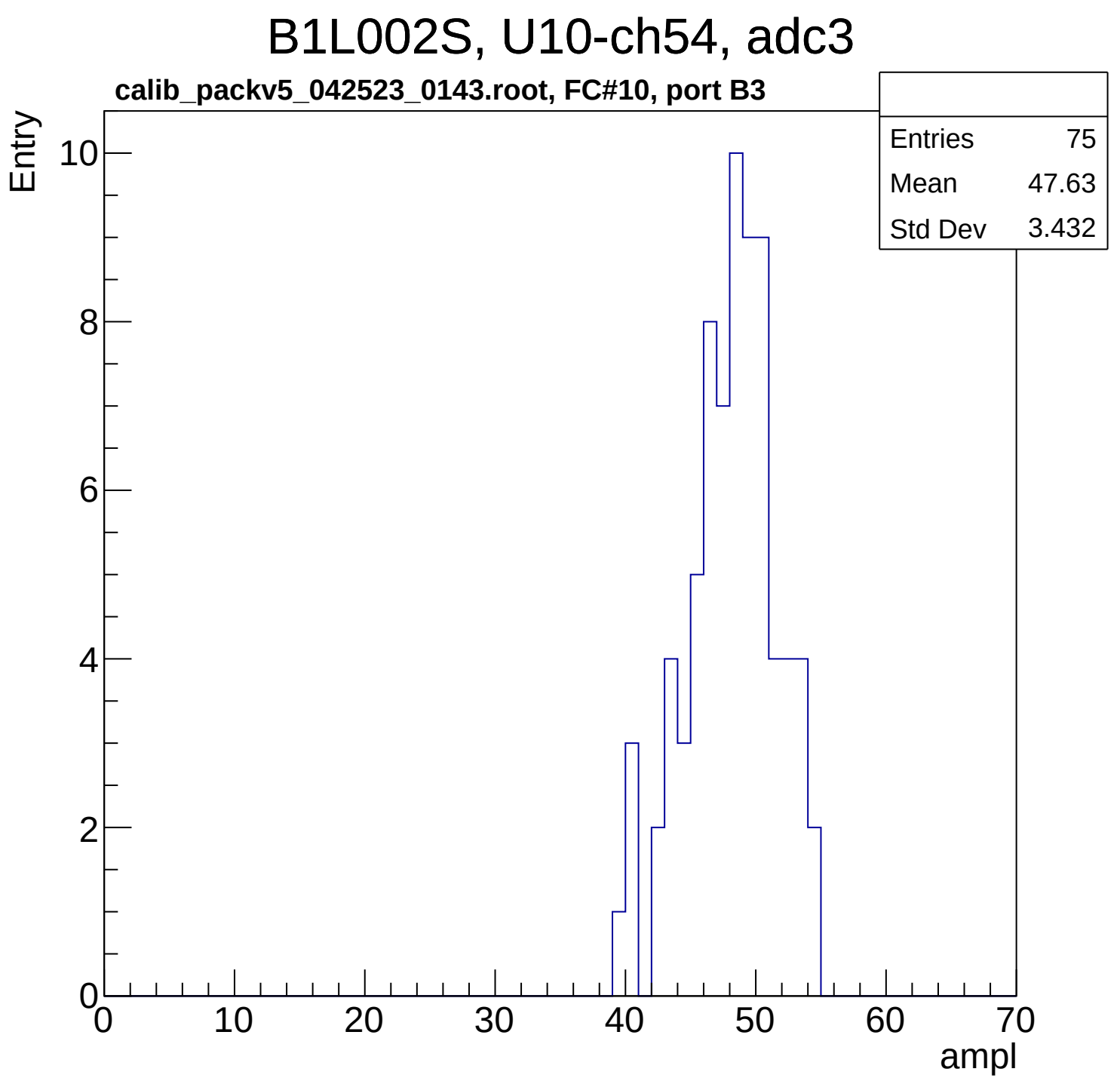
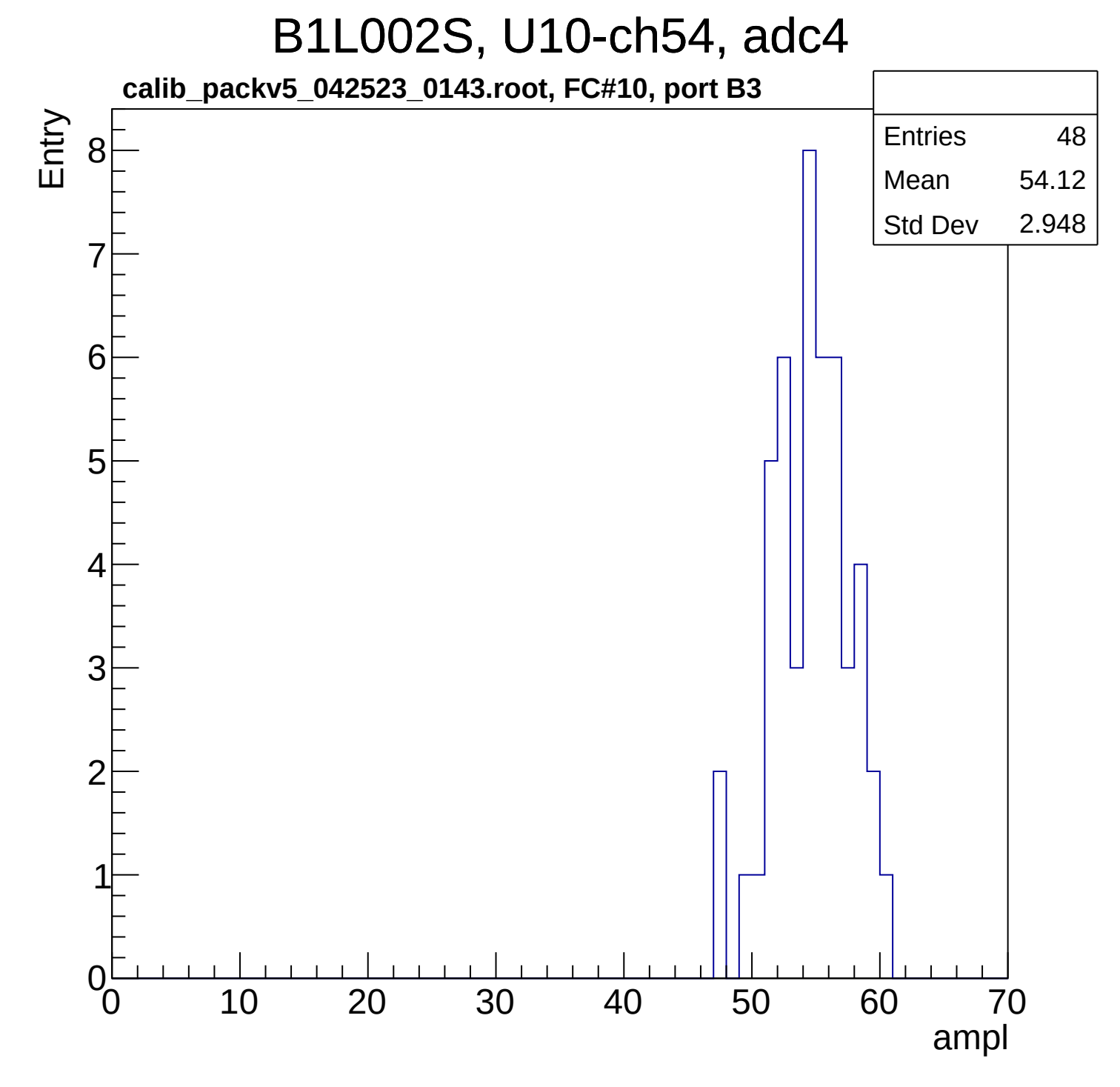
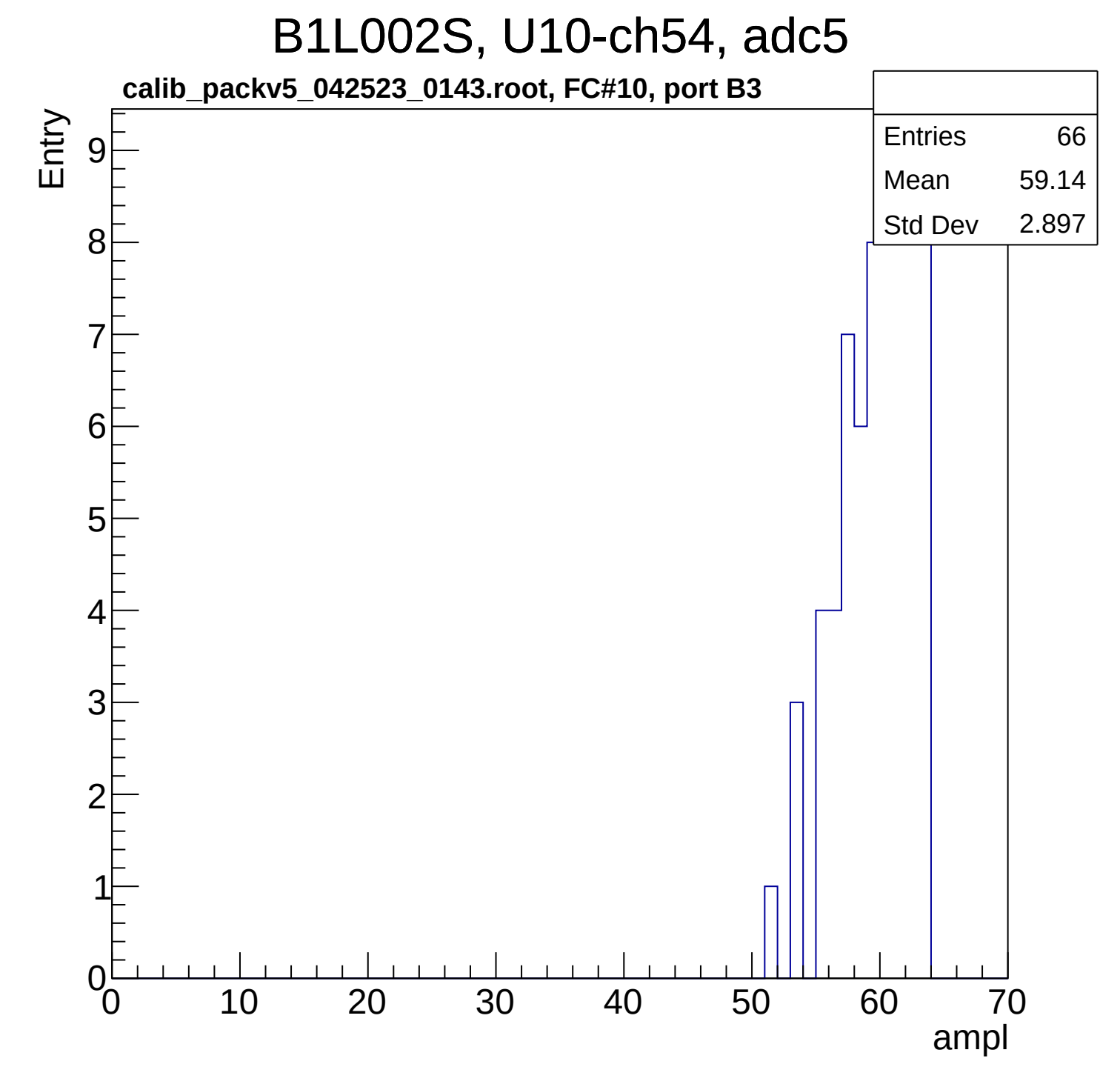
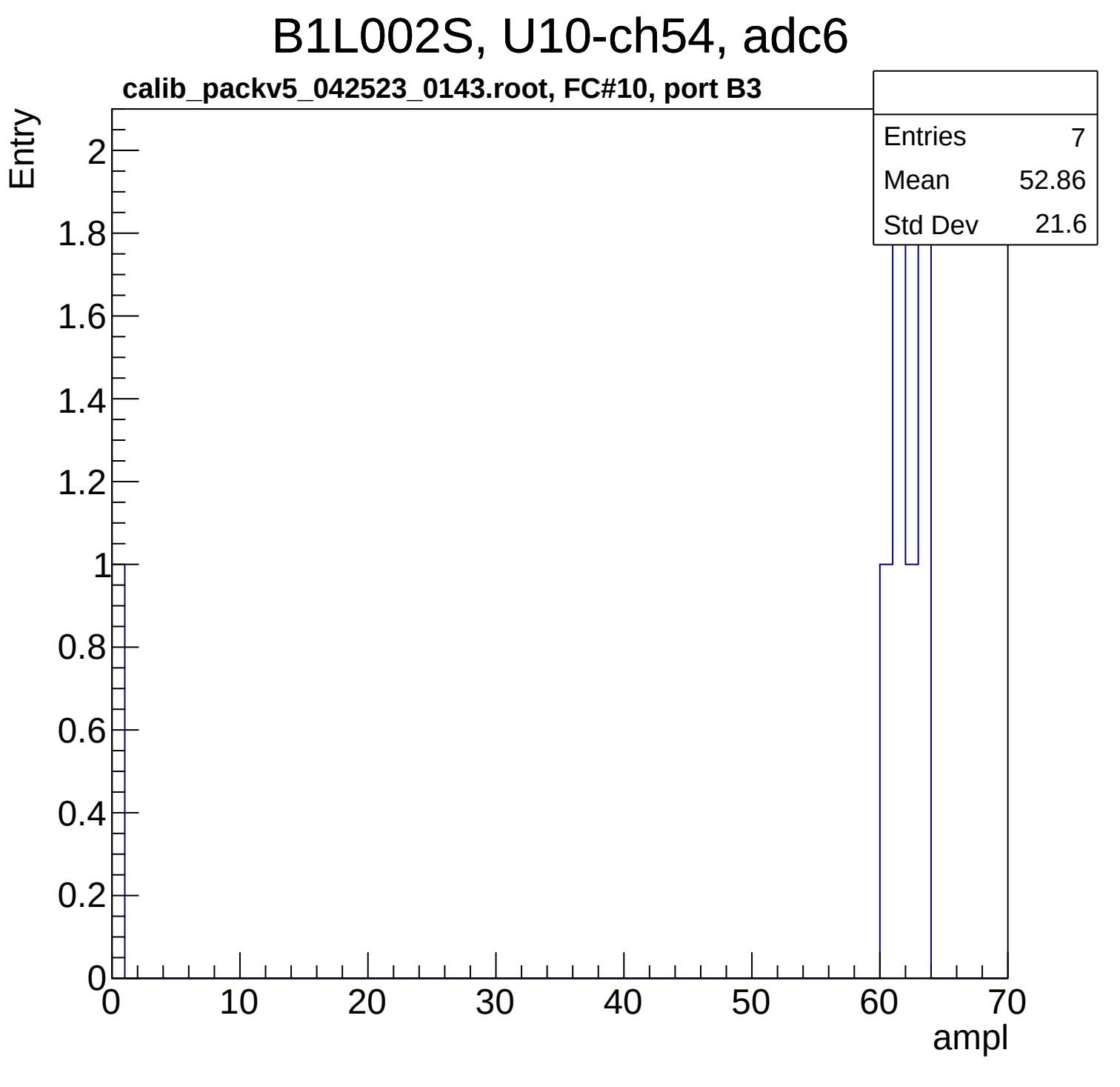
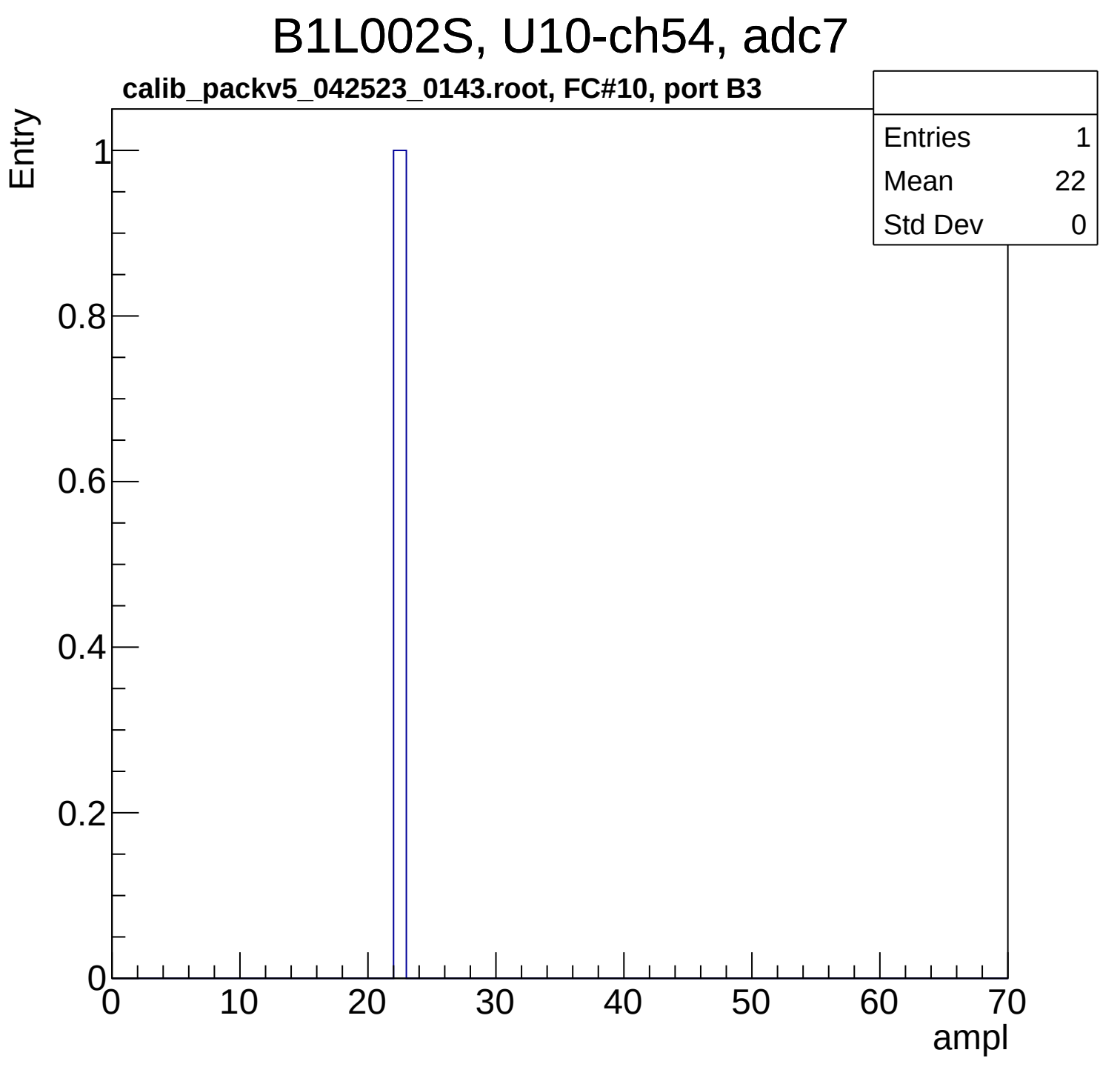
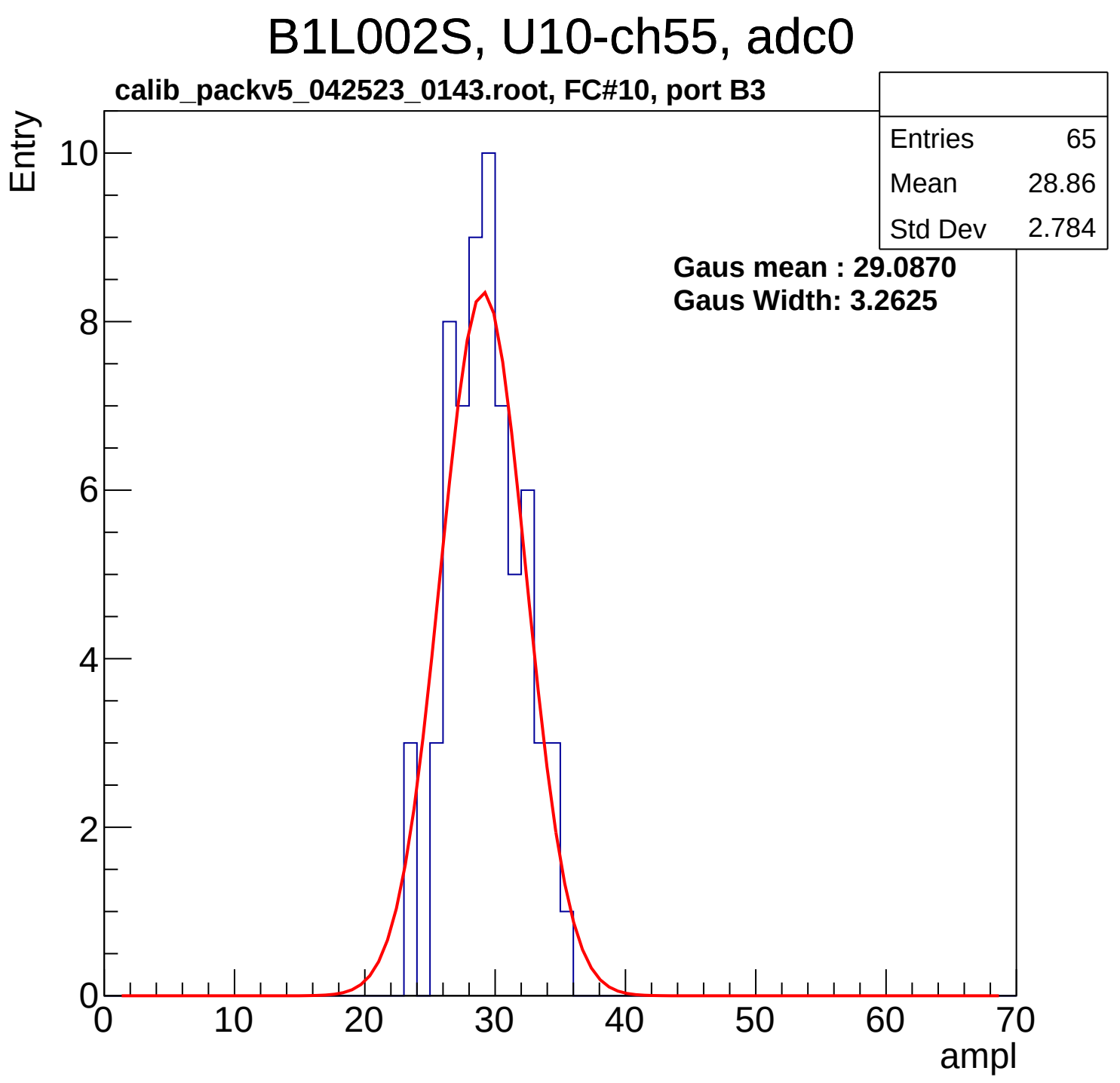

B1L002S, U10-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 47.63
Std Dev 3.432
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

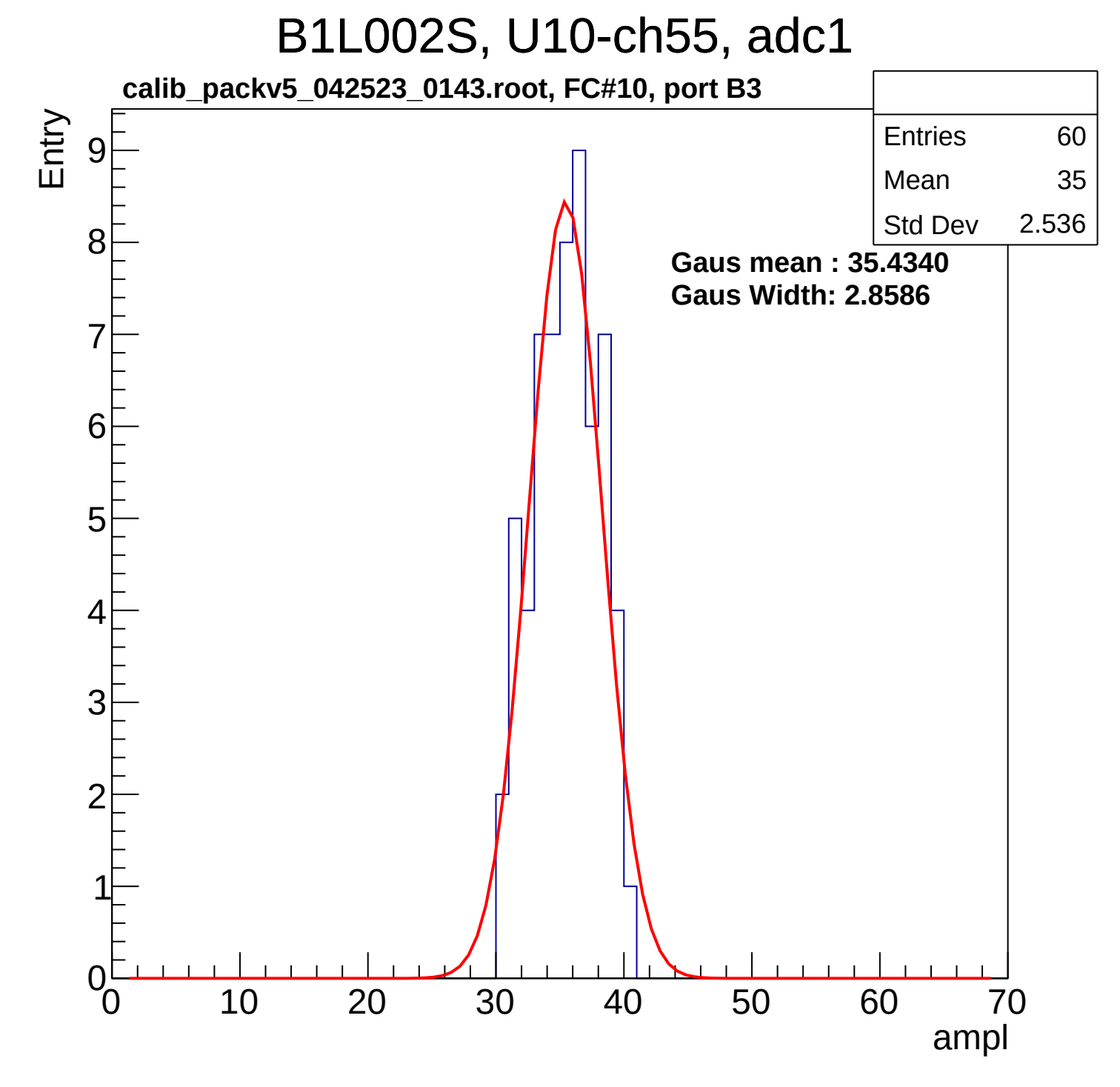

B1L002S, U10-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.12
Std Dev 2.948
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

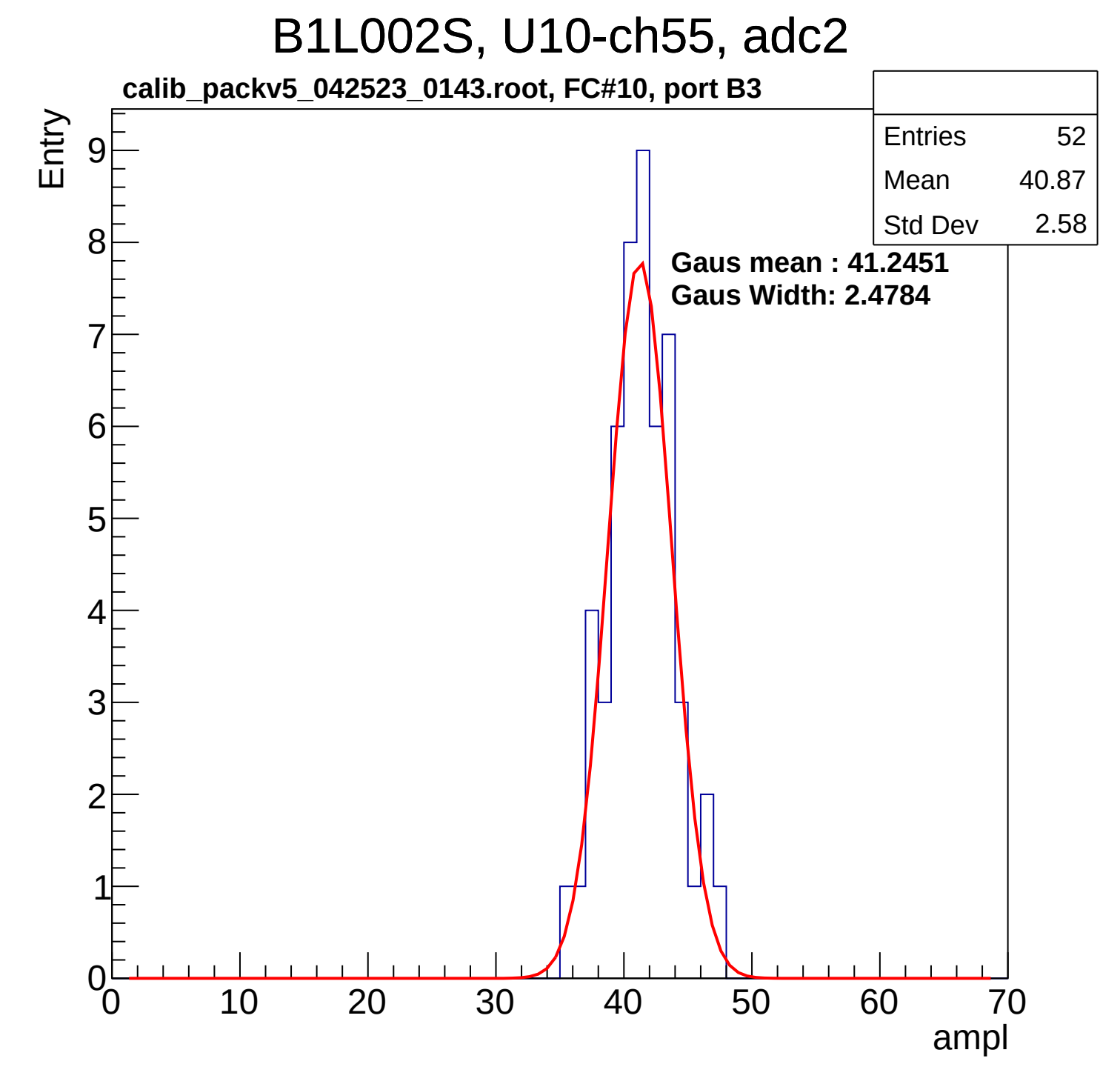

B1L002S, U10-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 59.14
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

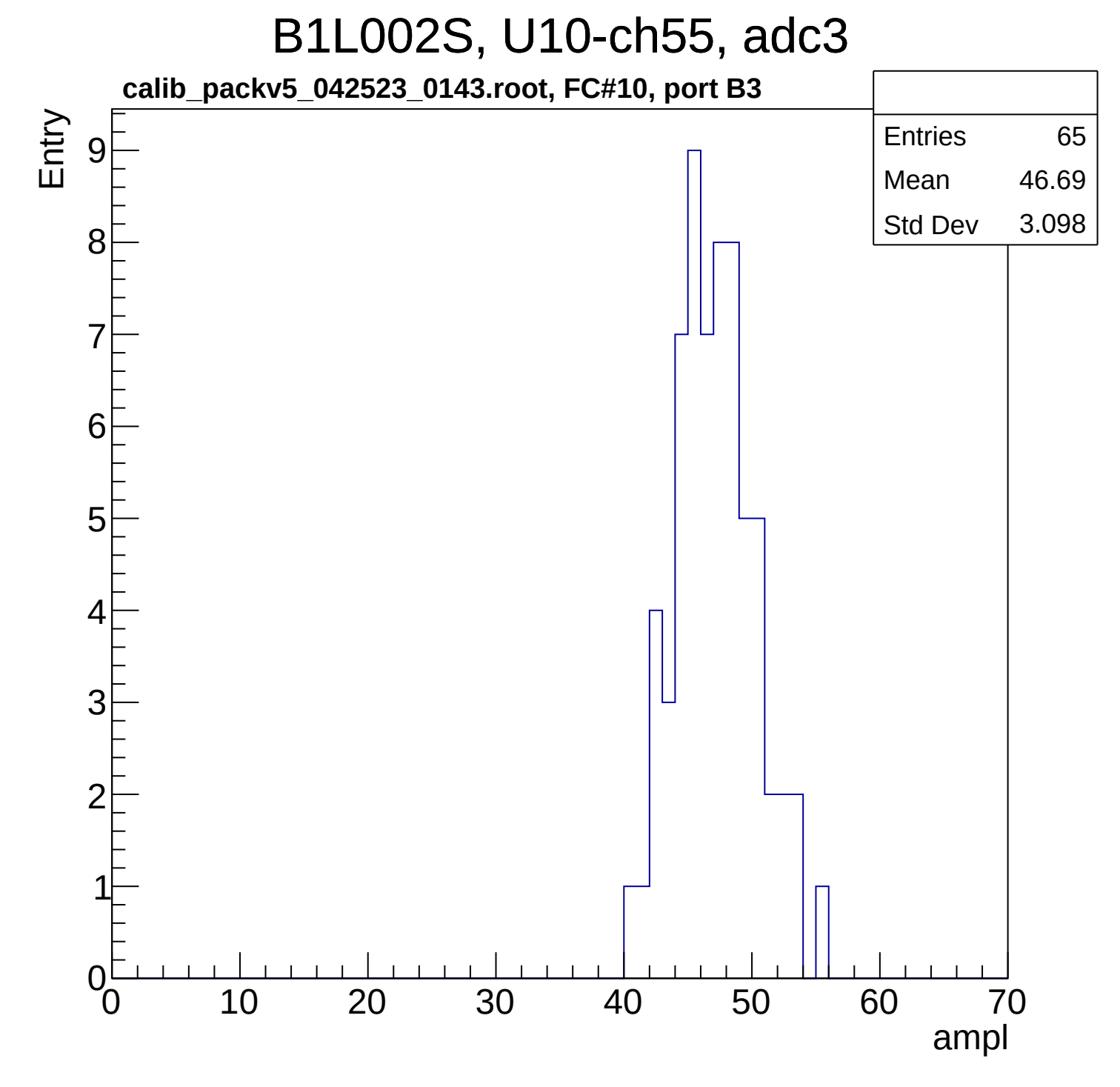

B1L002S, U10-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 52.86
Std Dev 21.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

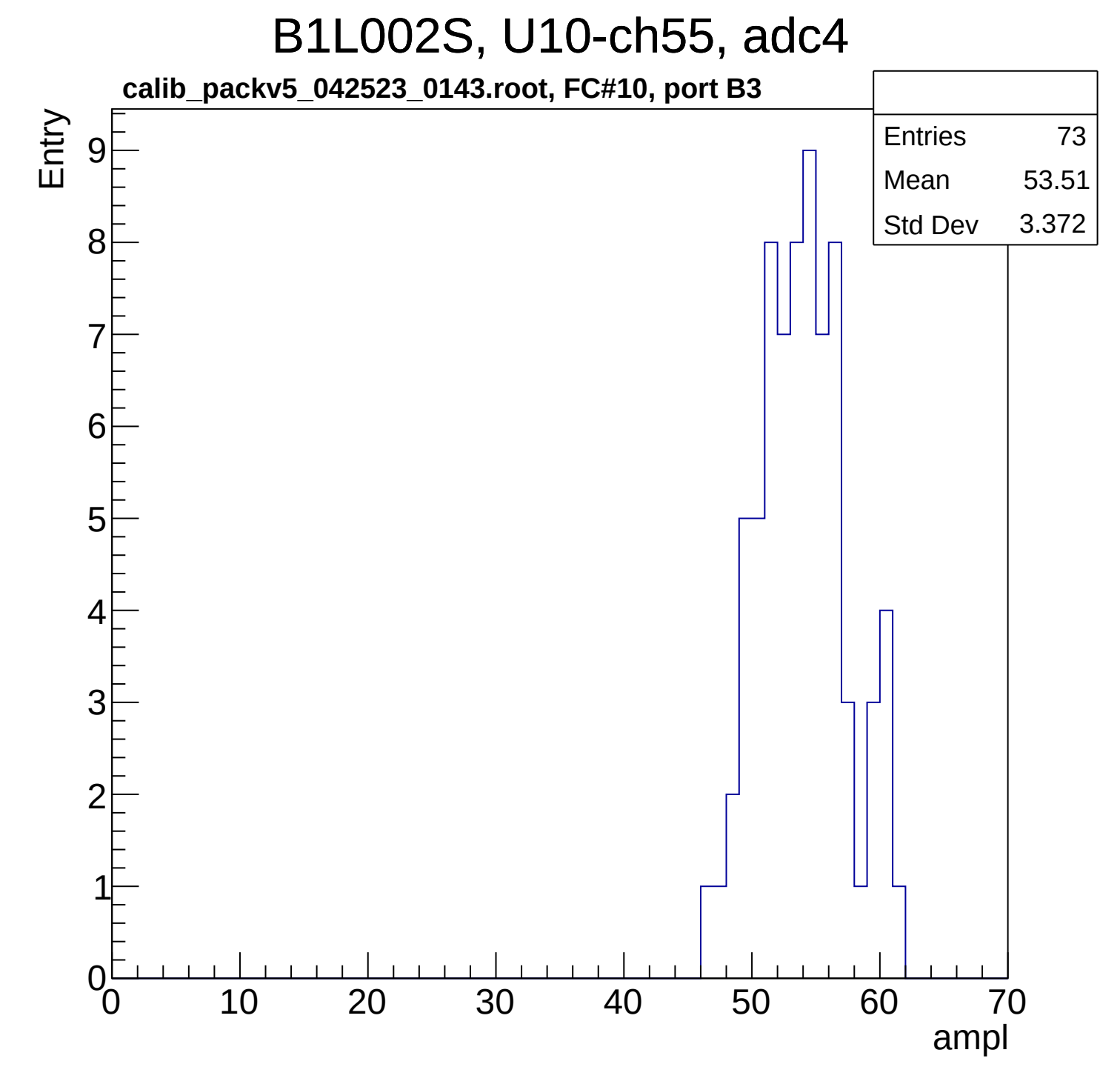

B1L002S, U10-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

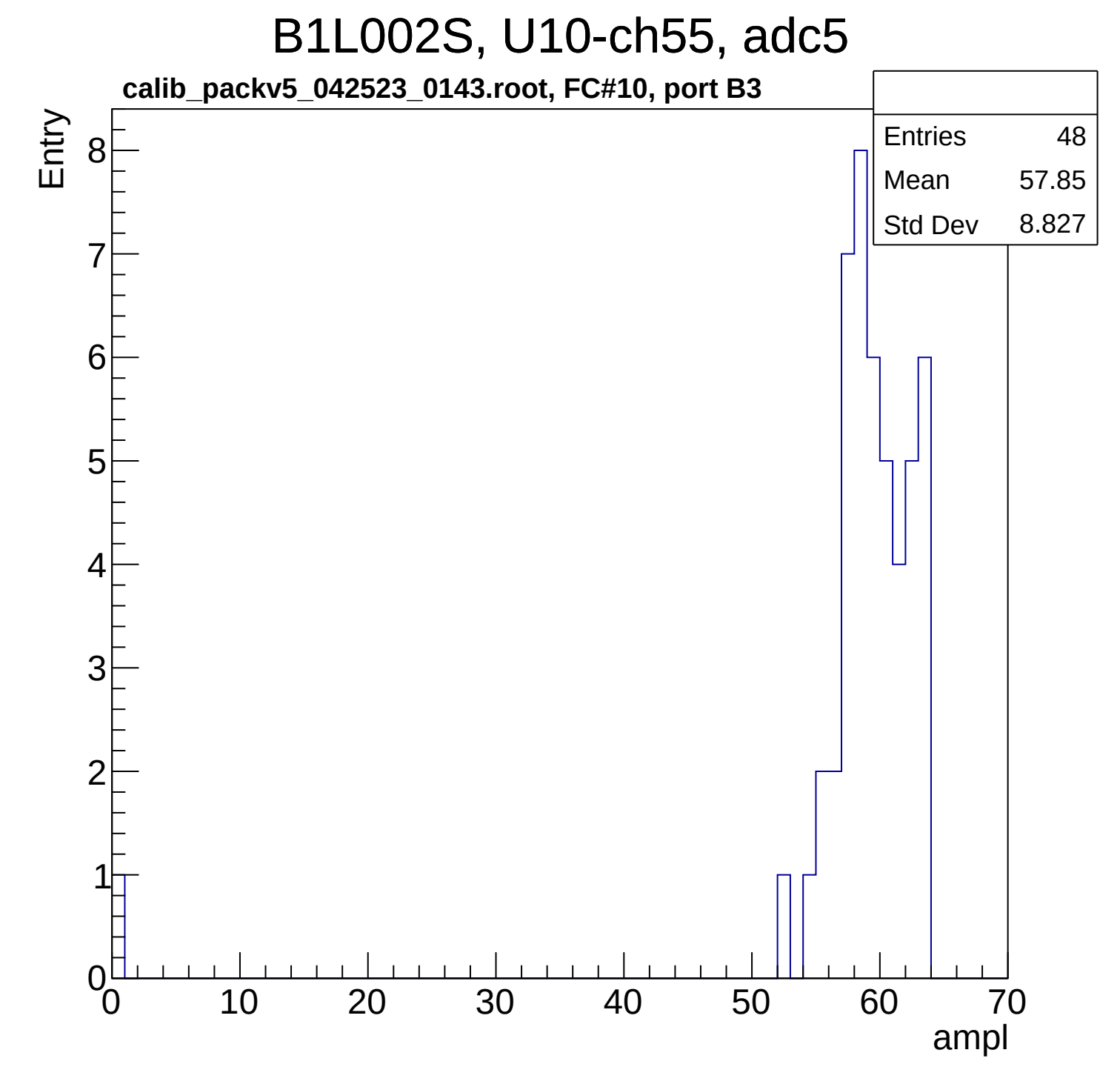

B1L002S, U10-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.86
Std Dev 2.784
Gaus mean : 29.0870
Gaus Width: 3.2625
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

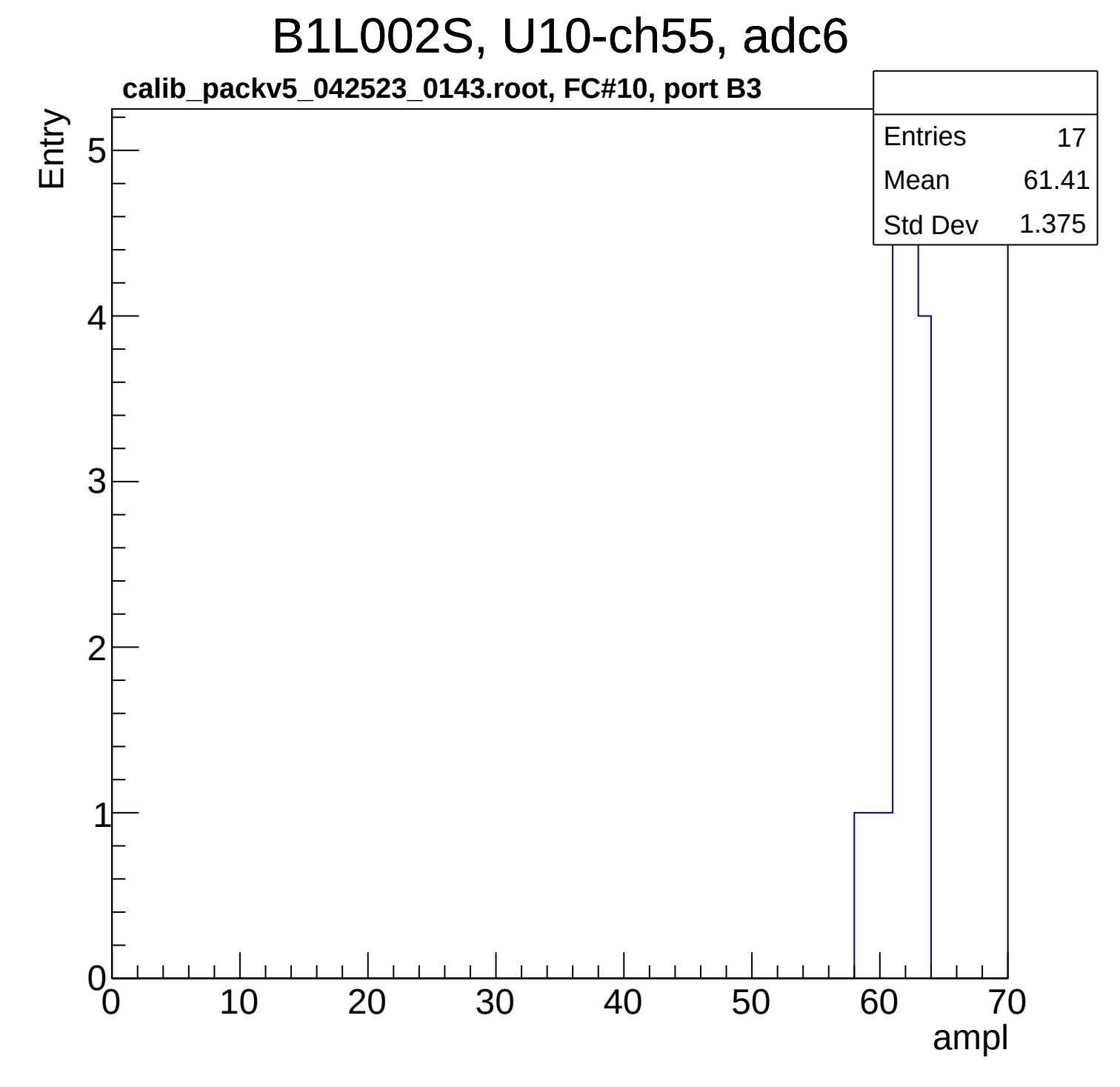

B1L002S, U10-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35
Std Dev 2.536
Gaus mean : 35.4340
Gaus Width: 2.8586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

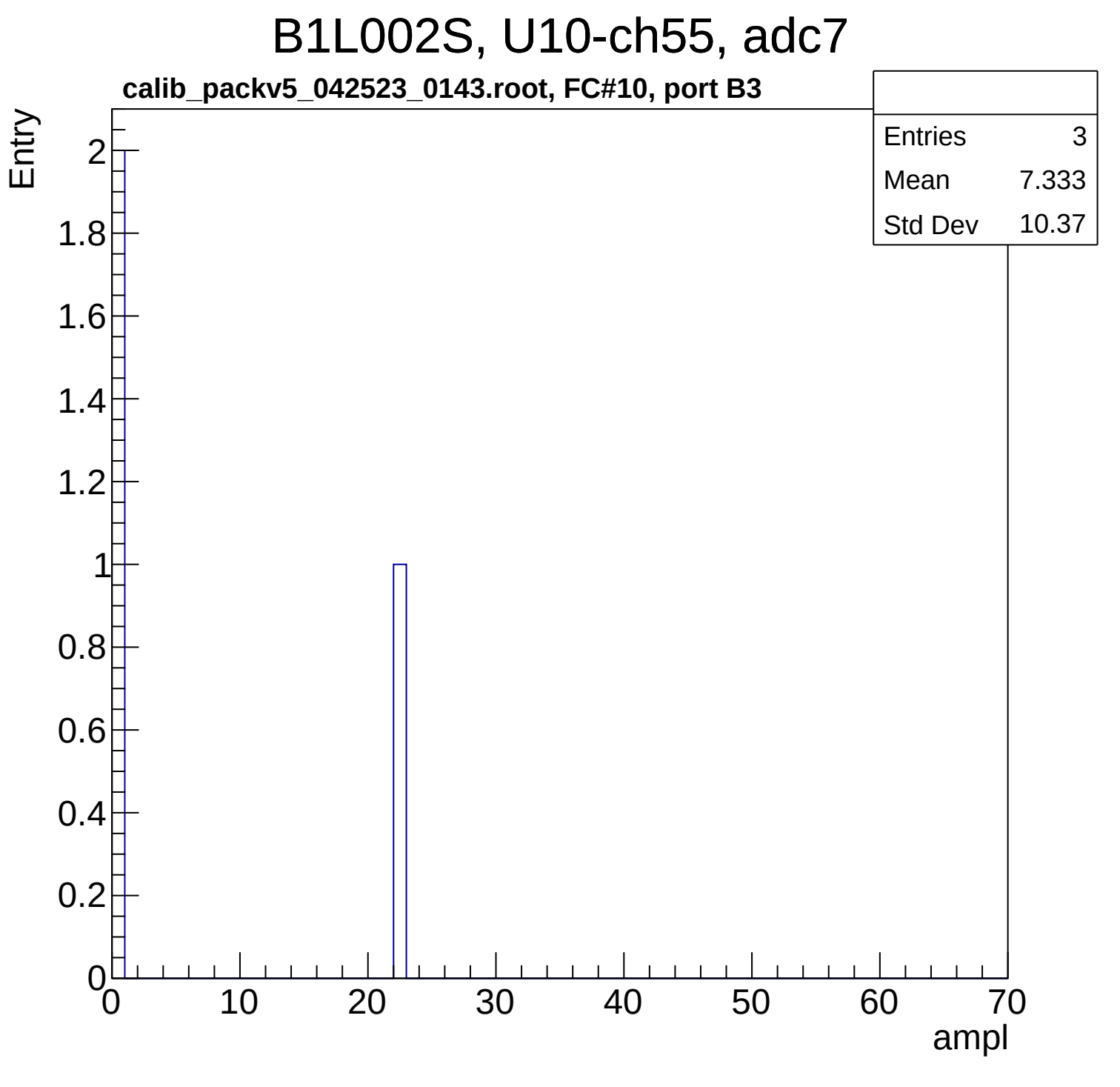

B1L002S, U10-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.87
Std Dev 2.58
Gaus mean : 41.2451
Gaus Width: 2.4784
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

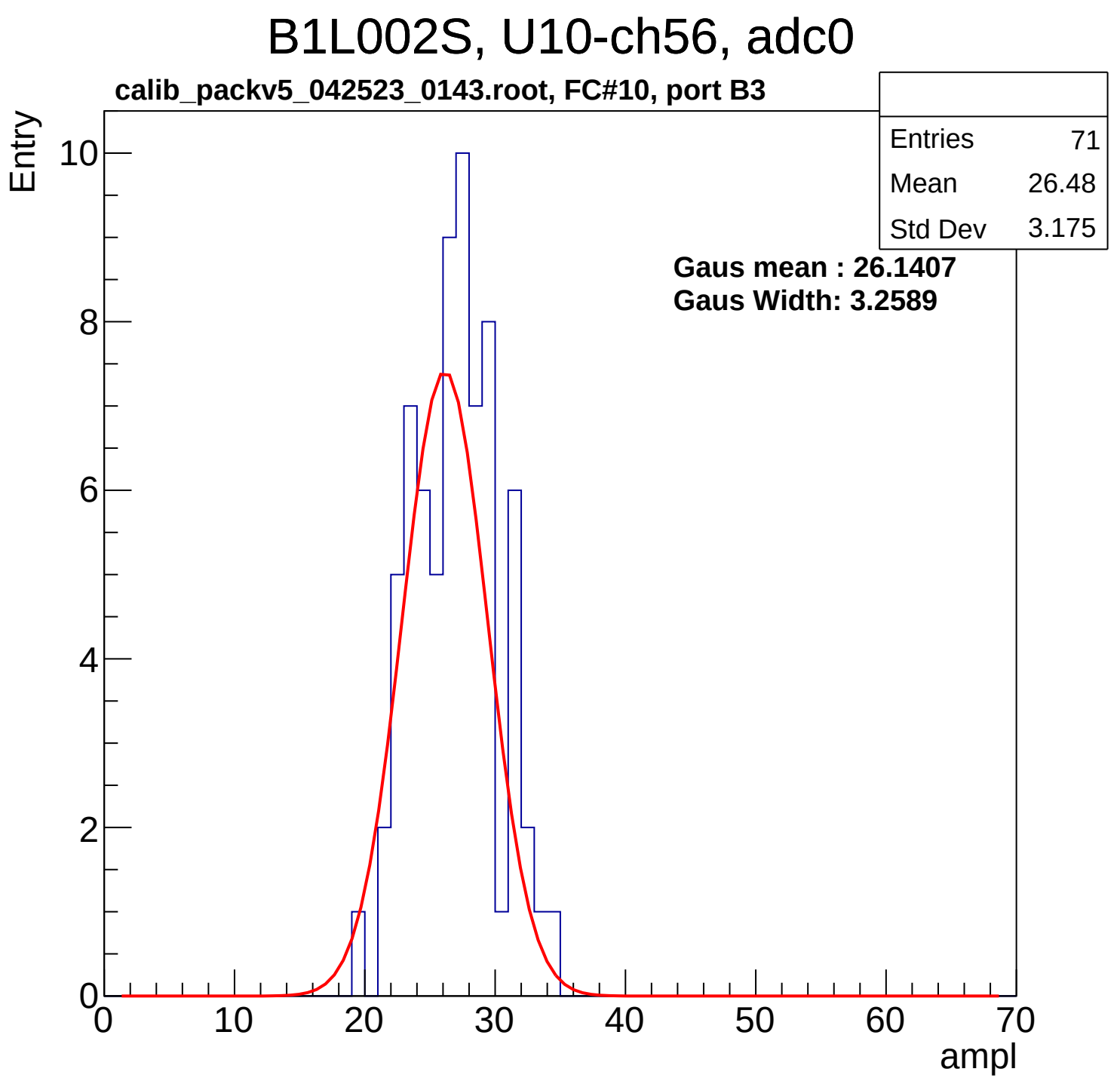

B1L002S, U10-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.69
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

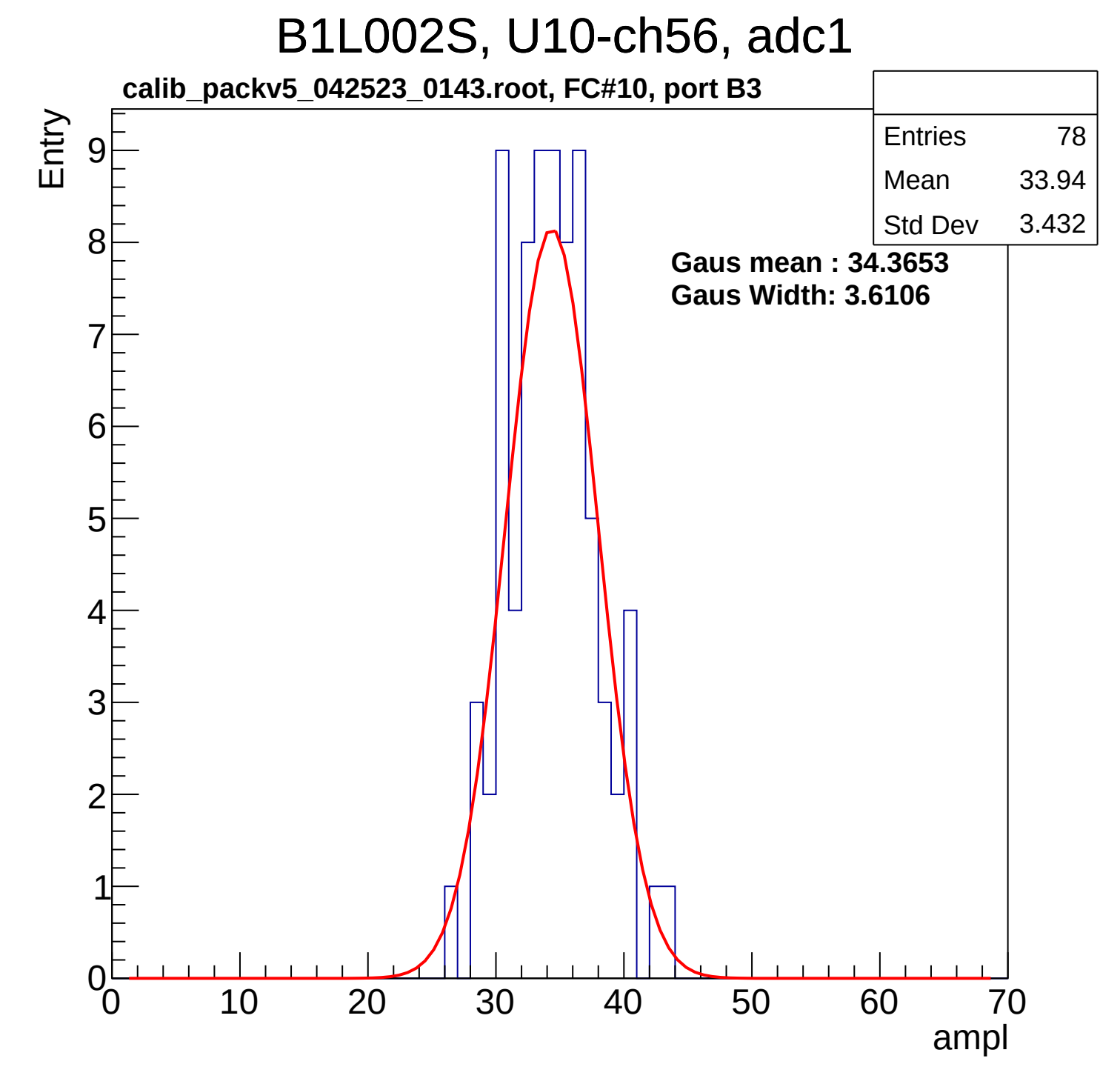

B1L002S, U10-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 53.51
Std Dev 3.372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

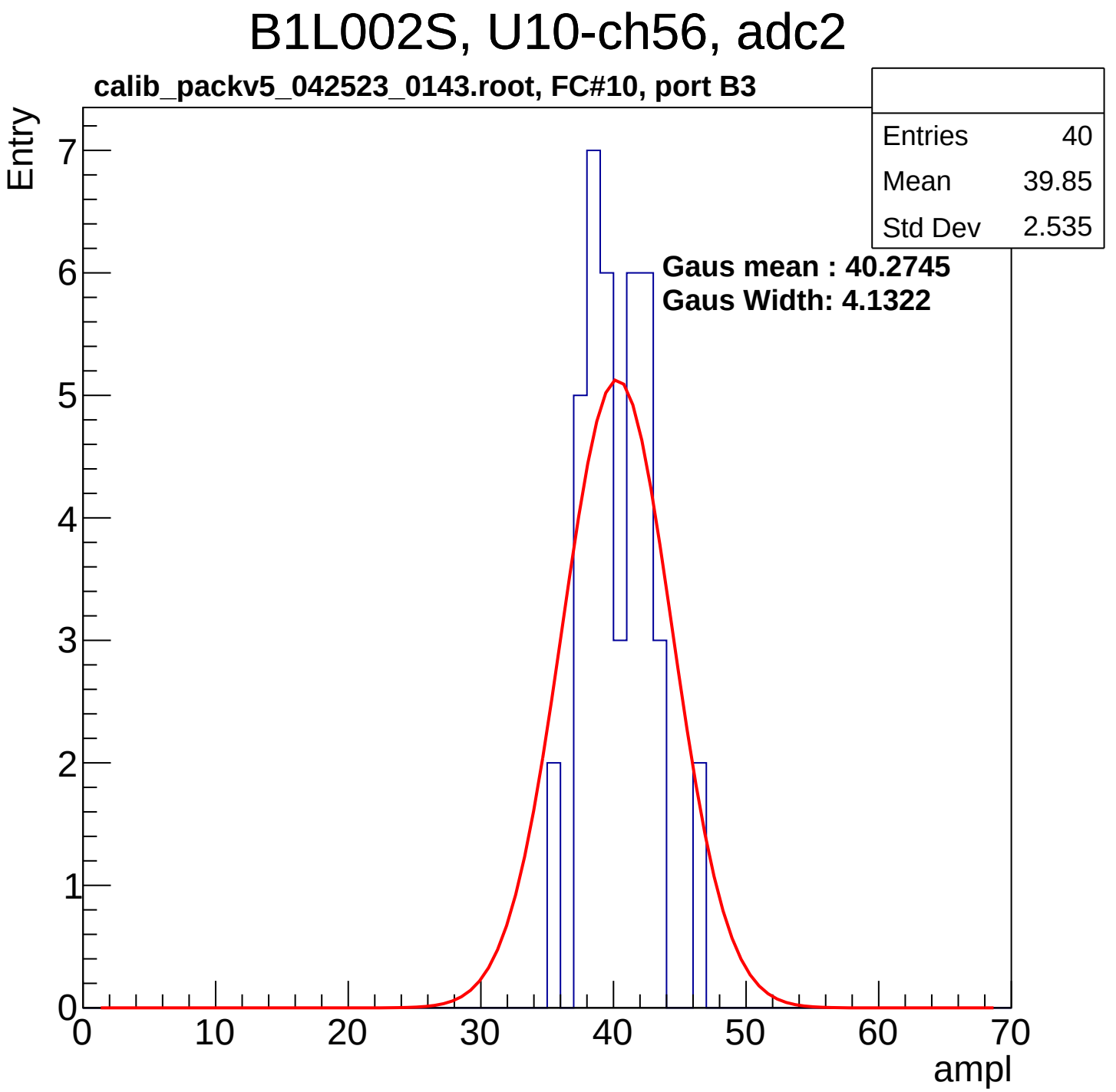

B1L002S, U10-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.85
Std Dev 8.827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

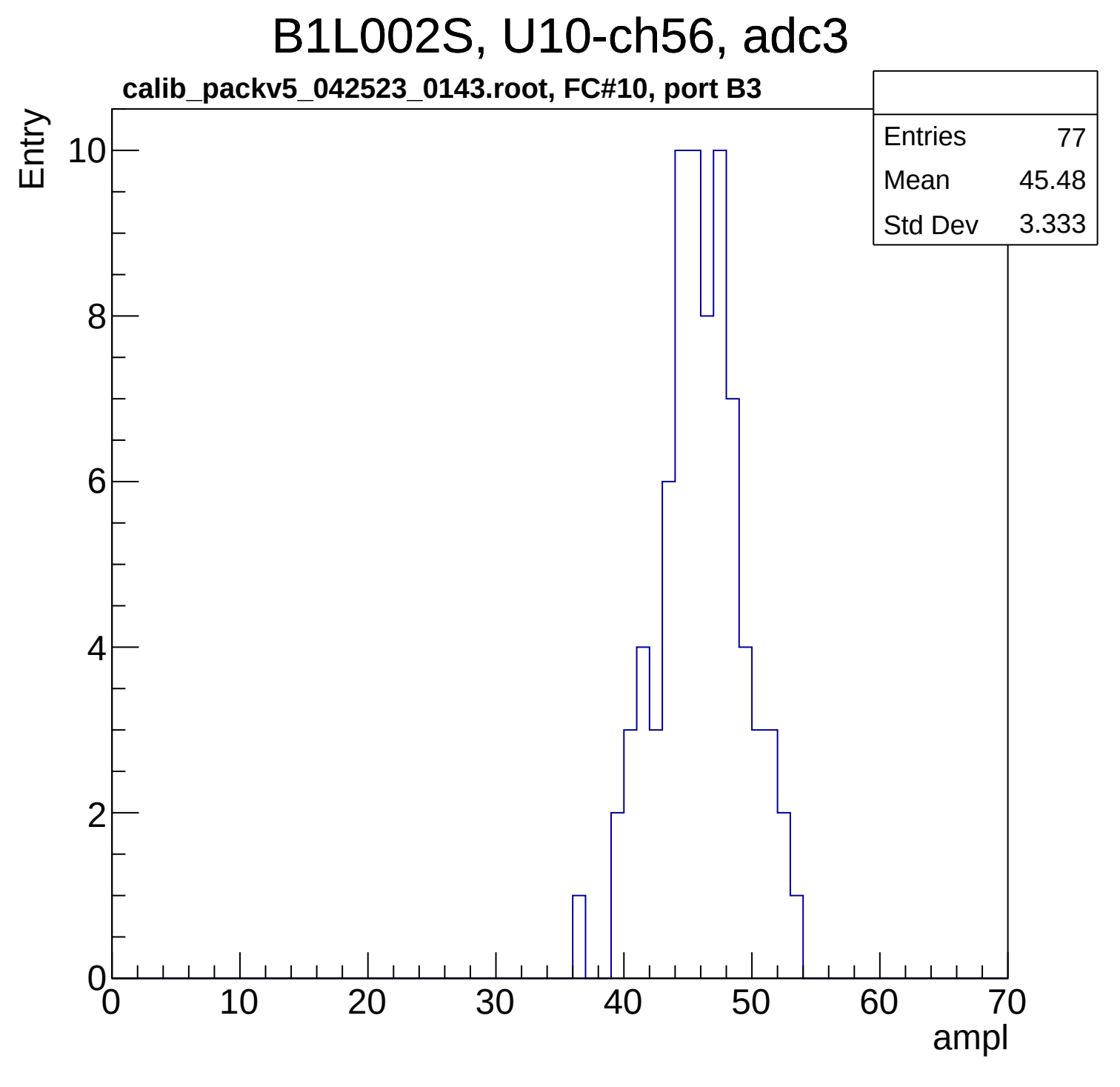

B1L002S, U10-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.41
Std Dev 1.375
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

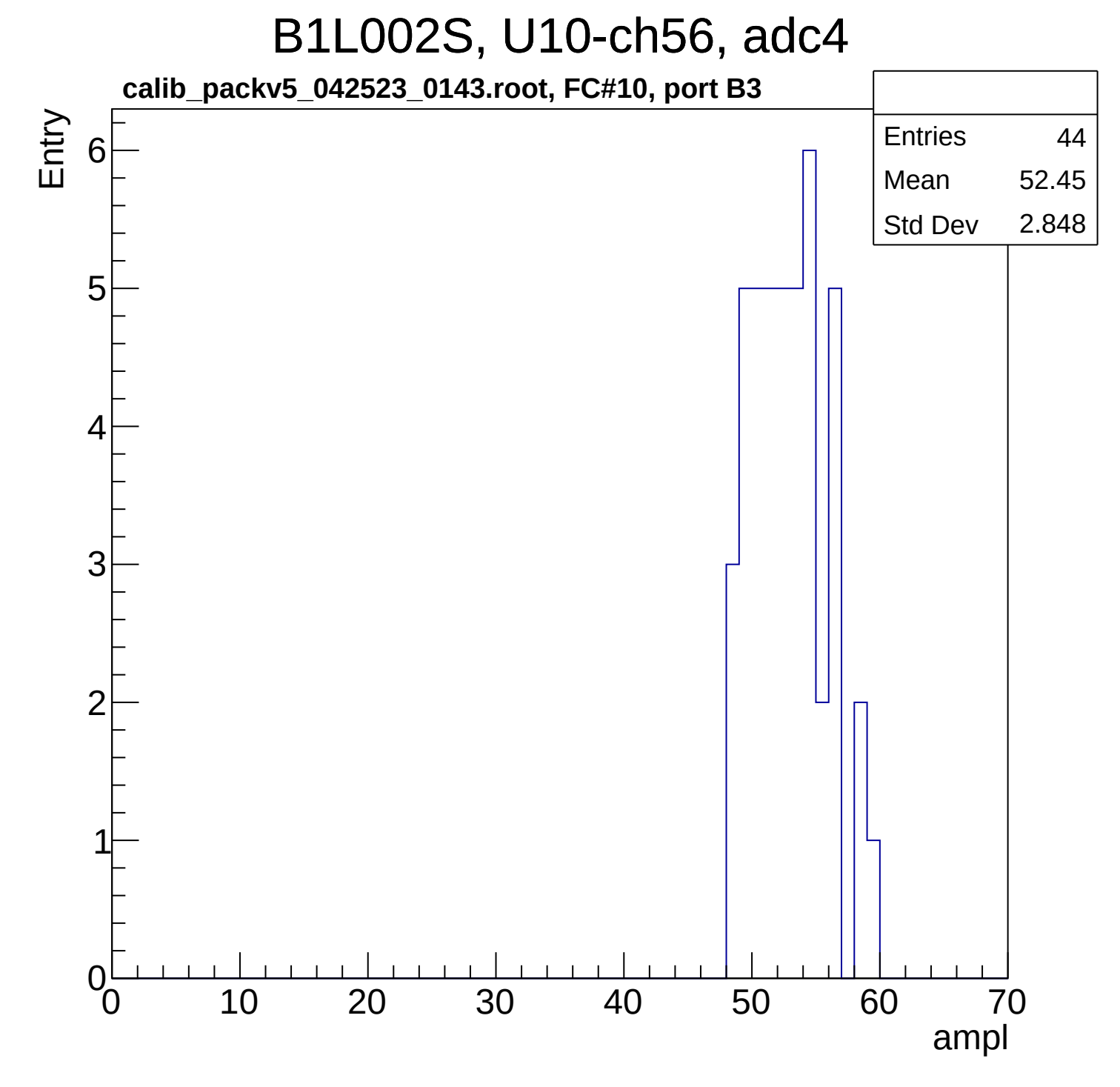

B1L002S, U10-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

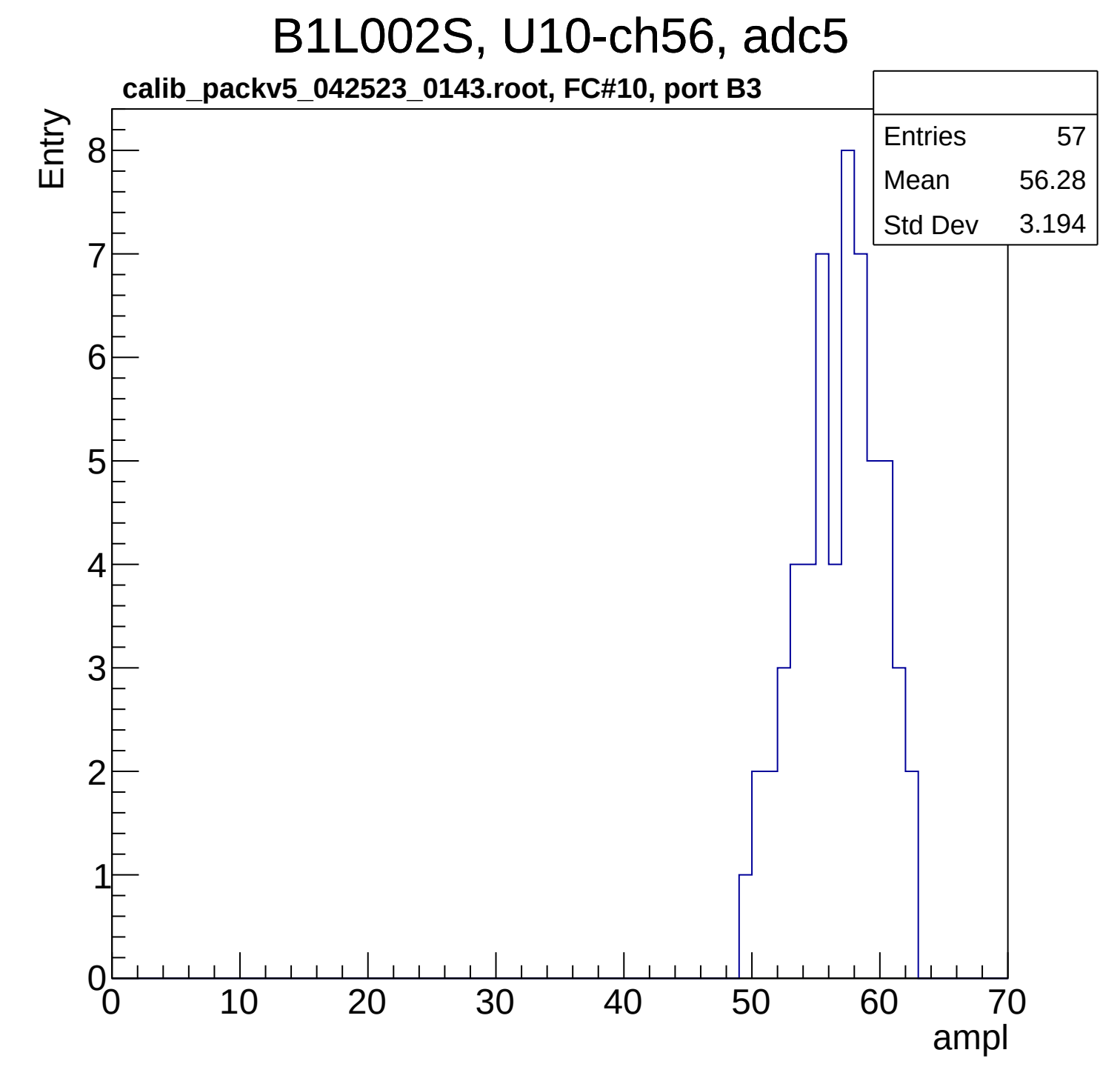

B1L002S, U10-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 26.48
Std Dev 3.175
Gaus mean : 26.1407
Gaus Width: 3.2589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 33.94
Std Dev 3.432
Gaus mean : 34.3653
Gaus Width: 3.6106
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 39.85
Std Dev 2.535
Gaus mean : 40.2745
Gaus Width: 4.1322
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 45.48
Std Dev 3.333
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.45
Std Dev 2.848
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 56.28
Std Dev 3.194
Entry
ampl
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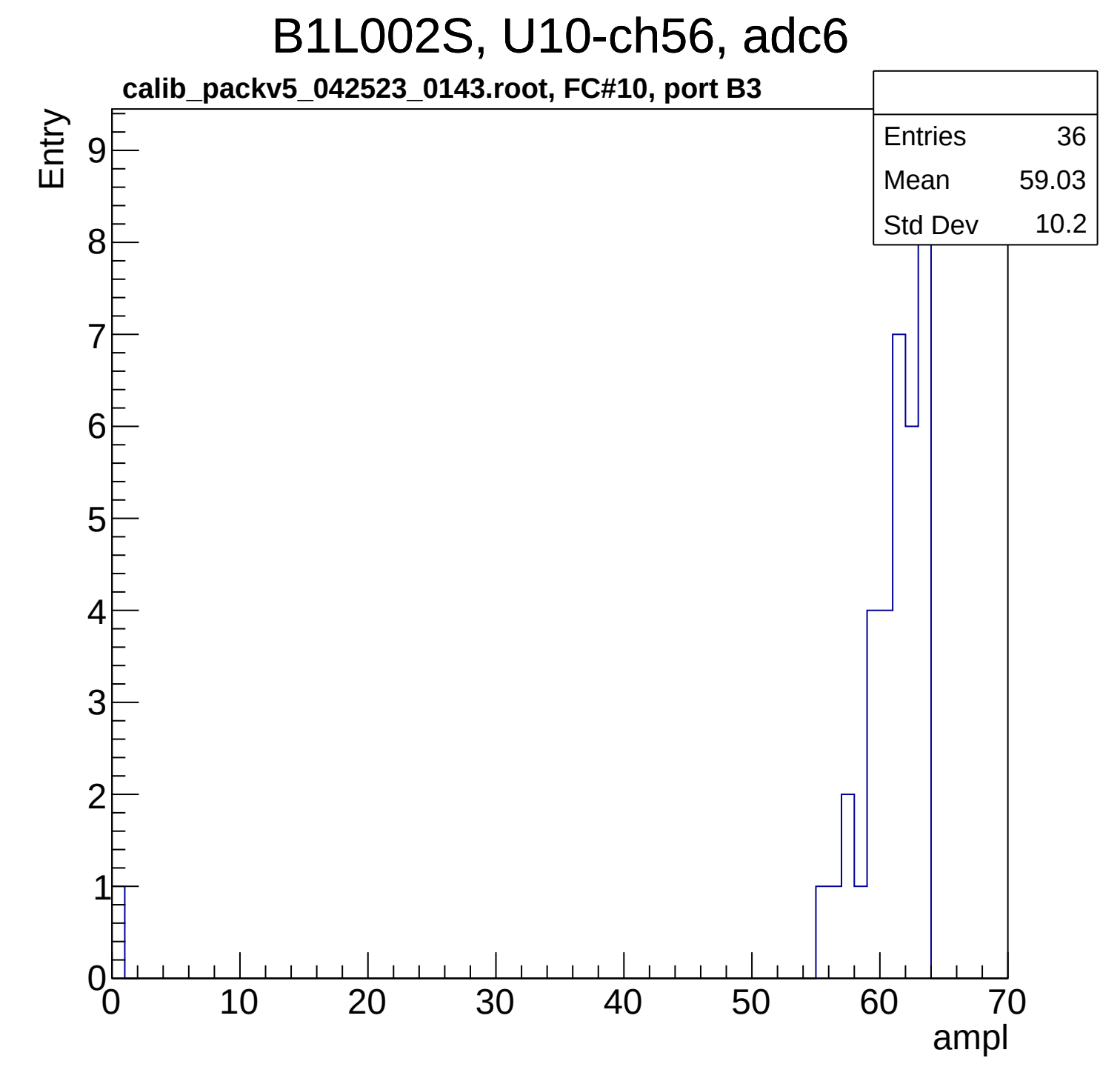
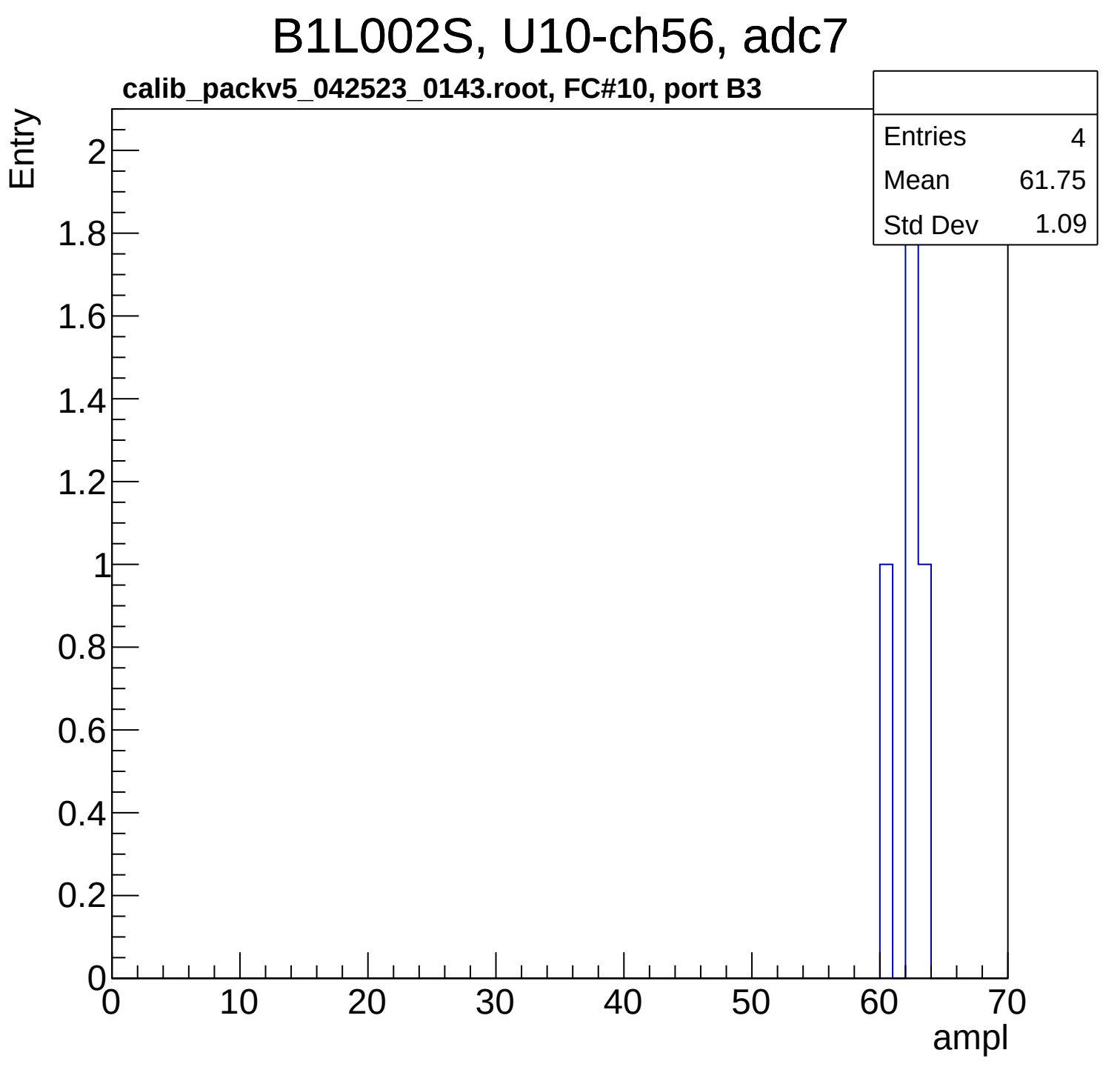
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 59.03
Std Dev 10.2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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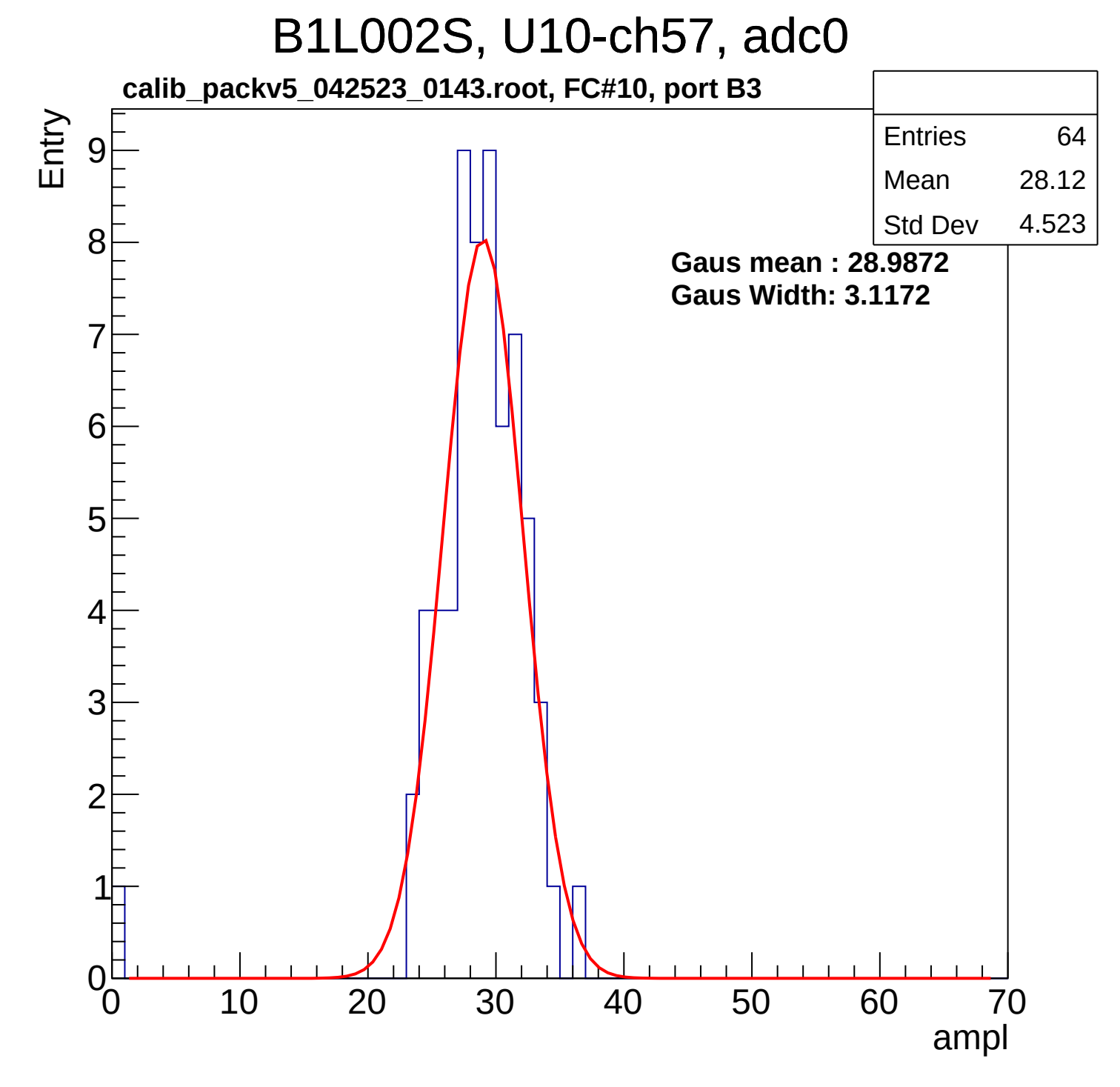
B1L002S, U10-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.12
Std Dev 4.523
Gaus mean : 28.9872
Gaus Width: 3.1172
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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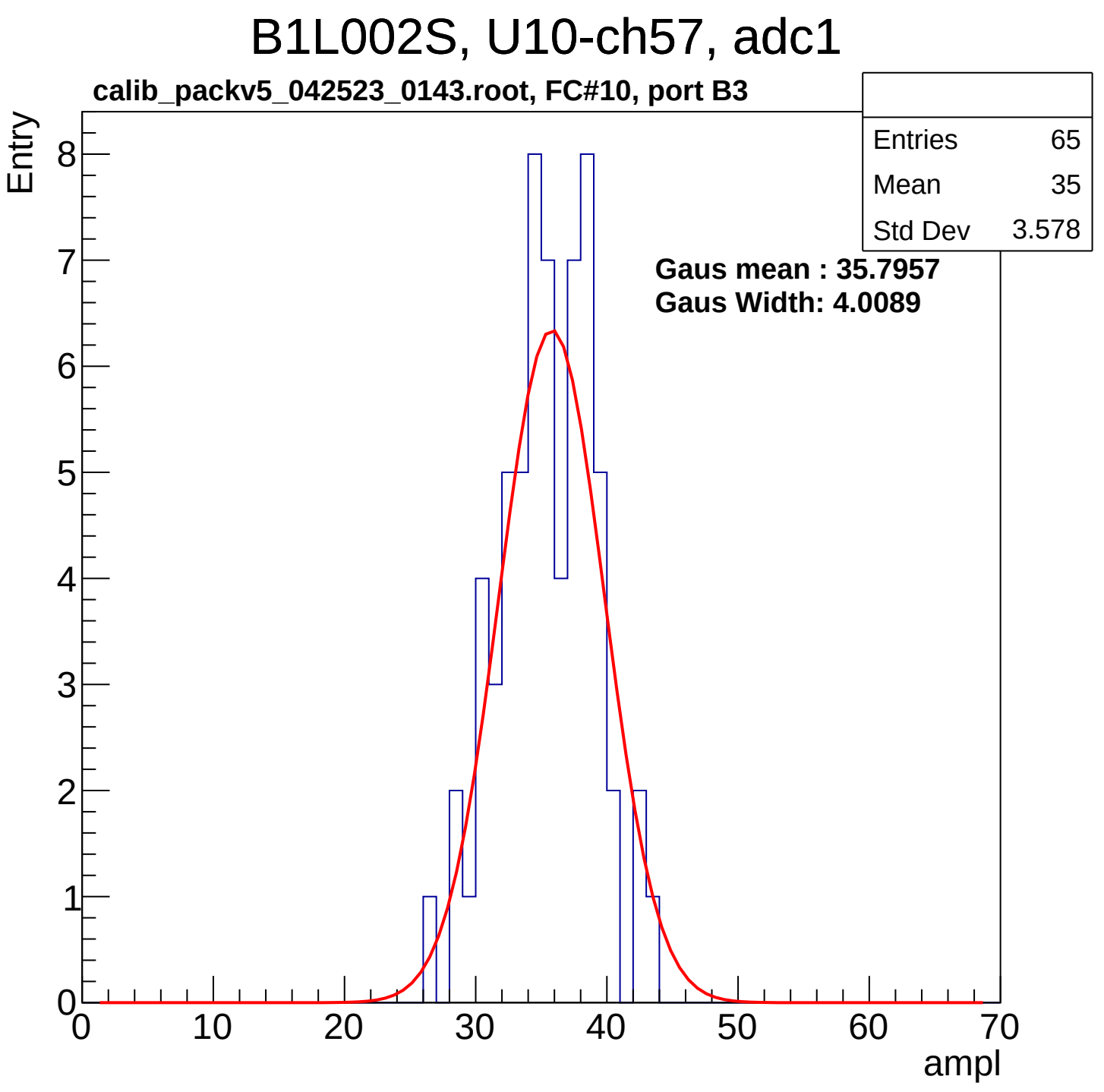
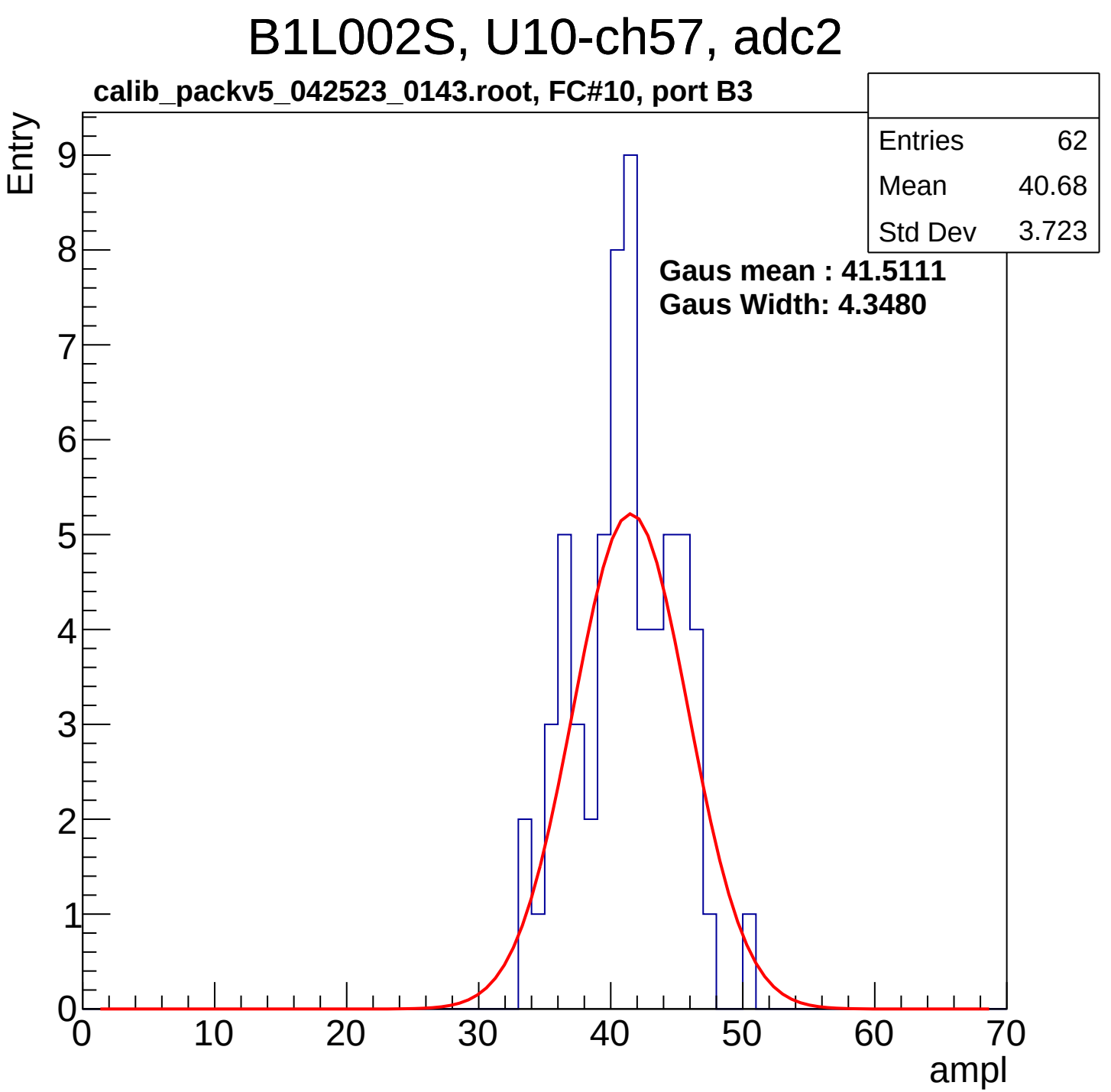

B1L002S, U10-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35
Std Dev 3.578
Gaus mean : 35.7957
Gaus Width: 4.0089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.68
Std Dev 3.723
Gaus mean : 41.5111
Gaus Width: 4.3480
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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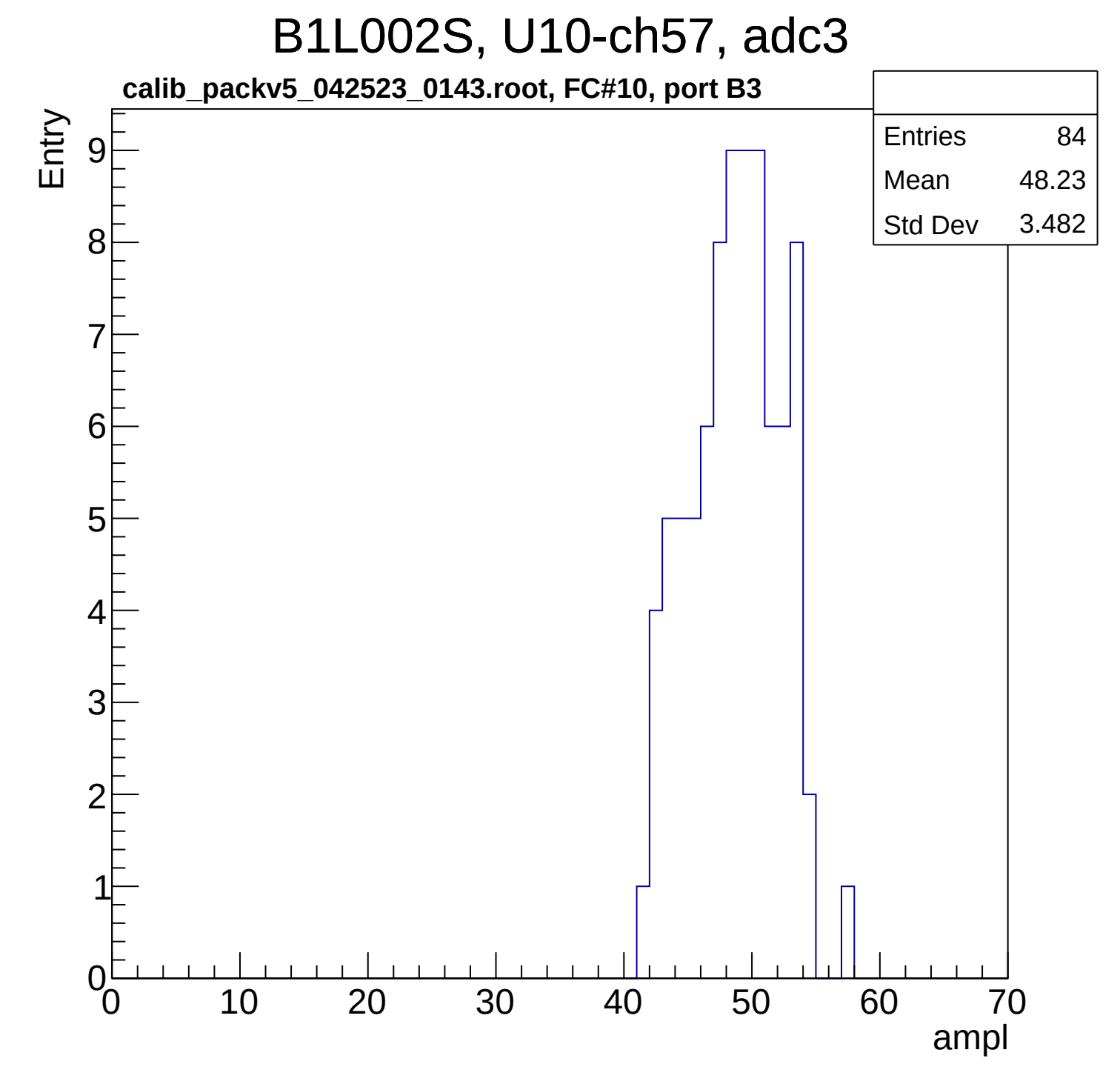
B1L002S, U10-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 48.23
Std Dev 3.482
Entry
ampl
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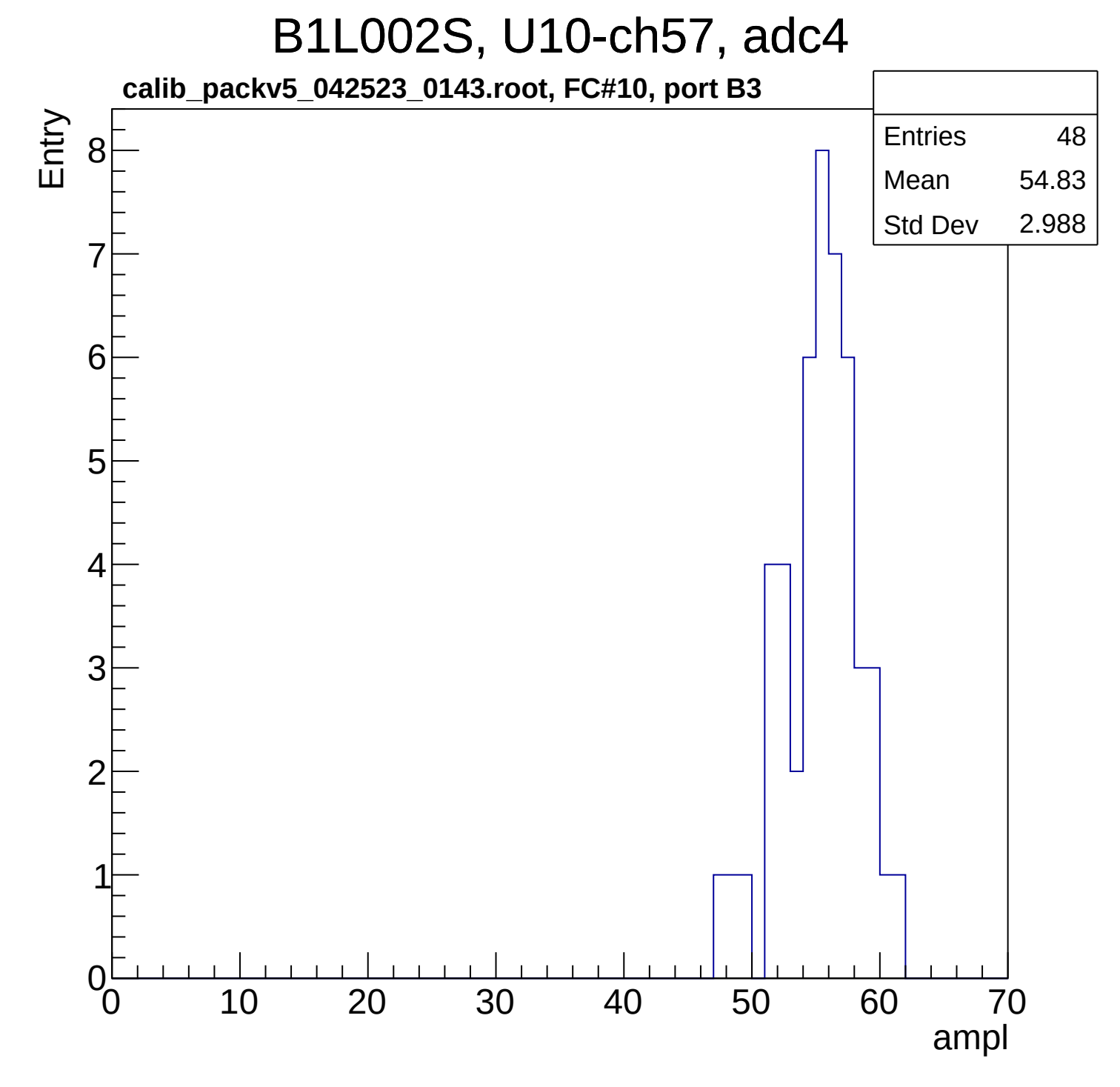
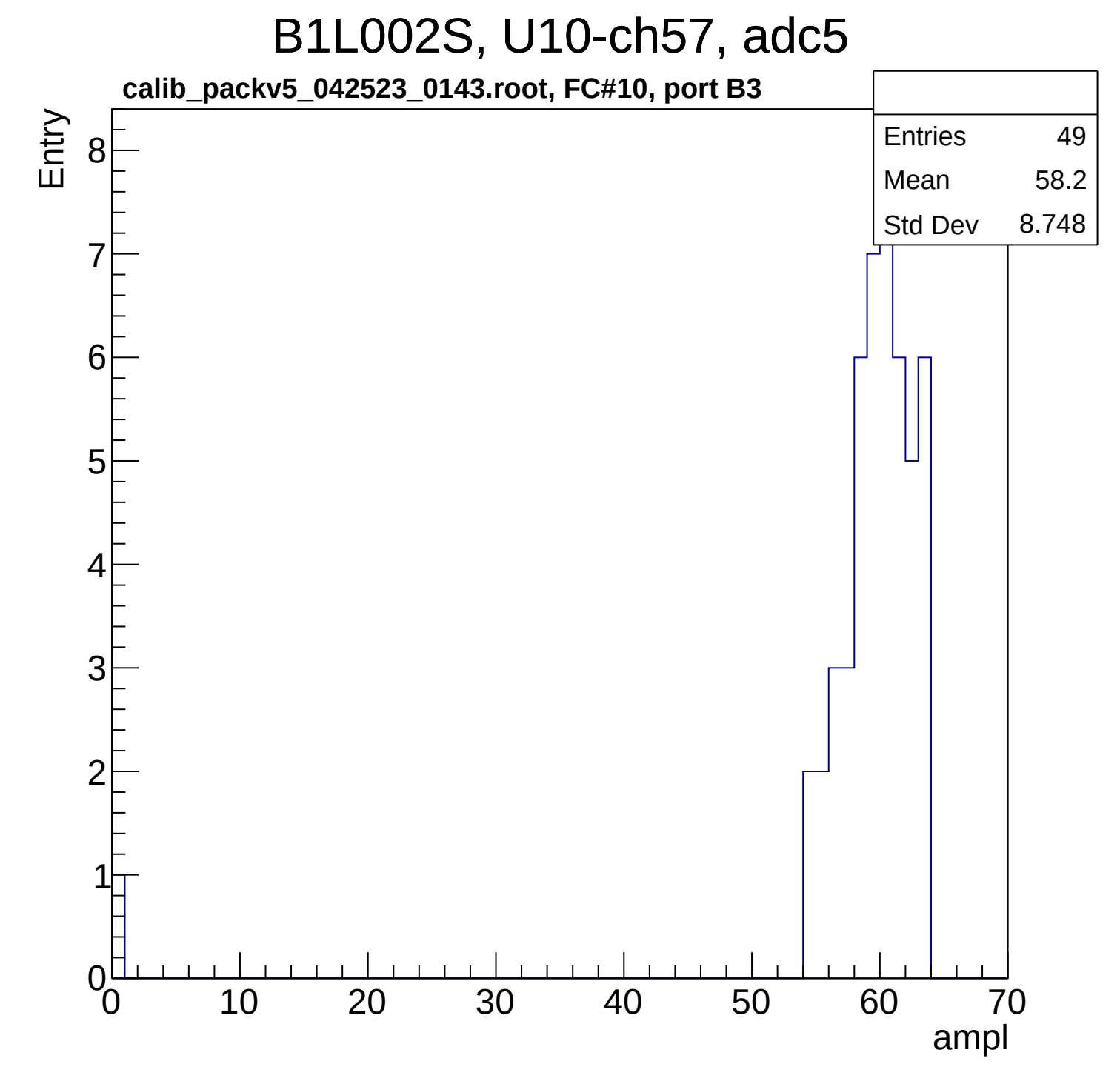
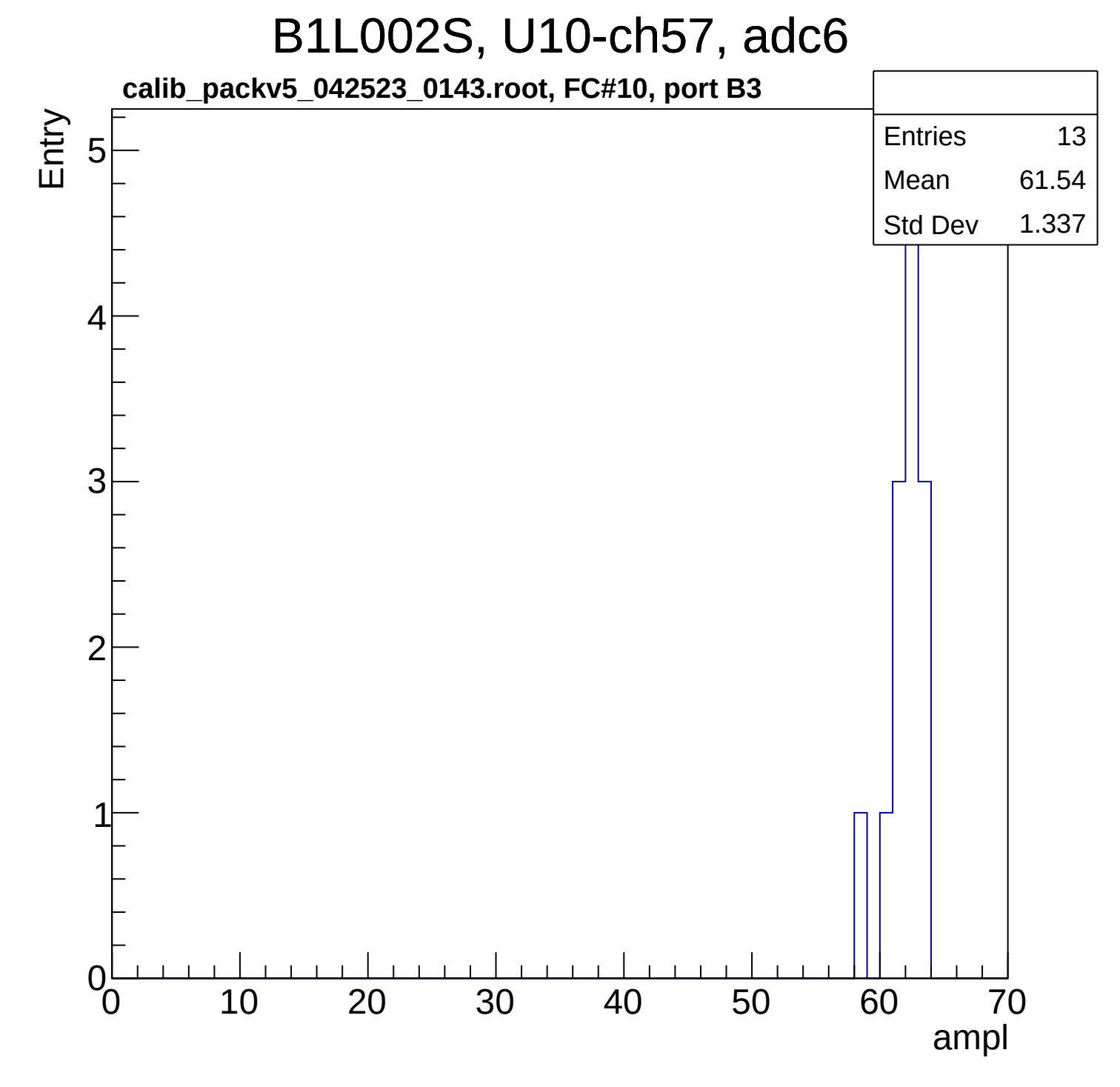
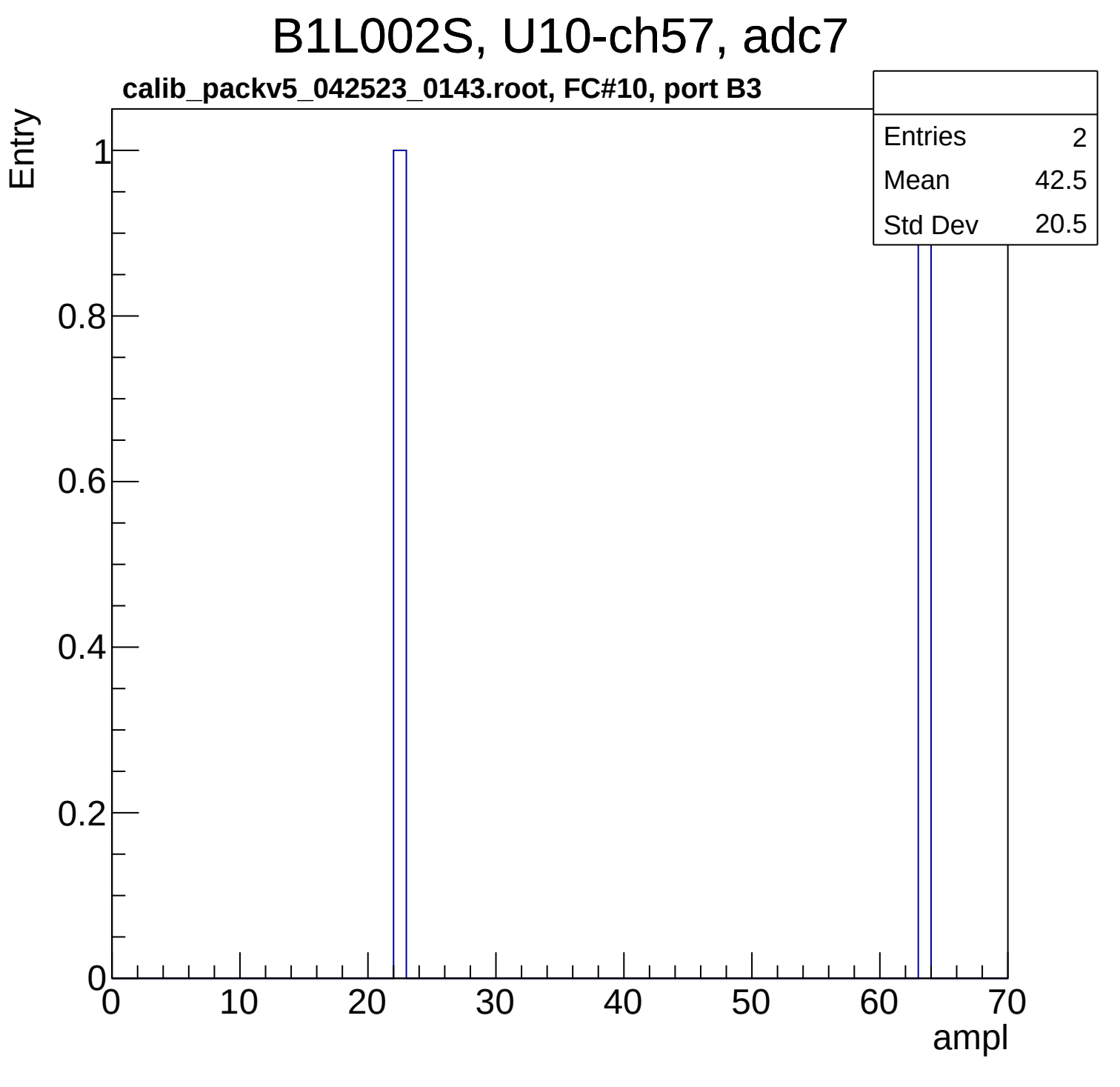
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.83
Std Dev 2.988
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.2
Std Dev 8.748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.54
Std Dev 1.337
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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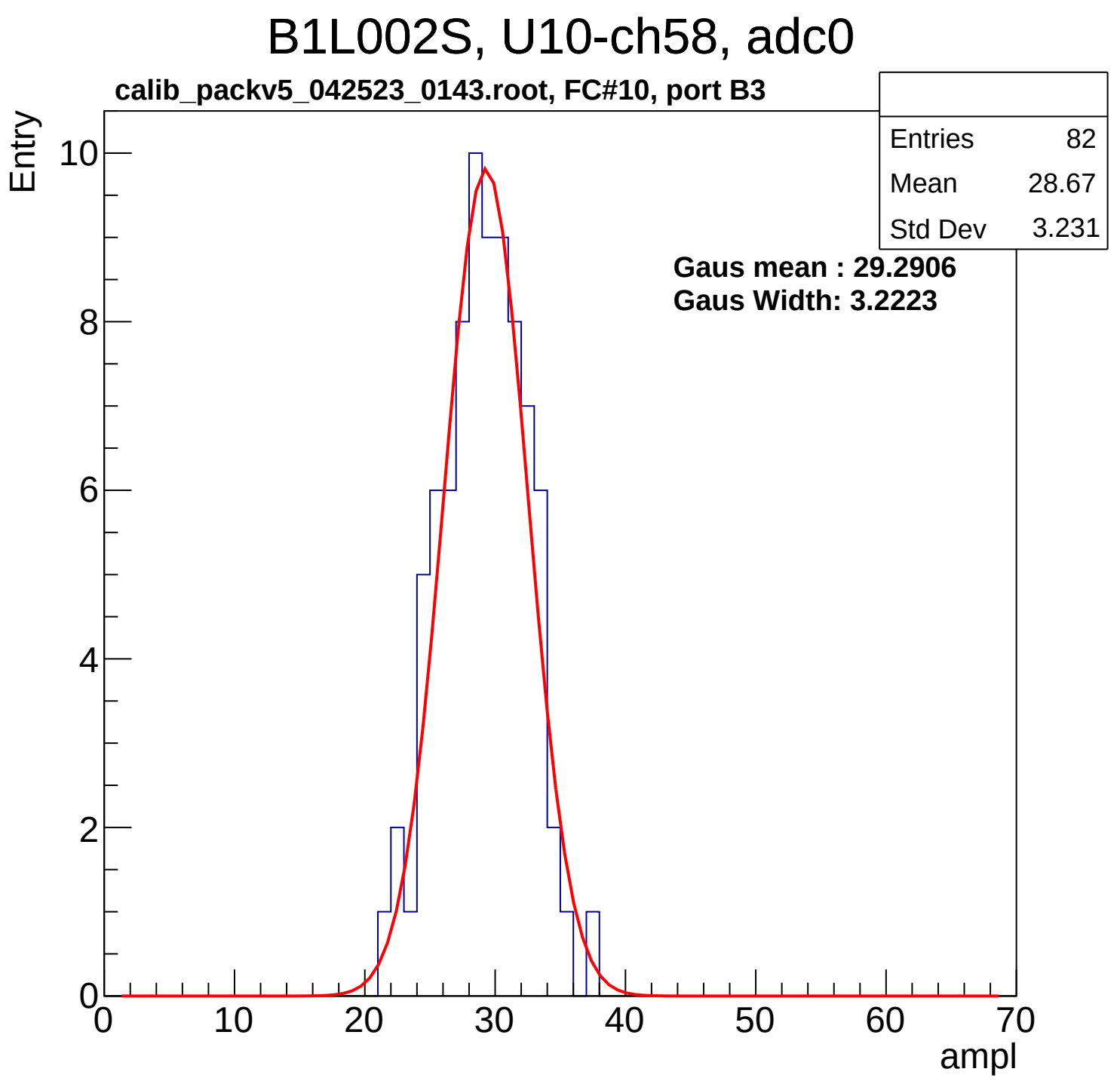
70
ampl


B1L002S, U10-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.67
Std Dev 3.231
Gaus mean : 29.2906
Gaus Width: 3.2223
Entry
ampl
0
2
4
6
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40
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60
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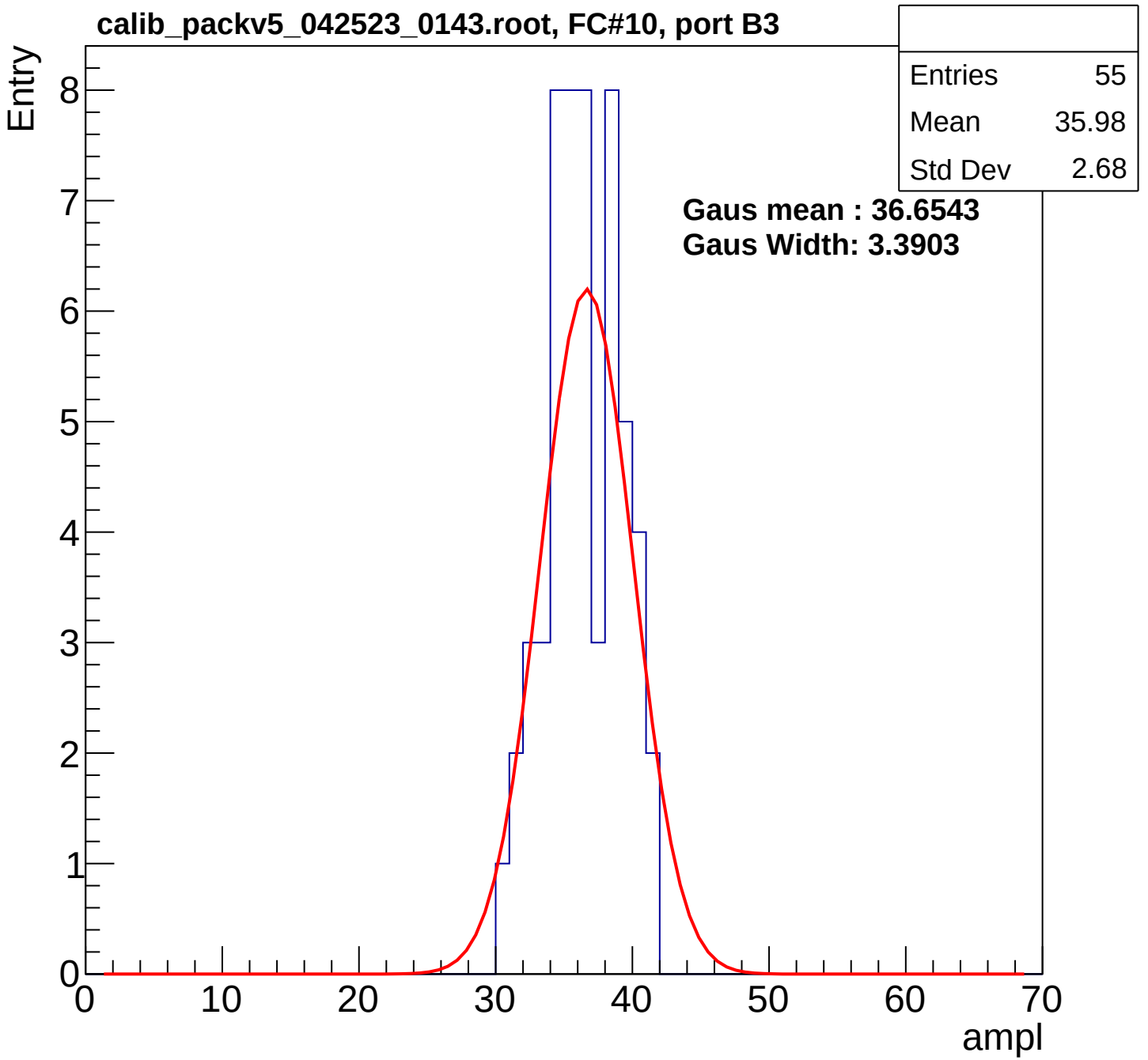
B1L002S, U10-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.98
Std Dev 2.68
Gaus mean : 36.6543
Gaus Width: 3.3903
Entry
ampl
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1
2
3
4
5
6
7
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0
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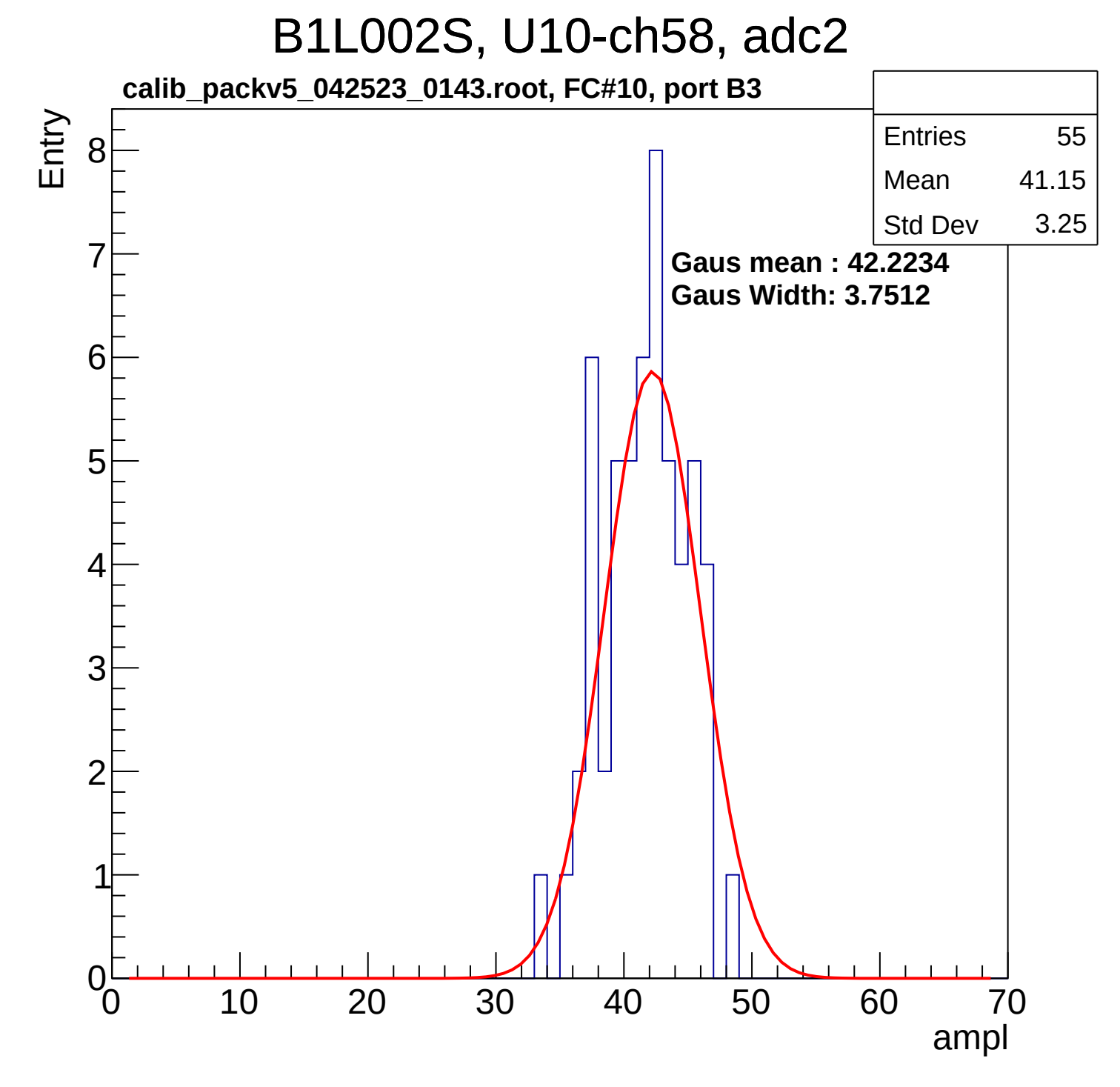
B1L002S, U10-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.15
Std Dev 3.25
Gaus mean : 42.2234
Gaus Width: 3.7512
Entry
ampl
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1
2
3
4
5
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0
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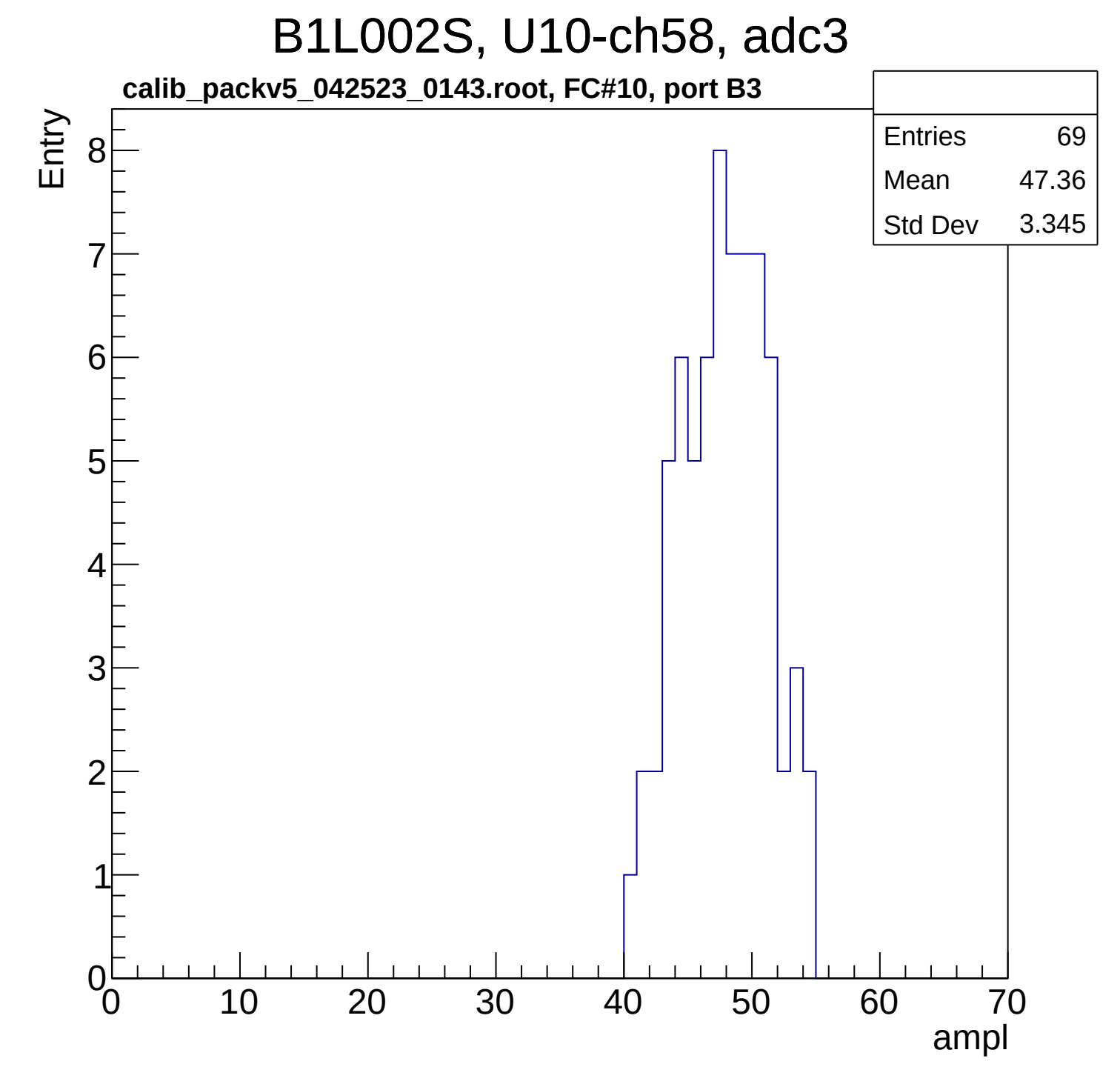
B1L002S, U10-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.36
Std Dev 3.345
Entry
ampl
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1
2
3
4
5
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60
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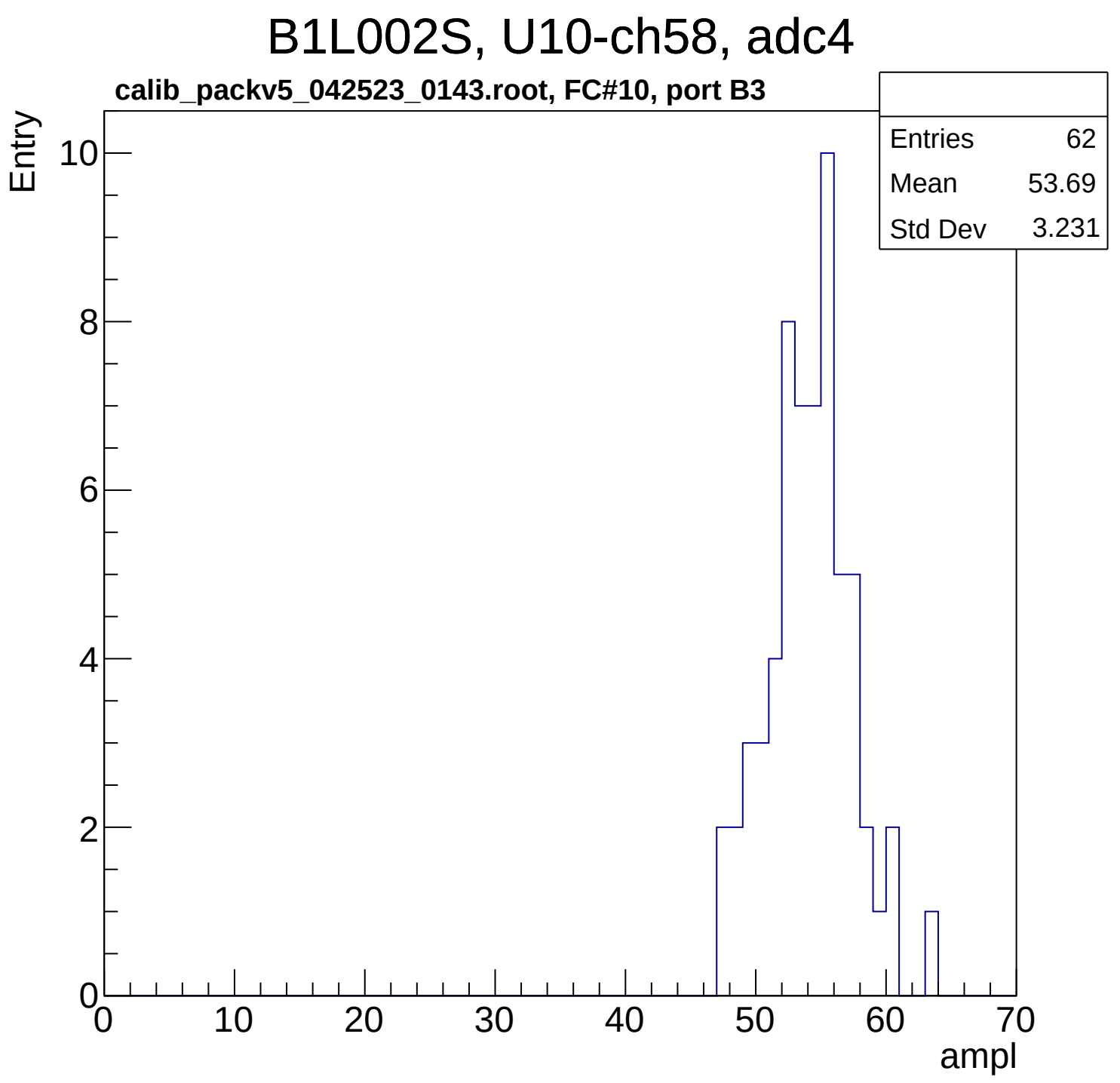
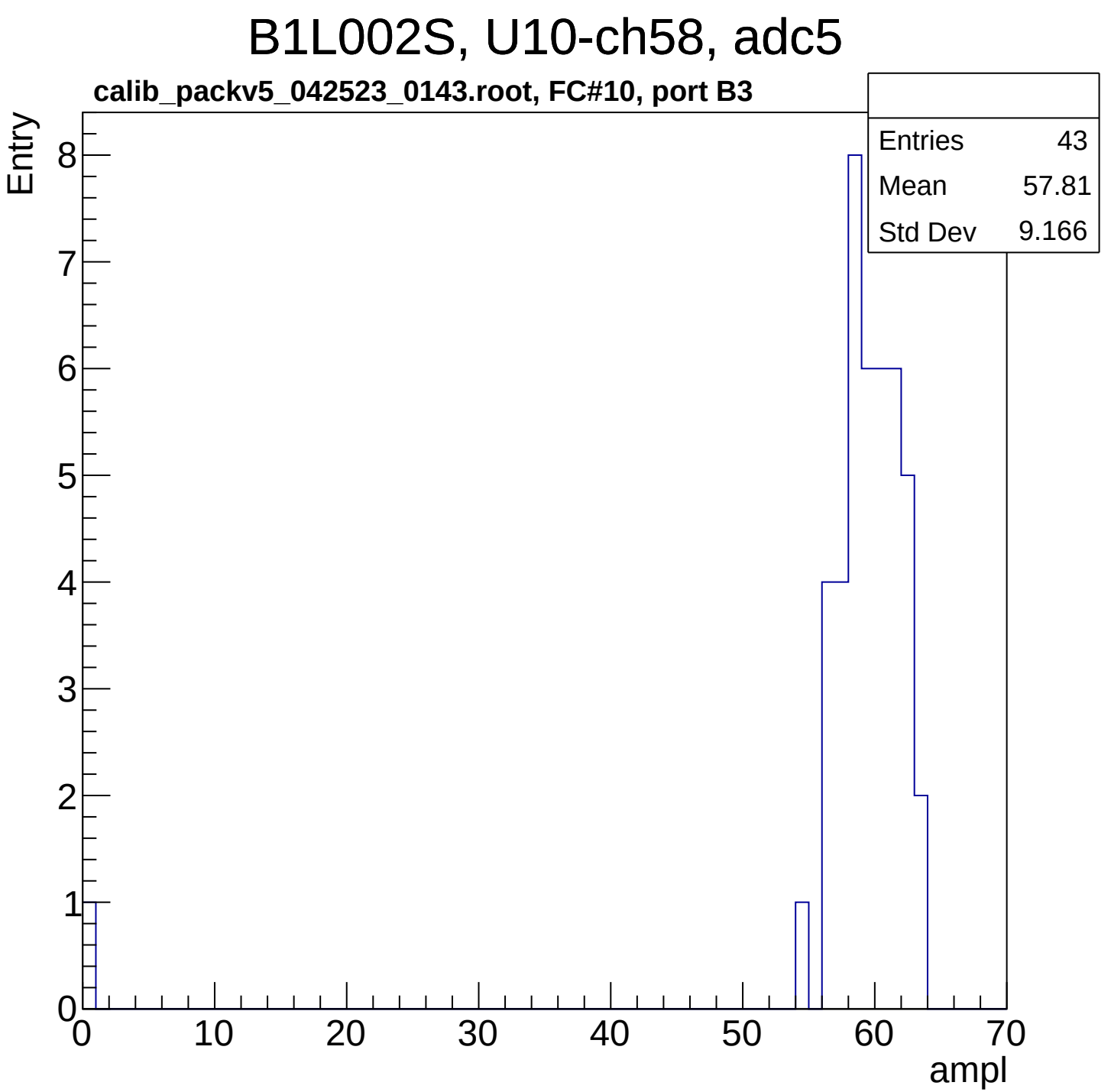

B1L002S, U10-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.69
Std Dev 3.231
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 57.81
Std Dev 9.166
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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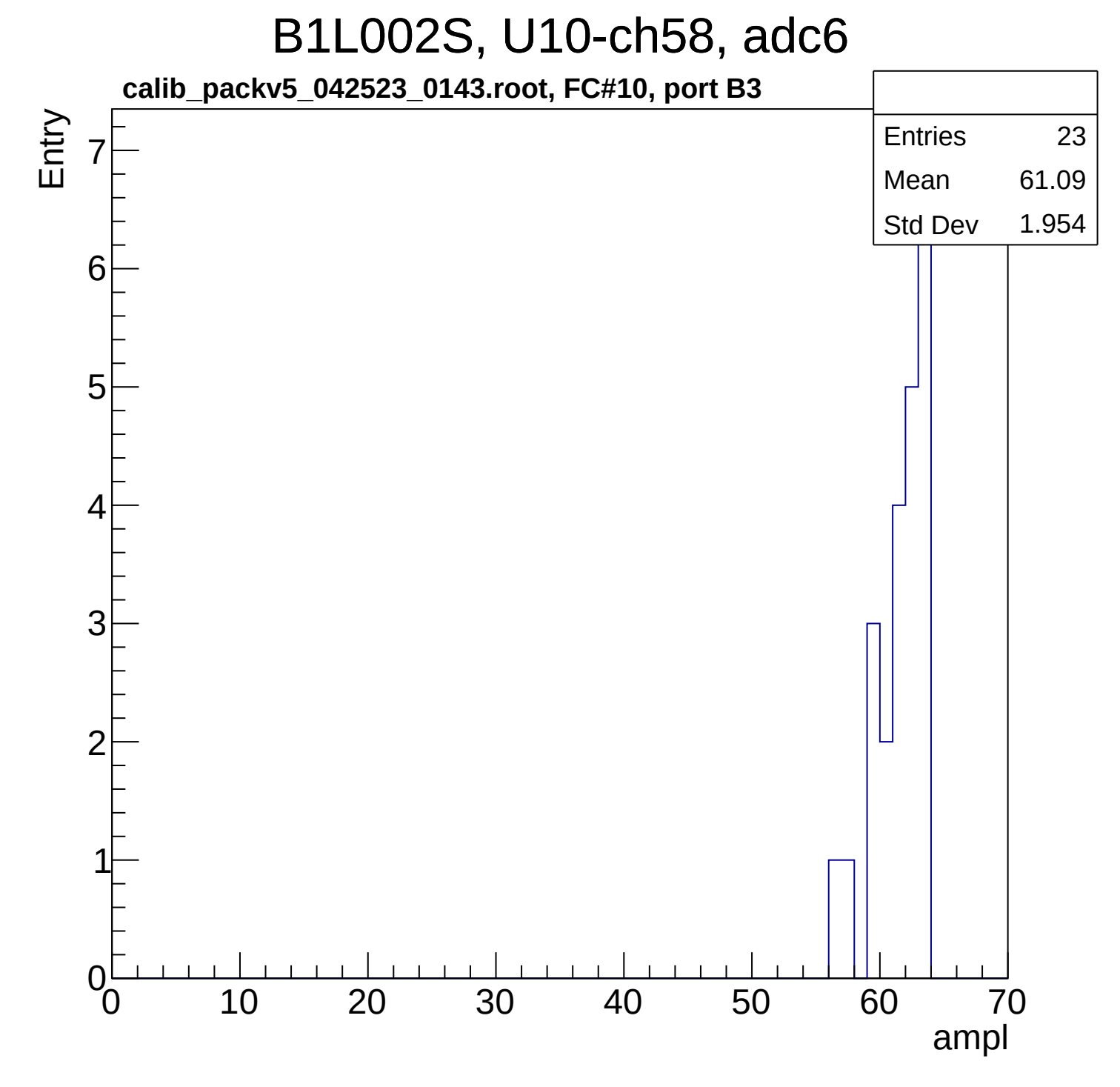
B1L002S, U10-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.09
Std Dev 1.954
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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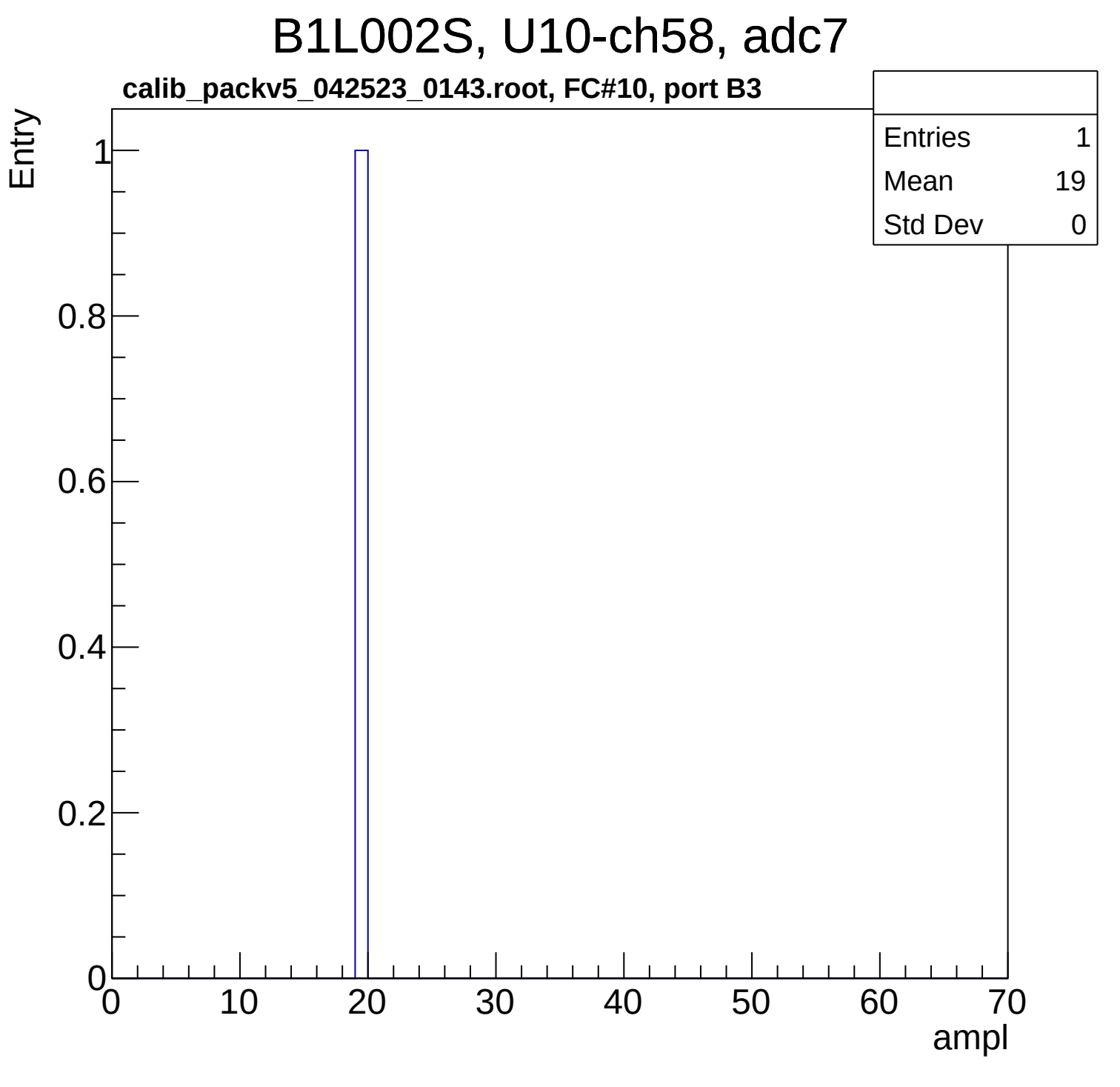
B1L002S, U10-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
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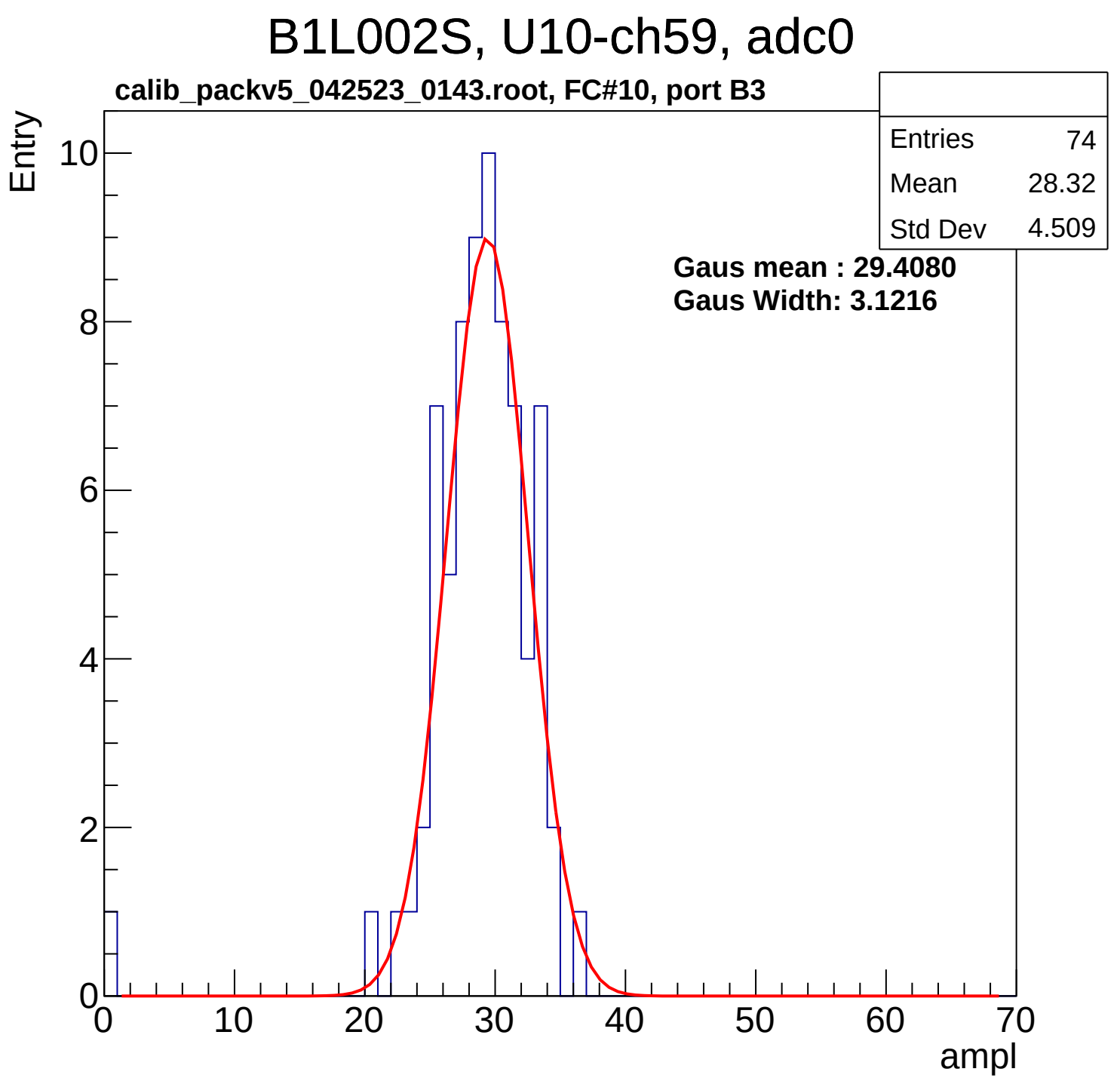
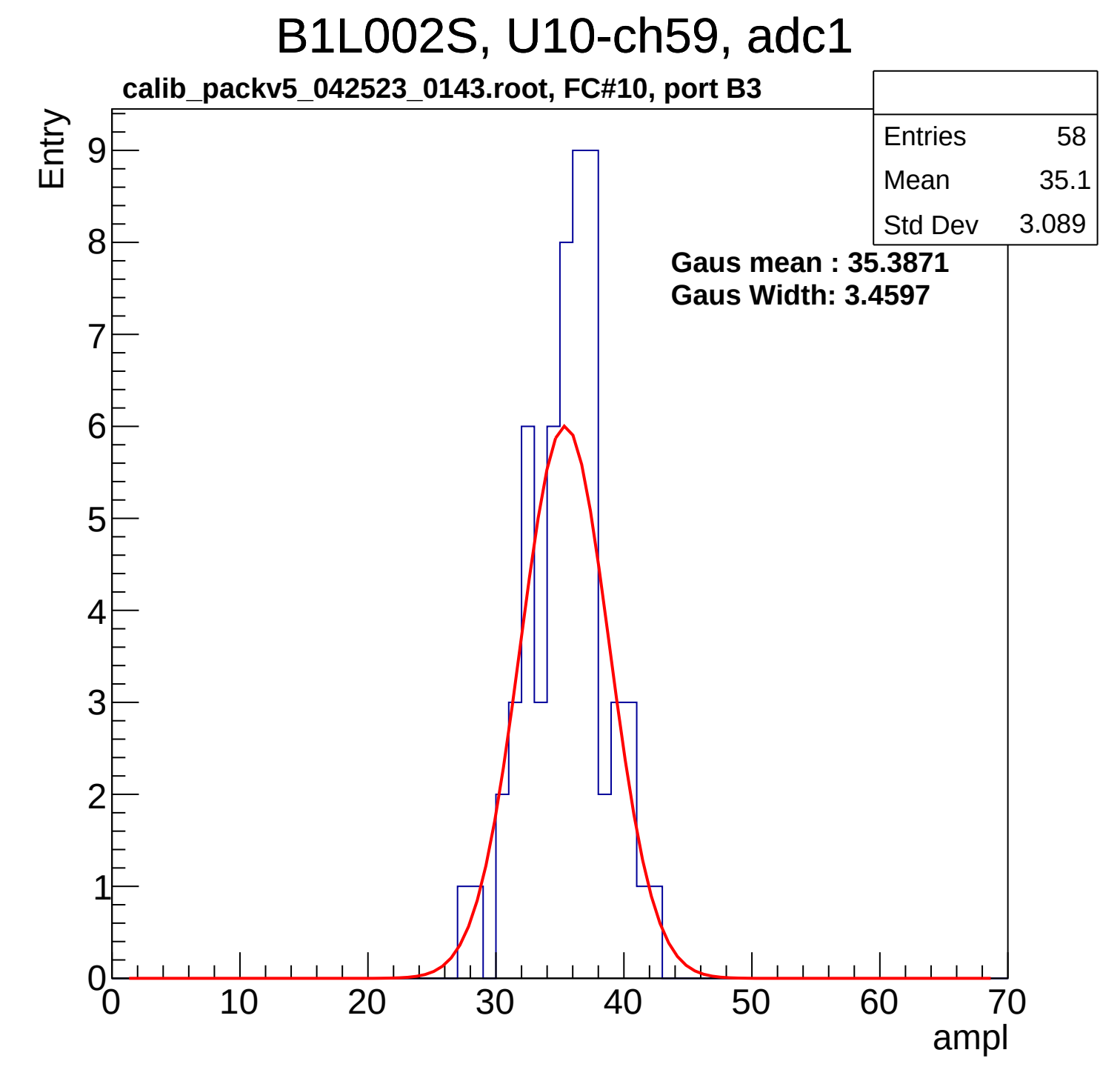
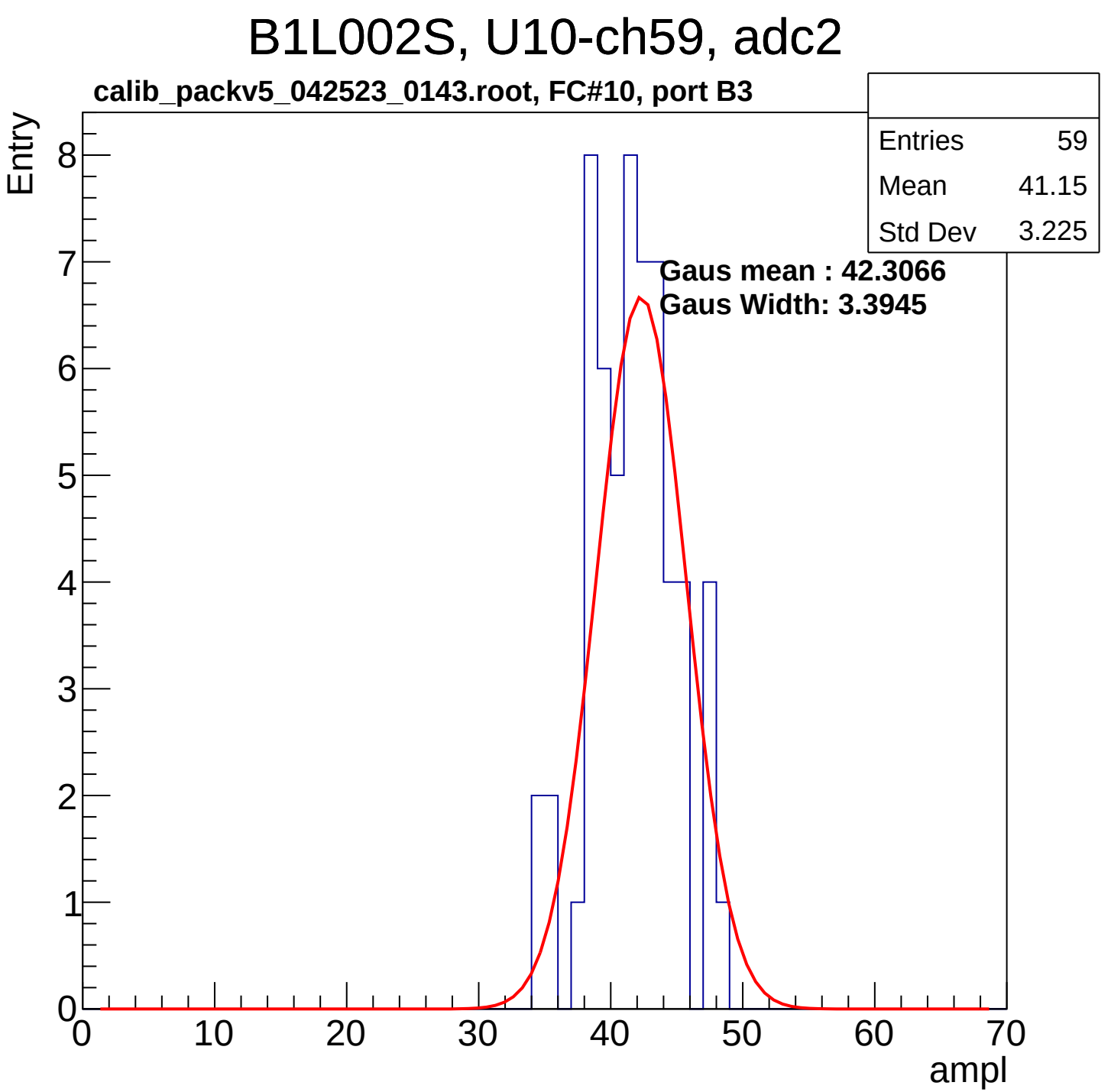
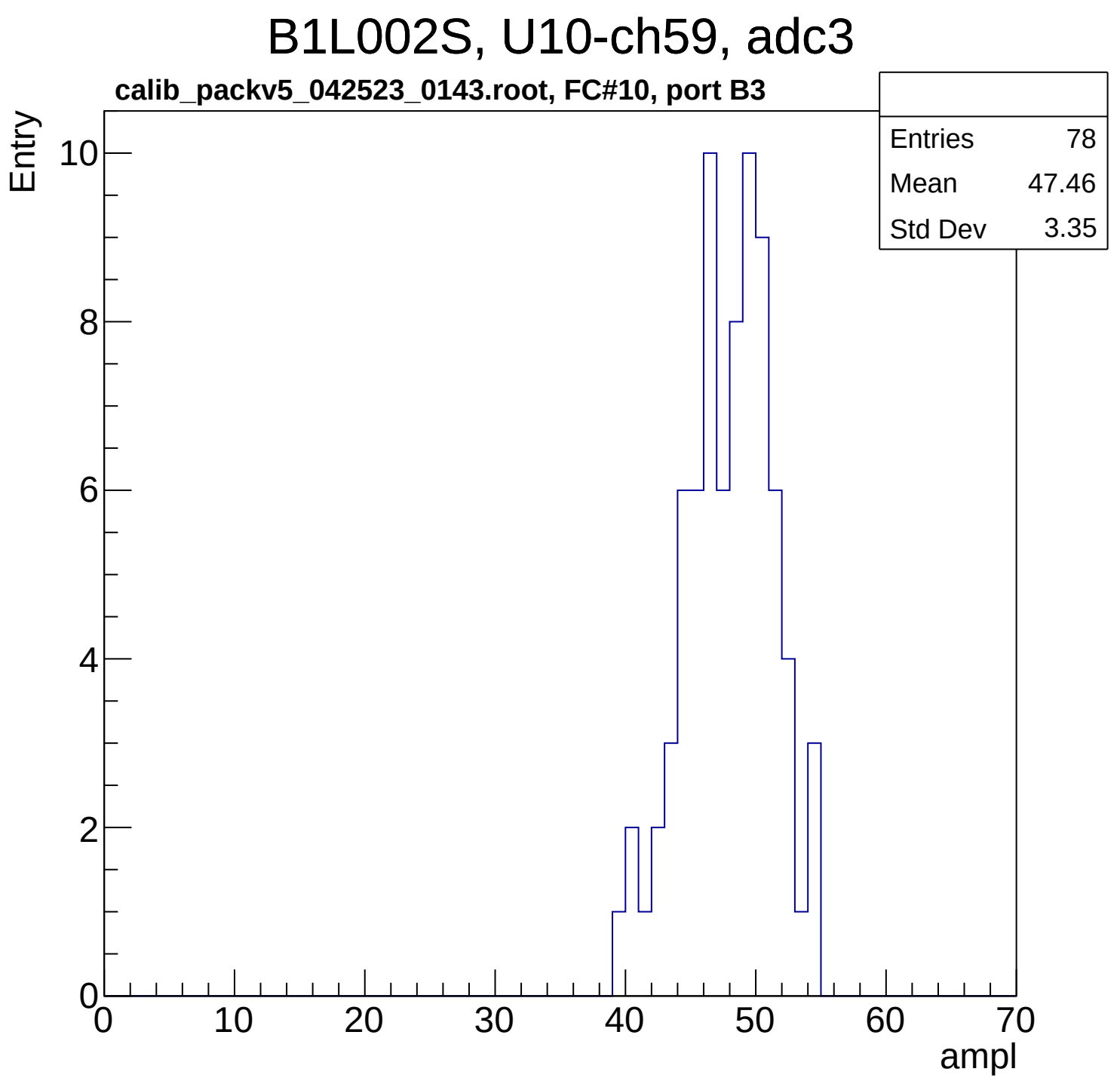
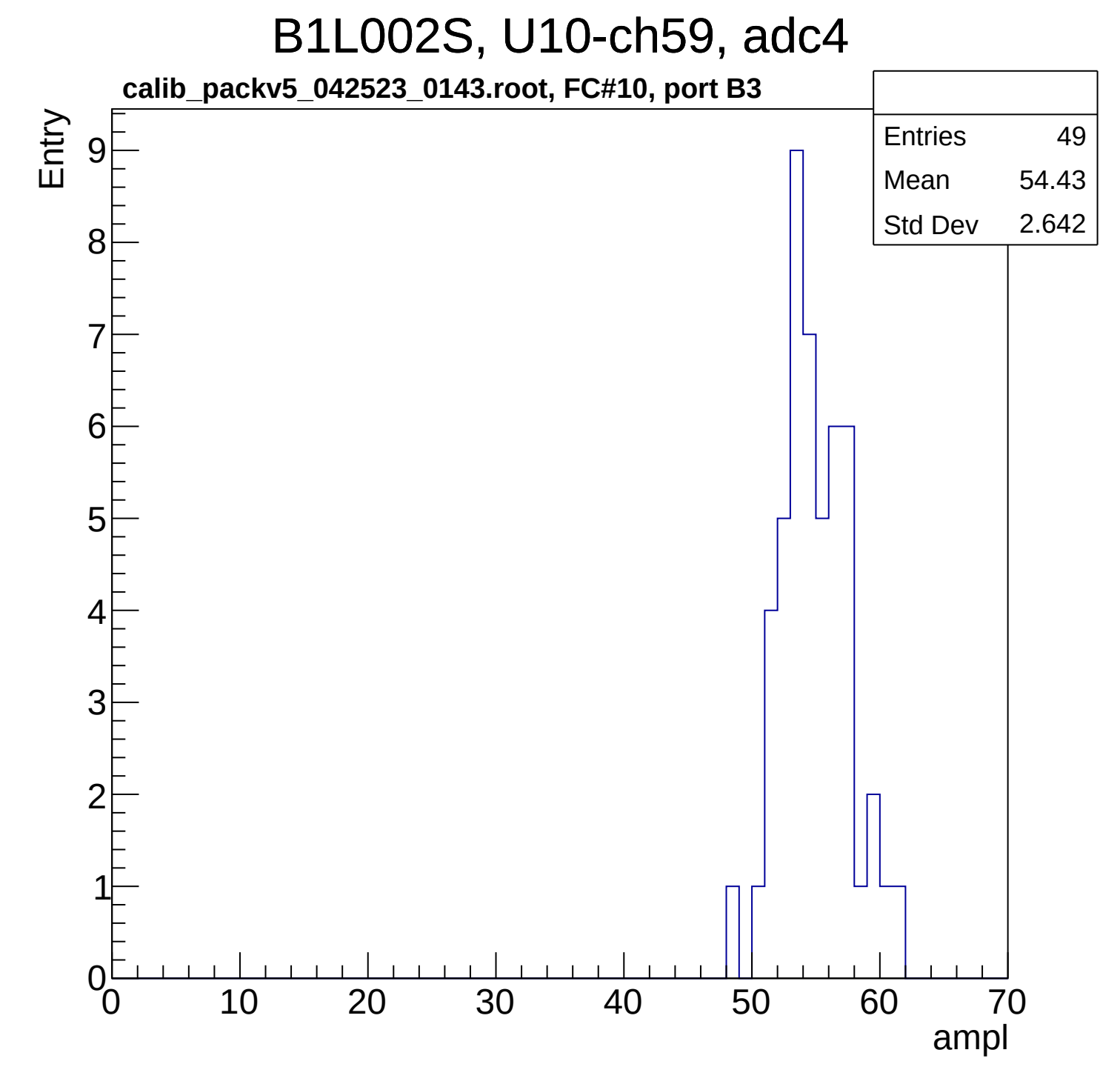
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.32
Std Dev 4.509
Gaus mean : 29.4080
Gaus Width: 3.1216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.1
Std Dev 3.089
Gaus mean : 35.3871
Gaus Width: 3.4597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.15
Std Dev 3.225
Gaus mean : 42.3066
Gaus Width: 3.3945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 47.46
Std Dev 3.35
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 54.43
Std Dev 2.642
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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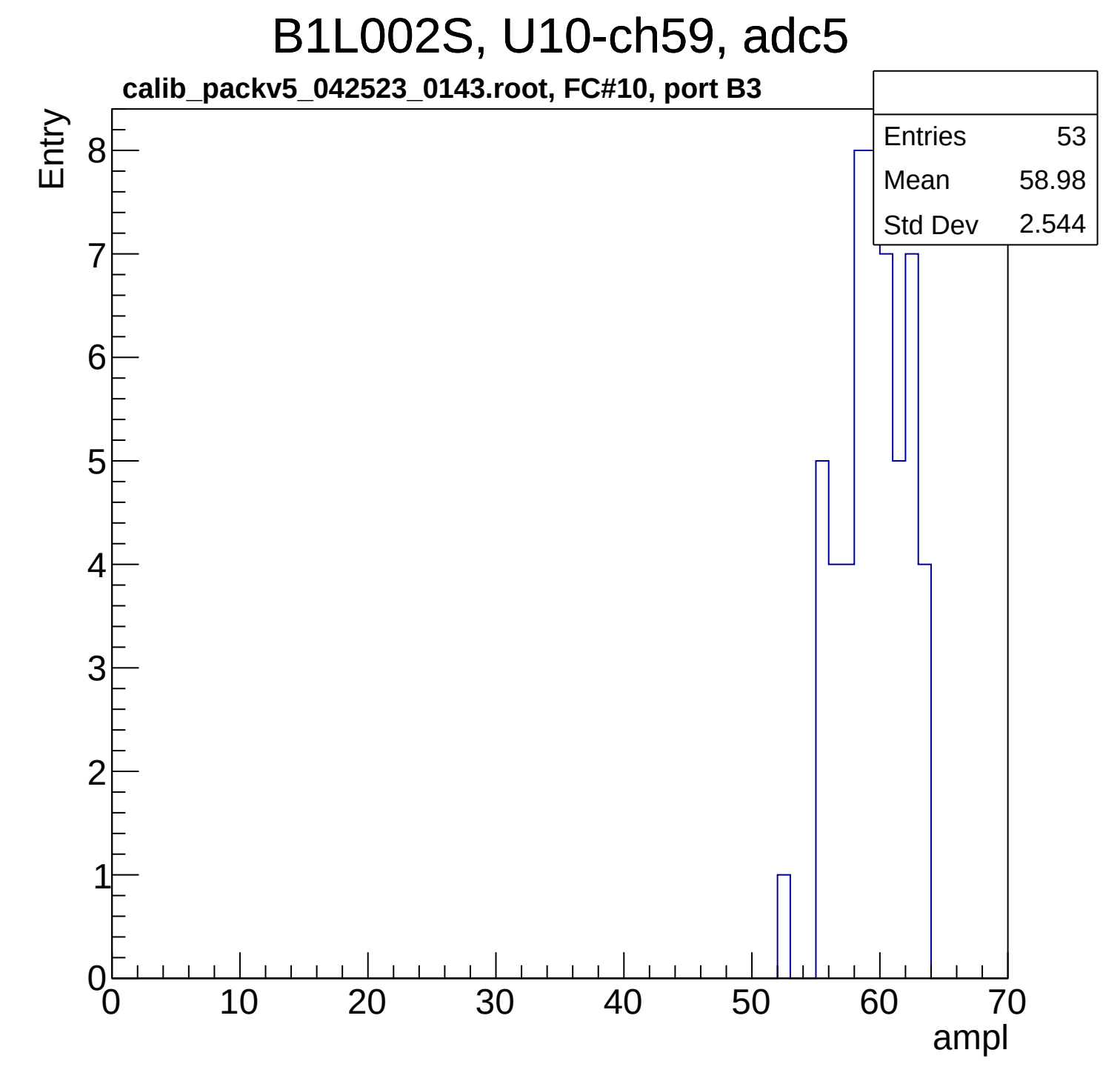
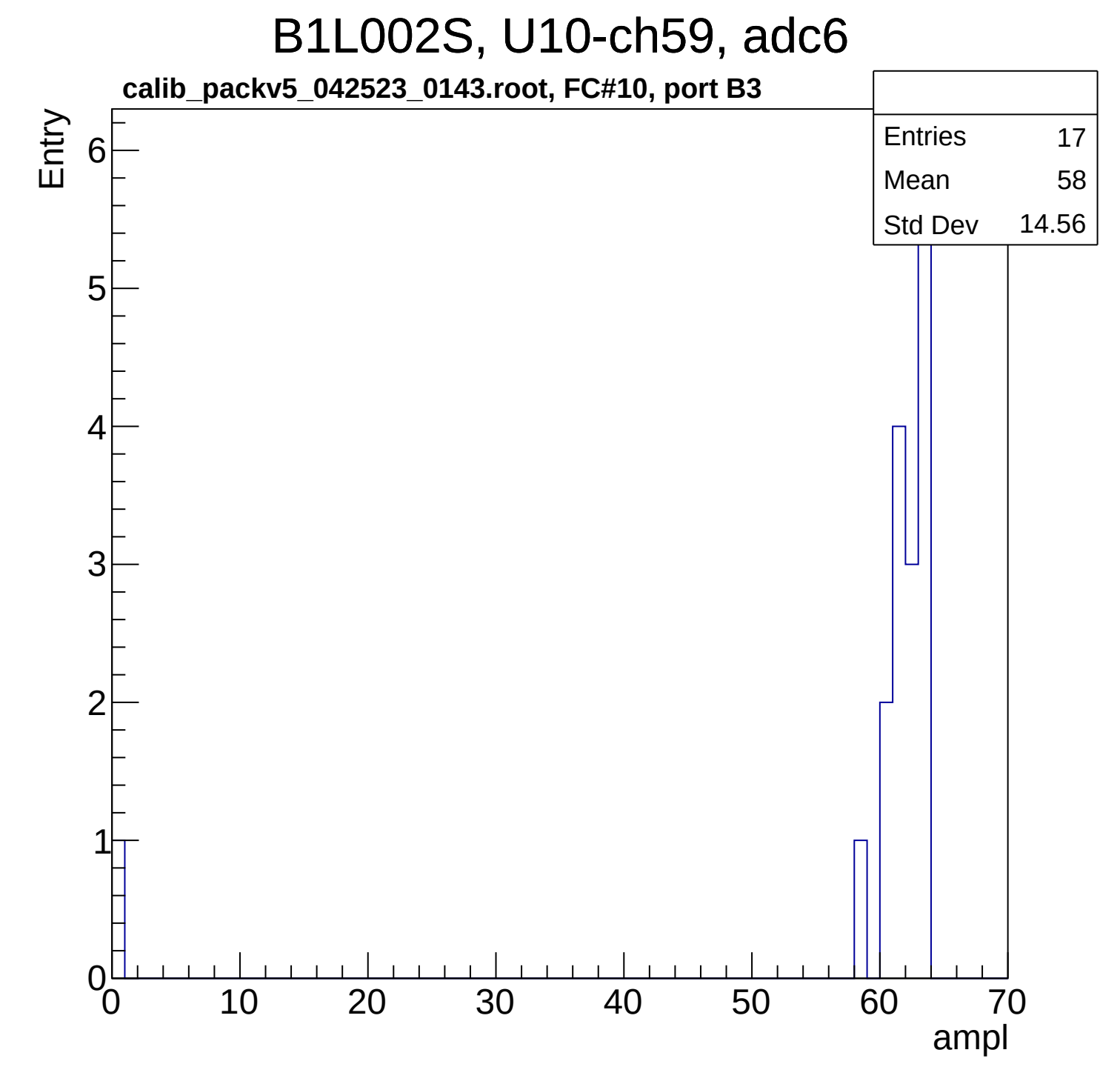
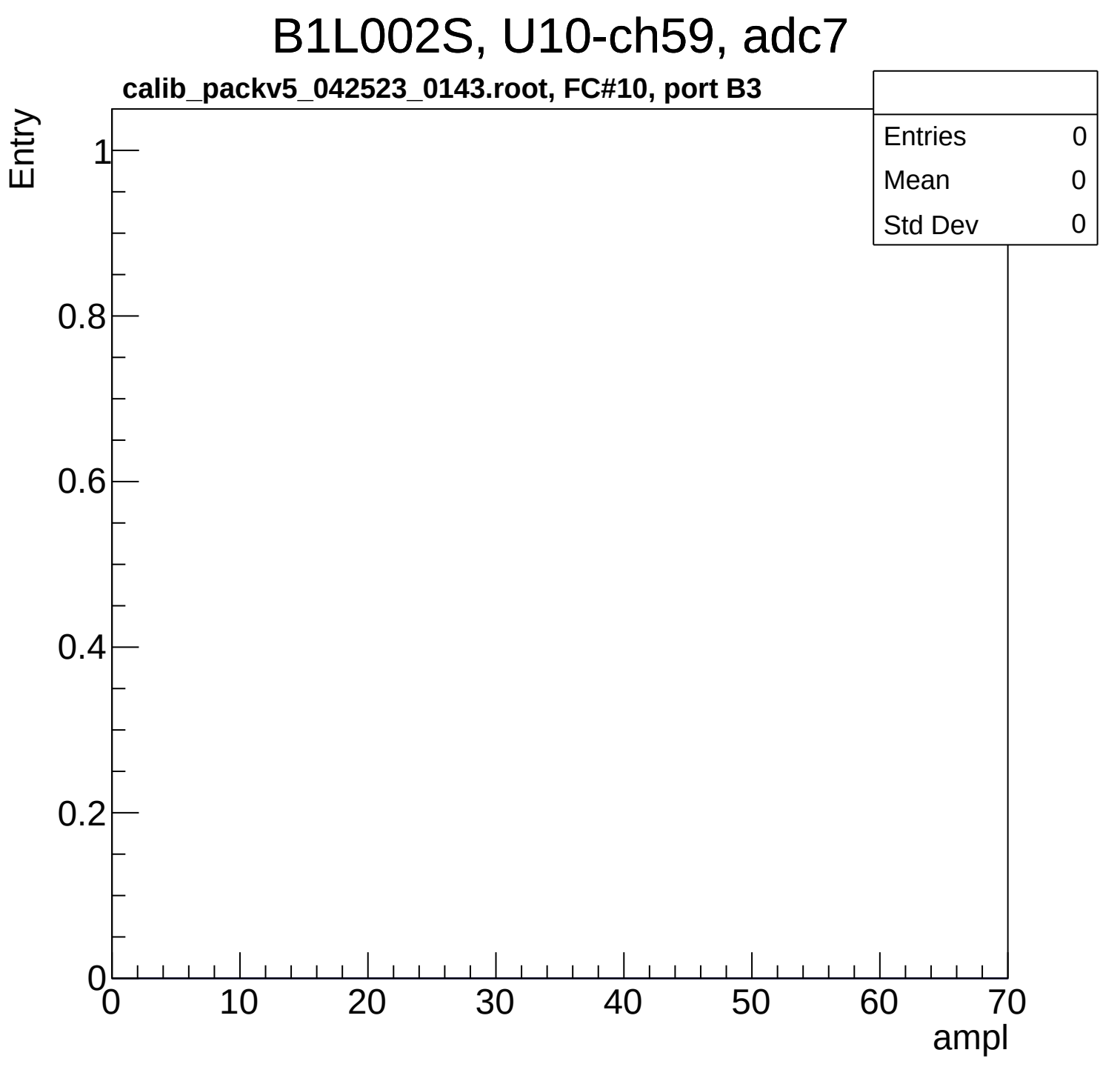
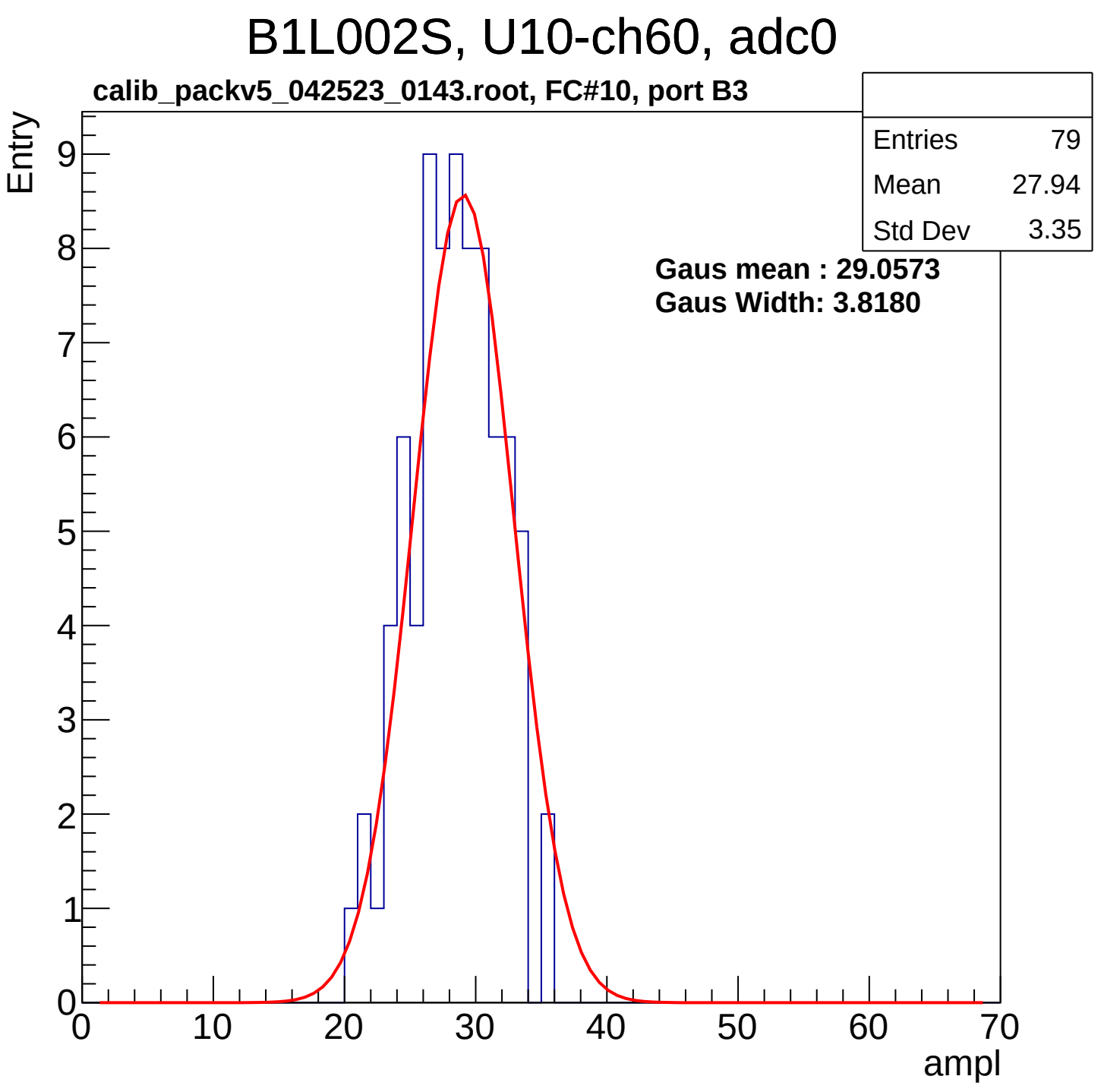
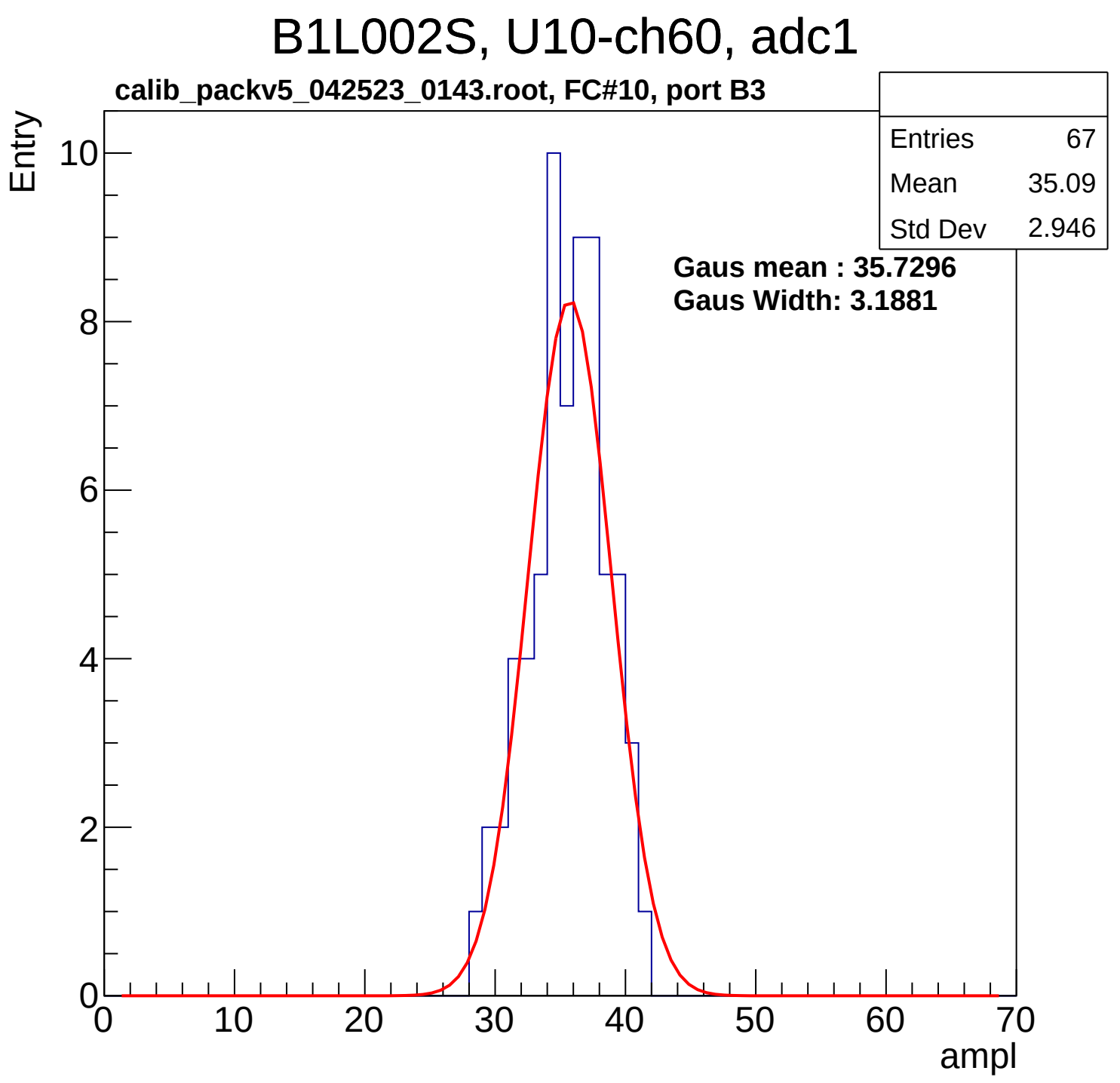
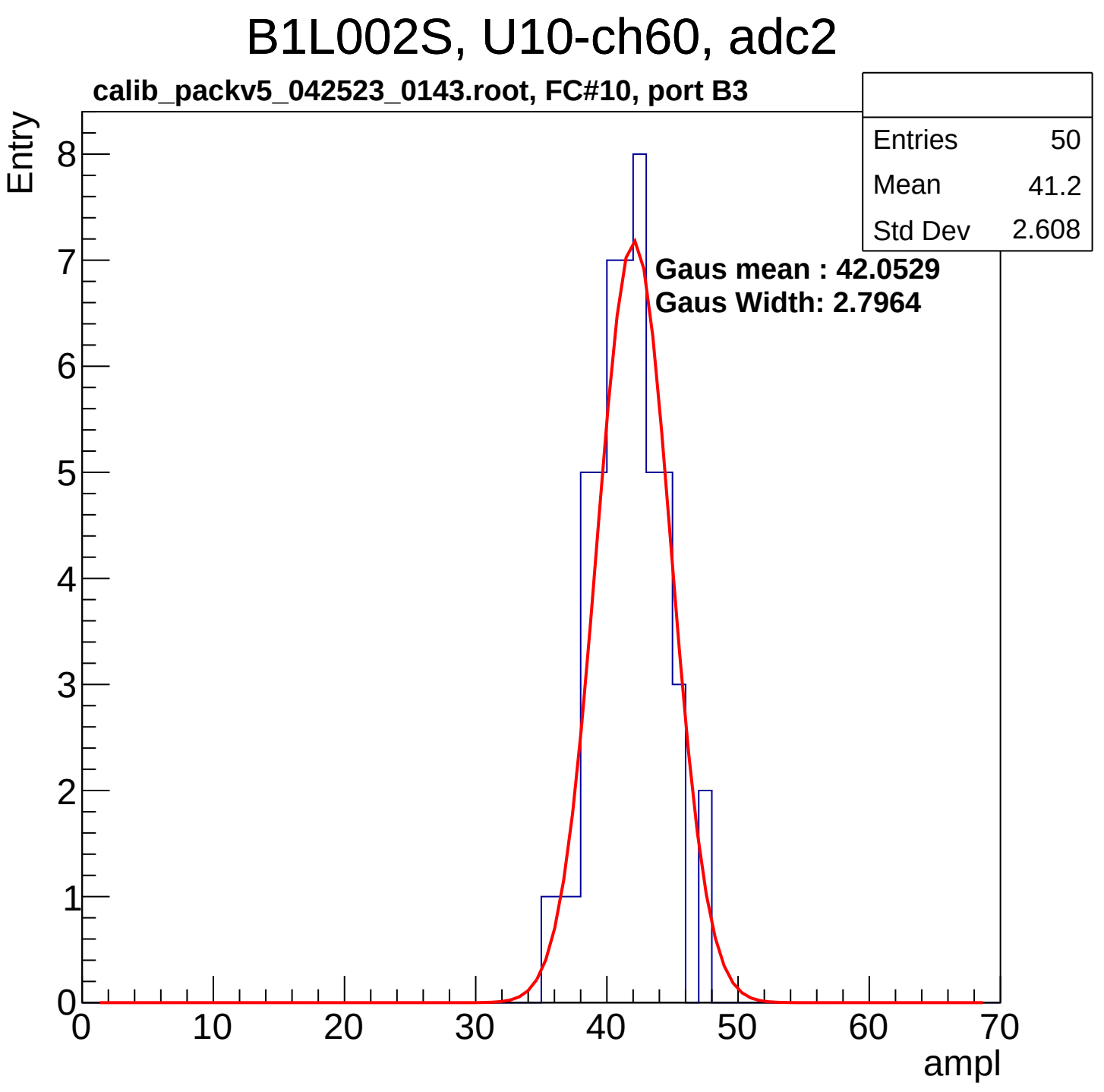

B1L002S, U10-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.98
Std Dev 2.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58
Std Dev 14.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.94
Std Dev 3.35
Gaus mean : 29.0573
Gaus Width: 3.8180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.09
Std Dev 2.946
Gaus mean : 35.7296
Gaus Width: 3.1881
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.2
Std Dev 2.608
Gaus mean : 42.0529
Gaus Width: 2.7964
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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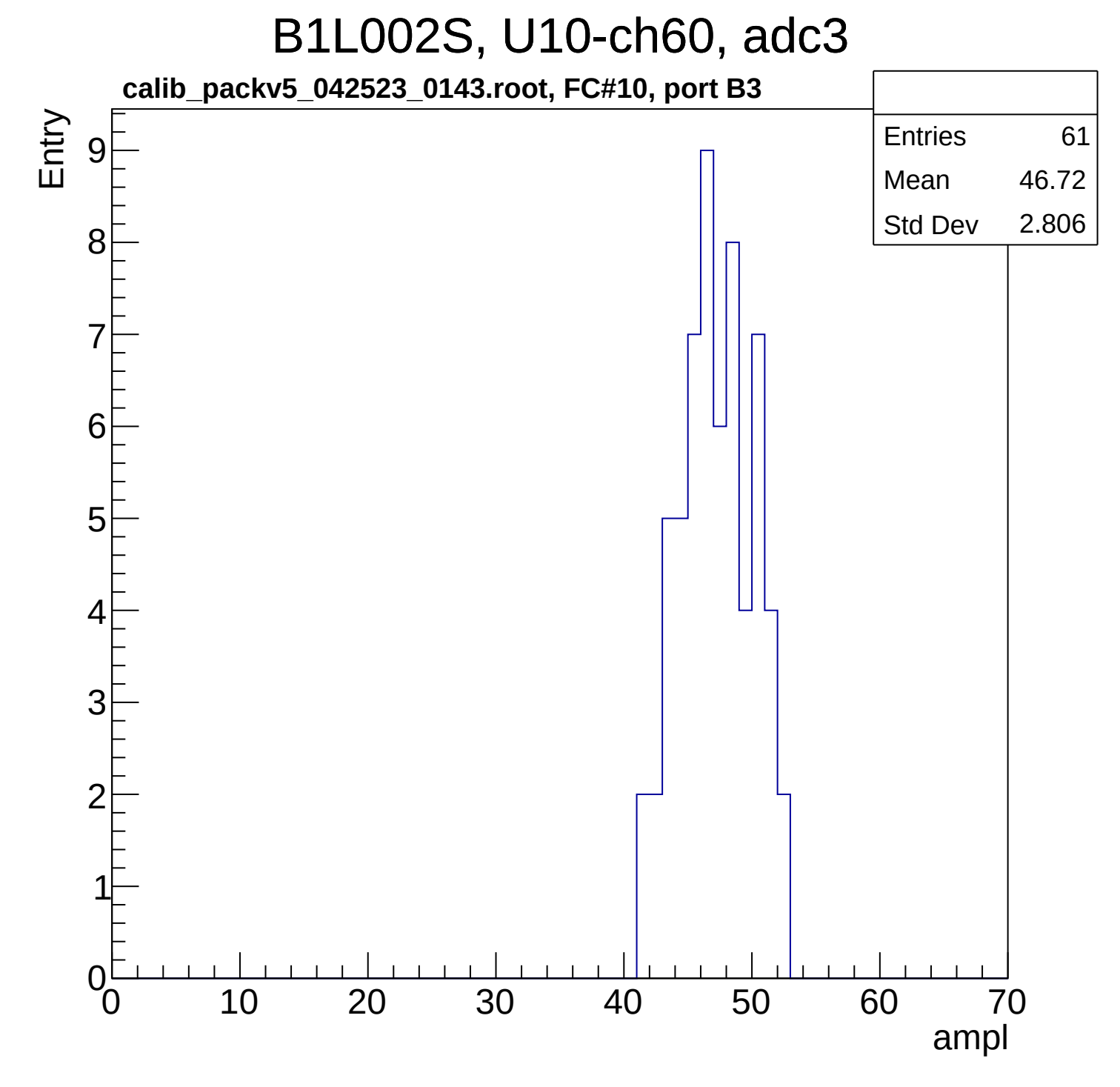
40
50
60
70


B1L002S, U10-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.72
Std Dev 2.806
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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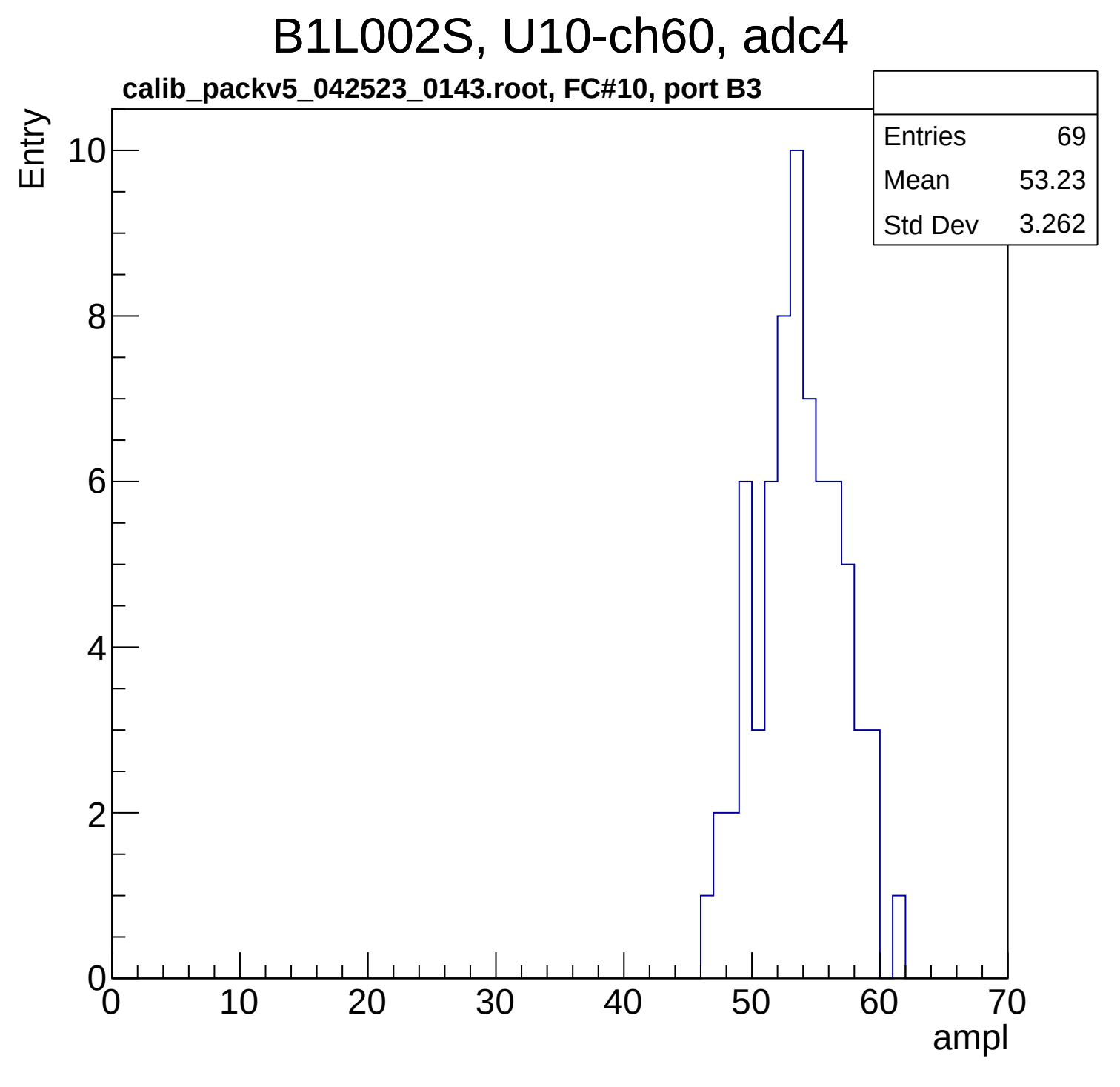
B1L002S, U10-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.23
Std Dev 3.262
Entry
ampl
0
2
4
6
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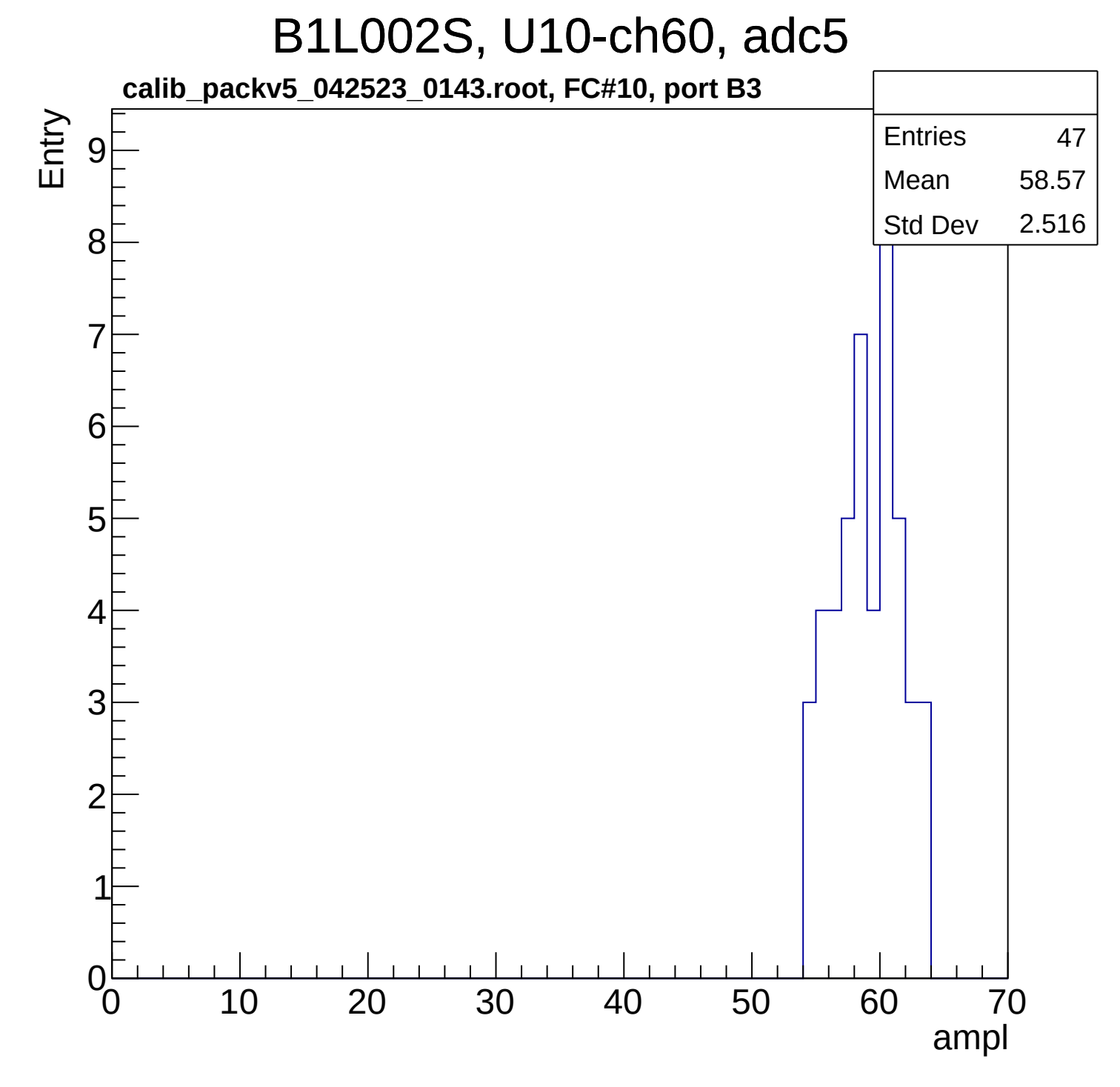
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.57
Std Dev 2.516
Entry
ampl
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2
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6
7
8
9
0
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20
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40
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60
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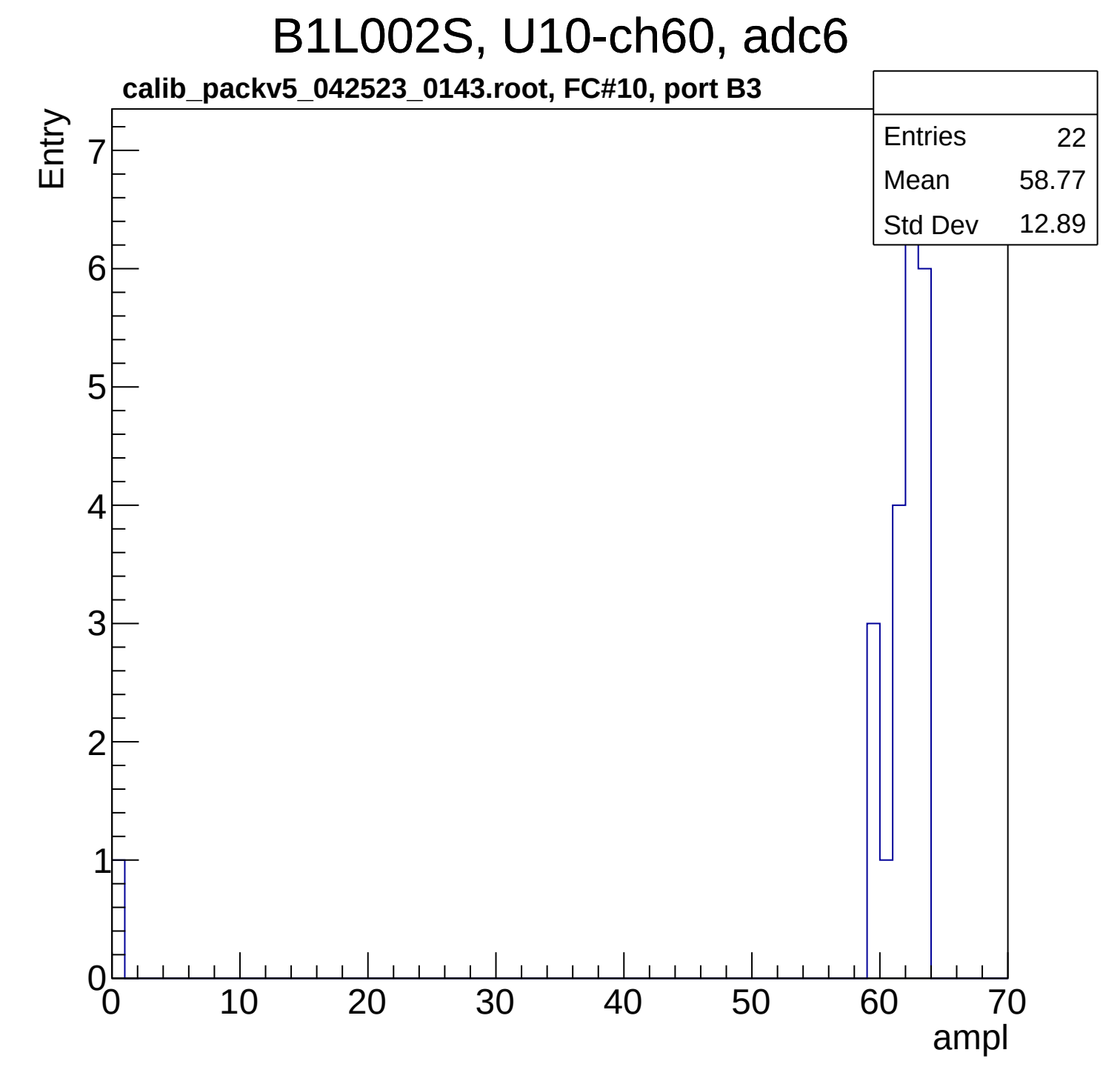
B1L002S, U10-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.77
Std Dev 12.89
Entry
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3
4
5
6
7
0
10
20
30
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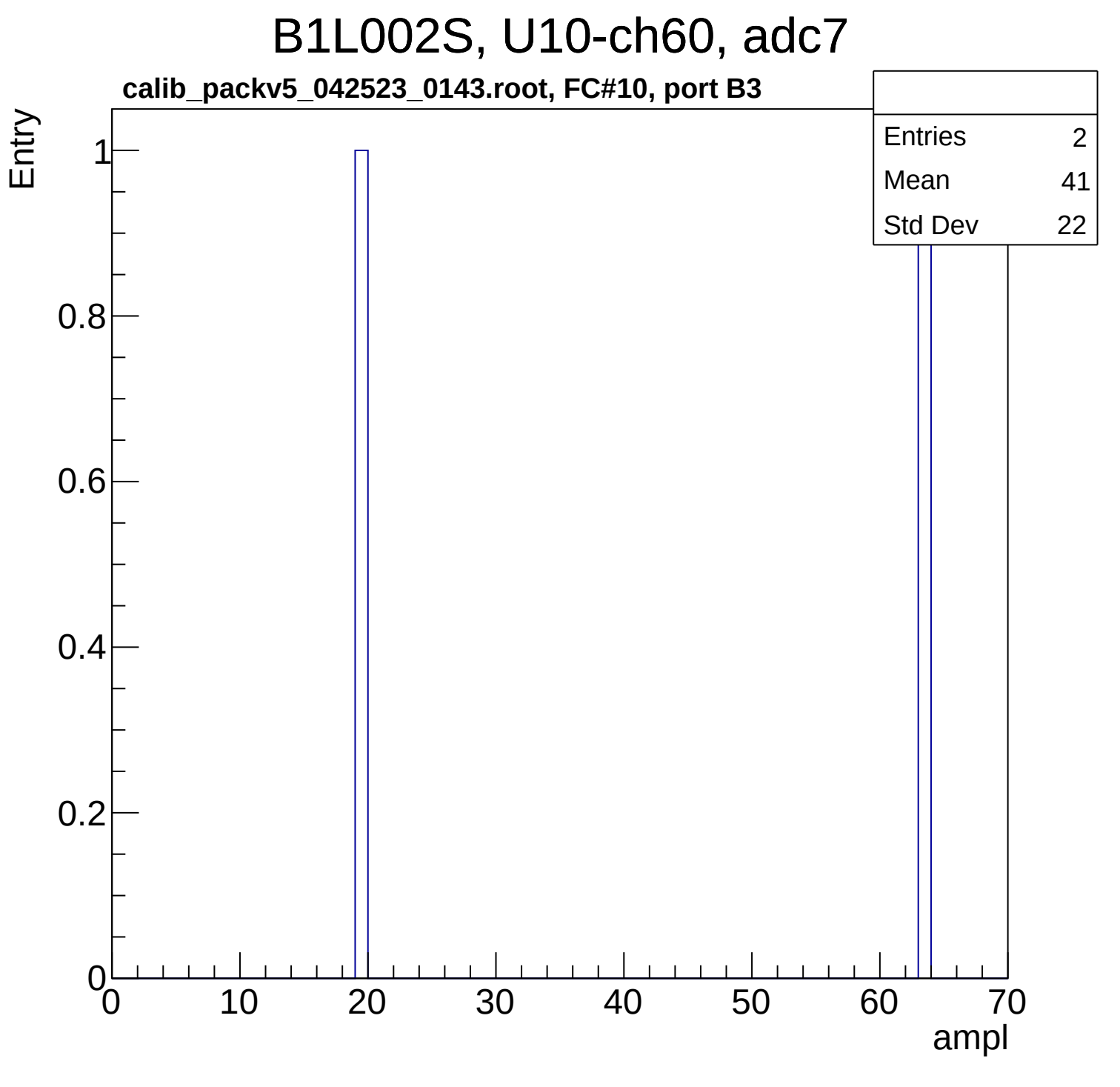
50
60
70
ampl


B1L002S, U10-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 22
Entry
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10
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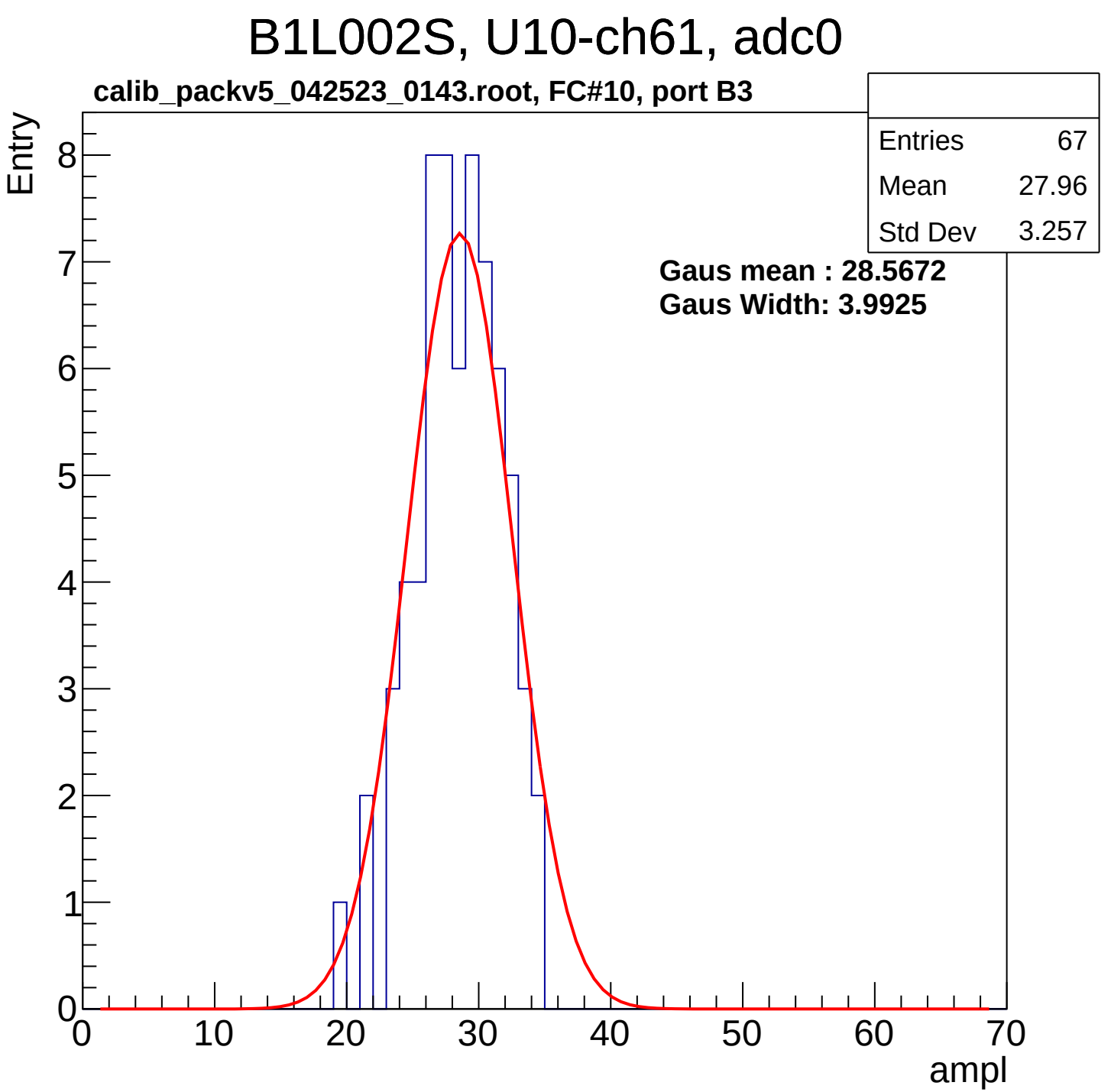
30
40
50
60
70
ampl


B1L002S, U10-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.96
Std Dev 3.257
Gaus mean : 28.5672
Gaus Width: 3.9925
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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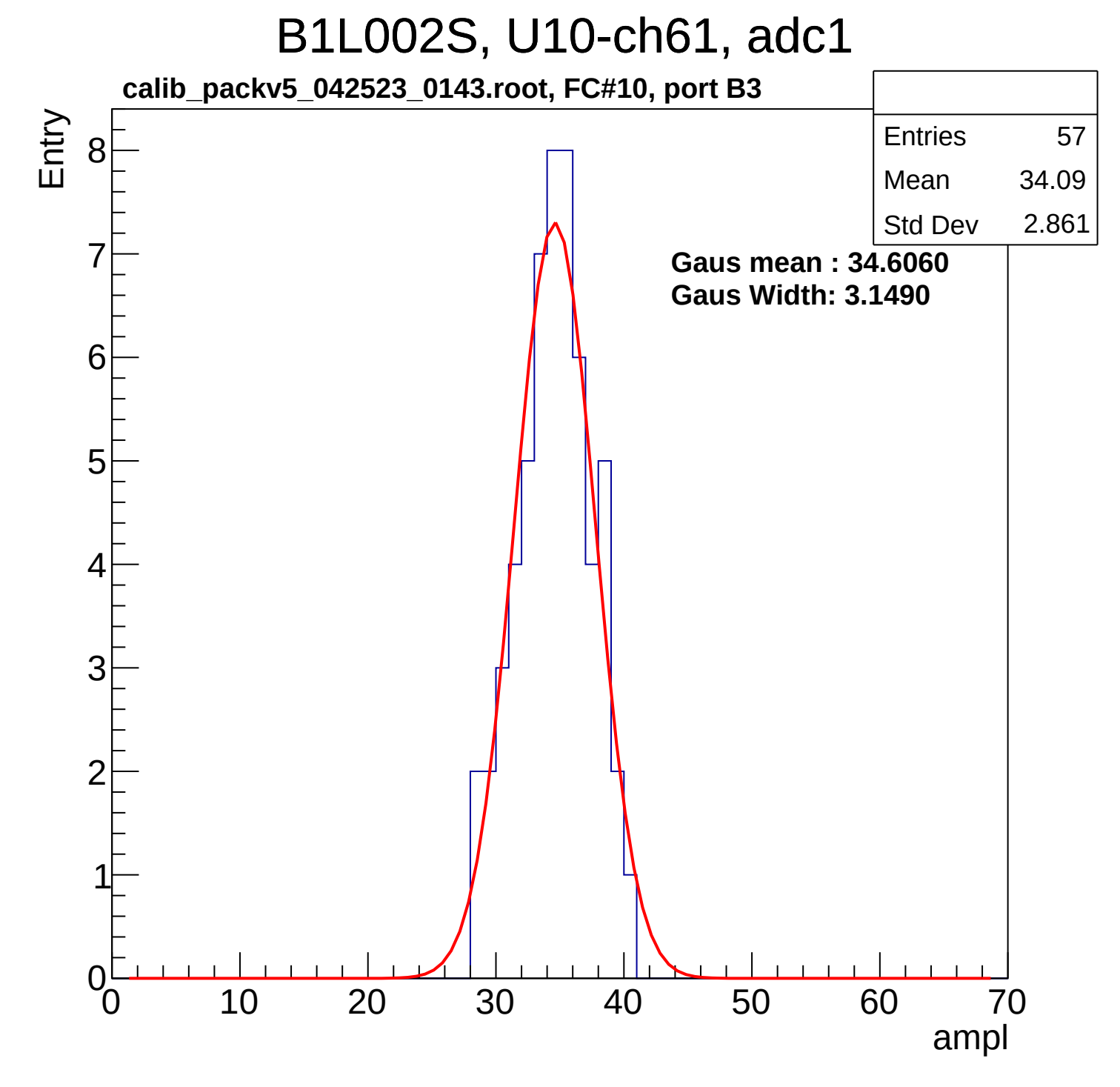
40
50
60
70


B1L002S, U10-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.09
Std Dev 2.861
Gaus mean : 34.6060
Gaus Width: 3.1490
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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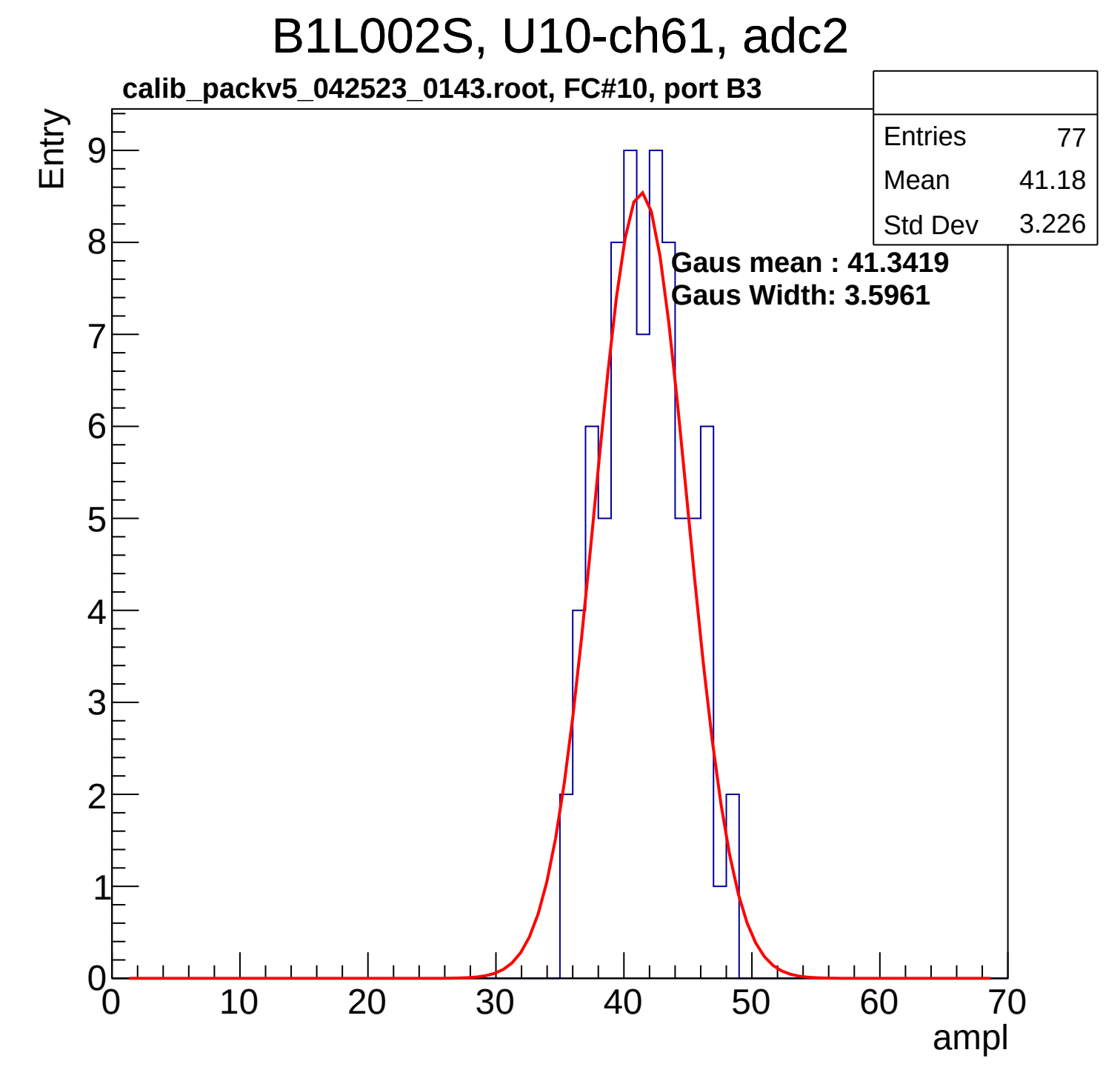
B1L002S, U10-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41.18
Std Dev 3.226
Gaus mean : 41.3419
Gaus Width: 3.5961
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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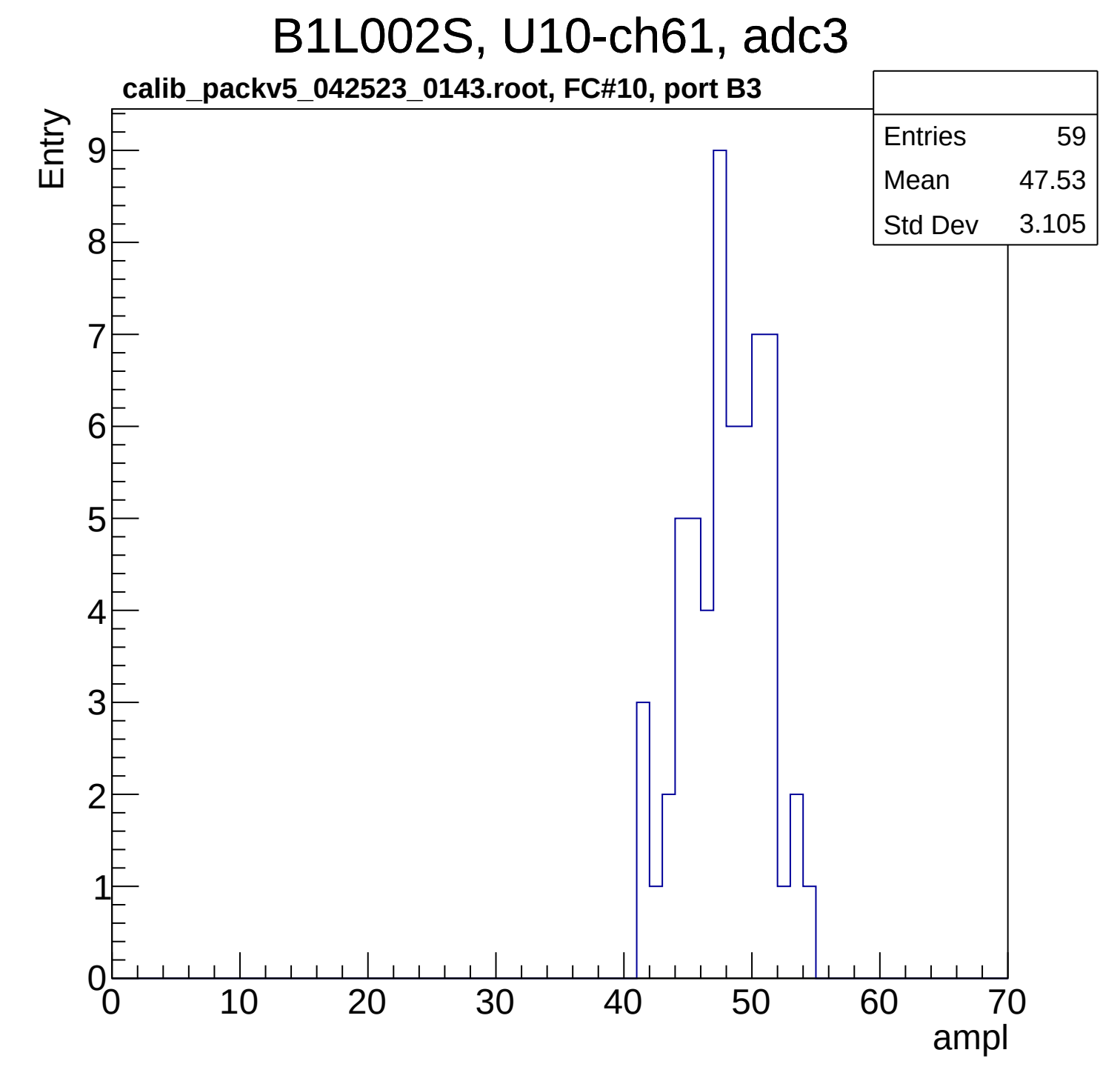
B1L002S, U10-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.53
Std Dev 3.105
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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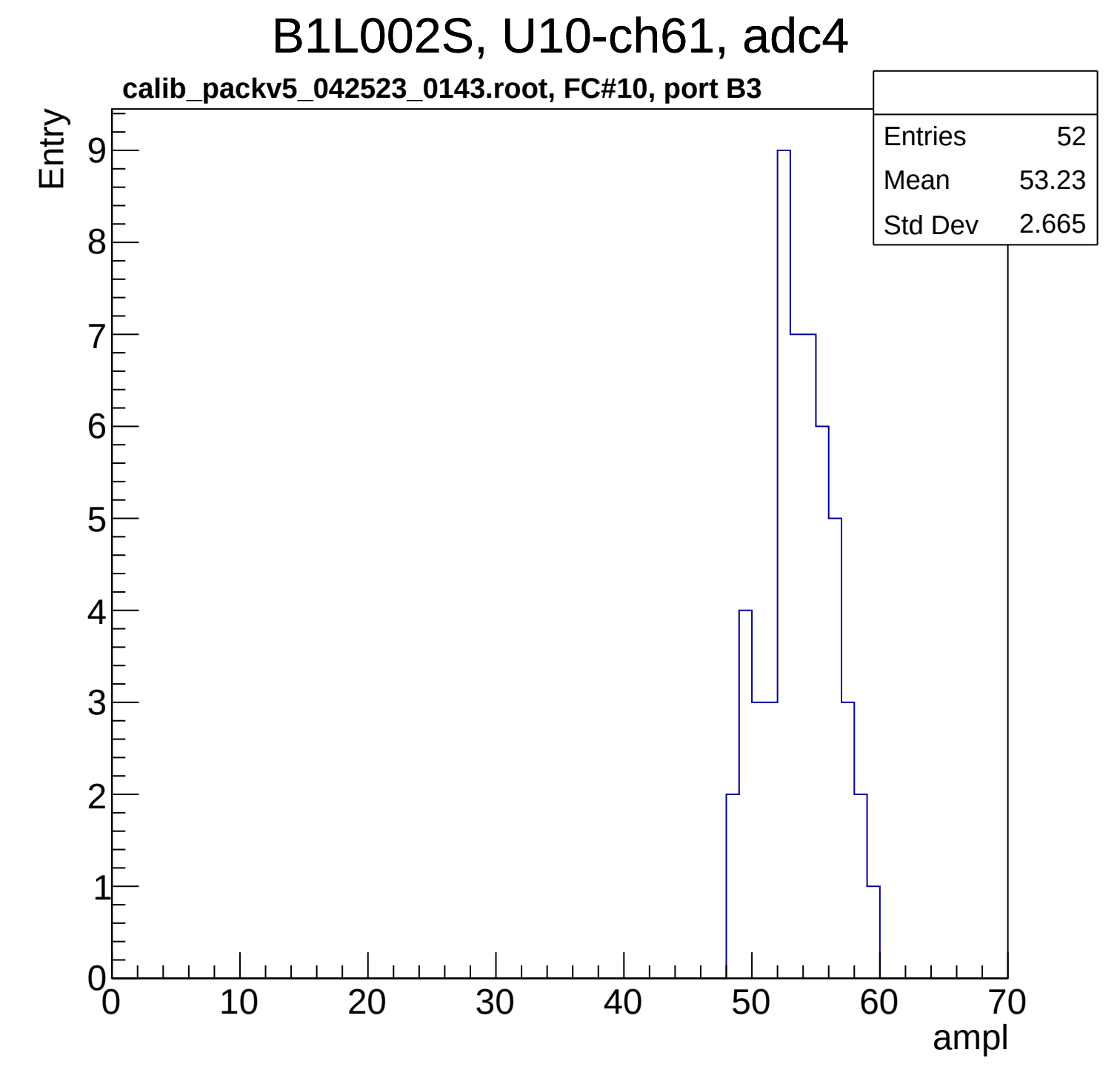
40
50
60
70


B1L002S, U10-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.23
Std Dev 2.665
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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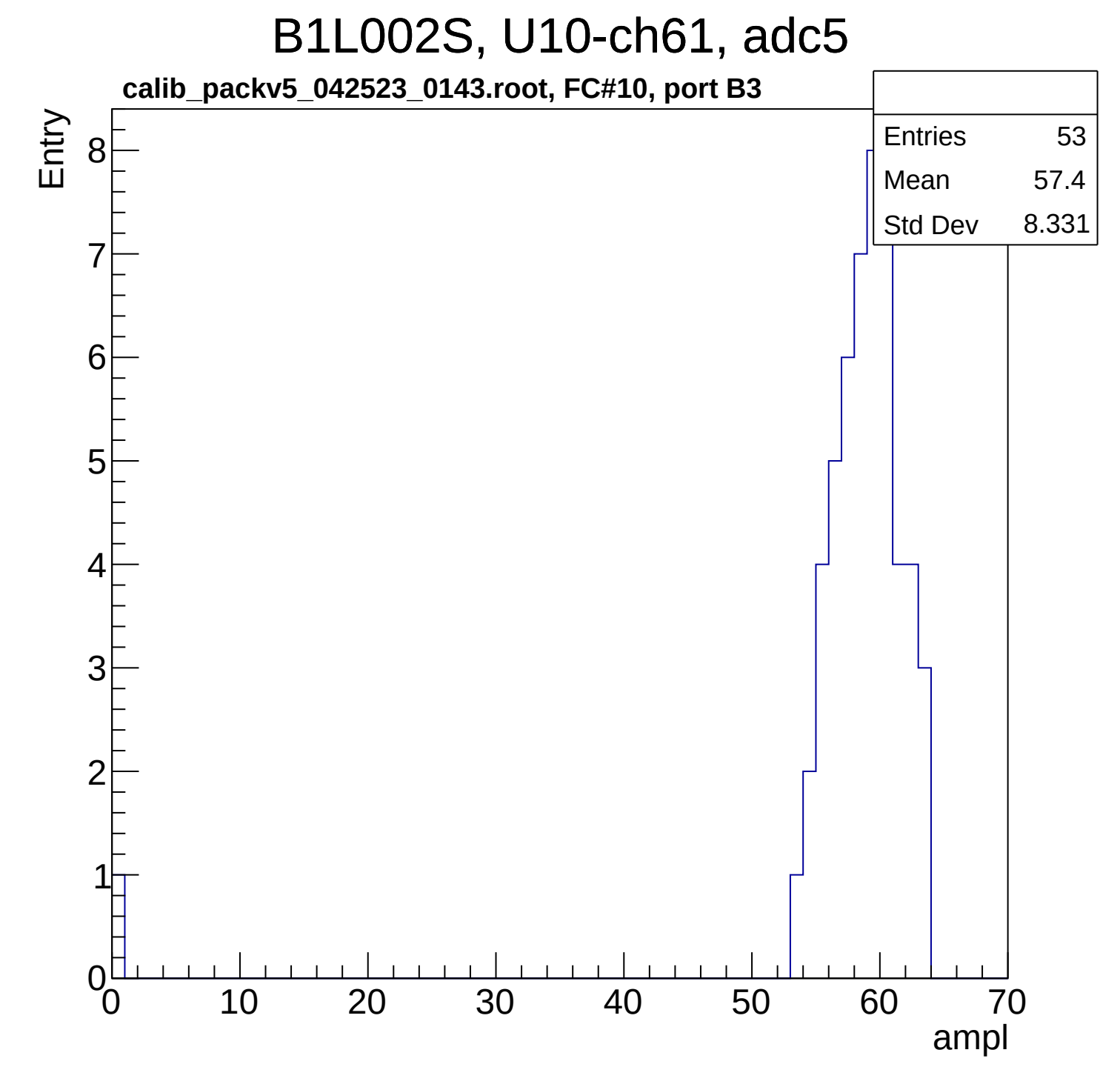
B1L002S, U10-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.4
Std Dev 8.331
Entry
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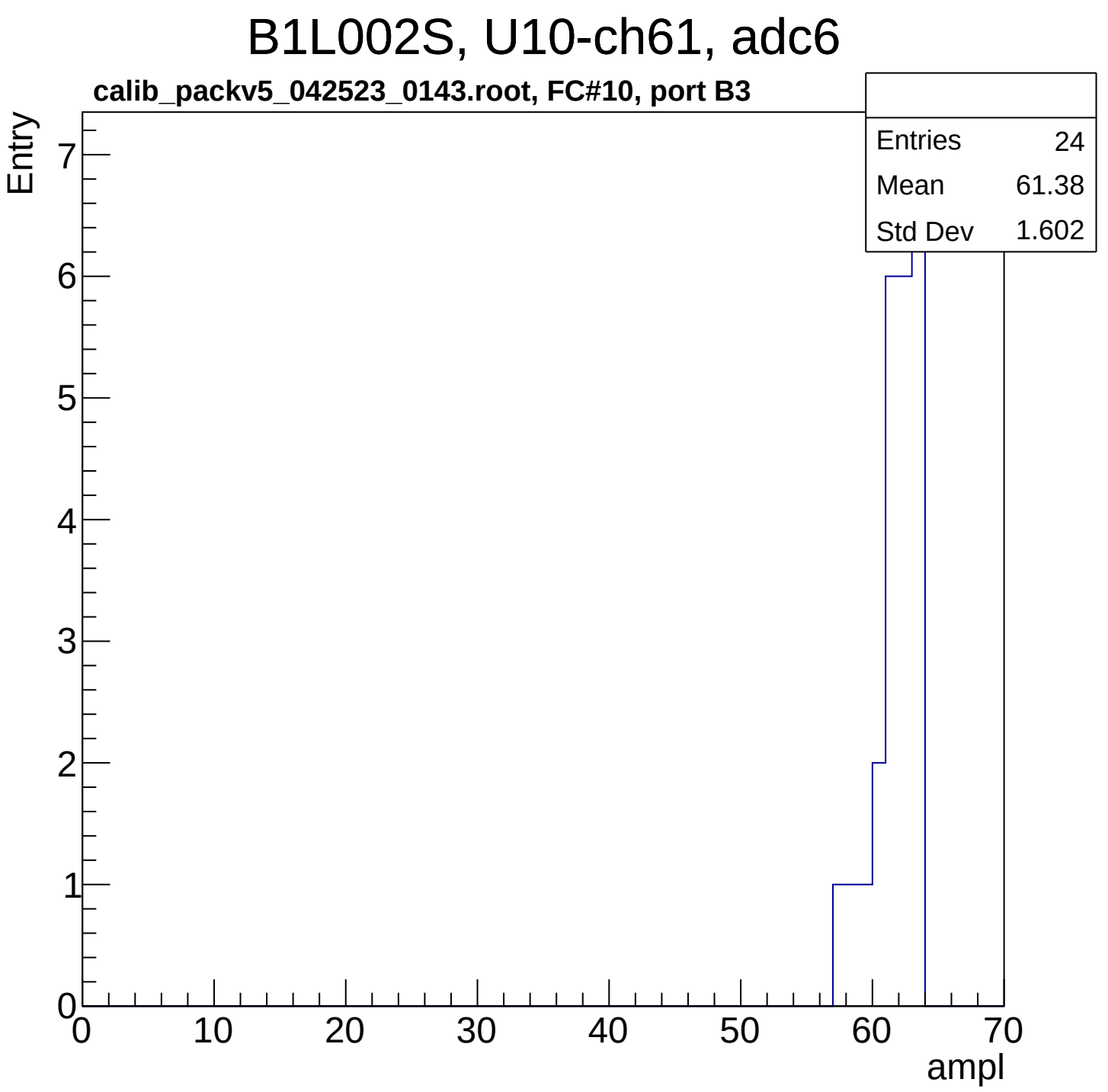
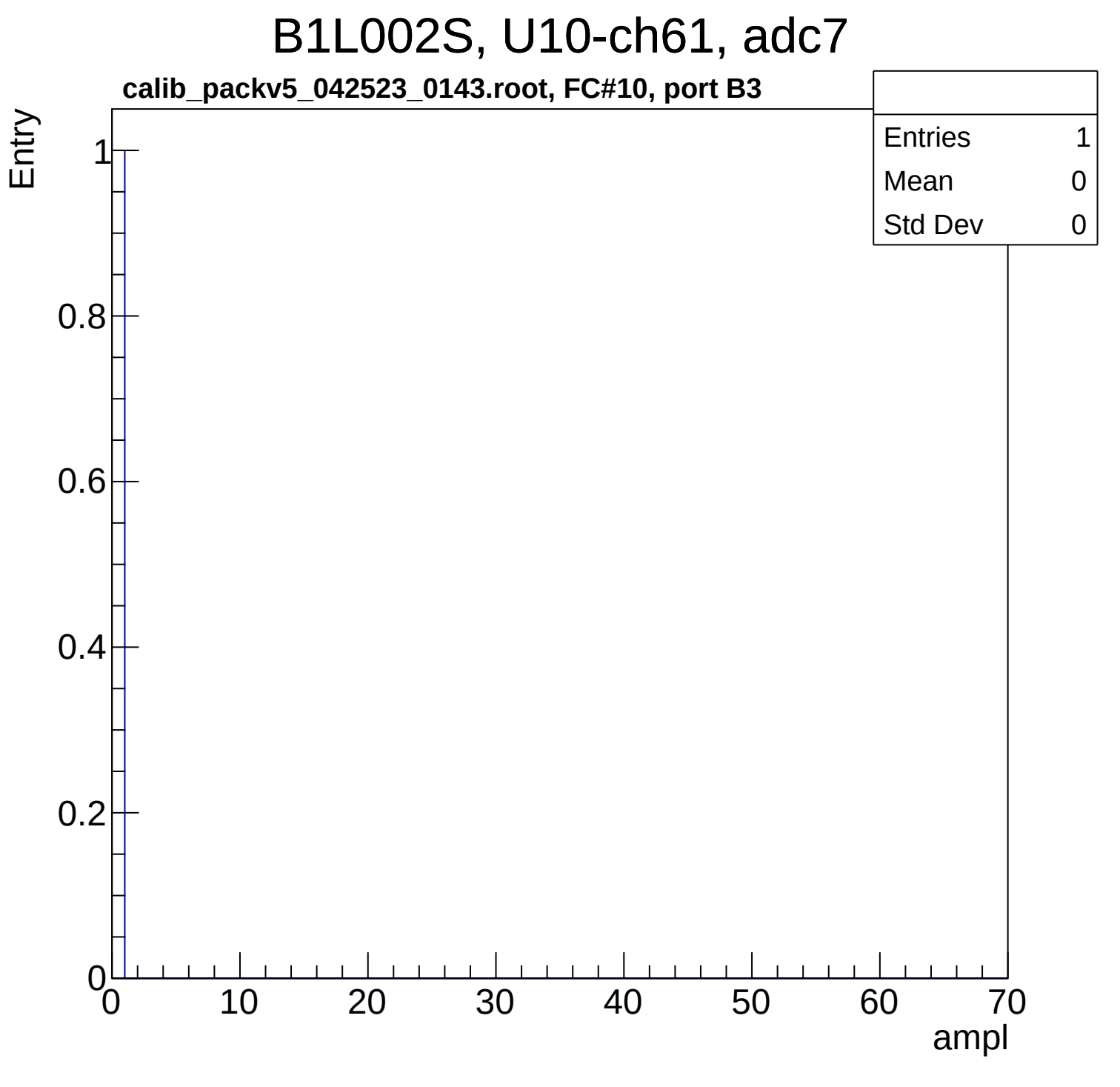
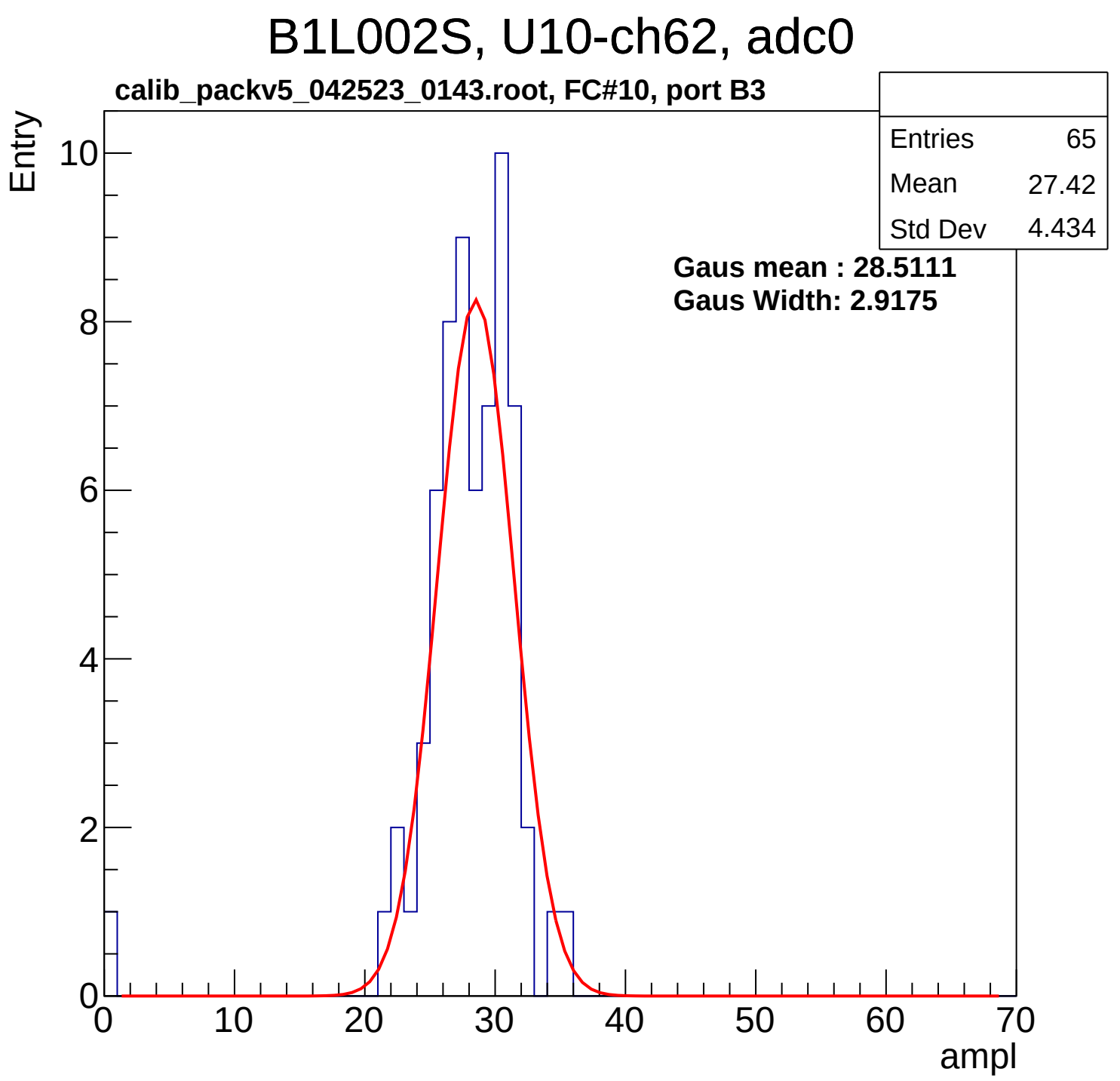
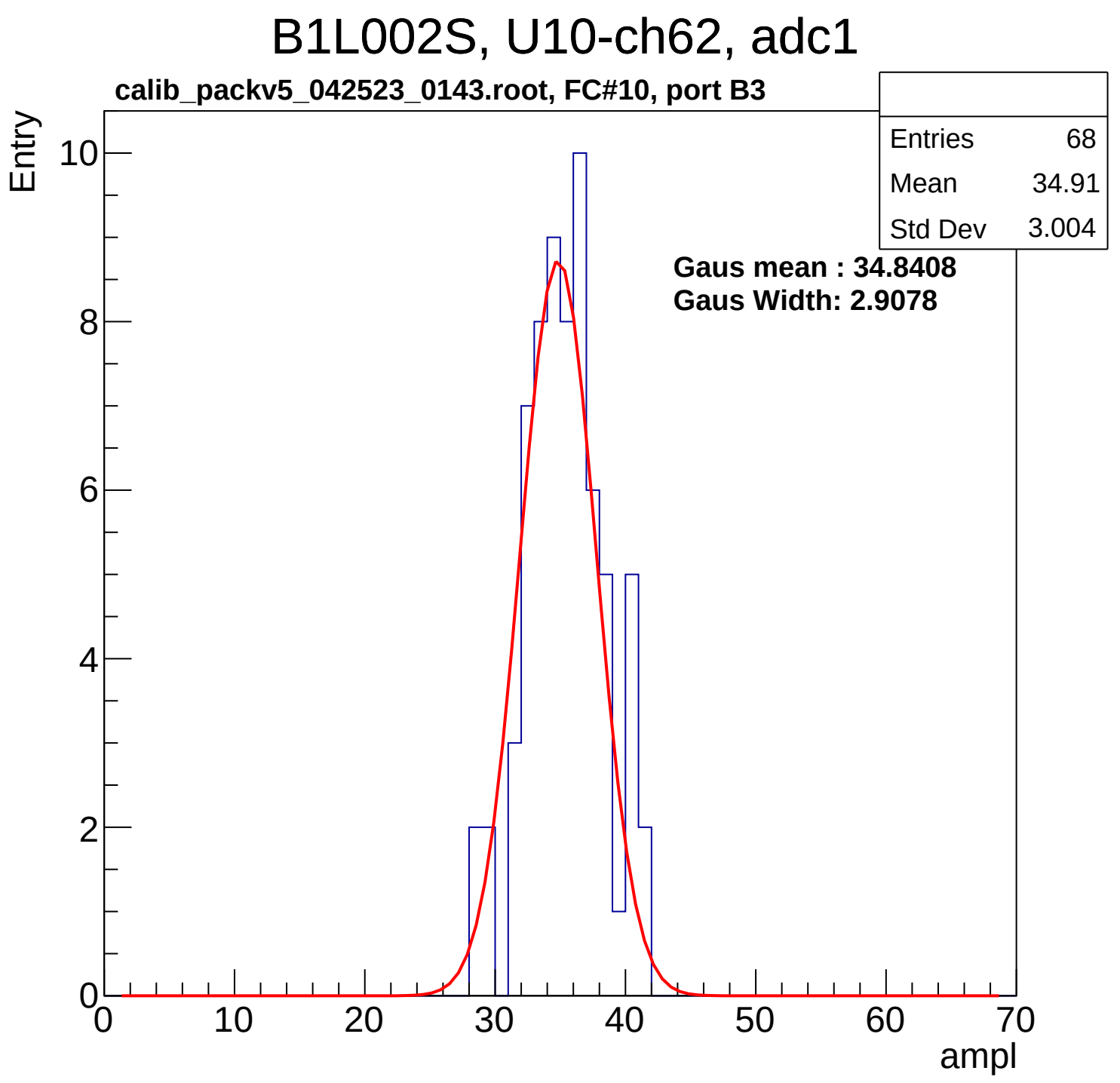
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.38
Std Dev 1.602
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.42
Std Dev 4.434
Gaus mean : 28.5111
Gaus Width: 2.9175
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.91
Std Dev 3.004
Gaus mean : 34.8408
Gaus Width: 2.9078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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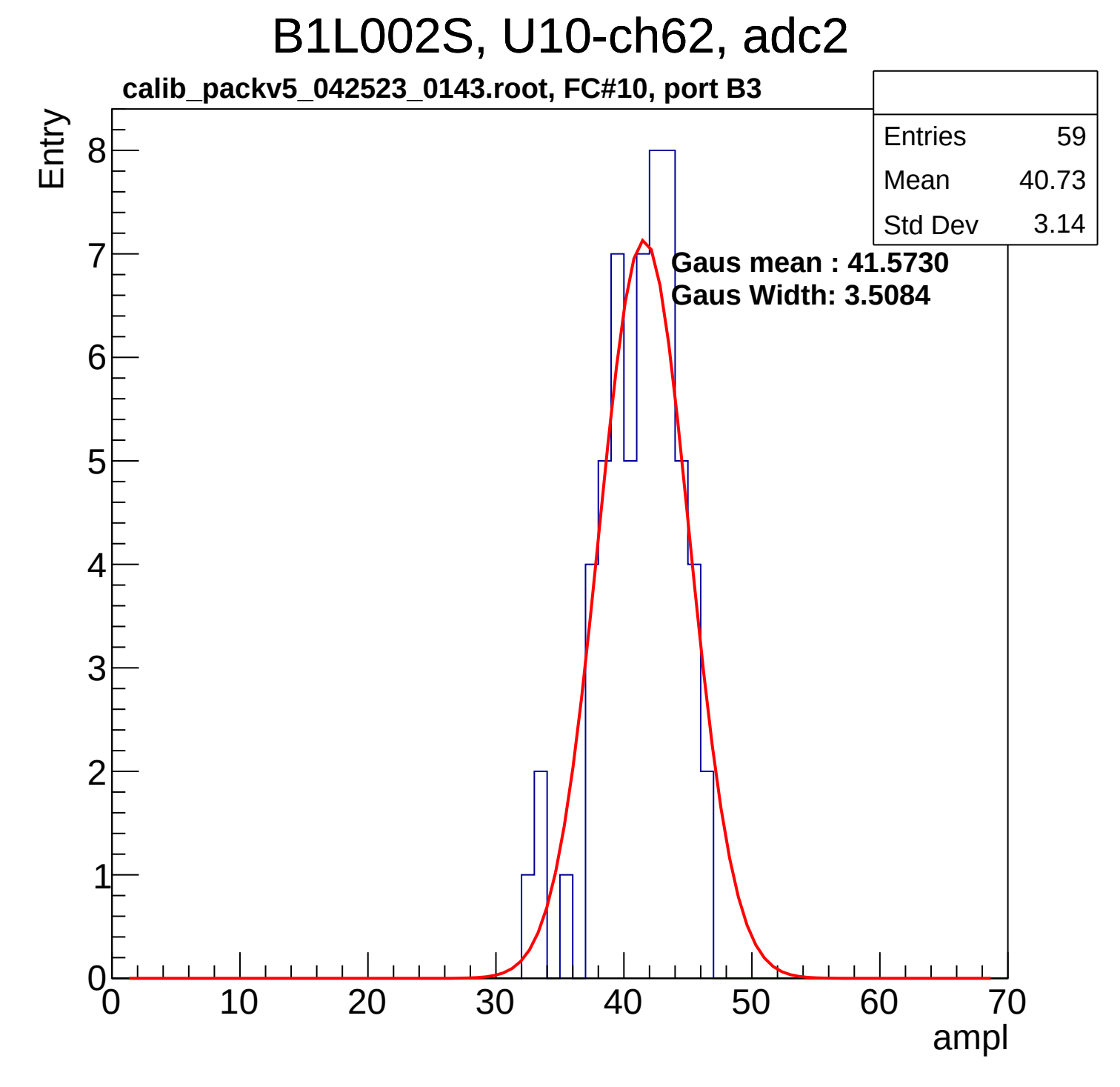
B1L002S, U10-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.73
Std Dev 3.14
Gaus mean : 41.5730
Gaus Width: 3.5084
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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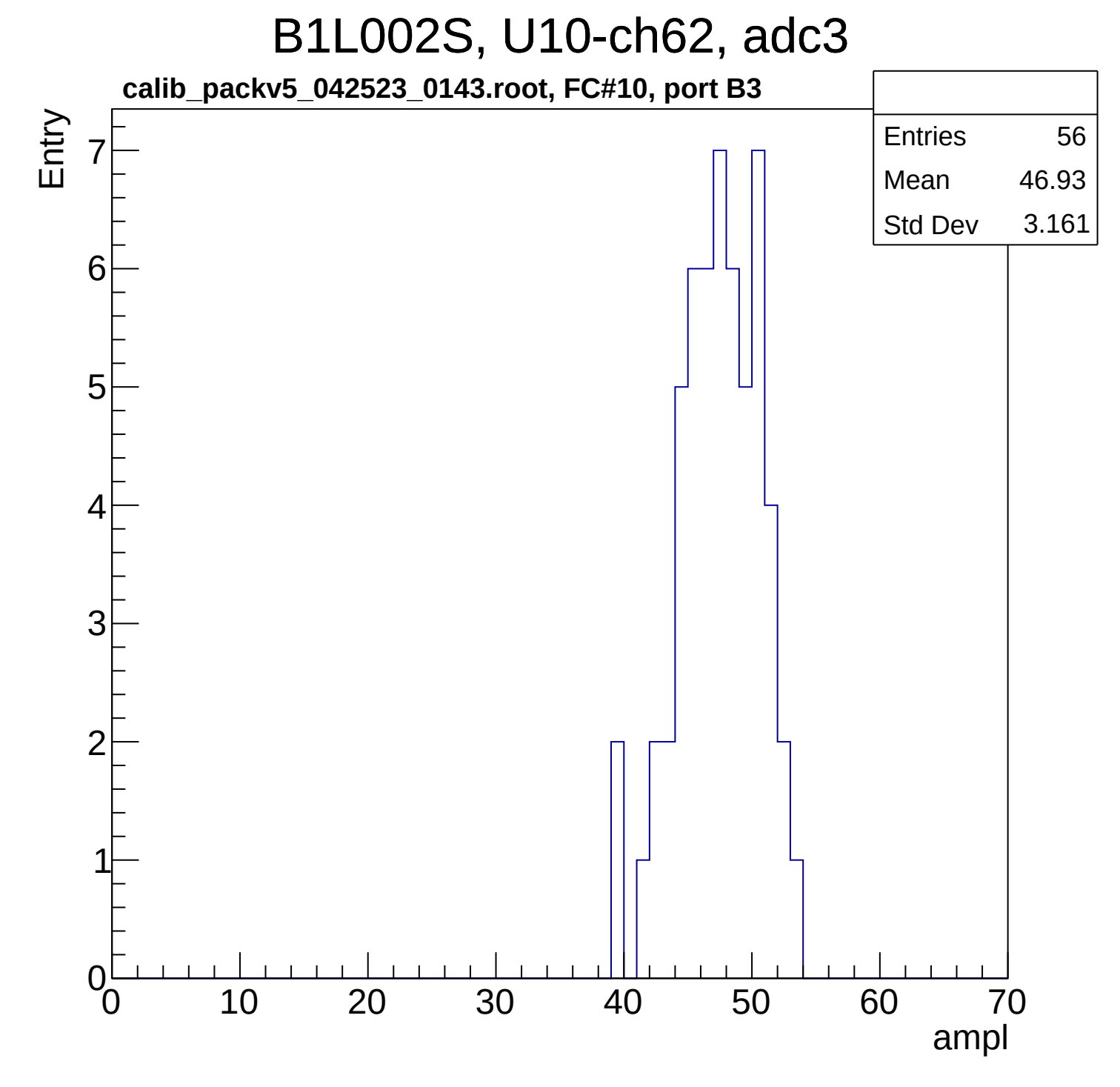
B1L002S, U10-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.93
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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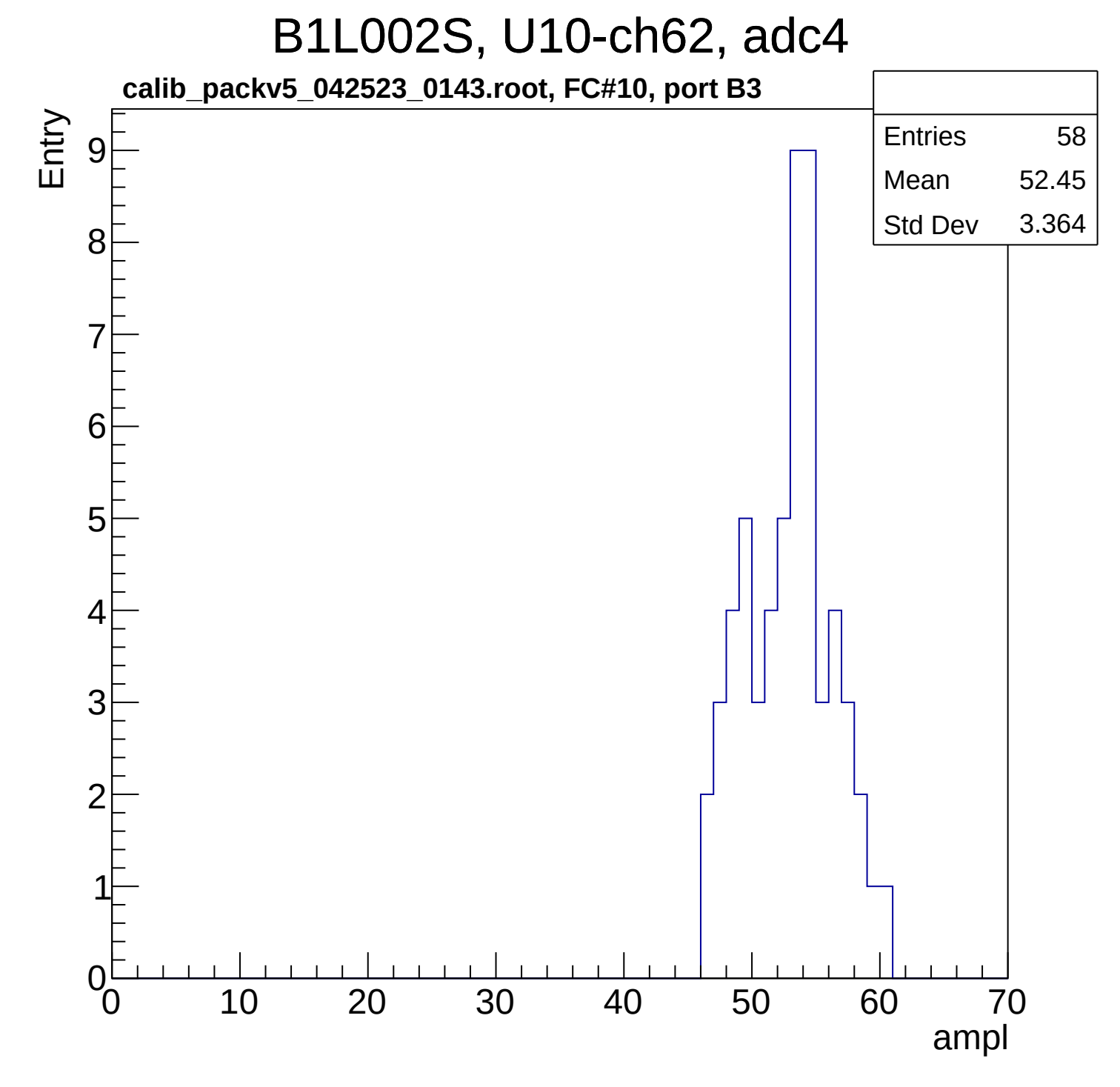
B1L002S, U10-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.45
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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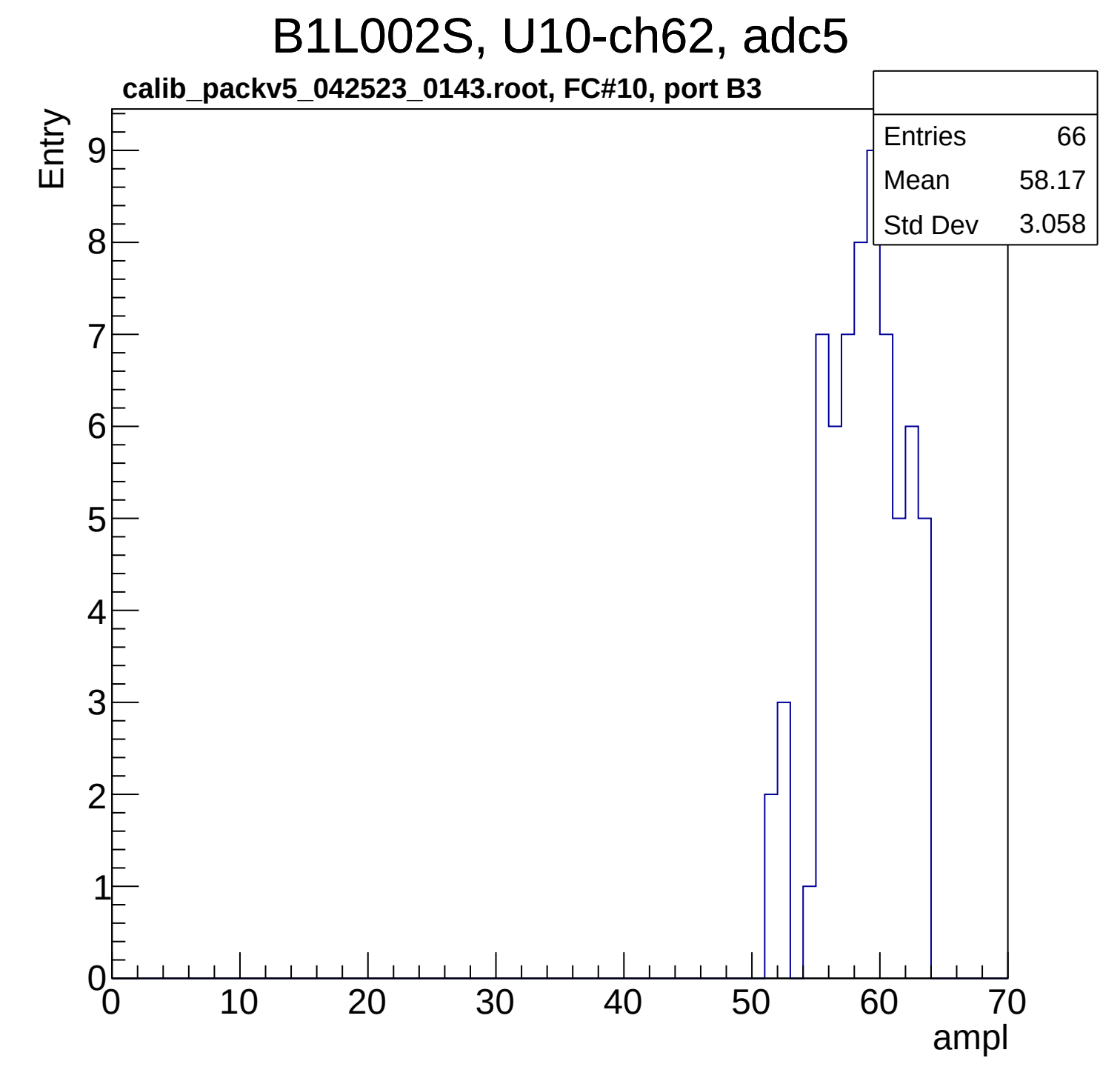
B1L002S, U10-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.17
Std Dev 3.058
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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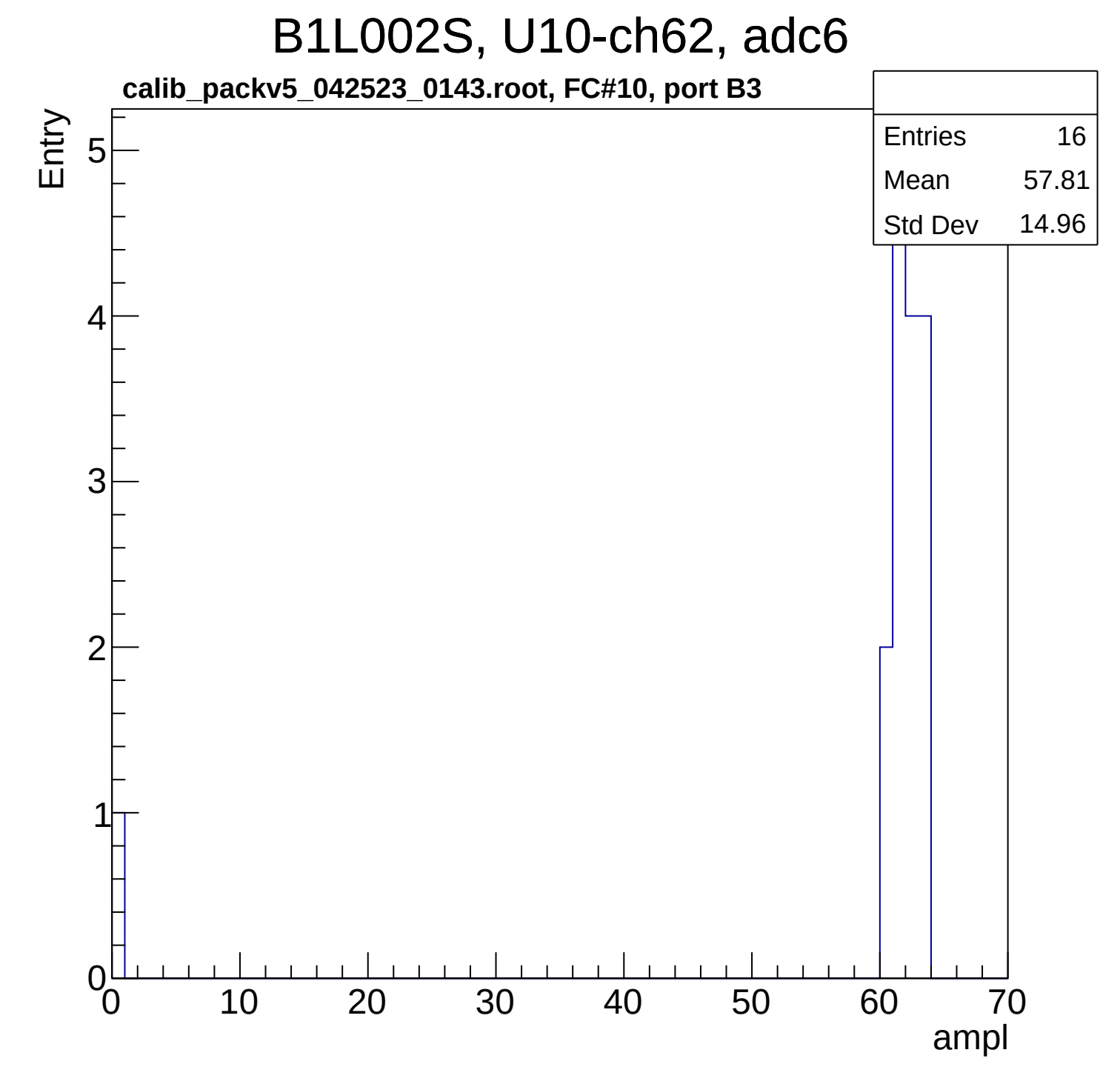
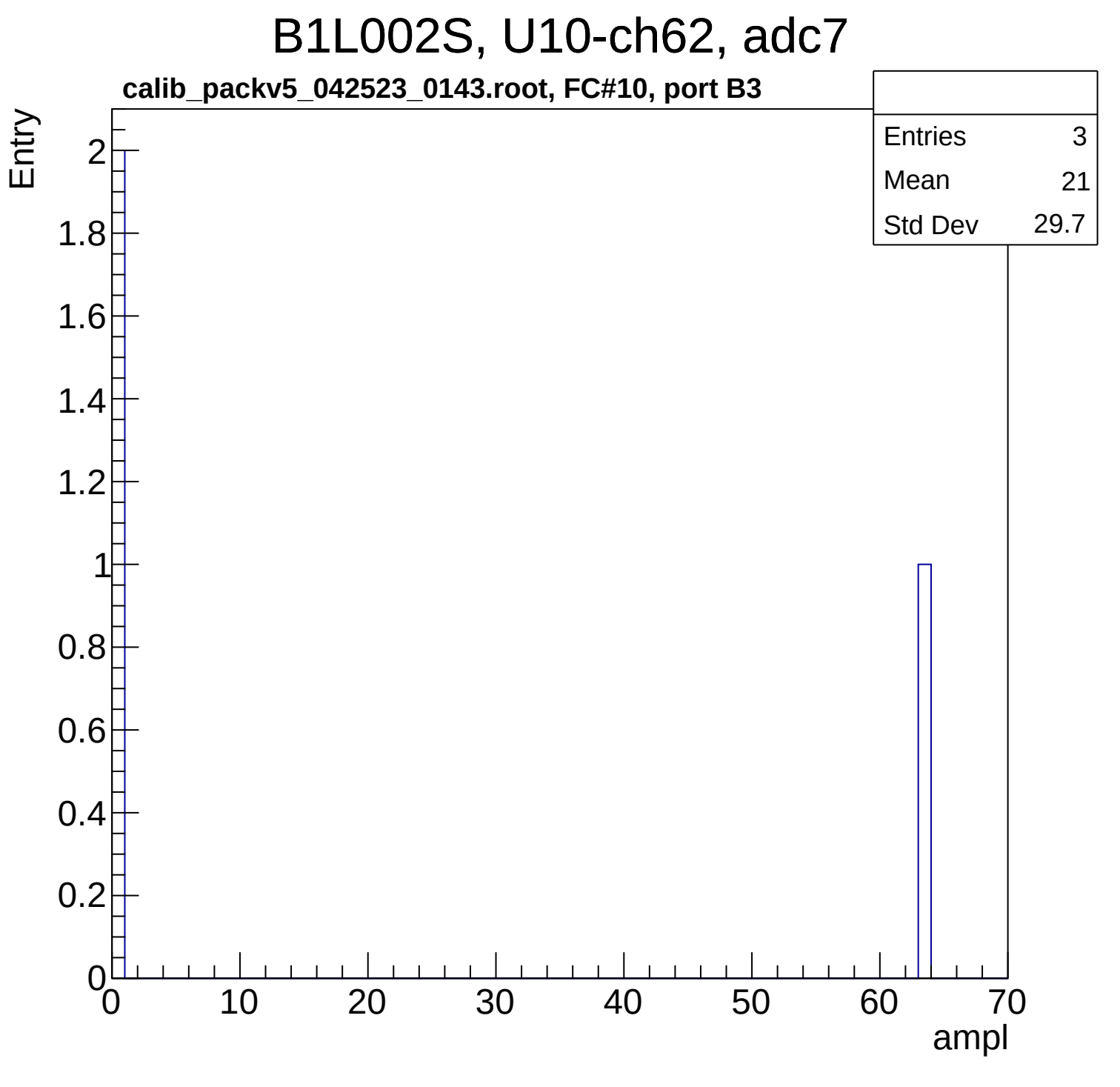
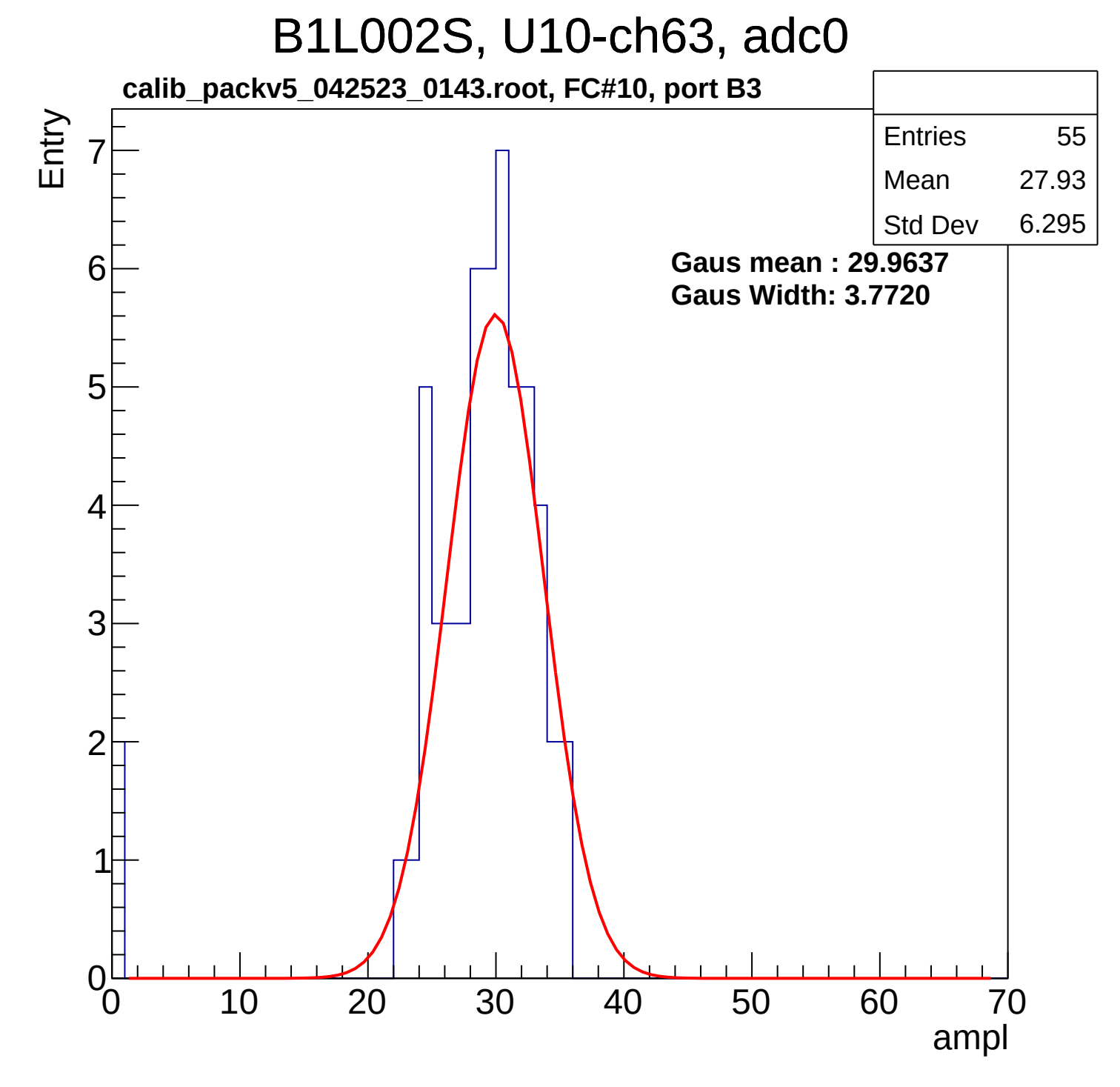

B1L002S, U10-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.81
Std Dev 14.96
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.93
Std Dev 6.295
Gaus mean : 29.9637
Gaus Width: 3.7720
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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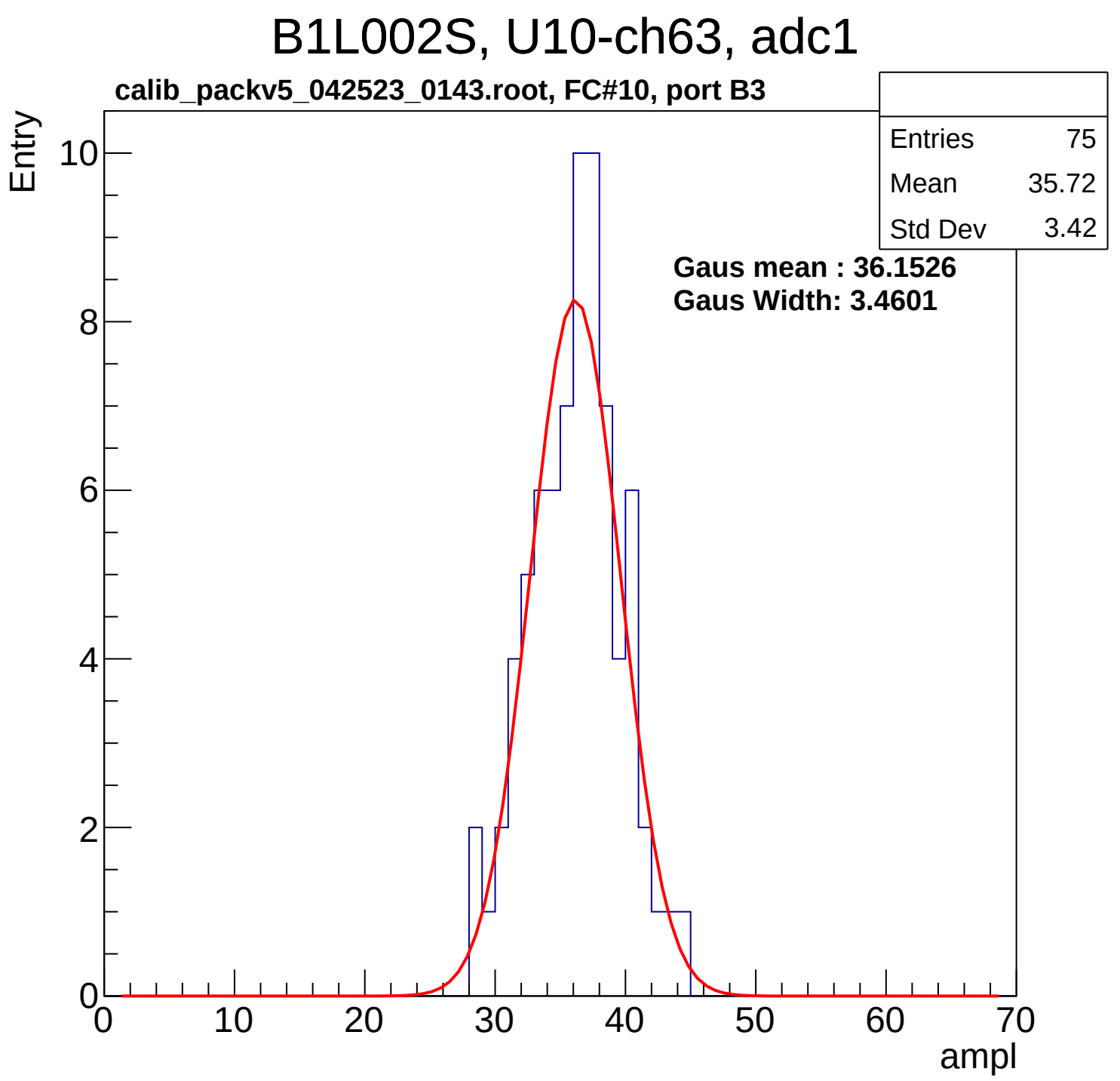
B1L002S, U10-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.72
Std Dev 3.42
Gaus mean : 36.1526
Gaus Width: 3.4601
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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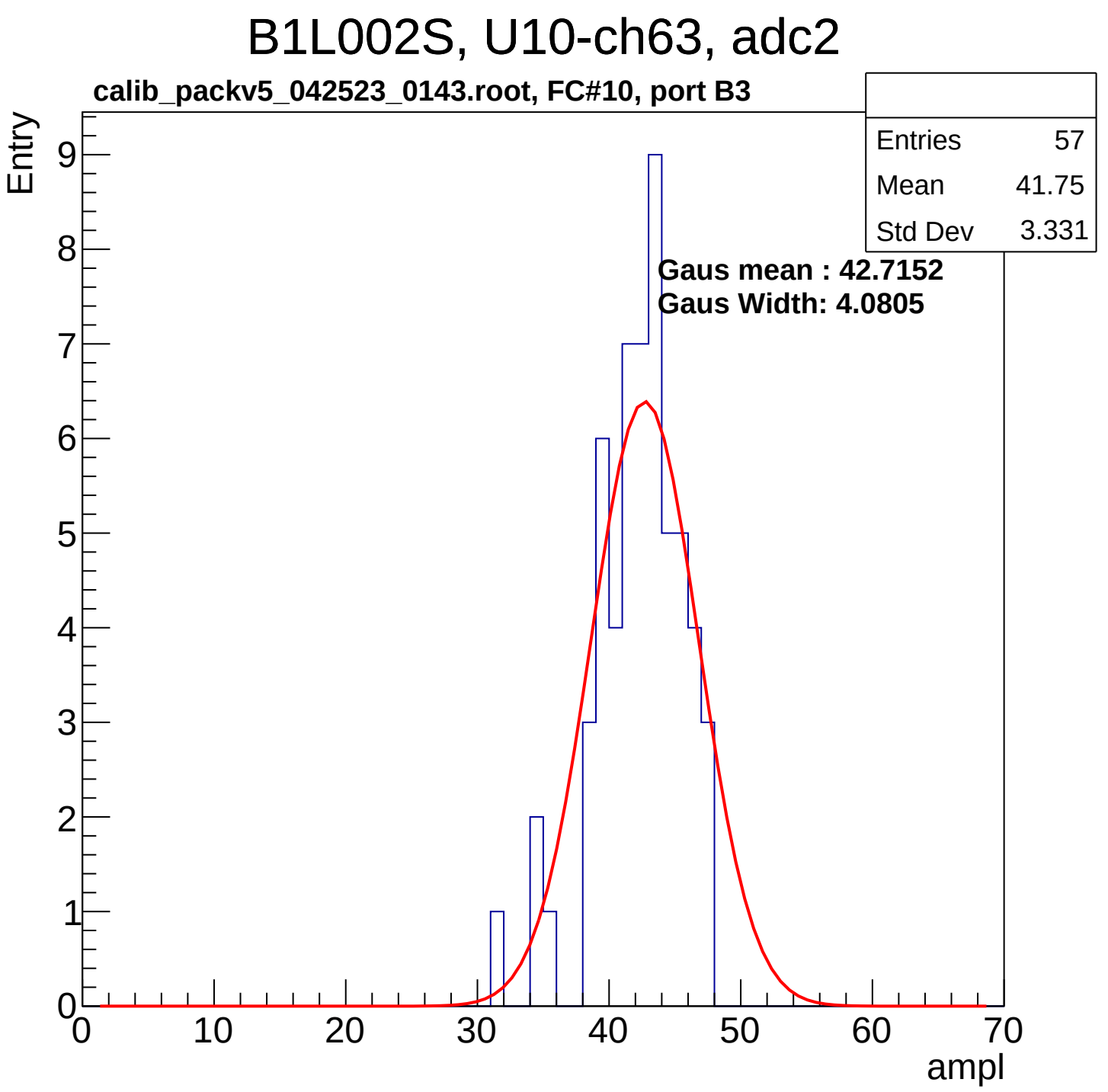
B1L002S, U10-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.75
Std Dev 3.331
Gaus mean : 42.7152
Gaus Width: 4.0805
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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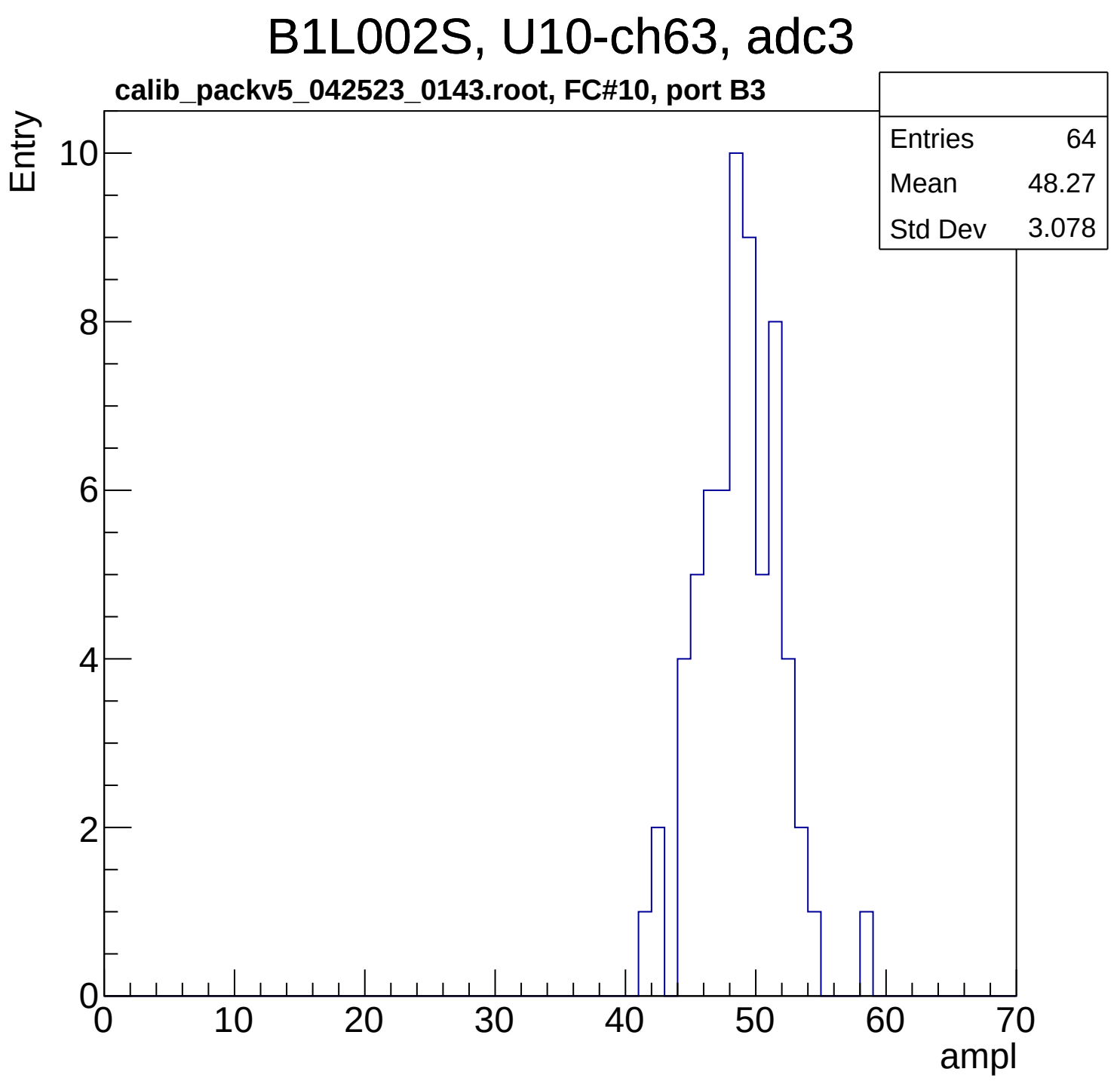
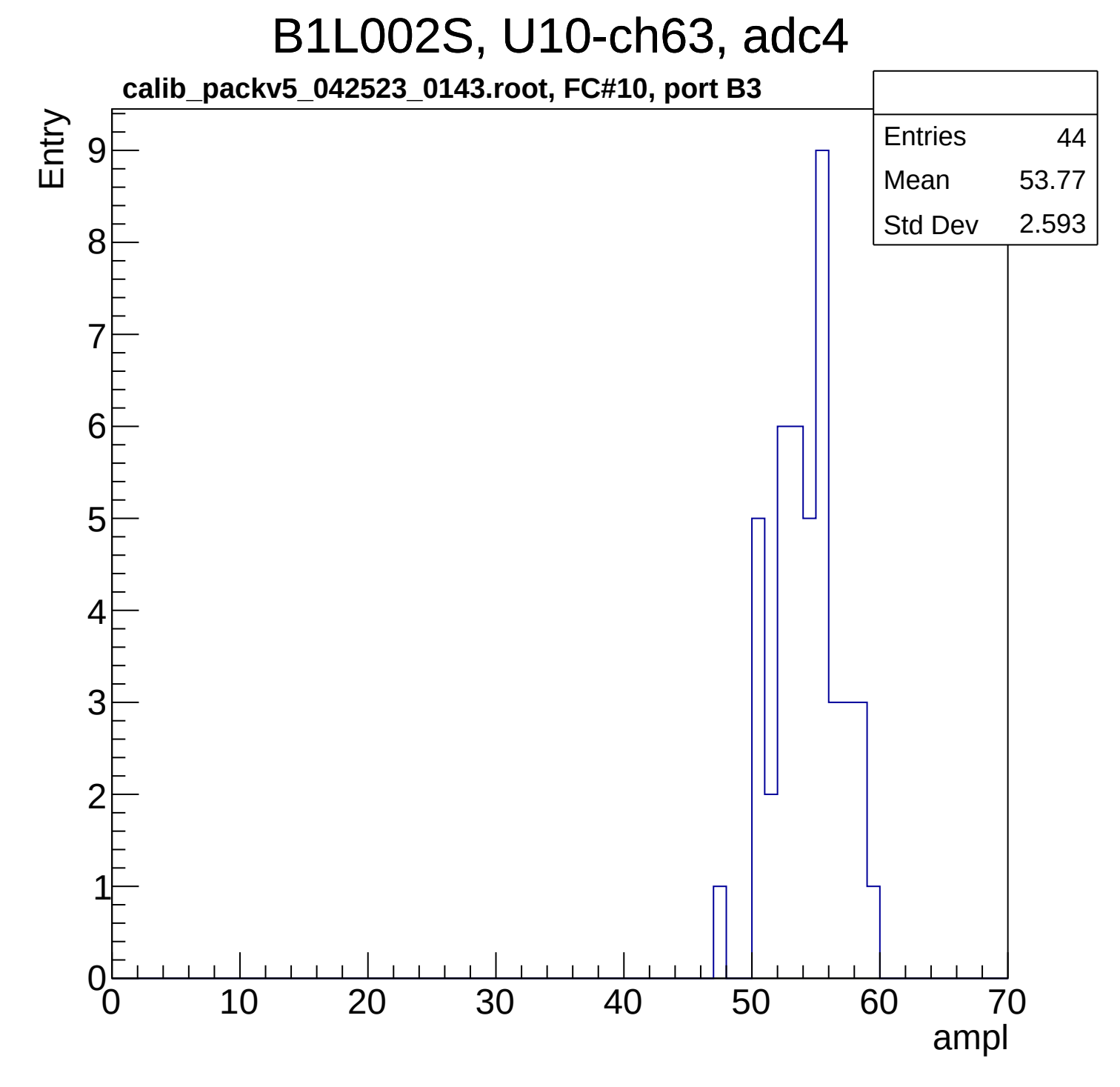

B1L002S, U10-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.27
Std Dev 3.078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.77
Std Dev 2.593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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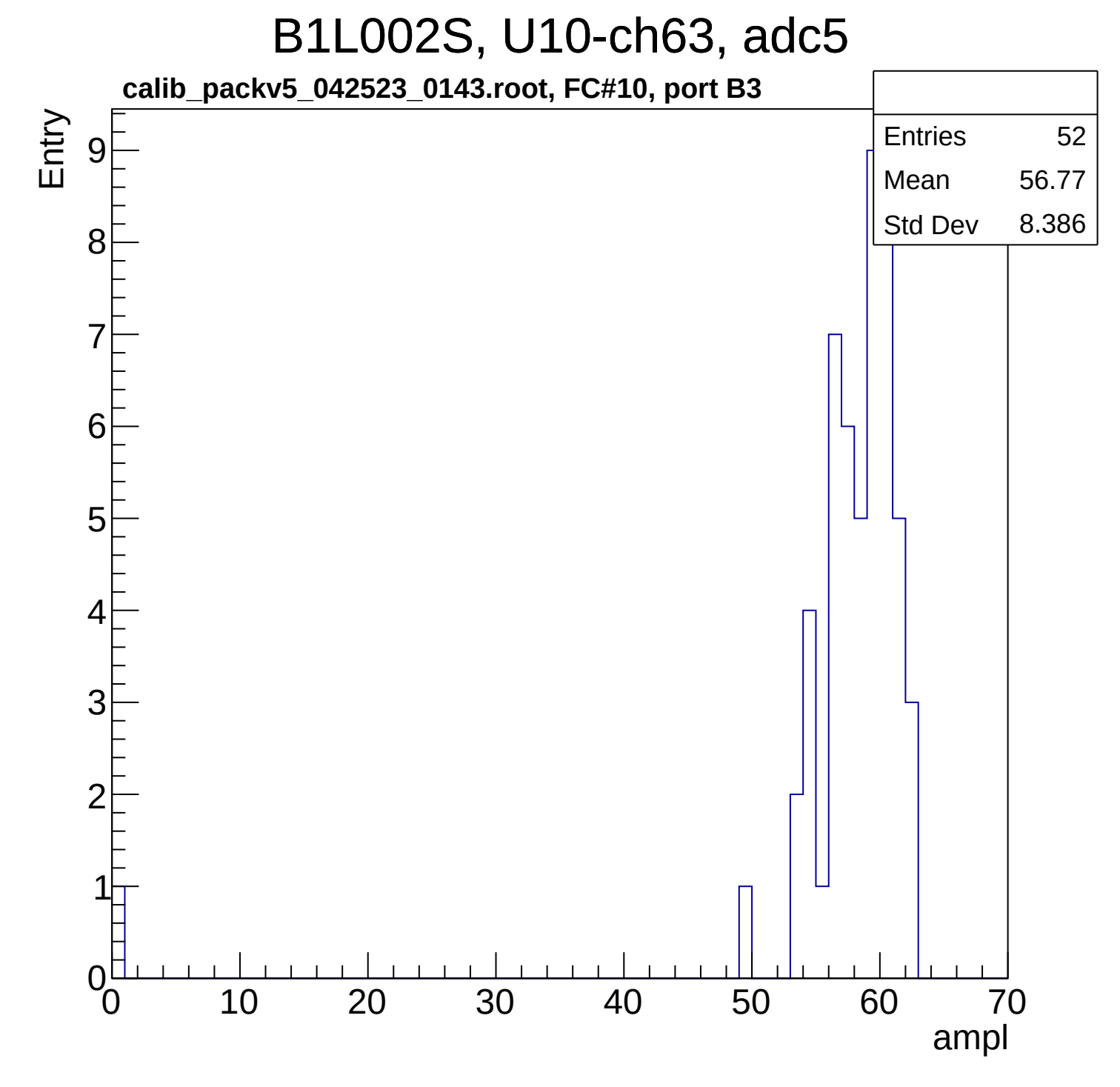
B1L002S, U10-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 56.77
Std Dev 8.386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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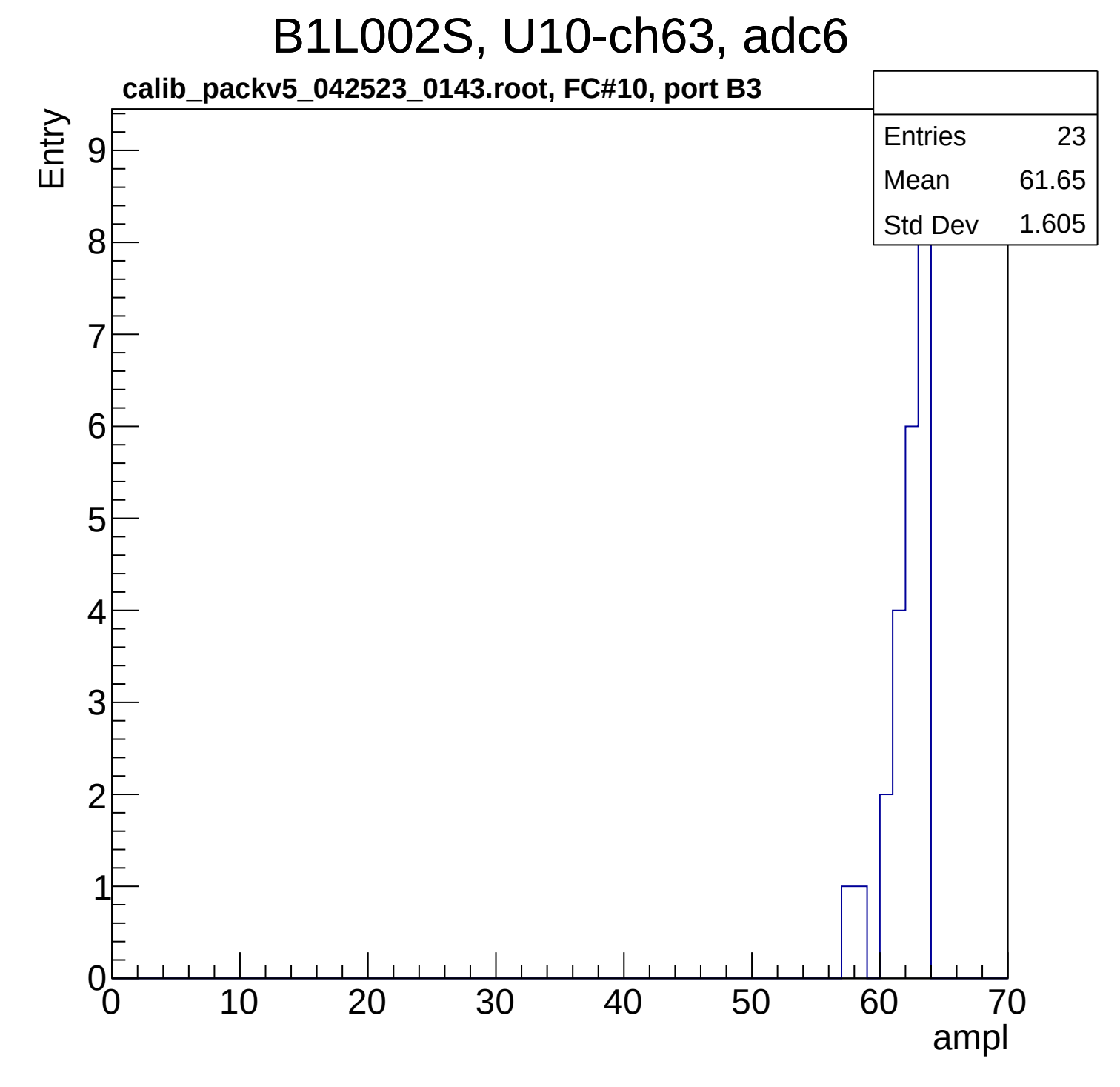
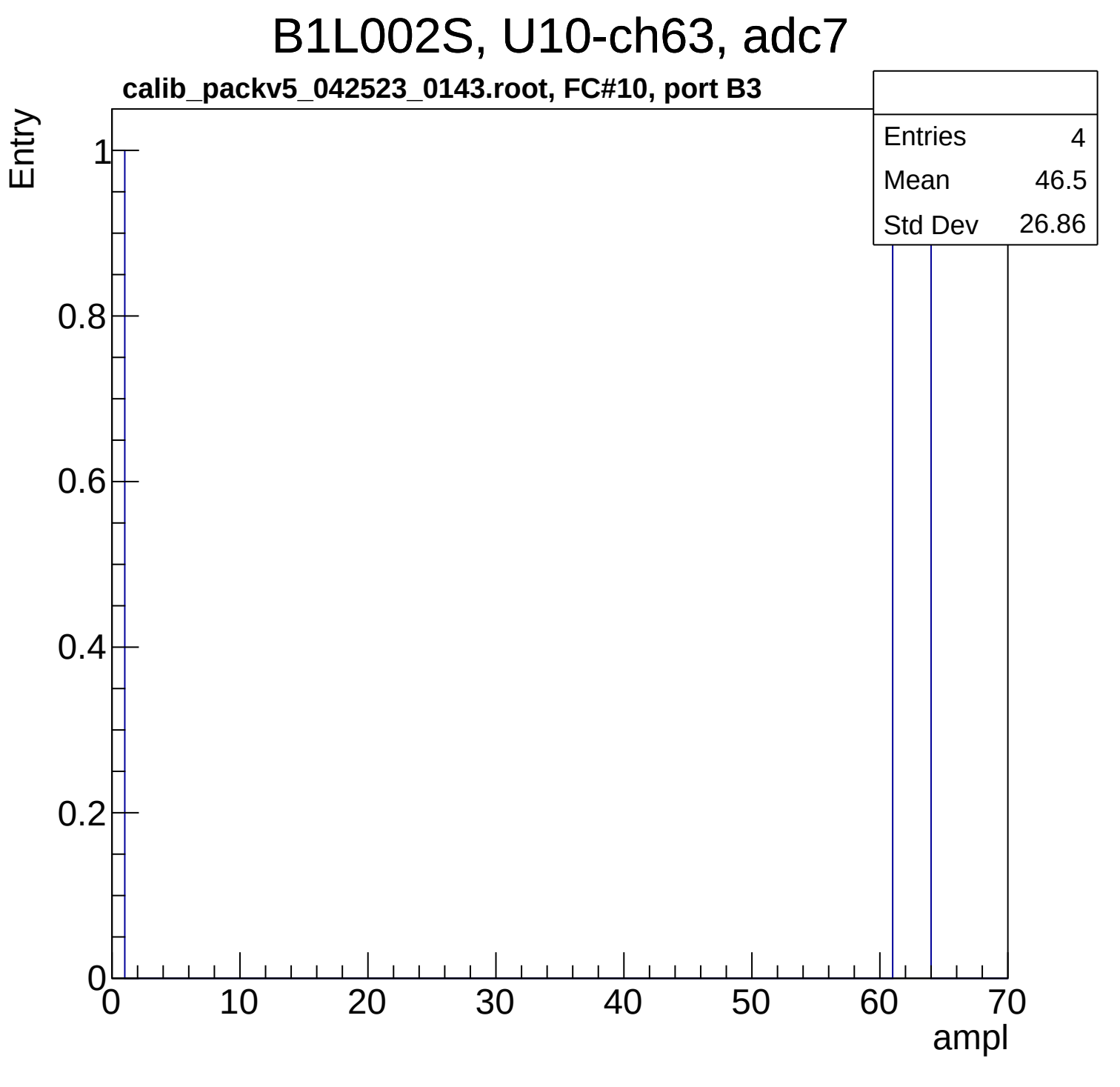

B1L002S, U10-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.65
Std Dev 1.605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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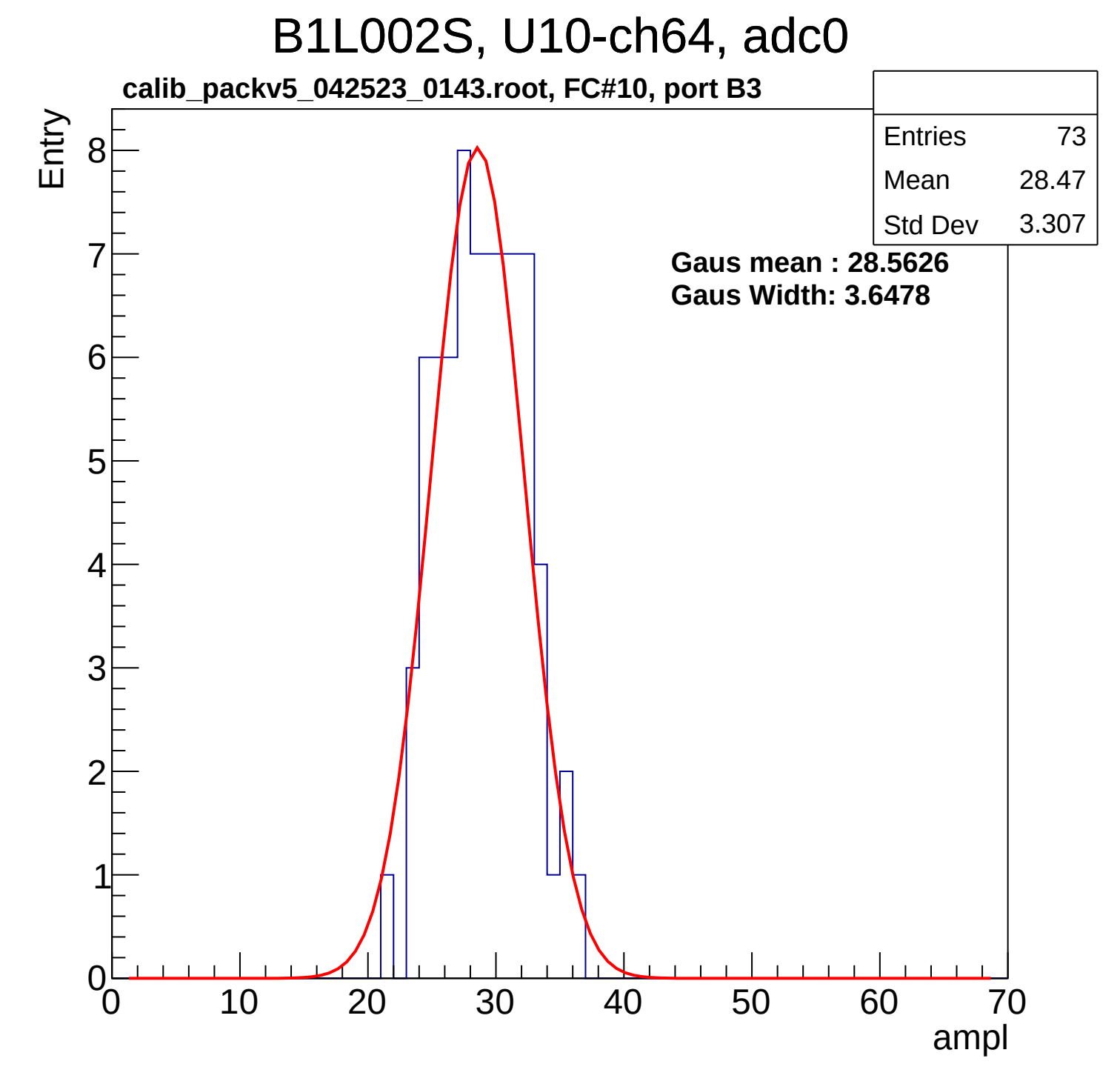
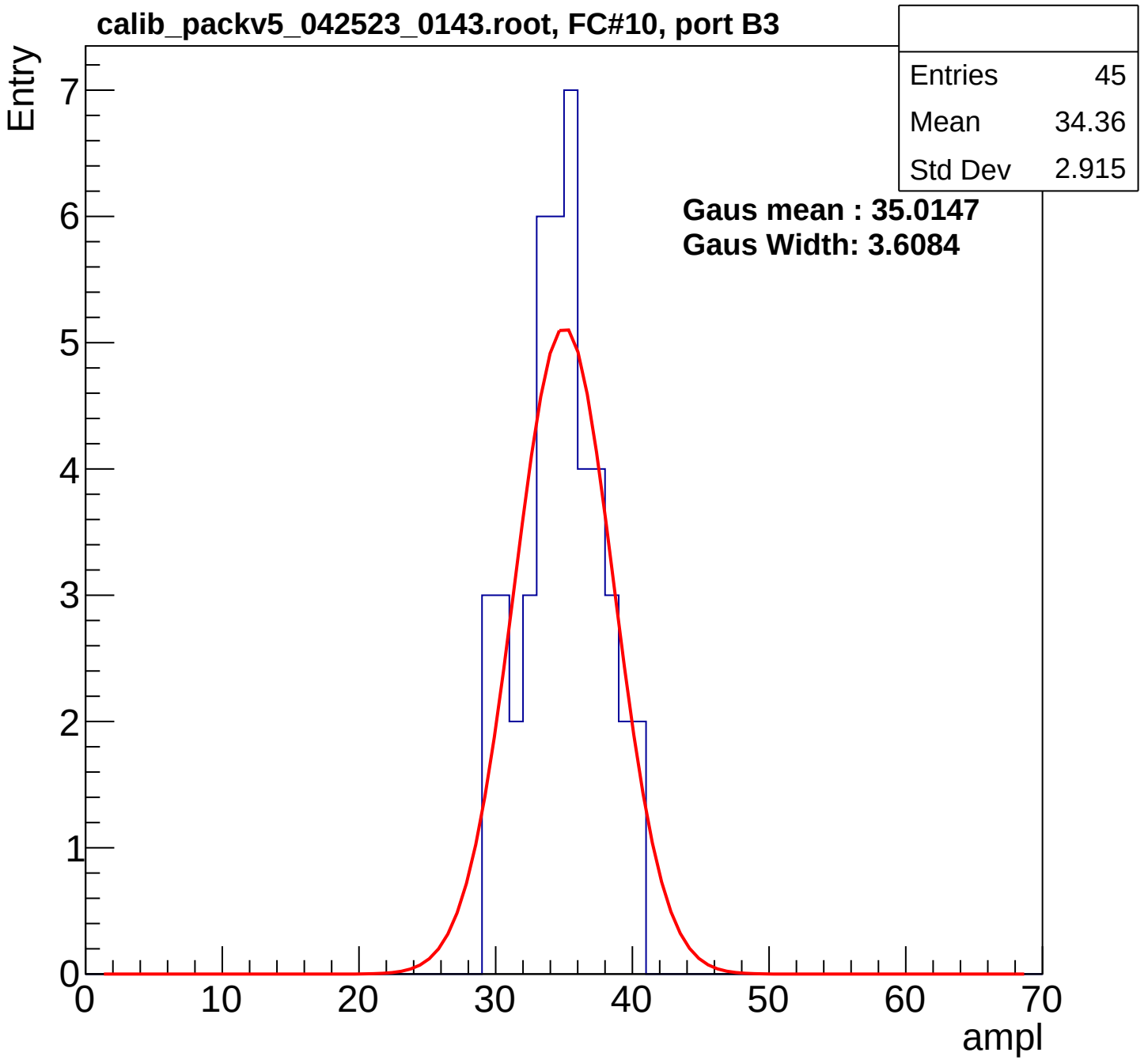
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.47
Std Dev 3.307
Gaus mean : 28.5626
Gaus Width: 3.6478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 34.36
Std Dev 2.915
Gaus mean : 35.0147
Gaus Width: 3.6084
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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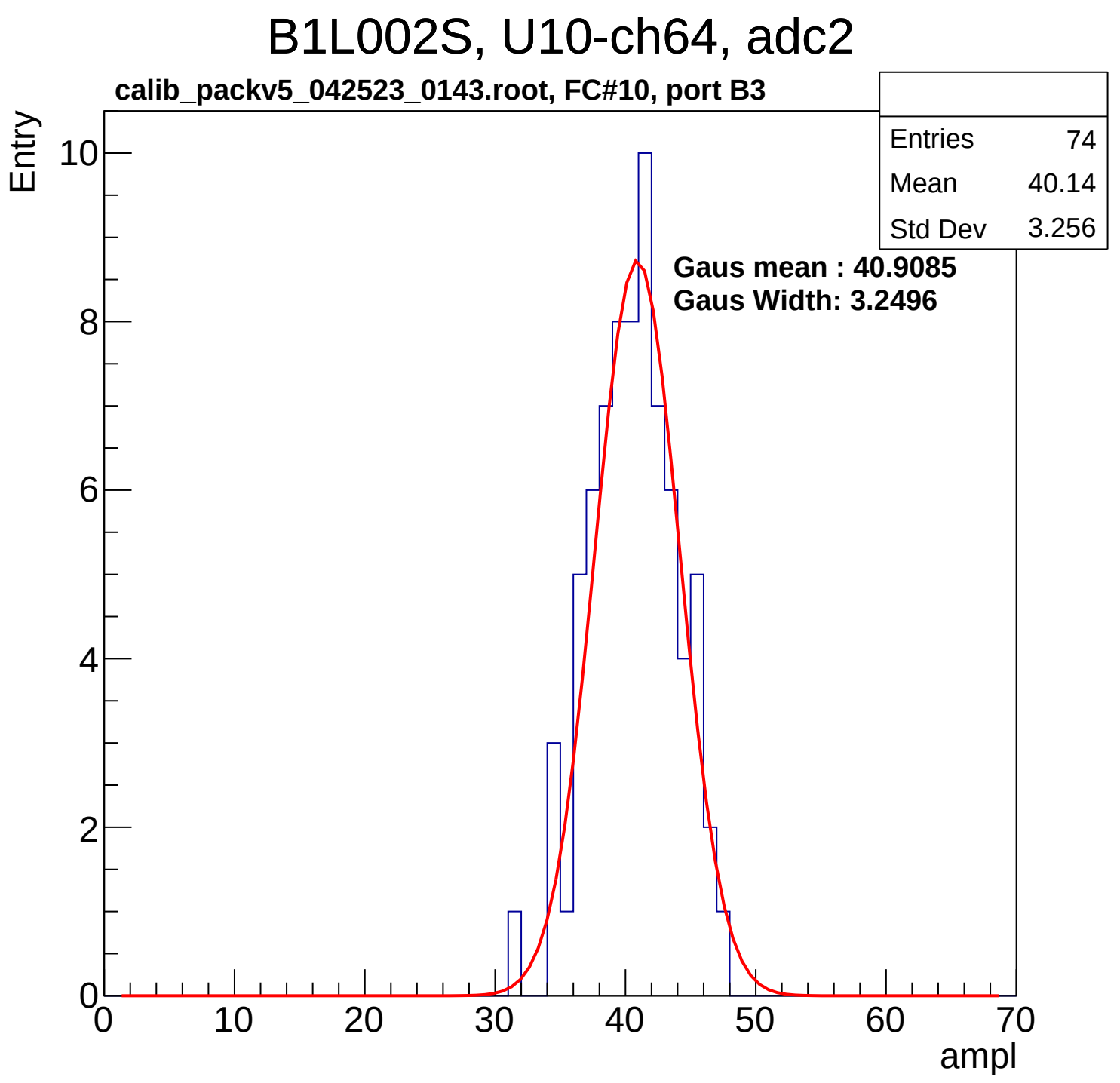
B1L002S, U10-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.14
Std Dev 3.256
Gaus mean : 40.9085
Gaus Width: 3.2496
Entry
ampl
0
2
4
6
8
10
0
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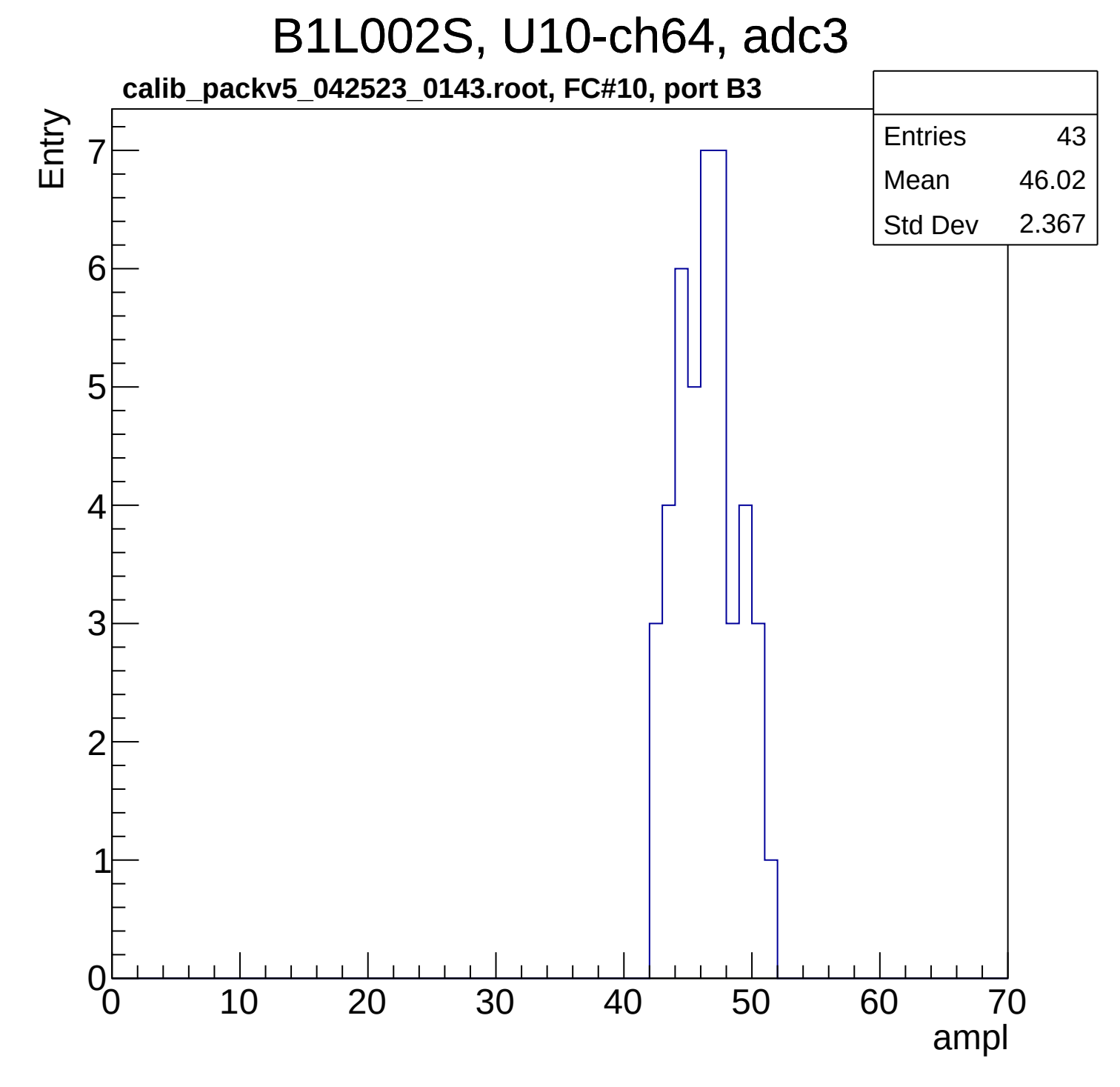
20
30
40
50
60
70


B1L002S, U10-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 46.02
Std Dev 2.367
Entry
ampl
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1
2
3
4
5
6
7
0
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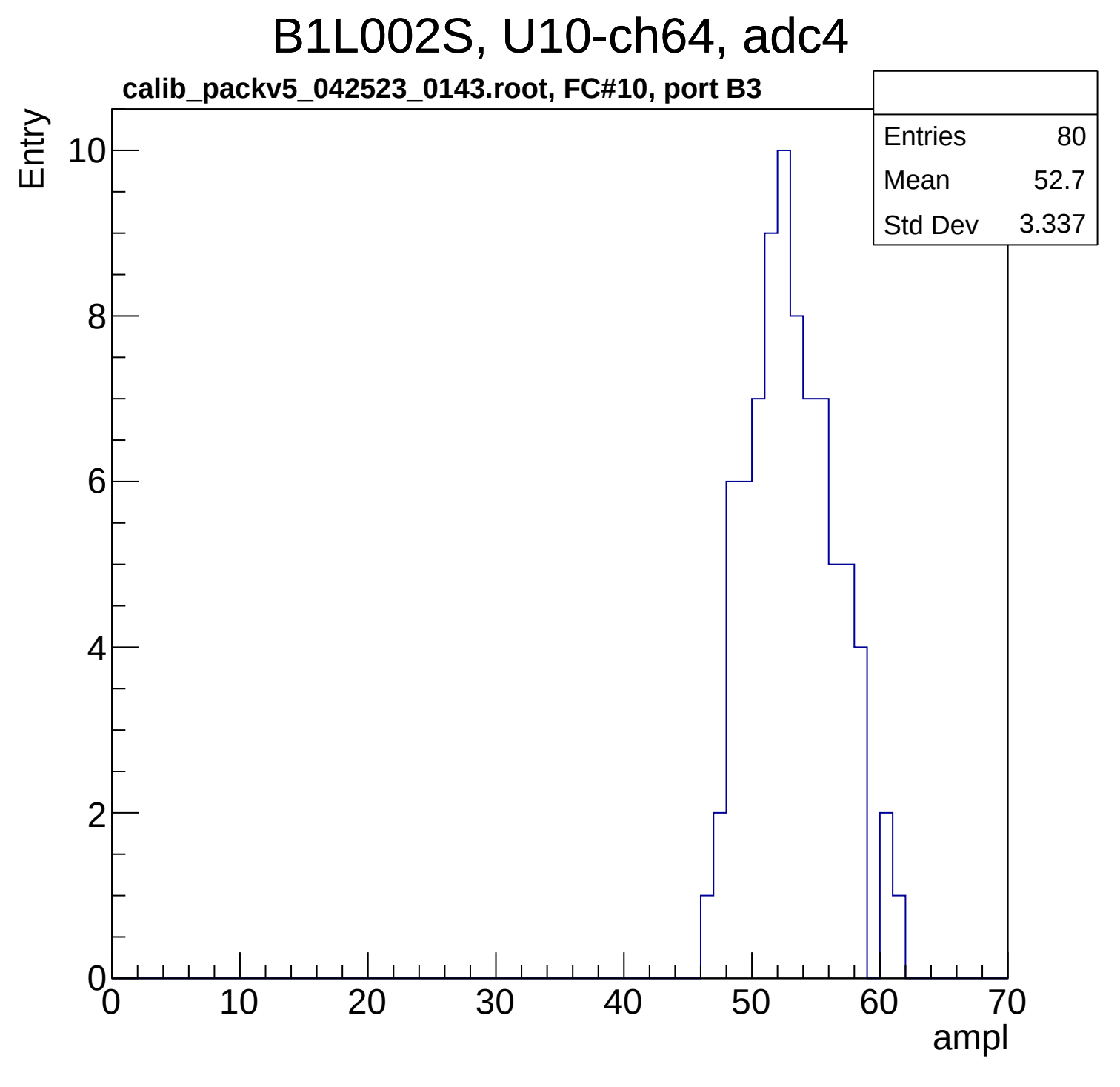
20
30
40
50
60
70


B1L002S, U10-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 52.7
Std Dev 3.337
Entry
ampl
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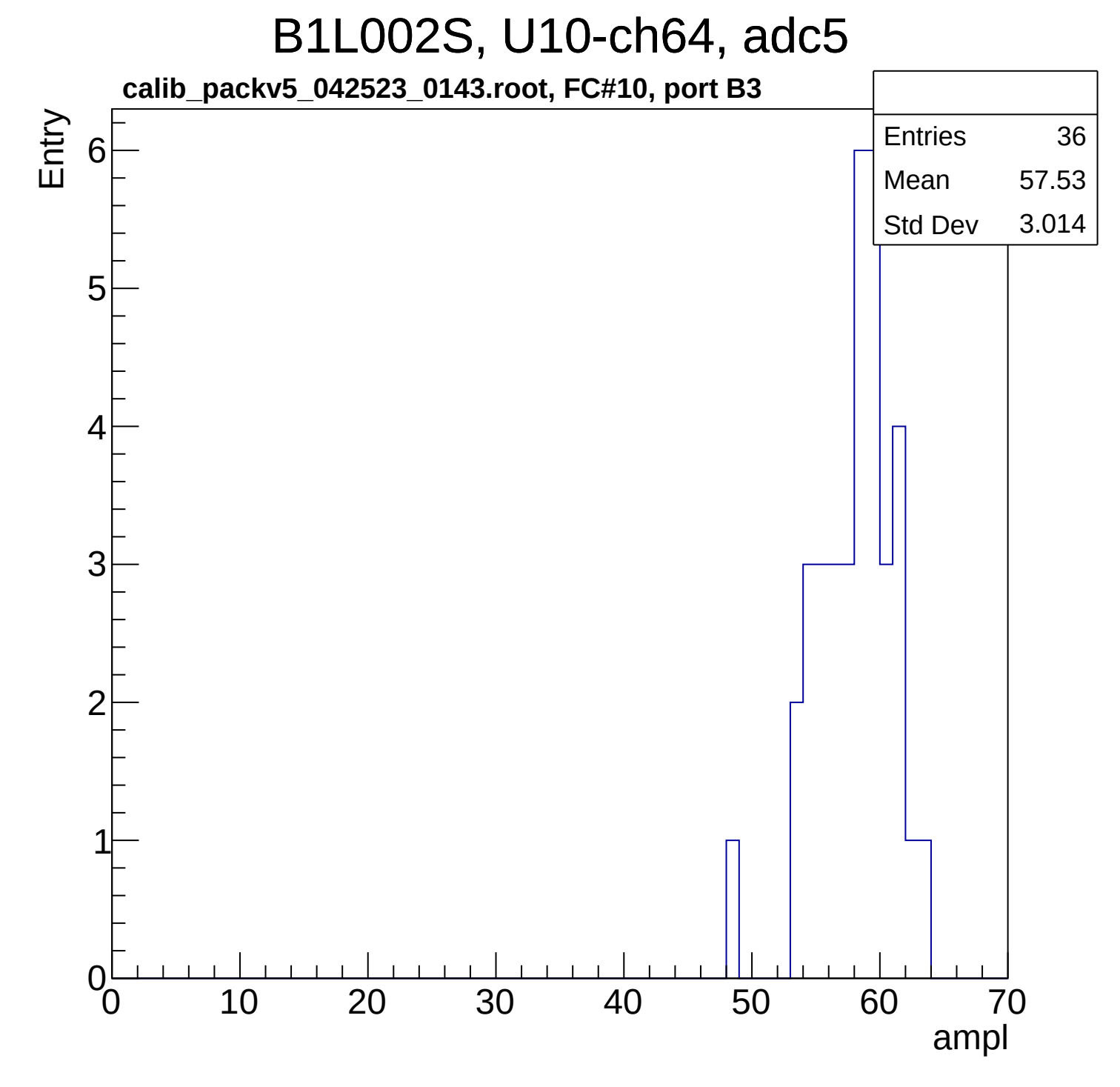
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 57.53
Std Dev 3.014
Entry
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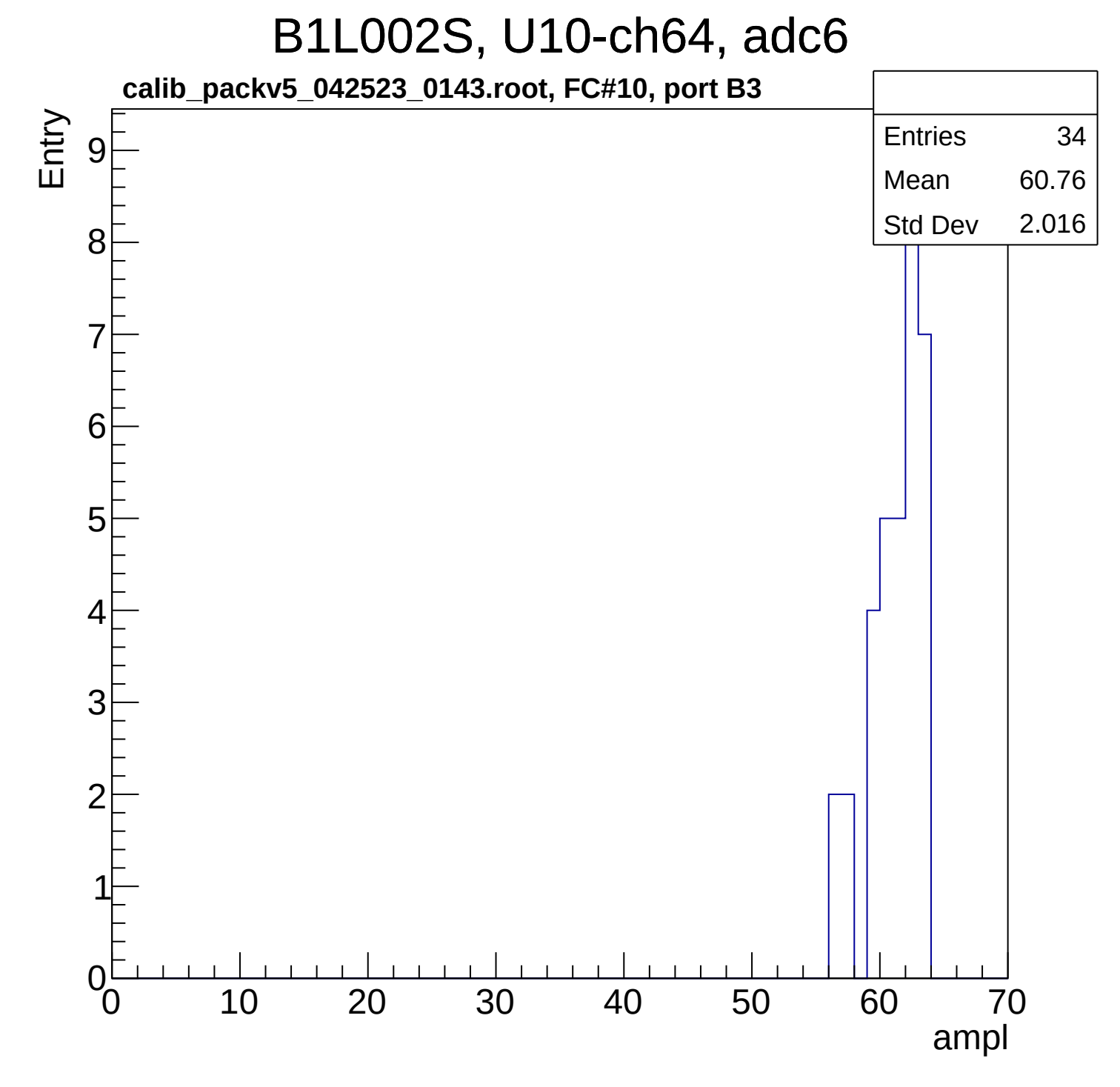
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 60.76
Std Dev 2.016
Entry
ampl
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3
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7
8
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0
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40
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60
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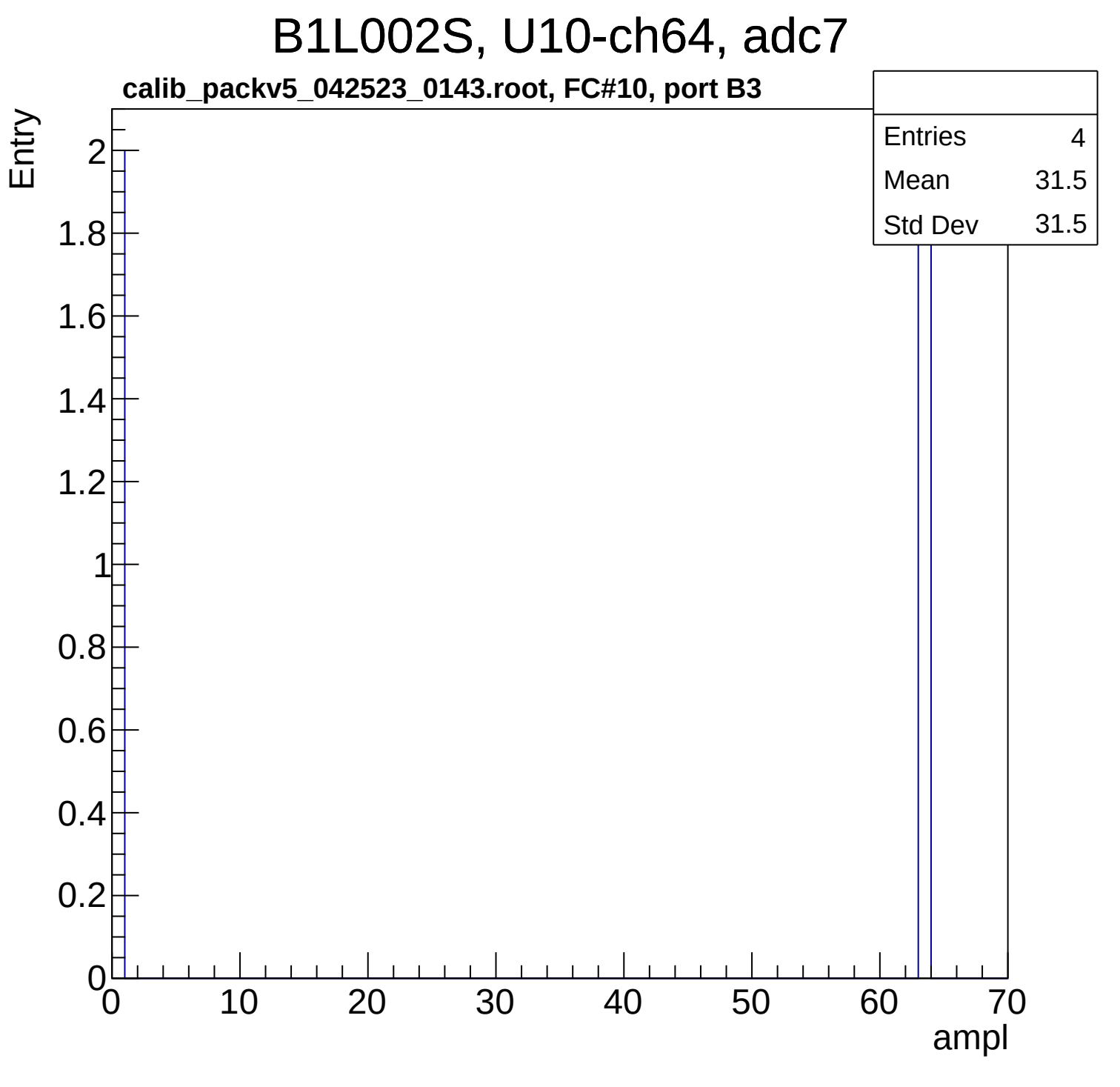
B1L002S, U10-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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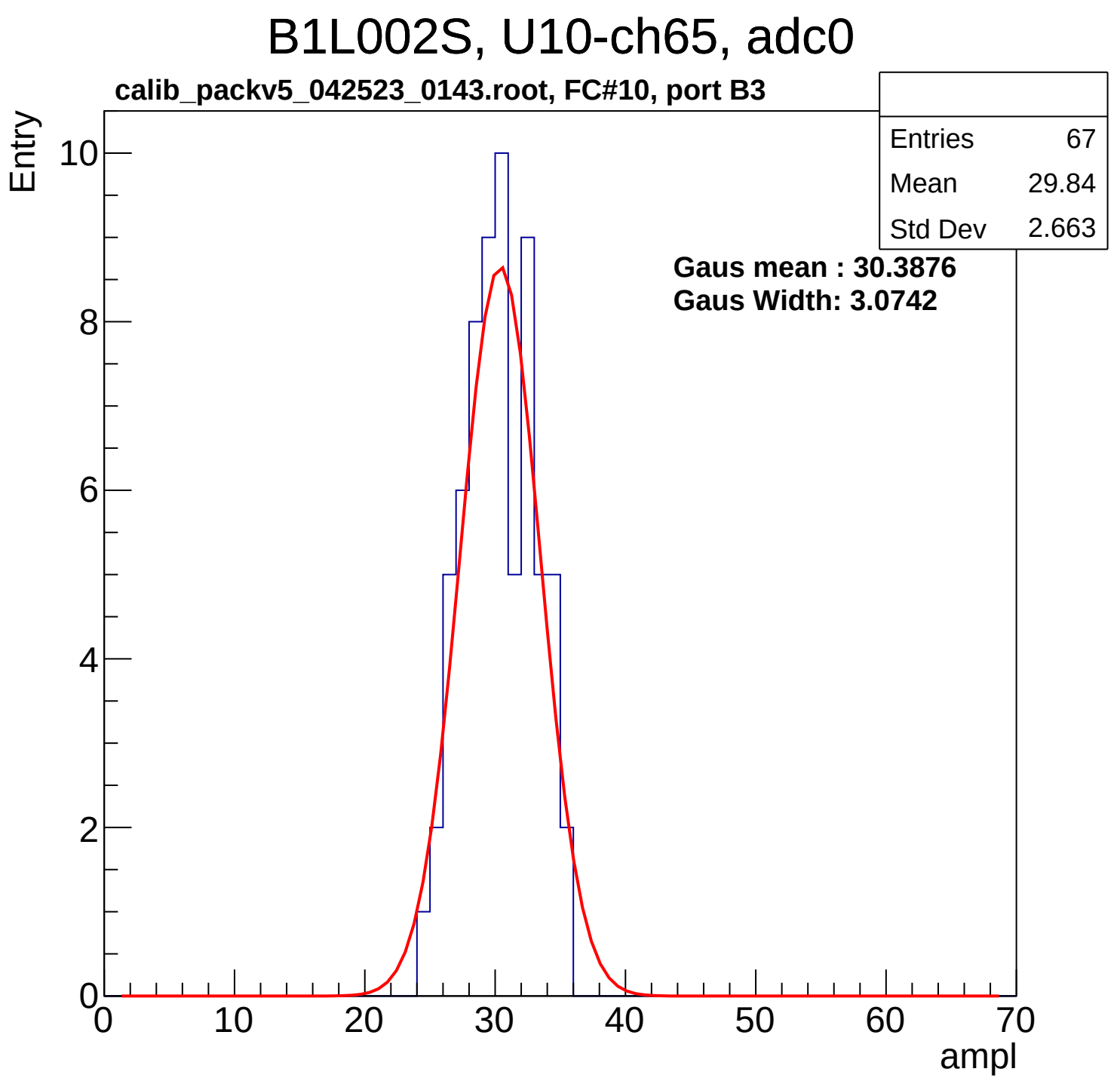
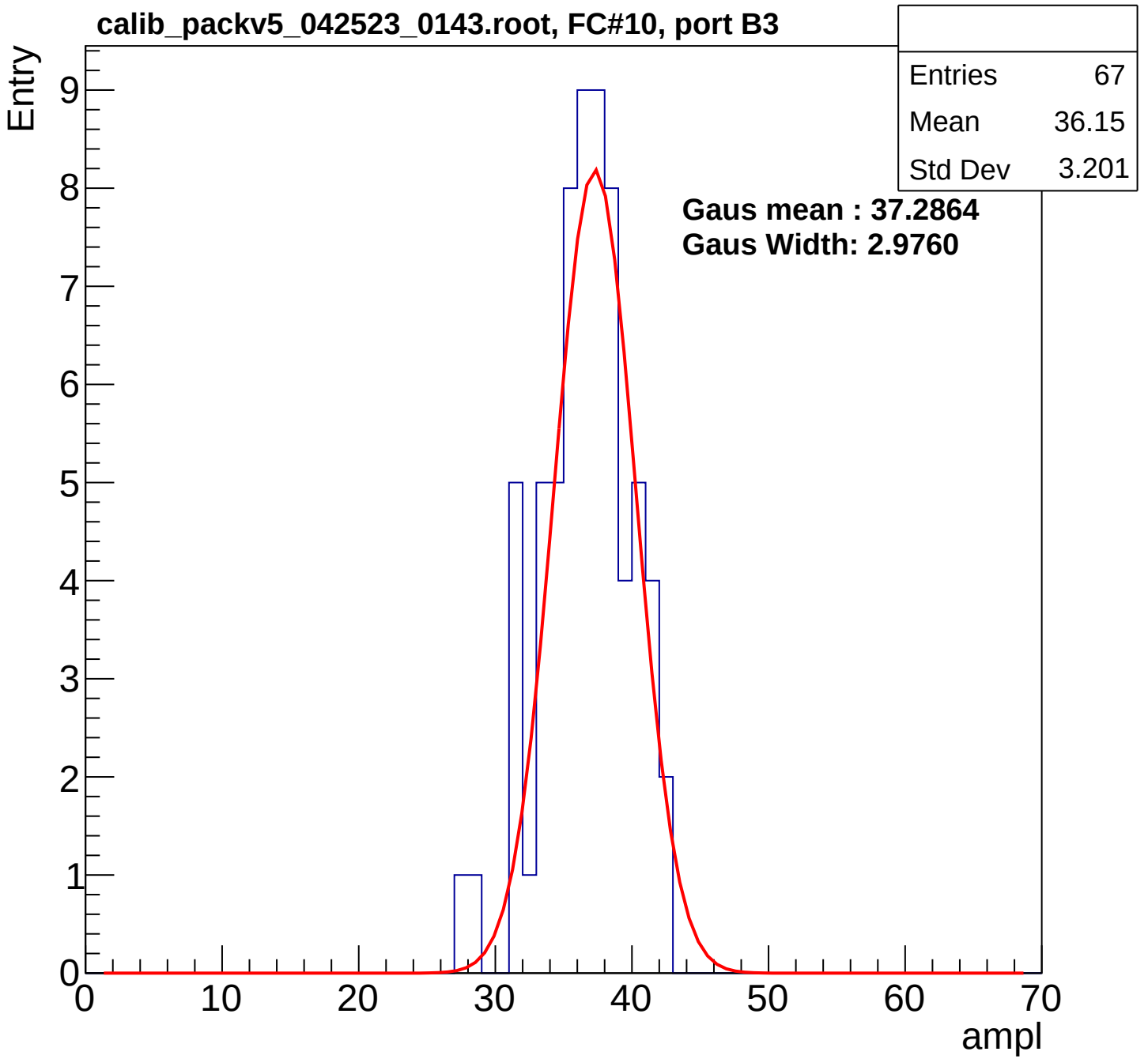

B1L002S, U10-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.84
Std Dev 2.663
Gaus mean : 30.3876
Gaus Width: 3.0742
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.15
Std Dev 3.201
Gaus mean : 37.2864
Gaus Width: 2.9760
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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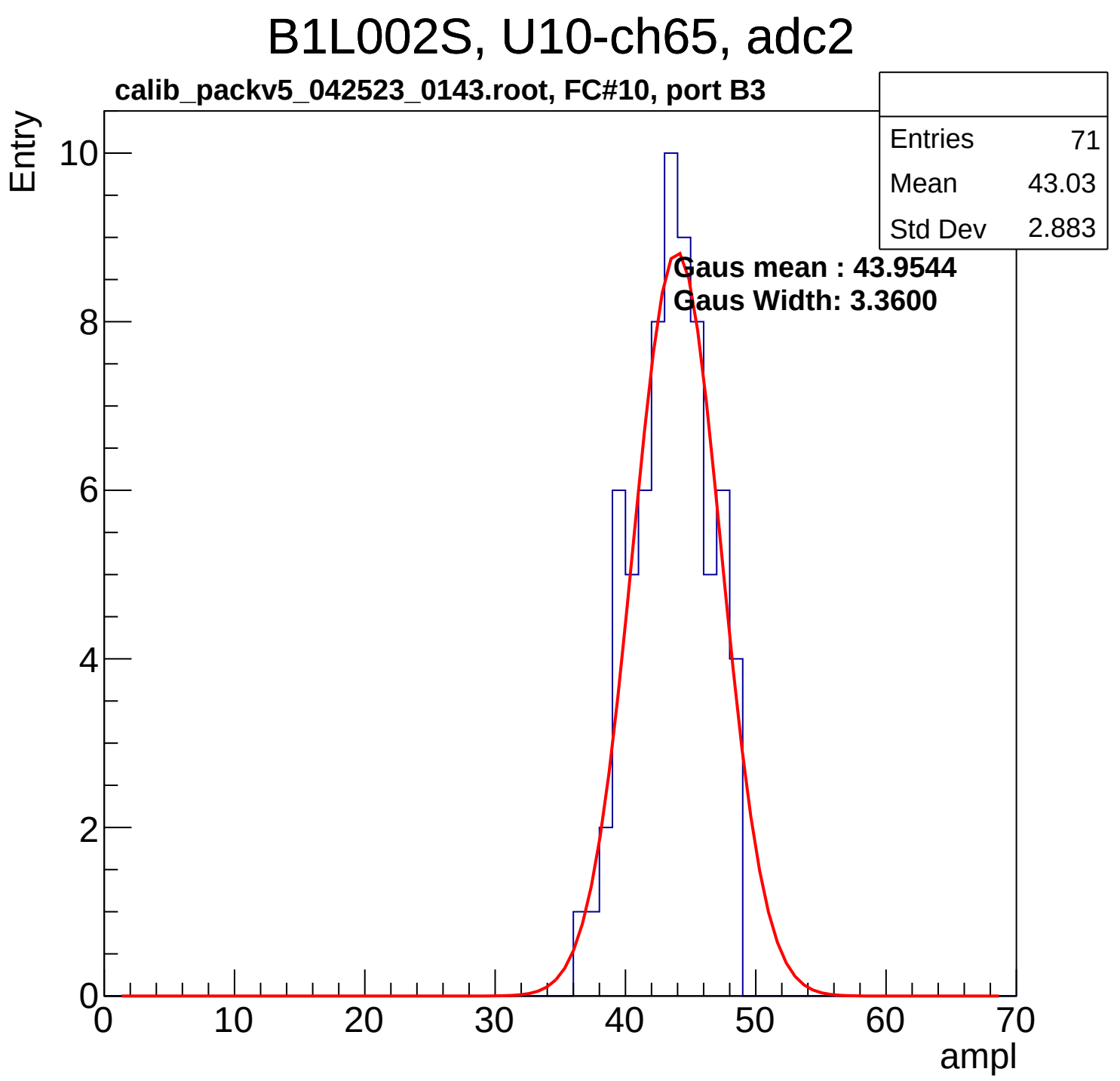
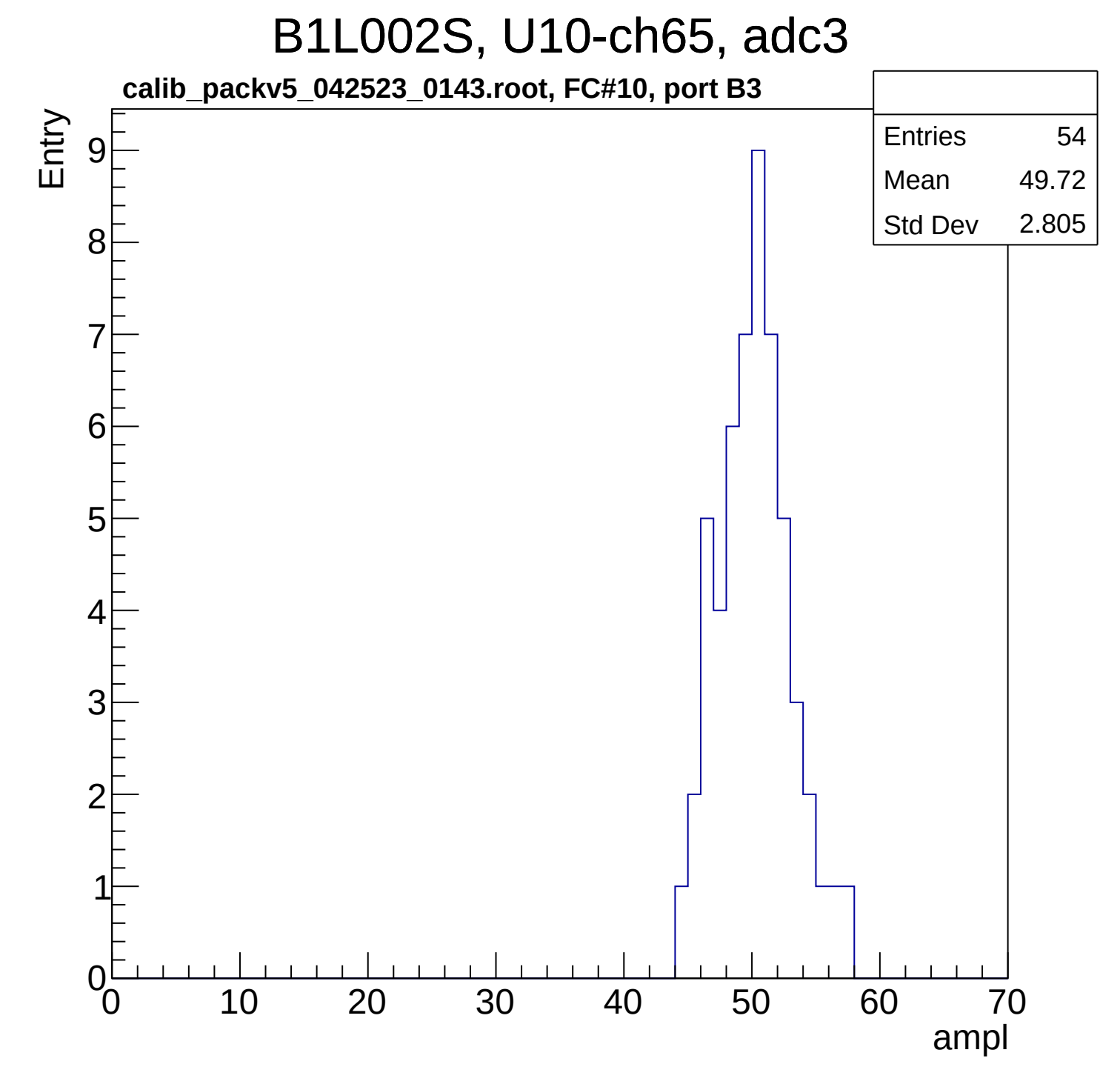

B1L002S, U10-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 43.03
Std Dev 2.883
Gaus mean : 43.9544
Gaus Width: 3.3600
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 49.72
Std Dev 2.805
Entry
ampl
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2
3
4
5
6
7
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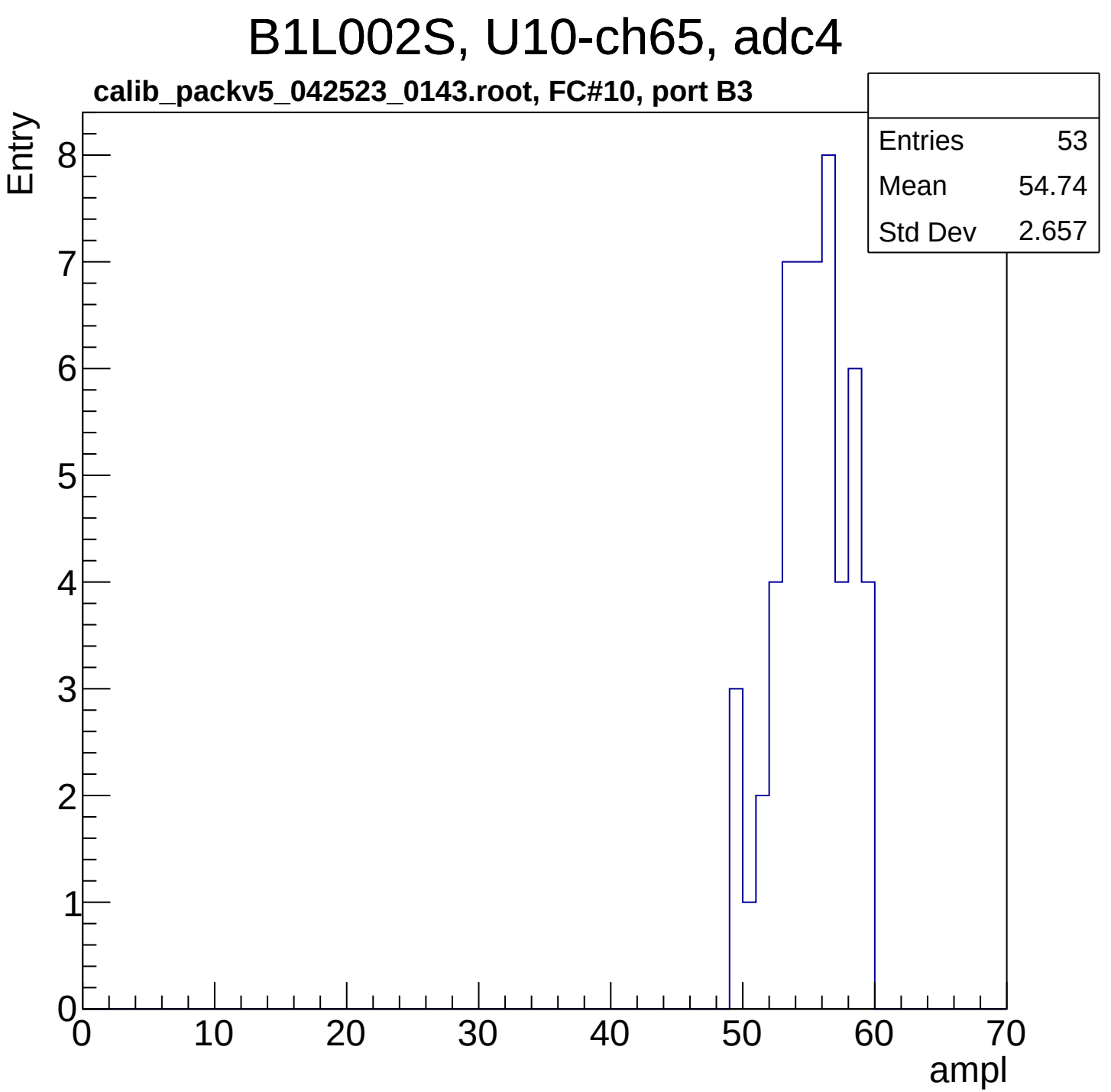
B1L002S, U10-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.74
Std Dev 2.657
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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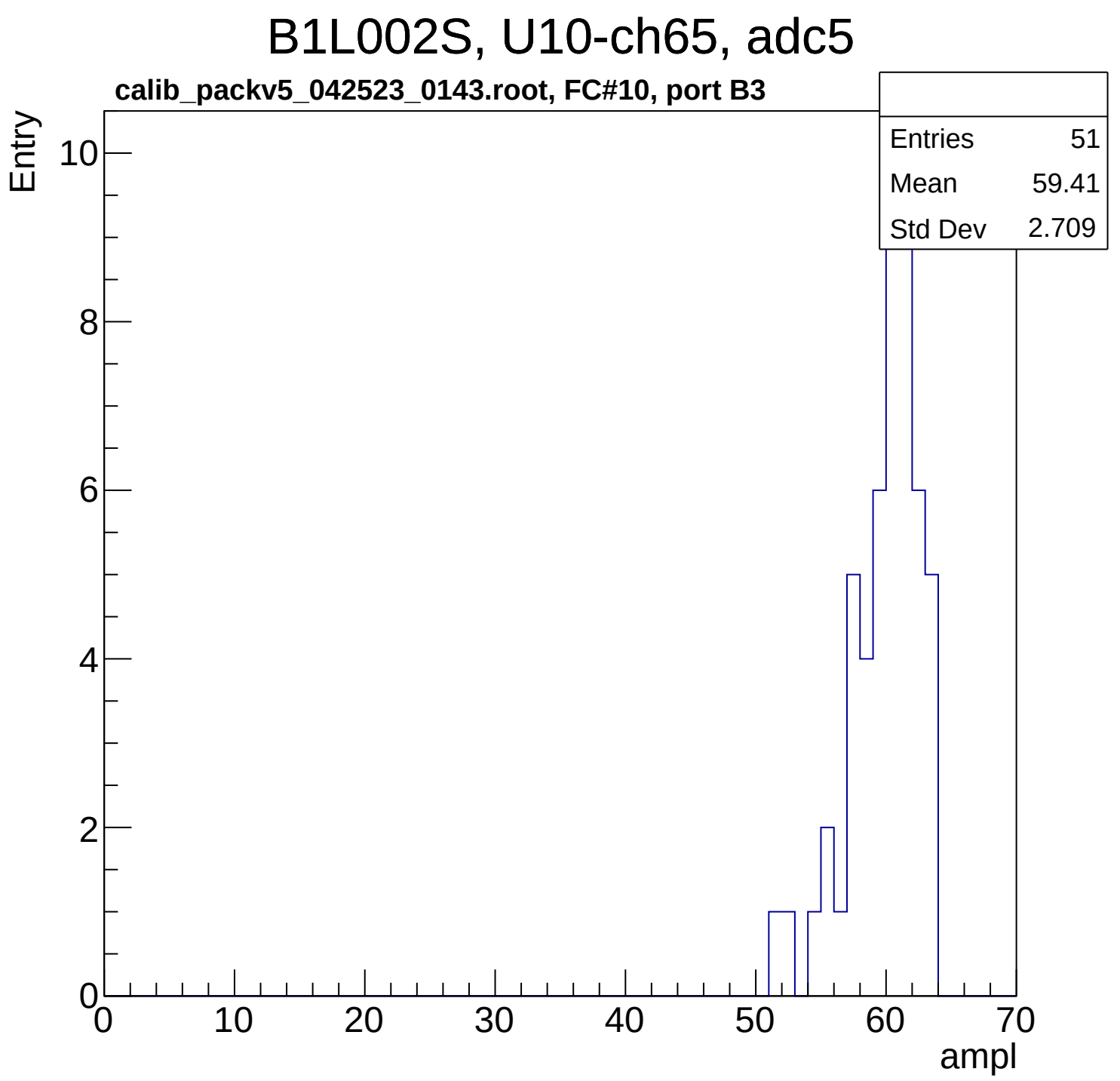
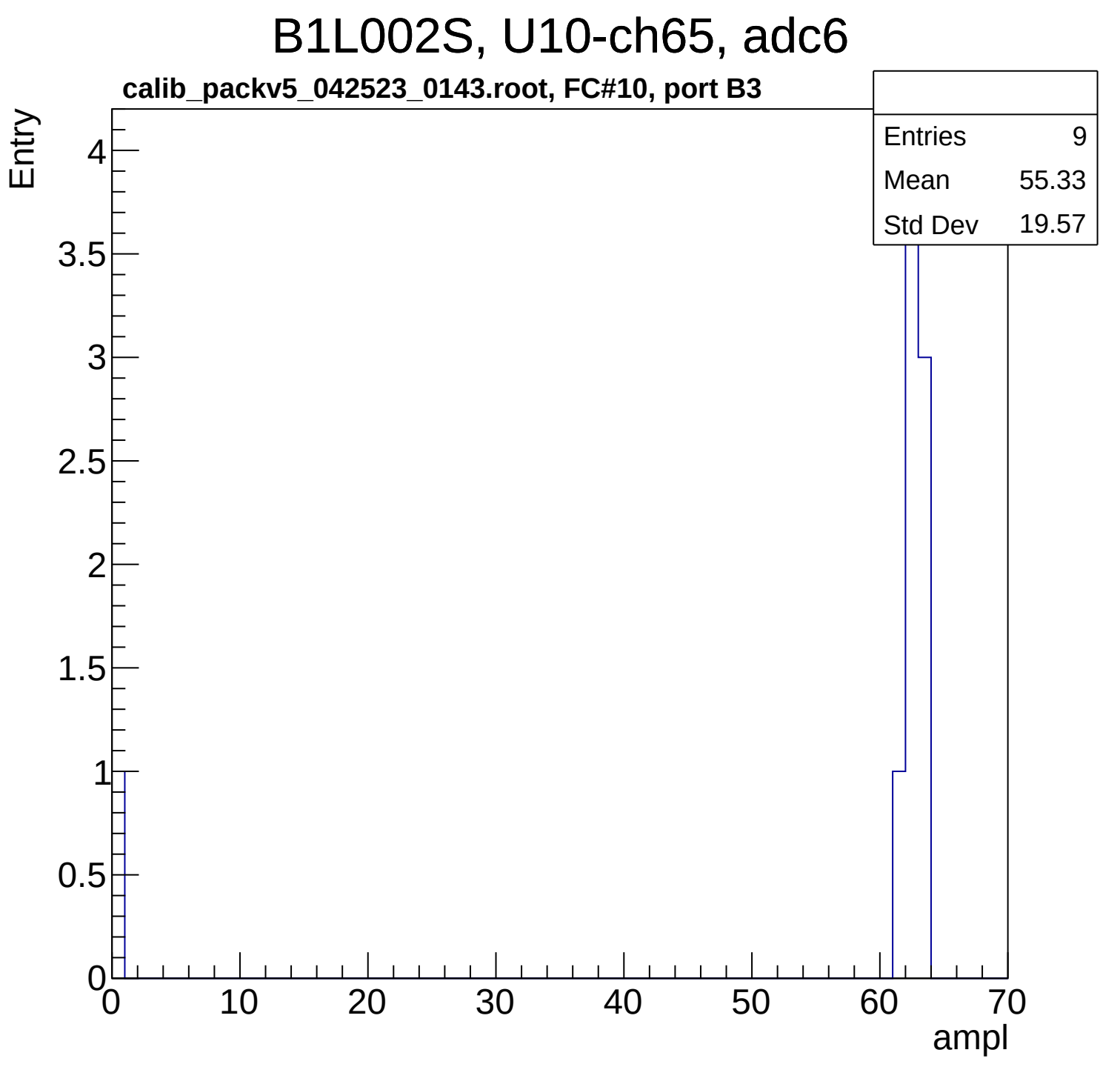

B1L002S, U10-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.41
Std Dev 2.709
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.33
Std Dev 19.57
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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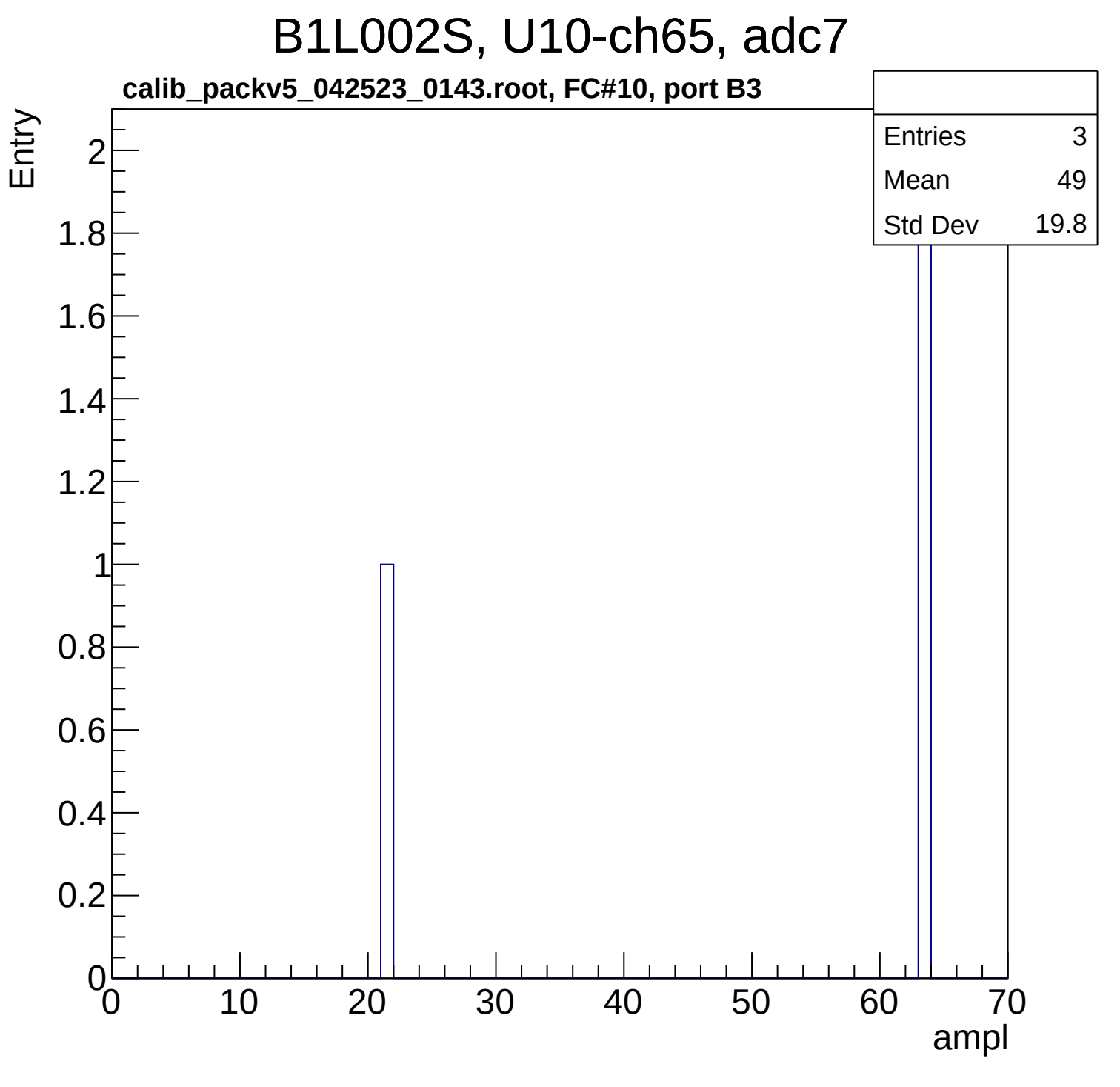
B1L002S, U10-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 49
Std Dev 19.8
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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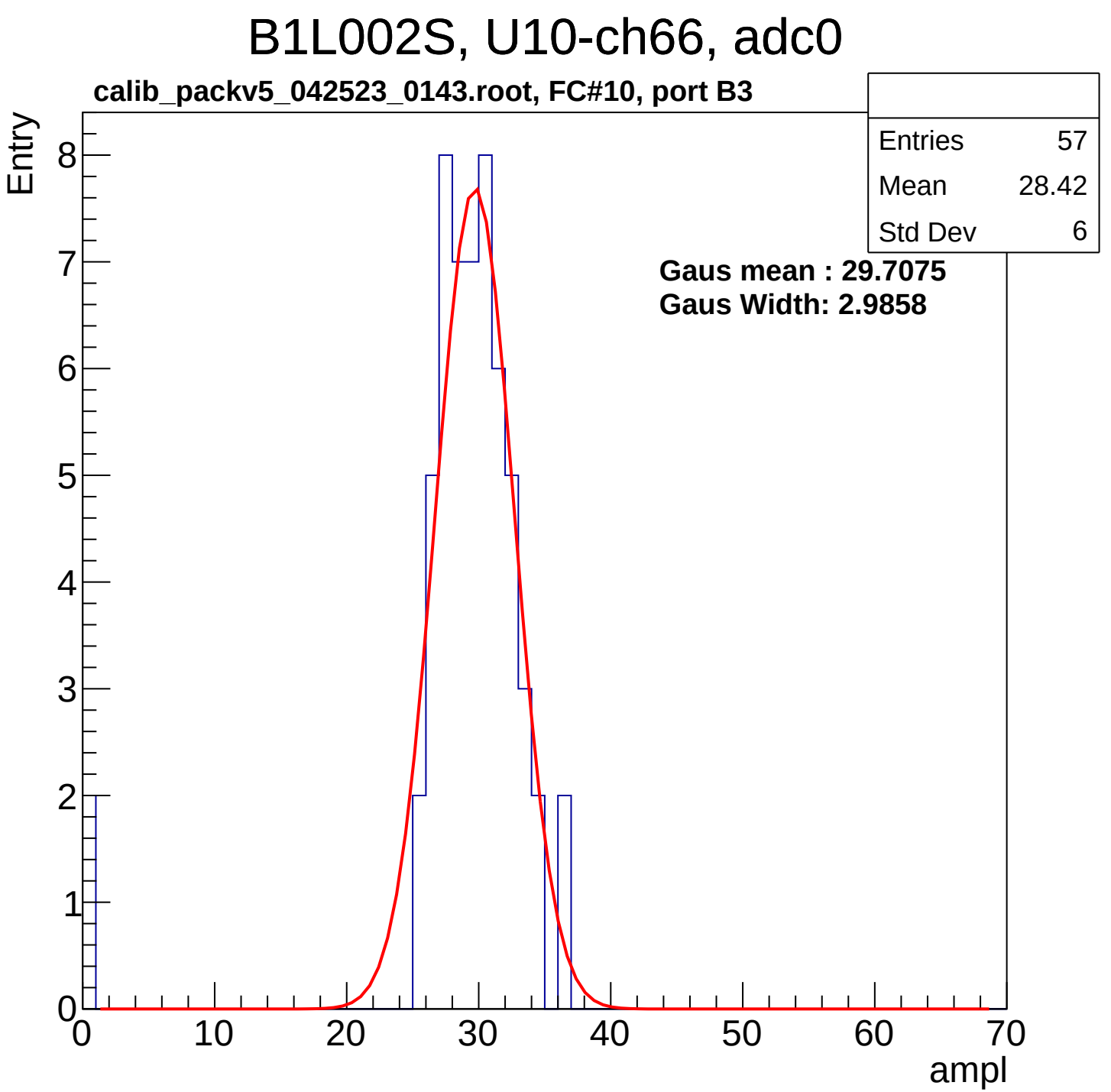
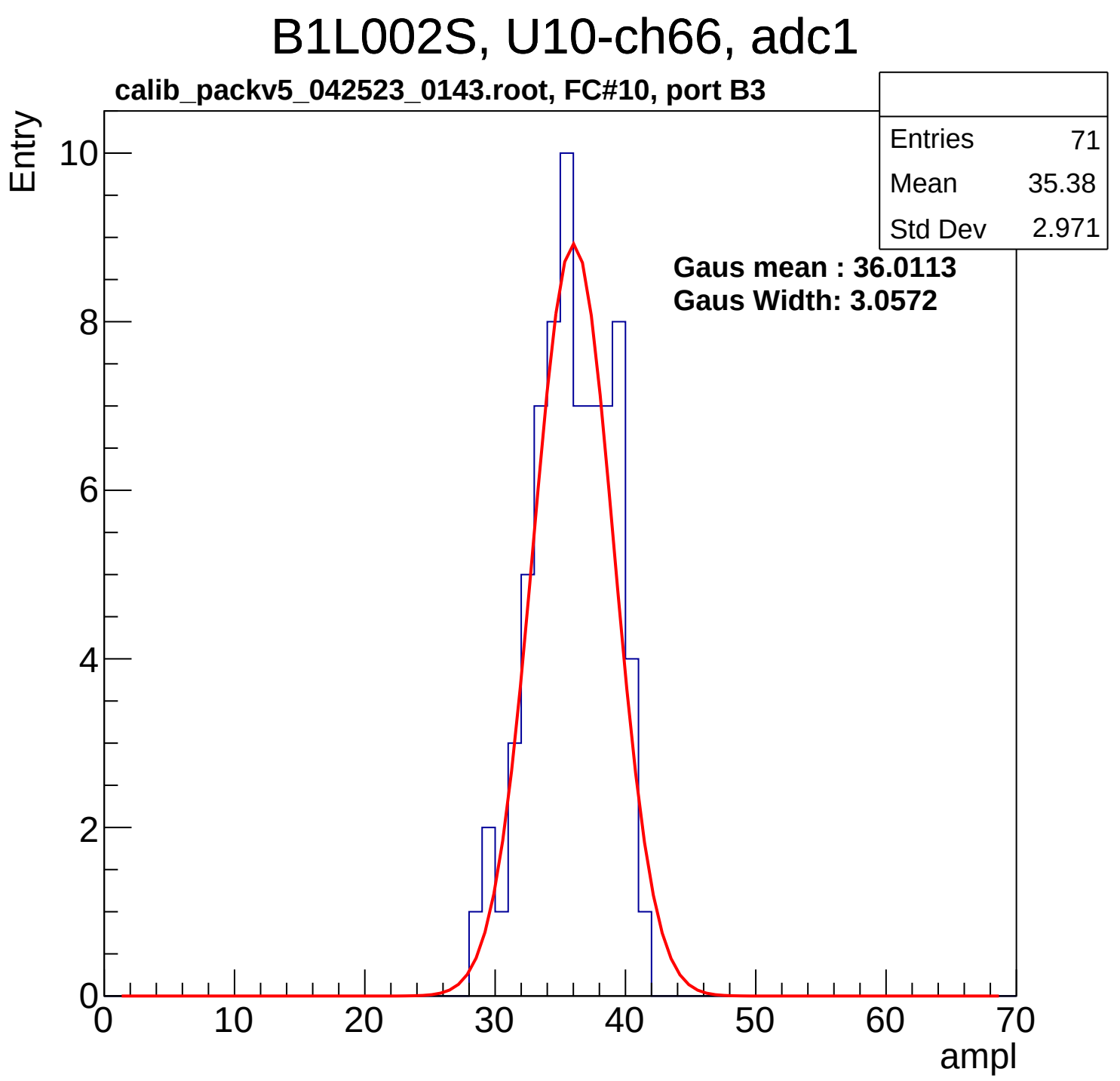
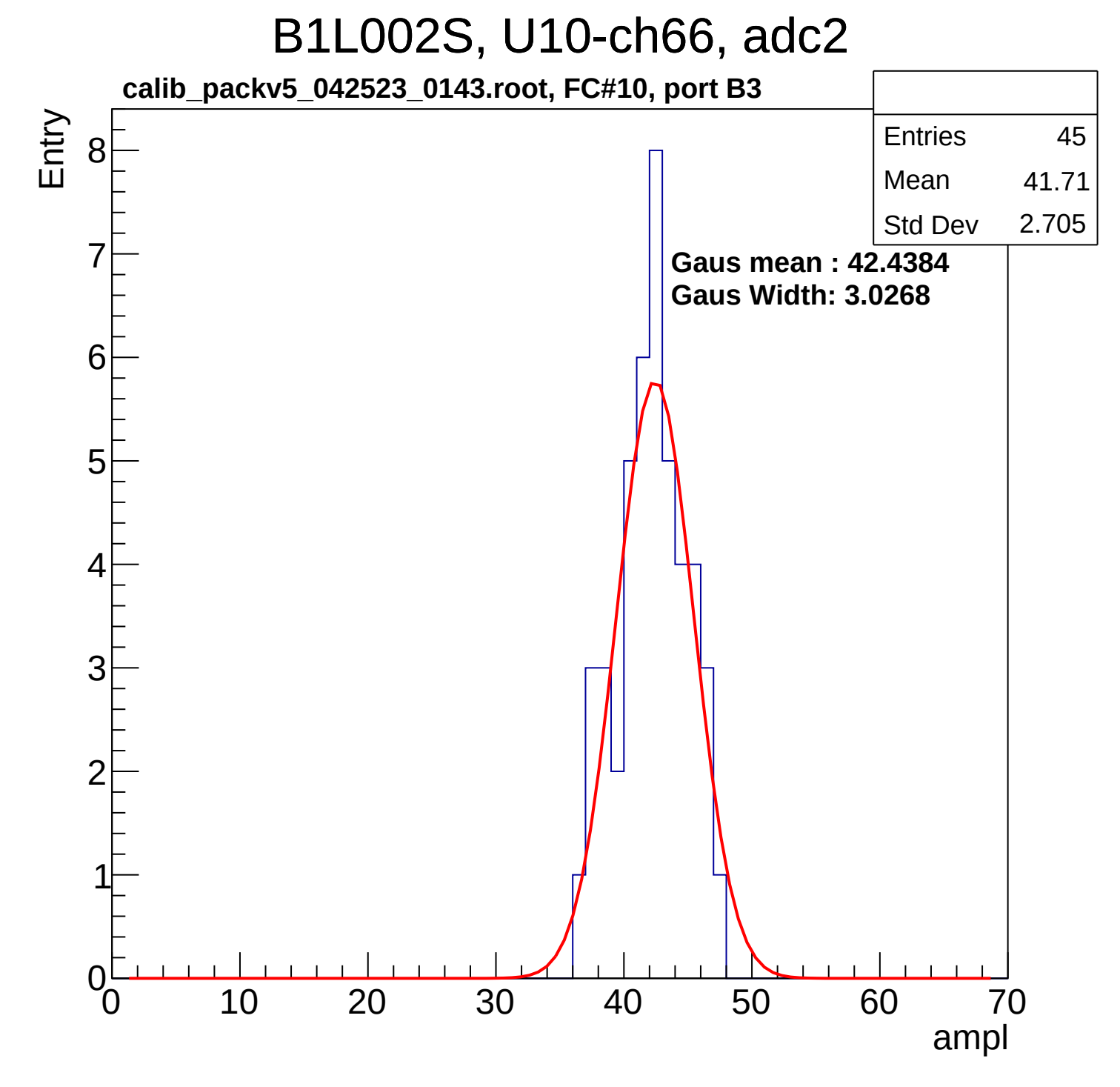
60
70


B1L002S, U10-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.42
Std Dev 6
Gaus mean : 29.7075
Gaus Width: 2.9858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.38
Std Dev 2.971
Gaus mean : 36.0113
Gaus Width: 3.0572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 41.71
Std Dev 2.705
Gaus mean : 42.4384
Gaus Width: 3.0268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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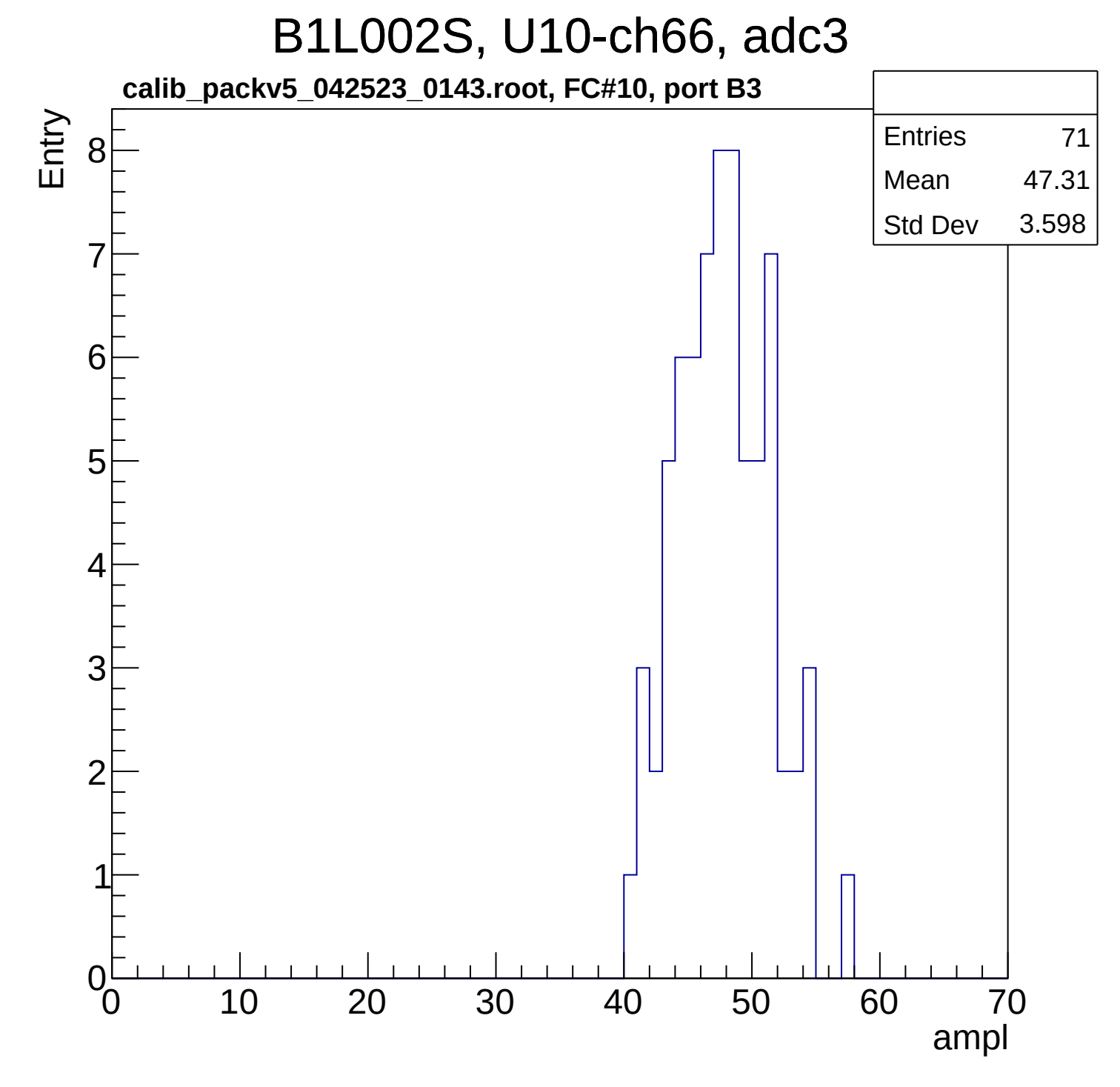
B1L002S, U10-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 47.31
Std Dev 3.598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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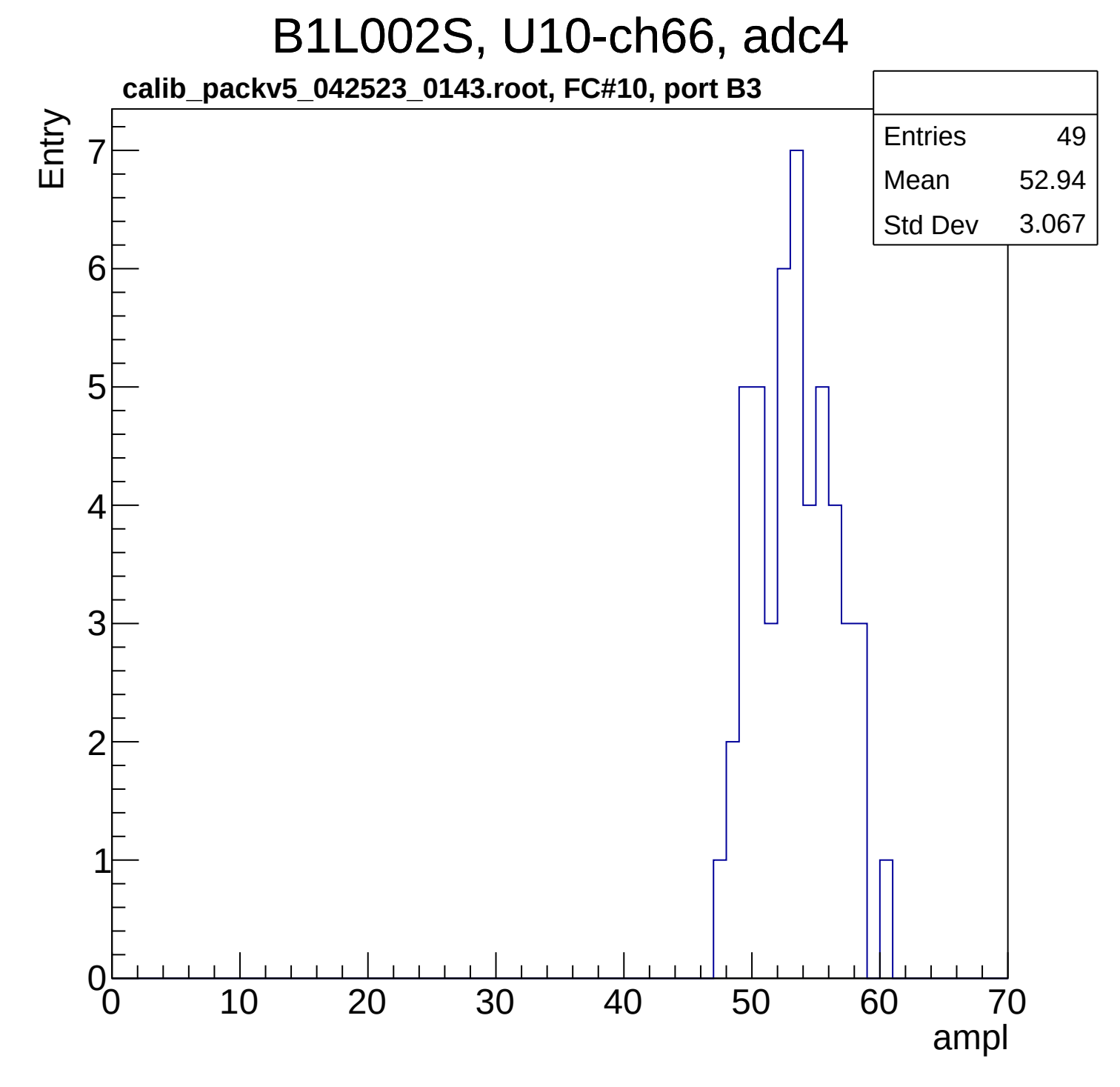
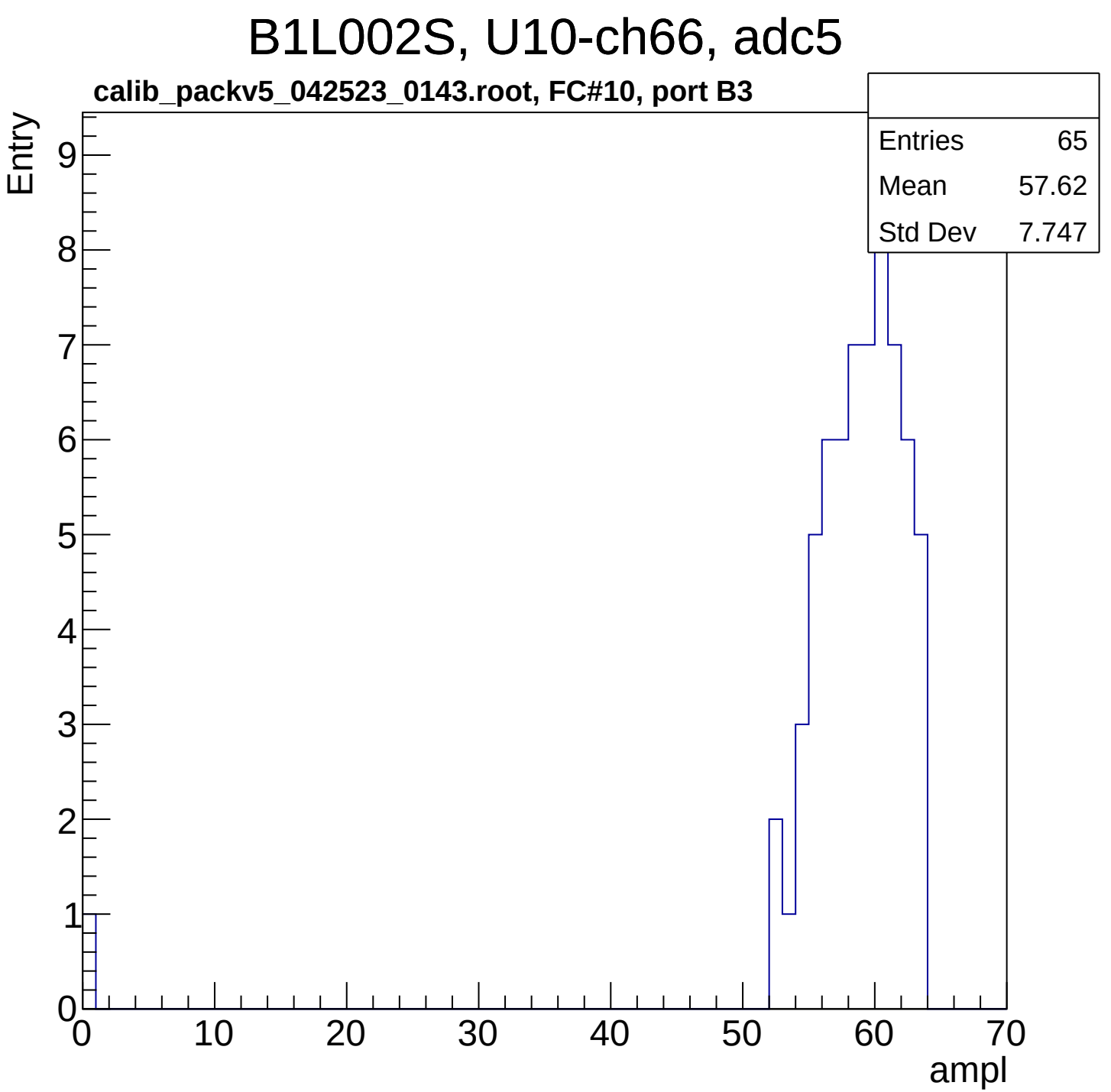

B1L002S, U10-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.94
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.62
Std Dev 7.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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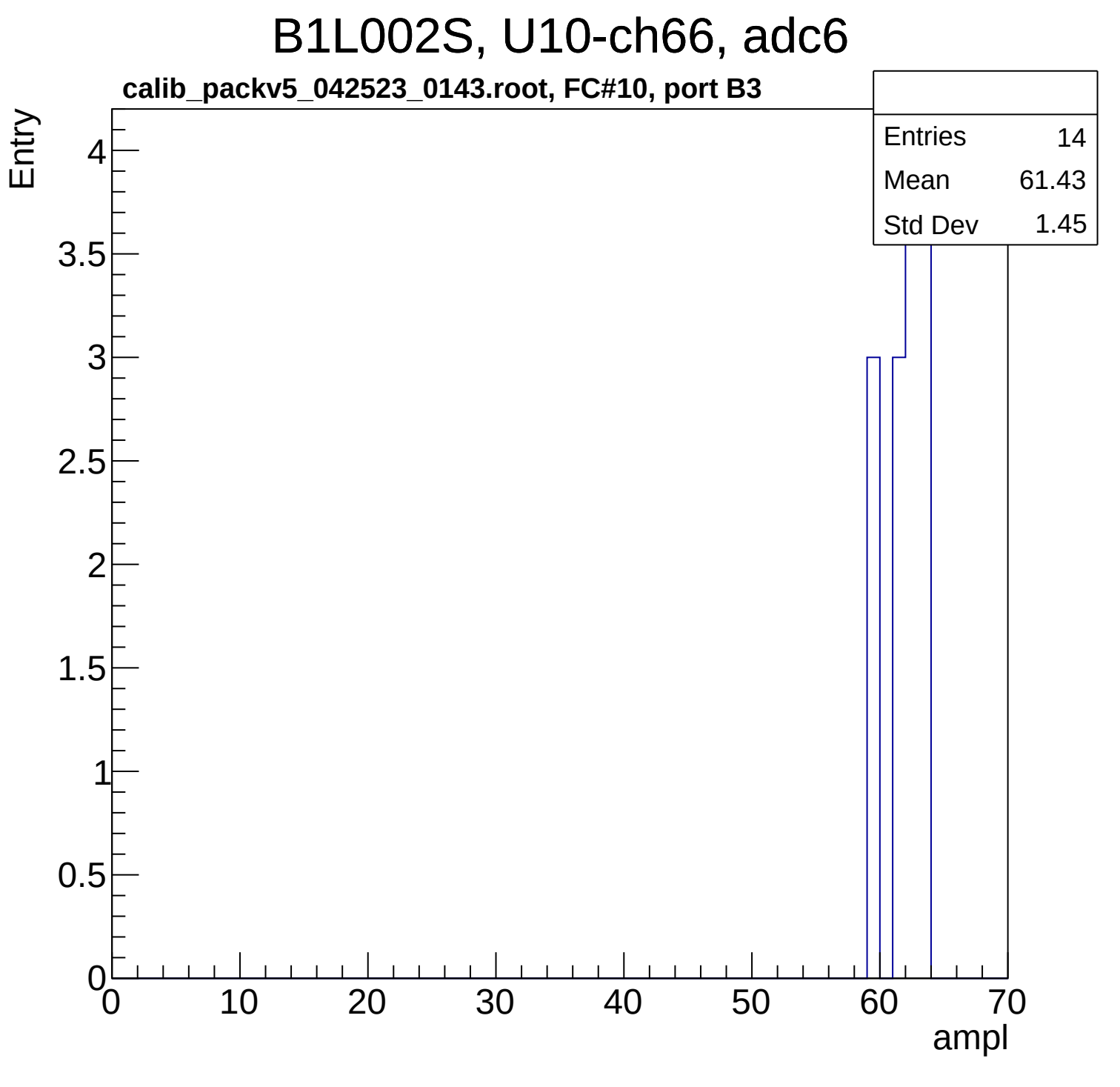
40
50
60
70


B1L002S, U10-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.43
Std Dev 1.45
Entry
ampl
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0.5
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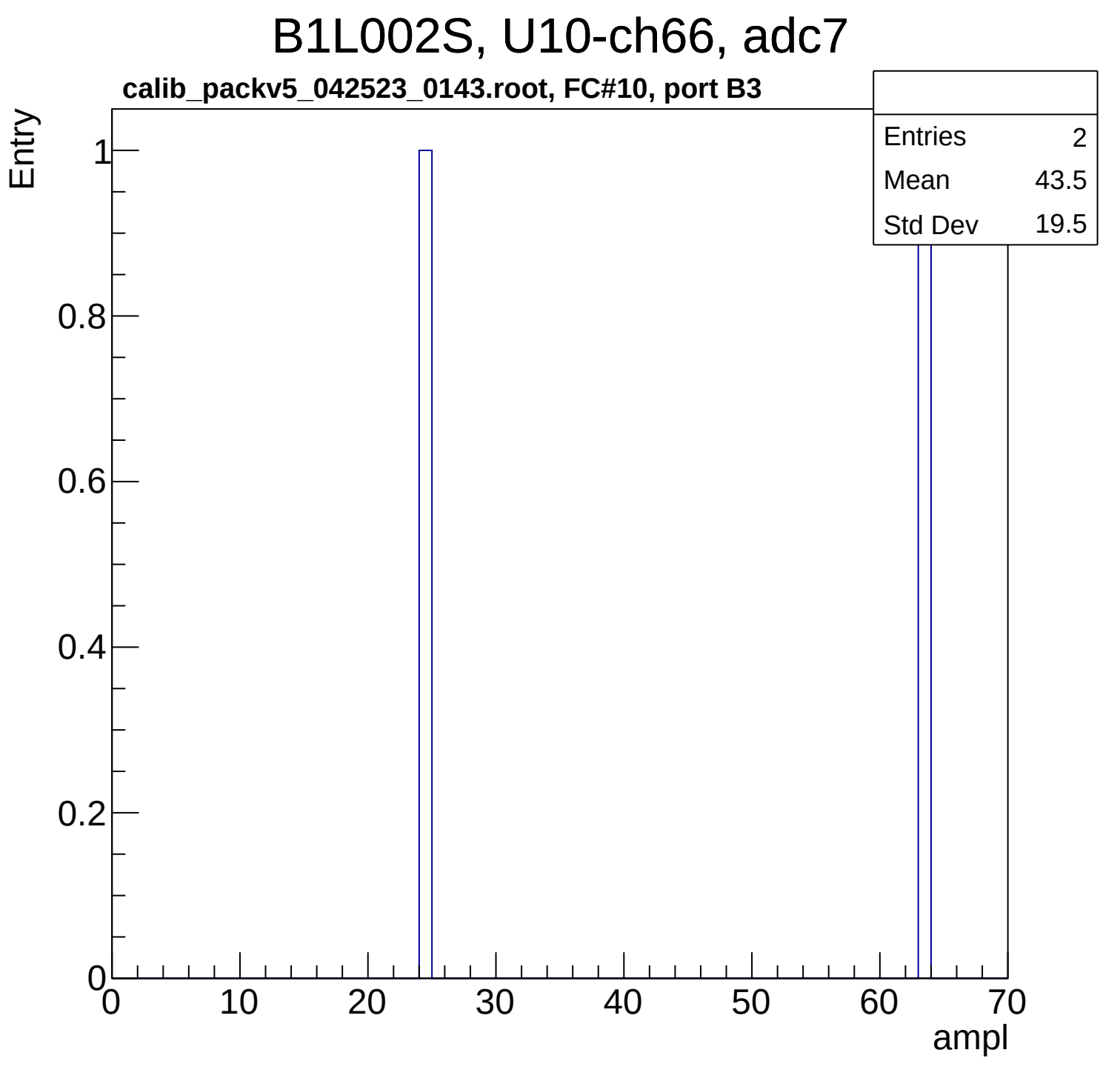
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 43.5
Std Dev 19.5
Entry
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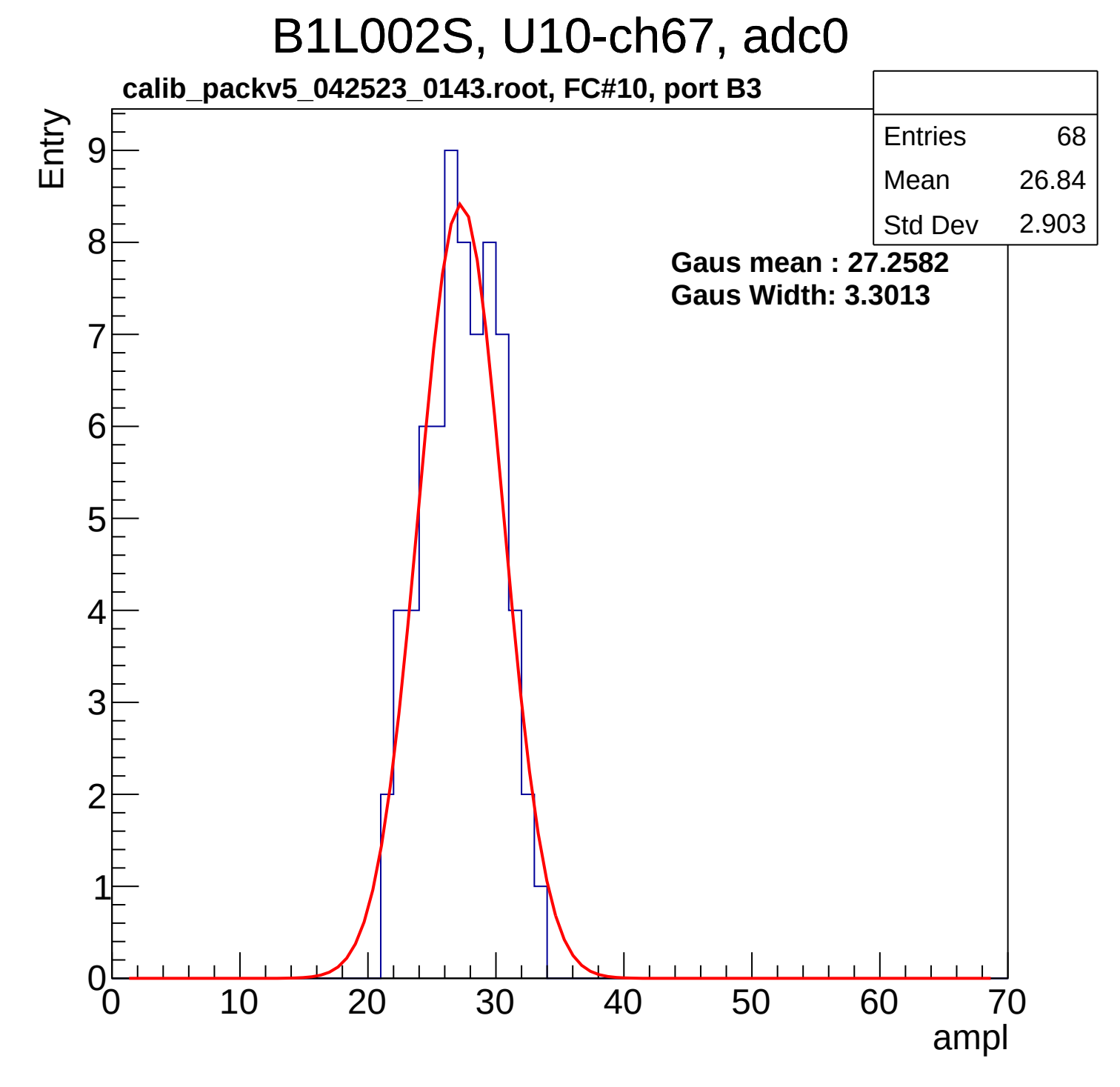
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 26.84
Std Dev 2.903
Gaus mean : 27.2582
Gaus Width: 3.3013
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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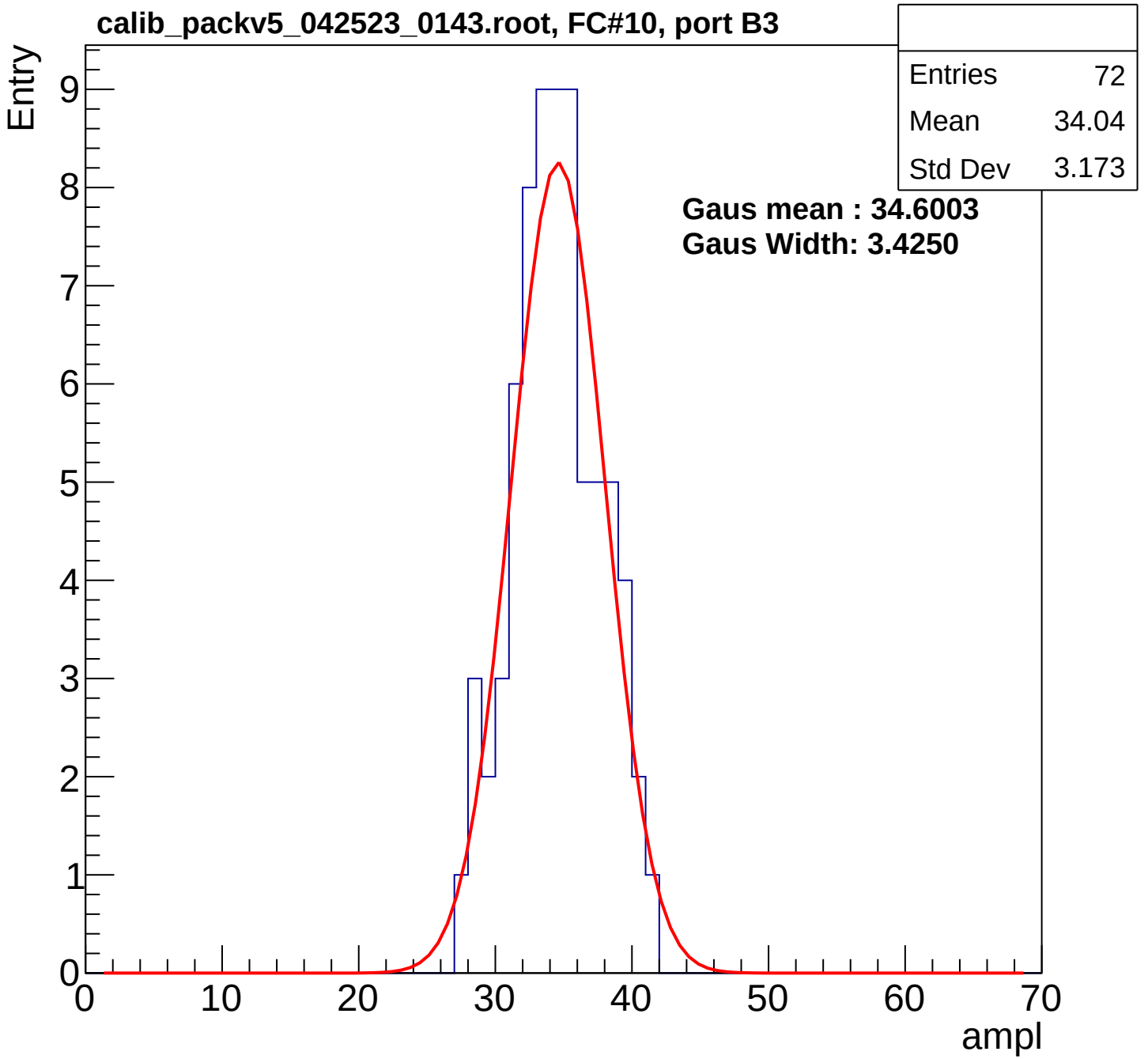
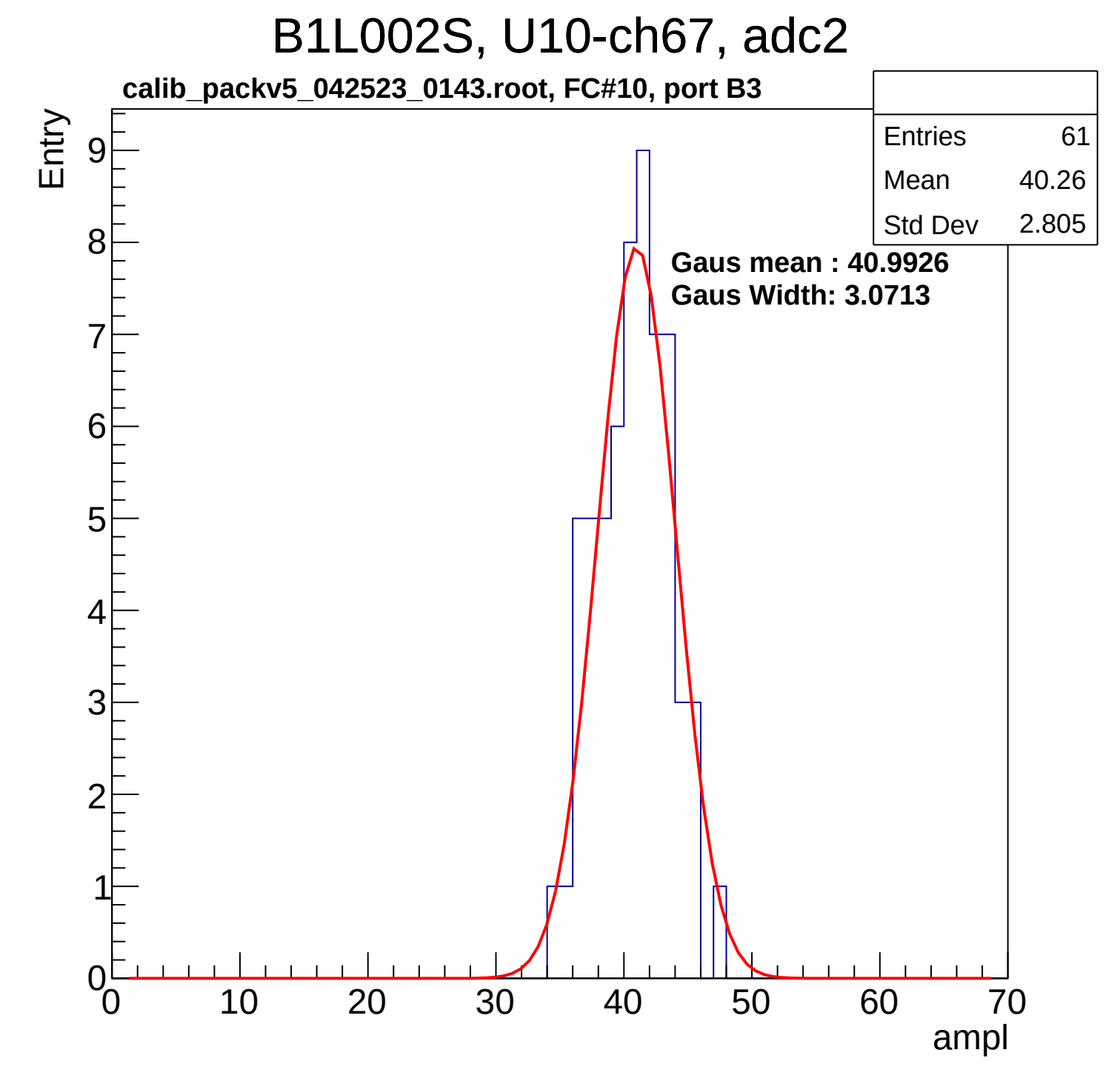

B1L002S, U10-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.04
Std Dev 3.173
Gaus mean : 34.6003
Gaus Width: 3.4250
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.26
Std Dev 2.805
Gaus mean : 40.9926
Gaus Width: 3.0713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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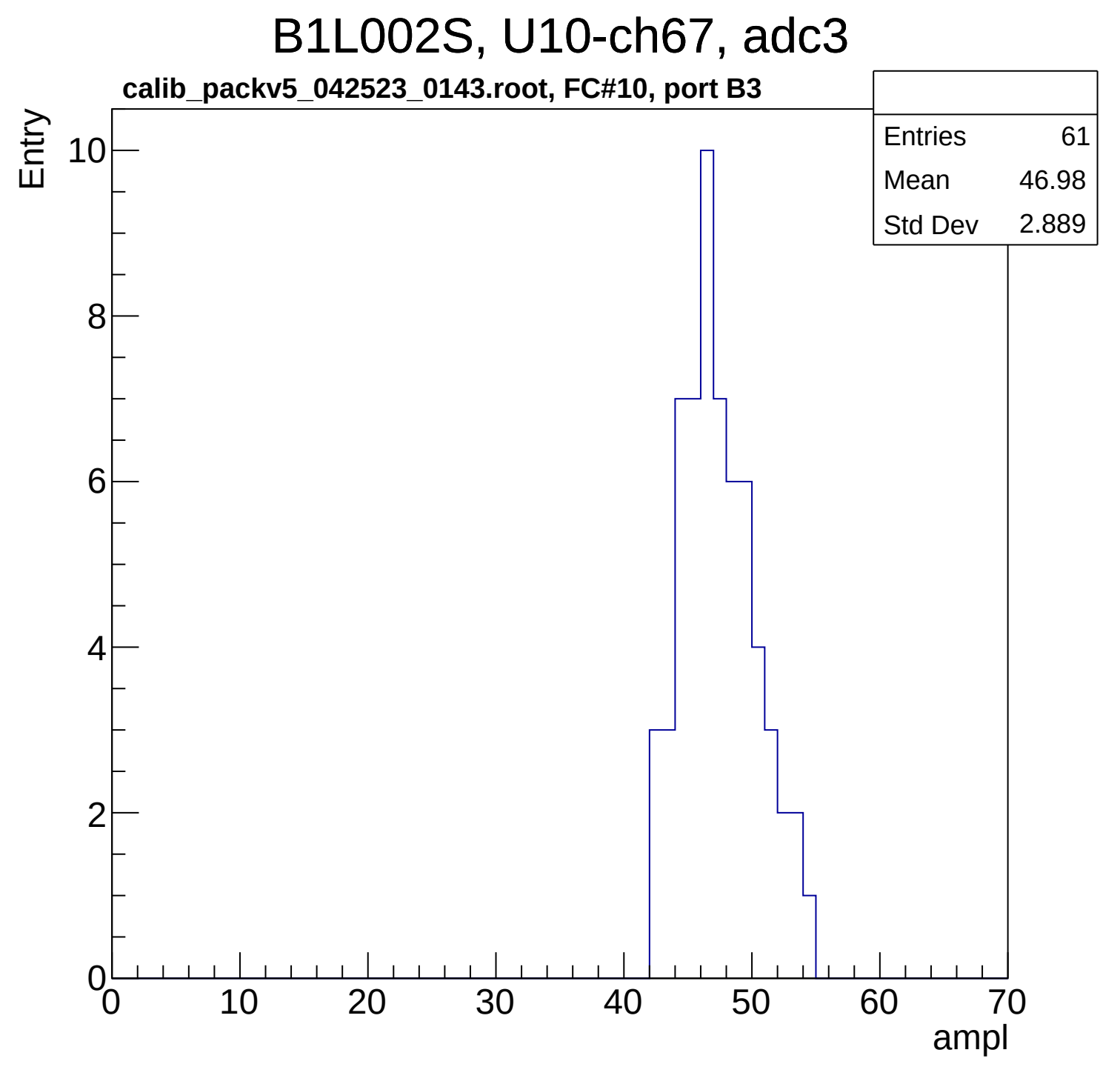
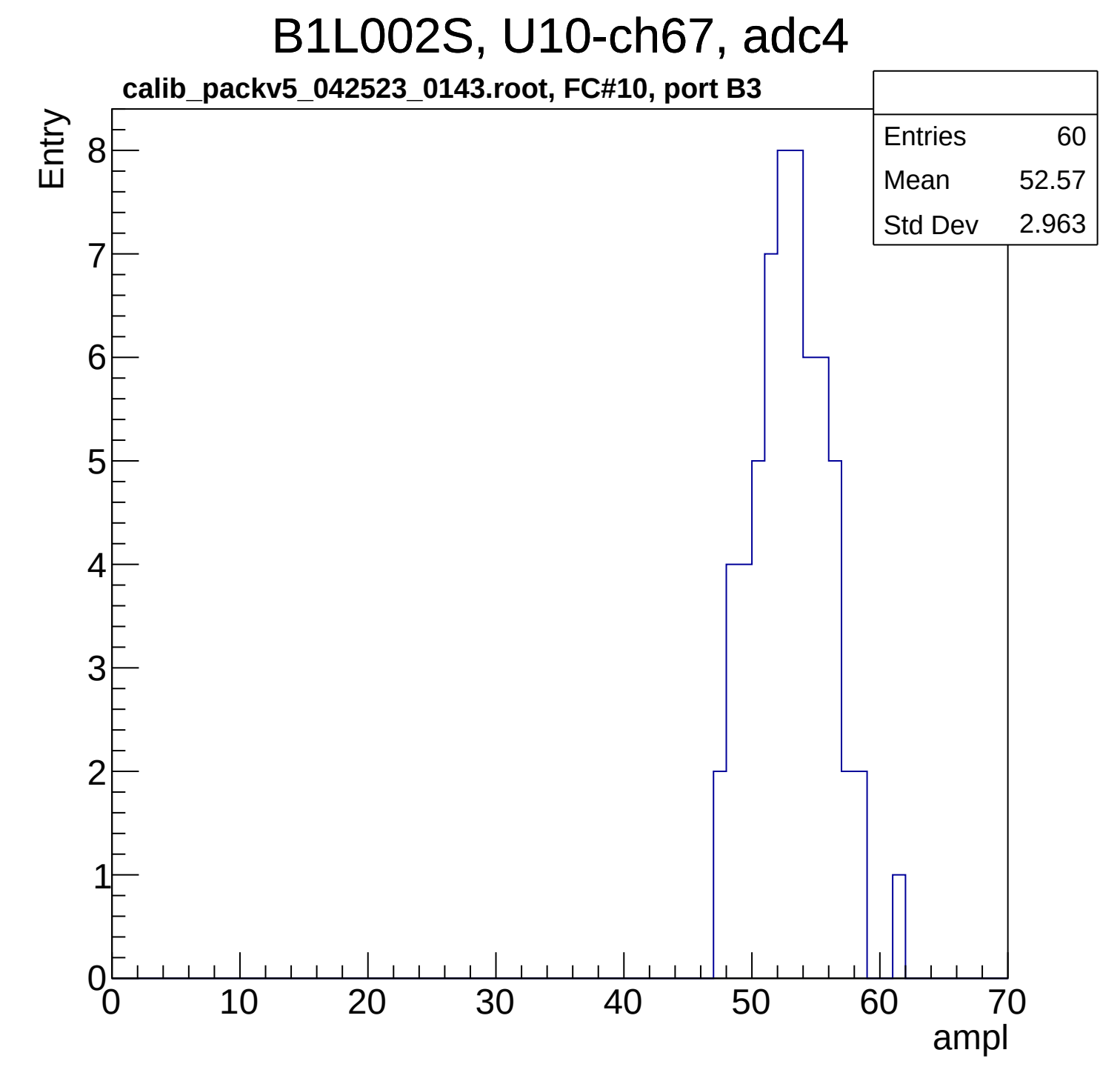

B1L002S, U10-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.98
Std Dev 2.889
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.57
Std Dev 2.963
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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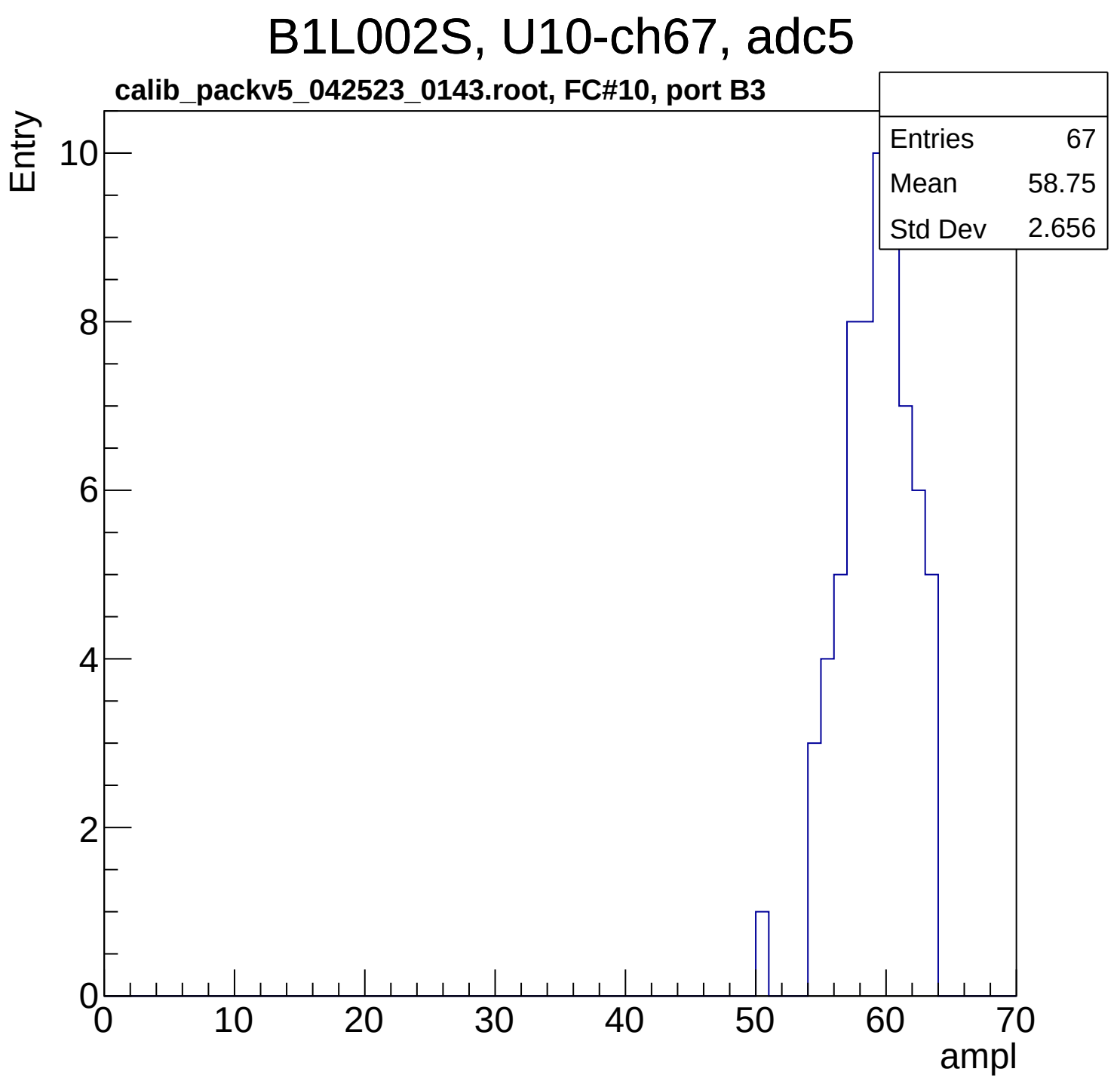
B1L002S, U10-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.75
Std Dev 2.656
Entry
ampl
0
2
4
6
8
10
0
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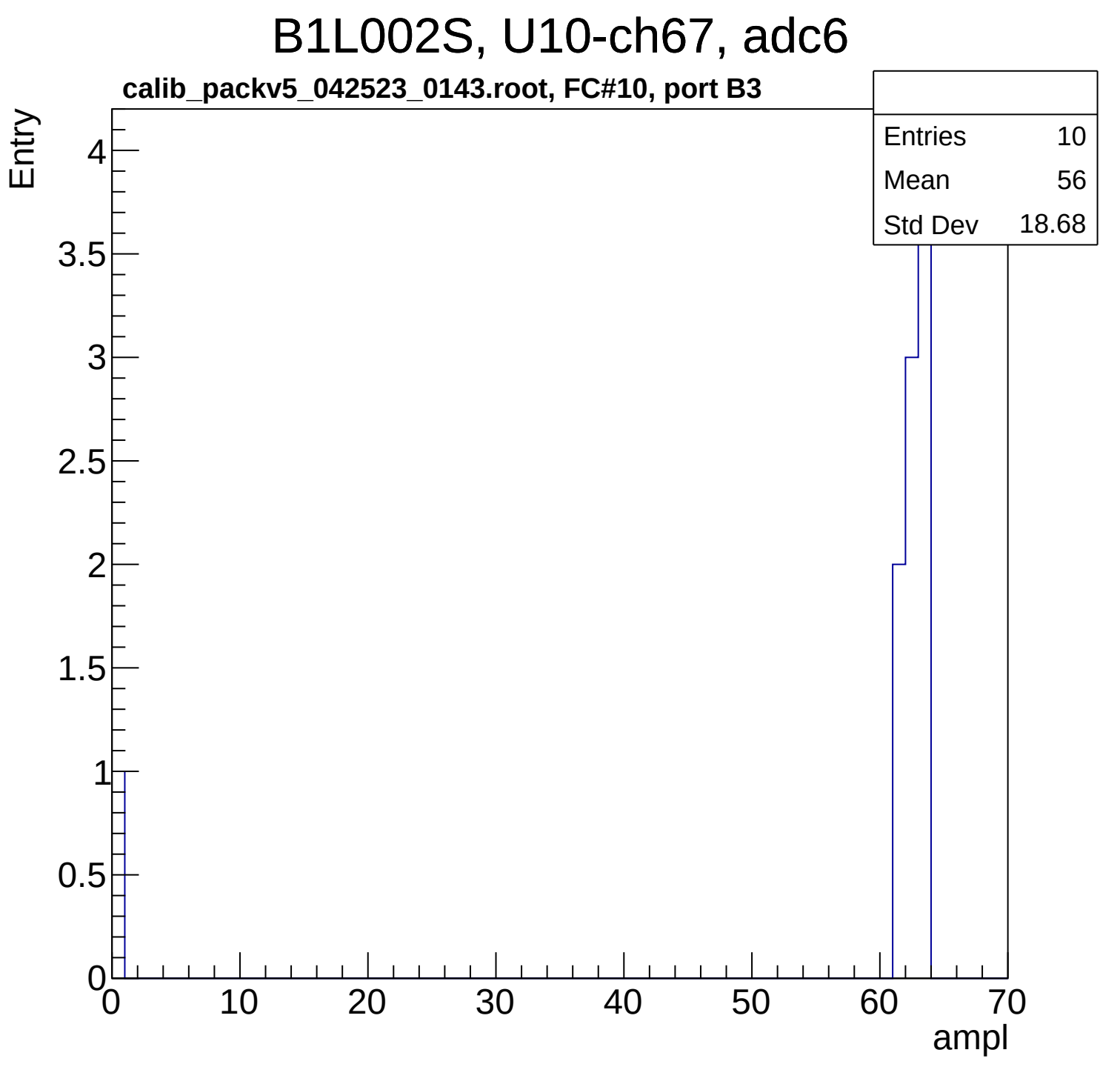
30
40
50
60
70


B1L002S, U10-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 56
Std Dev 18.68
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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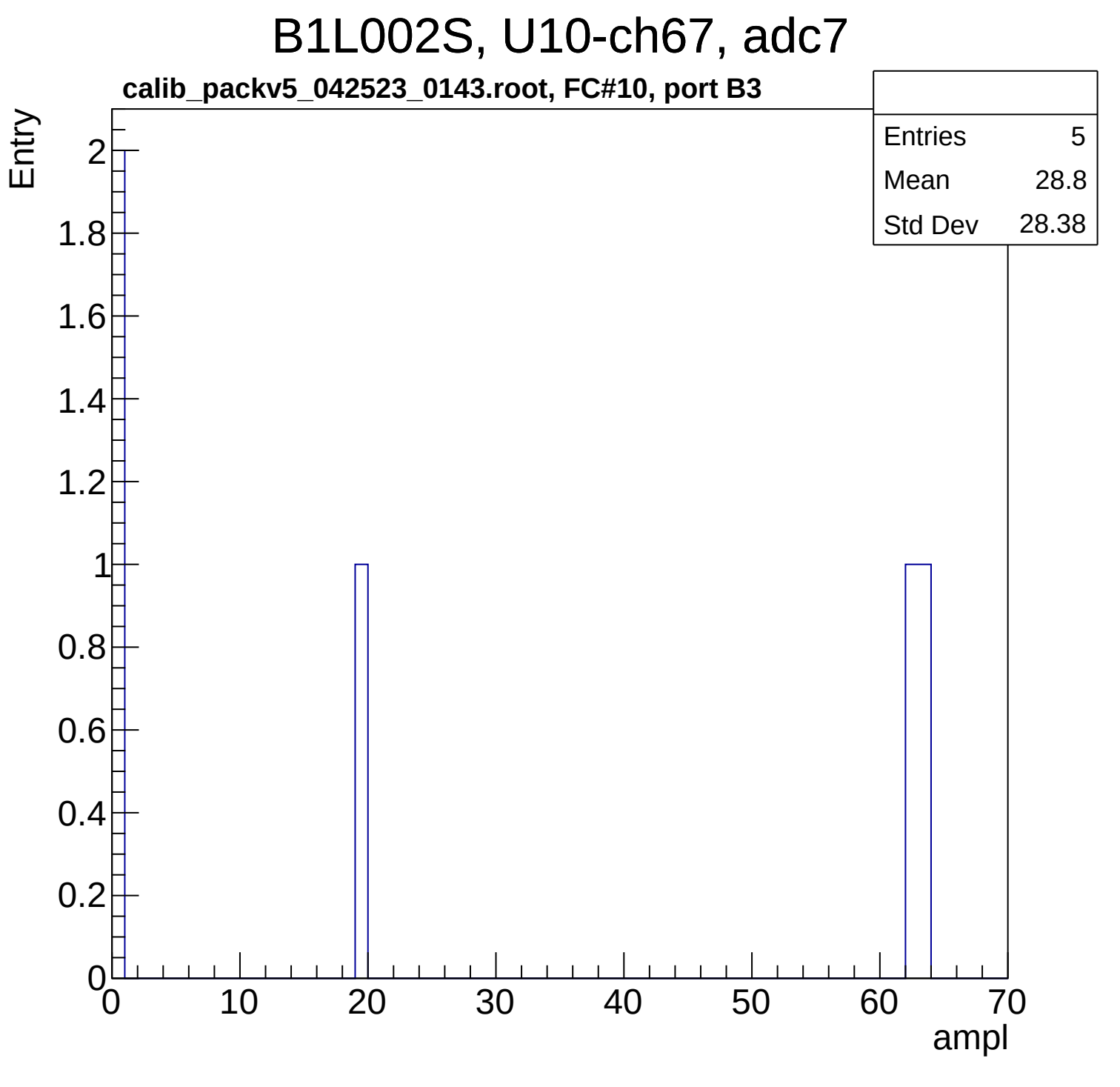
B1L002S, U10-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 28.8
Std Dev 28.38
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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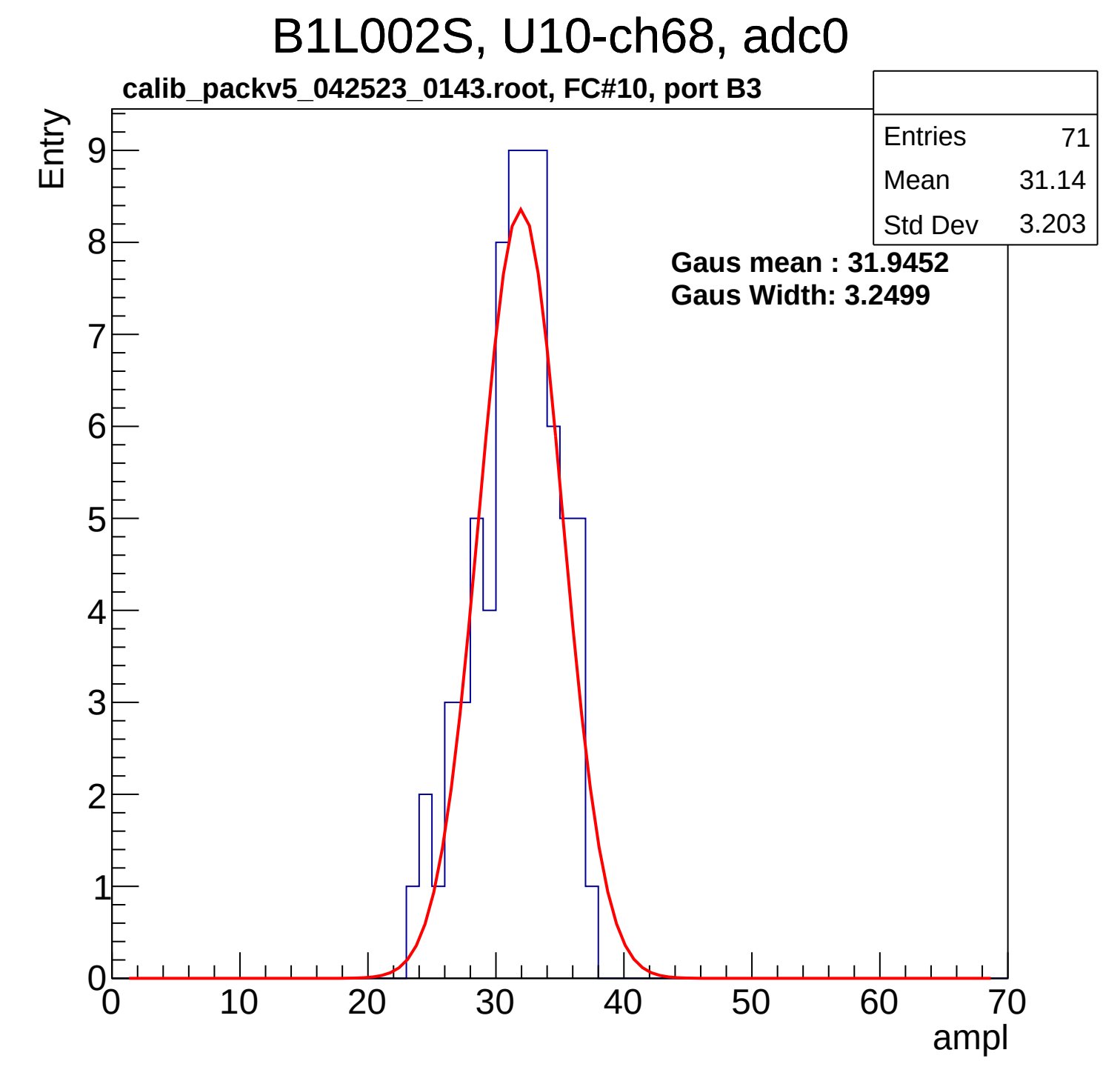
B1L002S, U10-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 31.14
Std Dev 3.203
Gaus mean : 31.9452
Gaus Width: 3.2499
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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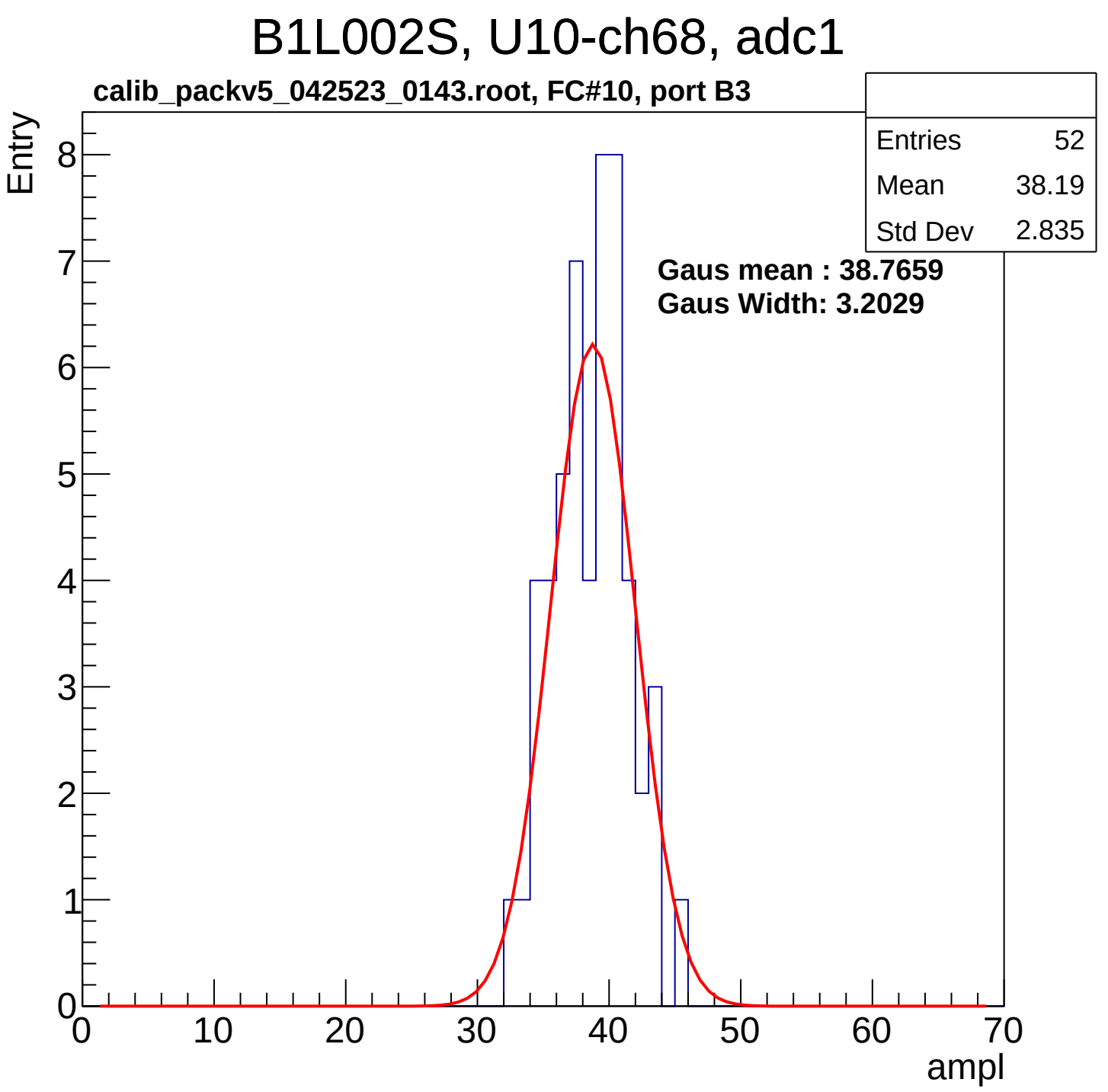
B1L002S, U10-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 38.19
Std Dev 2.835
Gaus mean : 38.7659
Gaus Width: 3.2029
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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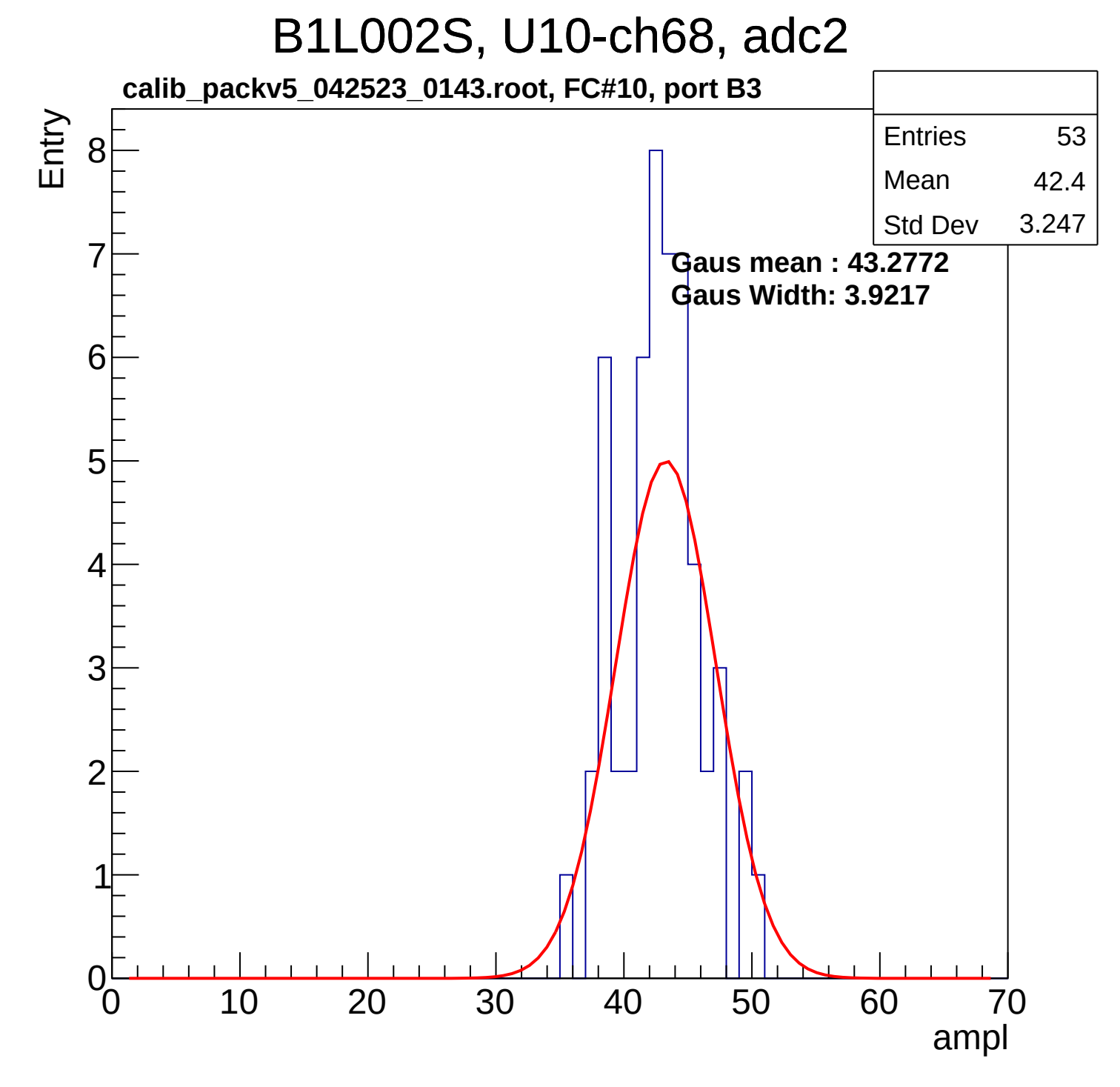
B1L002S, U10-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 42.4
Std Dev 3.247
Gaus mean : 43.2772
Gaus Width: 3.9217
Entry
ampl
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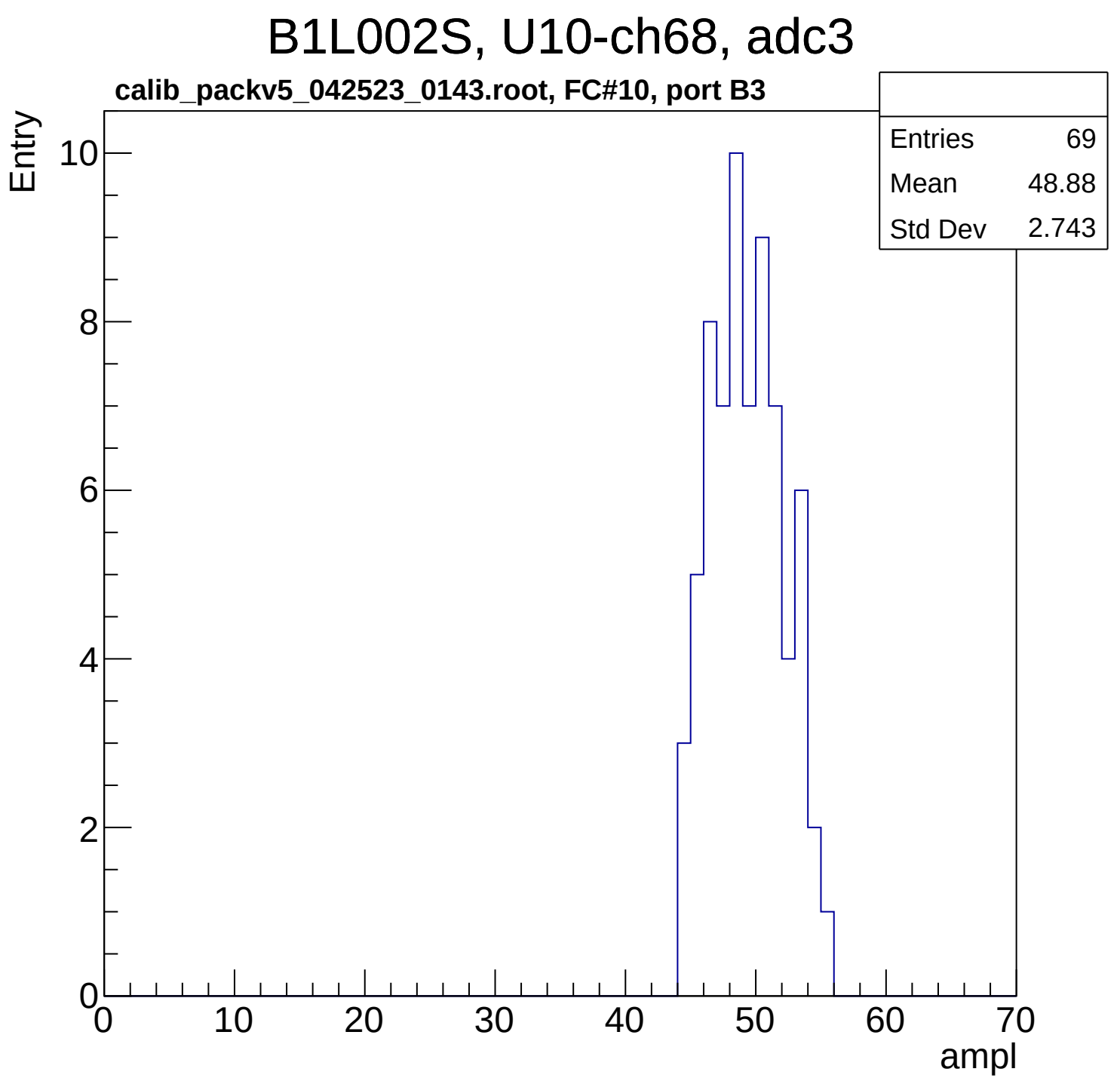
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.88
Std Dev 2.743
Entry
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2
4
6
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10
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10
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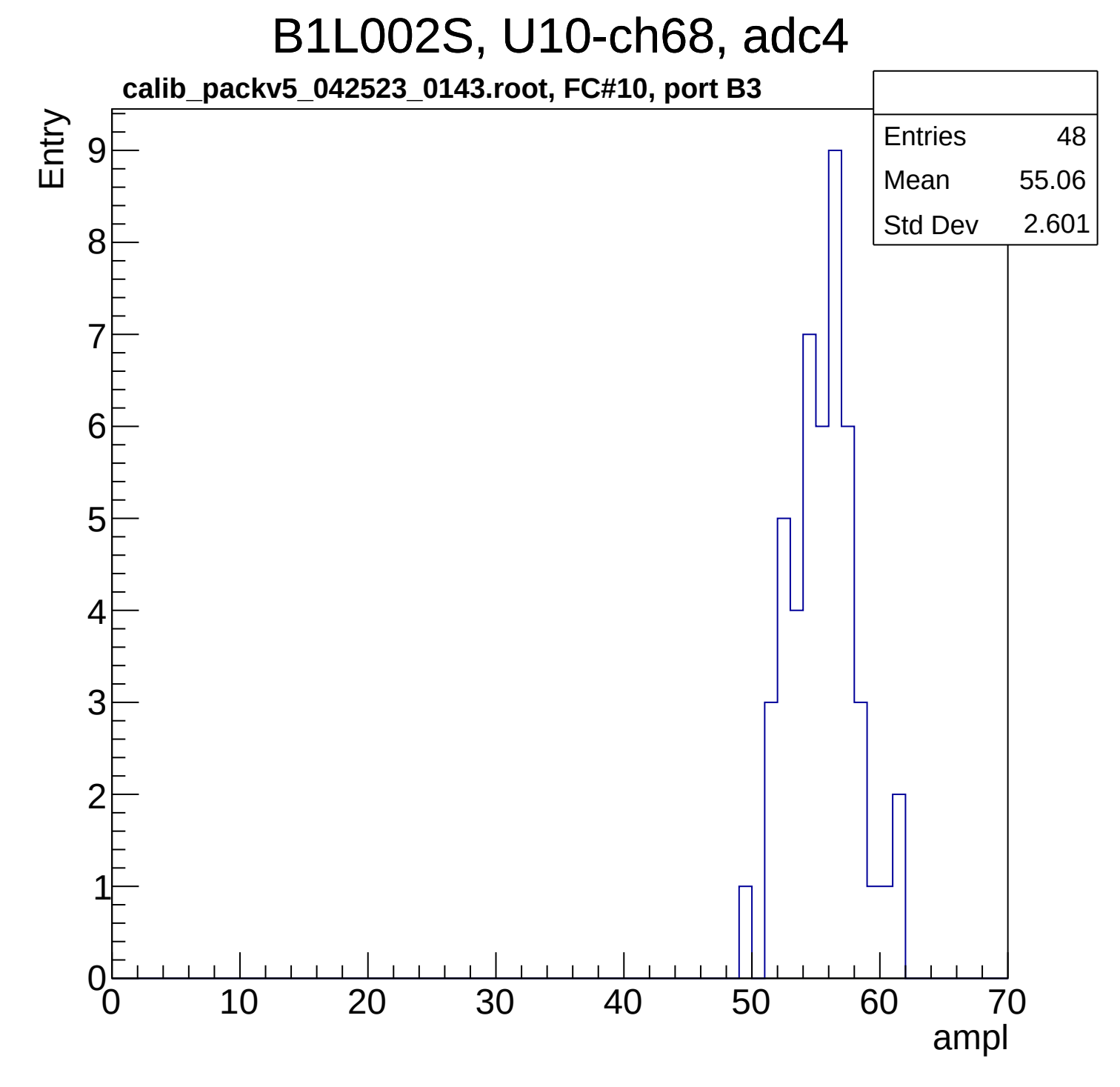
30
40
50
60
70
ampl


B1L002S, U10-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 55.06
Std Dev 2.601
Entry
ampl
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0
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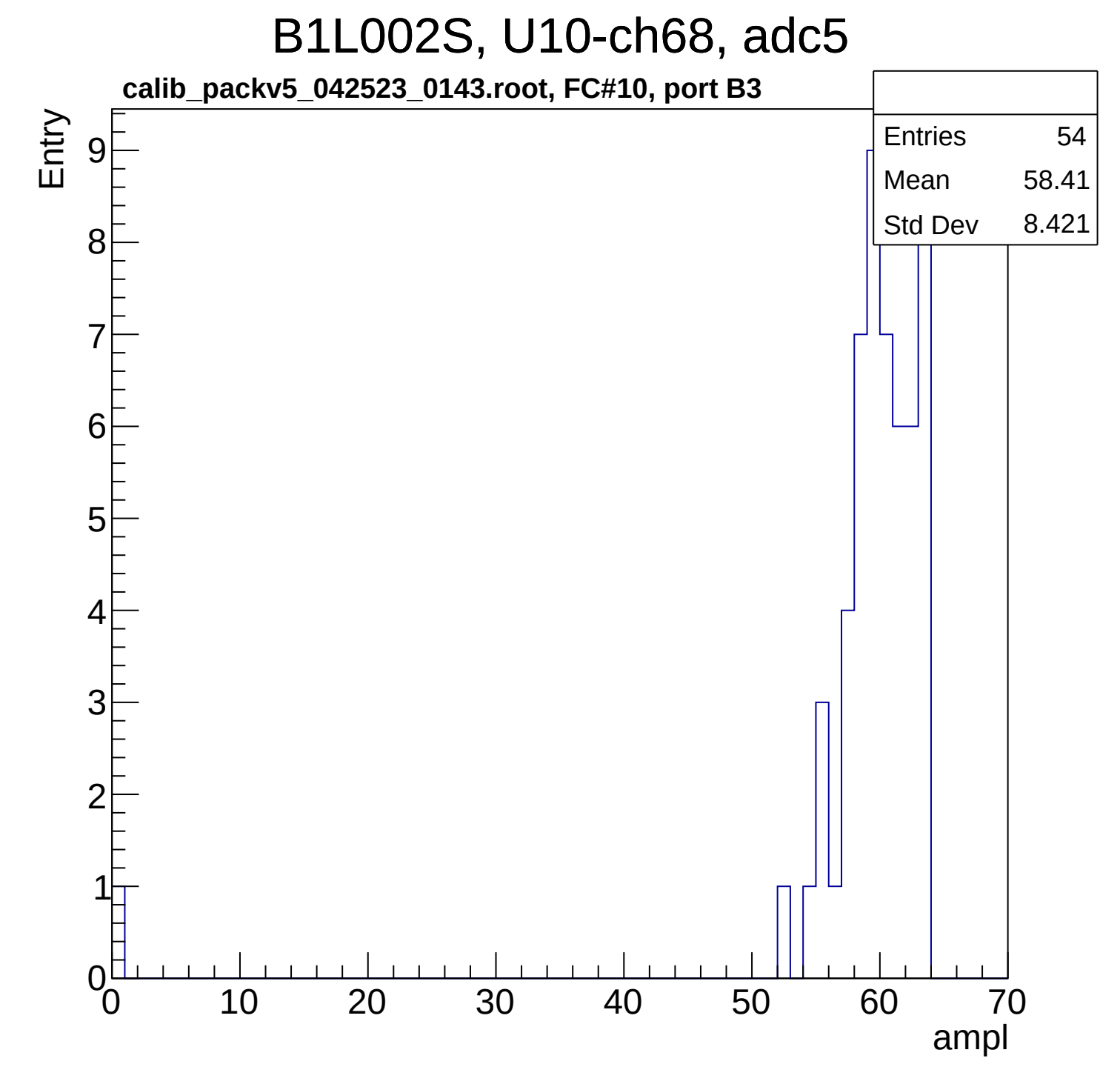
B1L002S, U10-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.41
Std Dev 8.421
Entry
ampl
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0
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60
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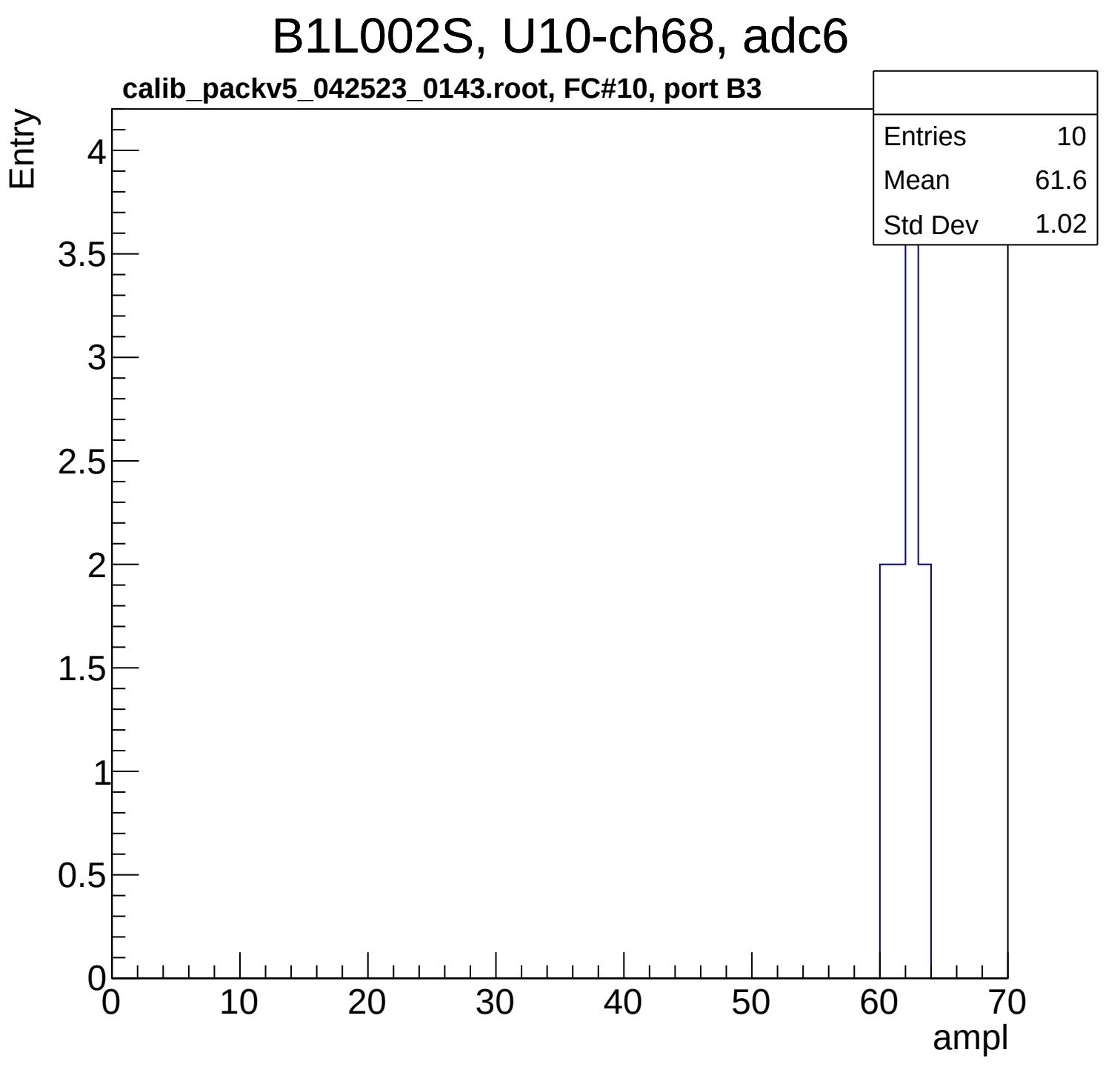
B1L002S, U10-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.6
Std Dev 1.02
Entry
ampl
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1.5
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3.5
4
0
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60
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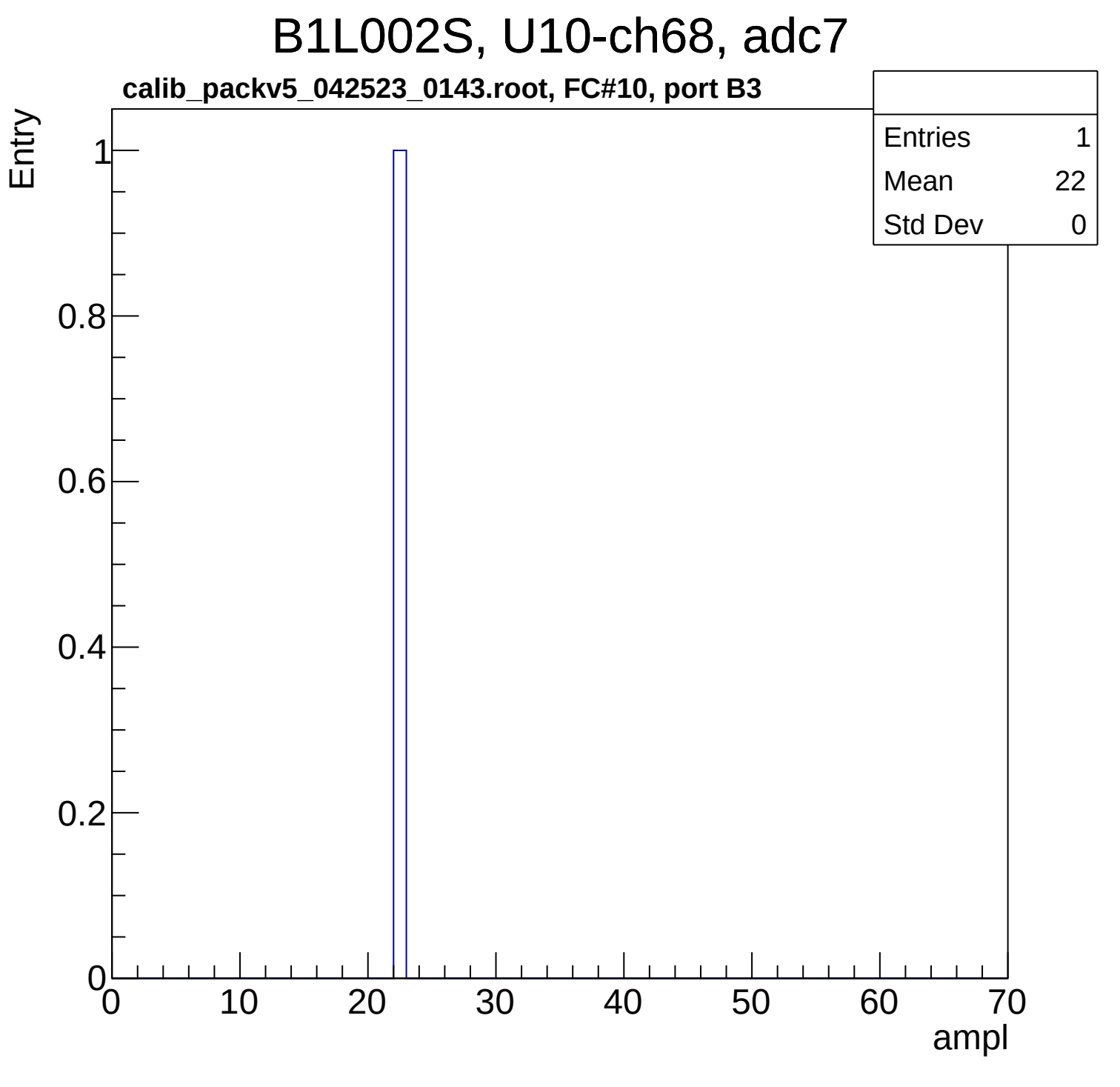
B1L002S, U10-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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10
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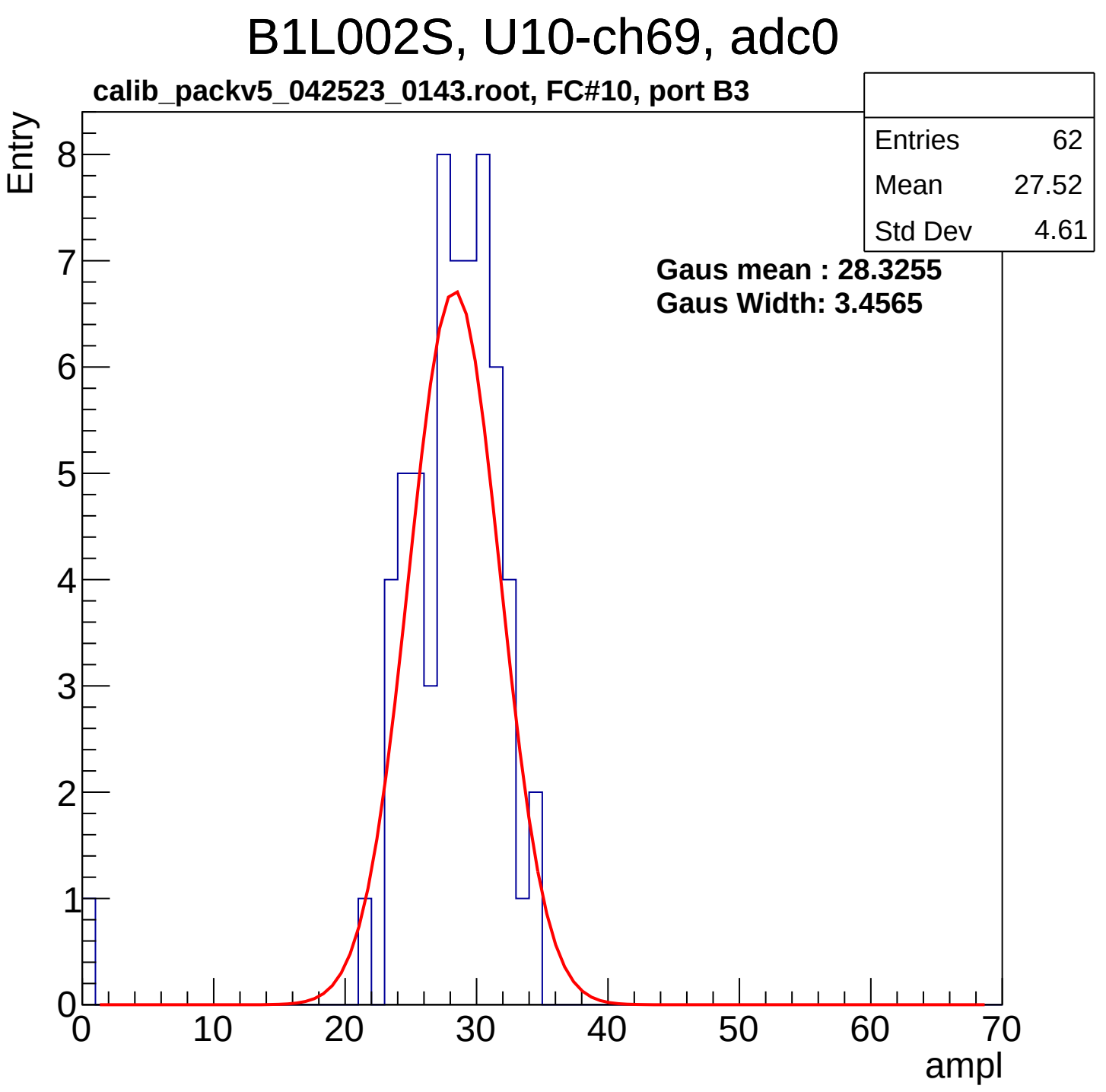
40
50
60
70
ampl


B1L002S, U10-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.52
Std Dev 4.61
Gaus mean : 28.3255
Gaus Width: 3.4565
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
30
40
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60
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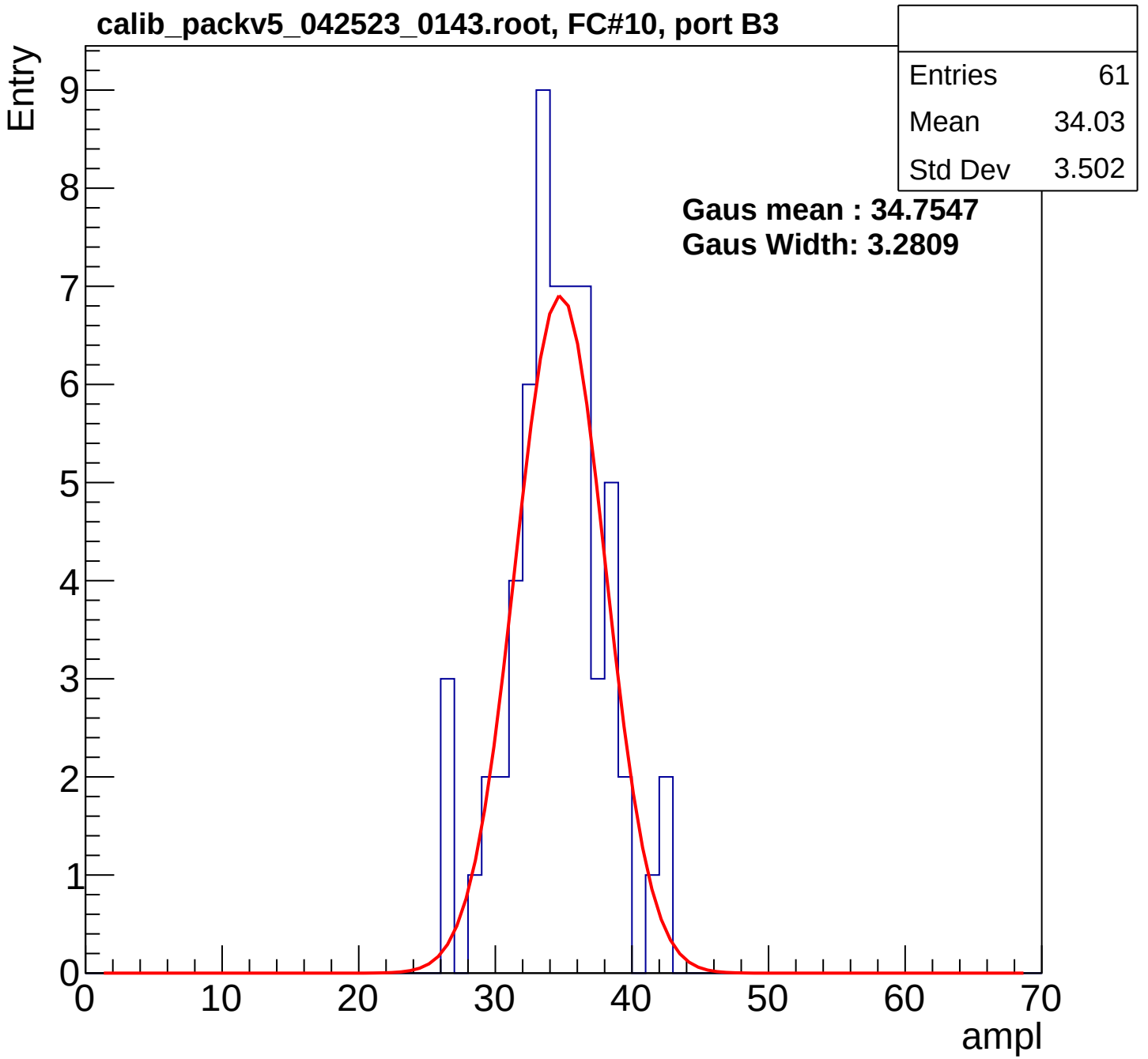
B1L002S, U10-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.03
Std Dev 3.502
Gaus mean : 34.7547
Gaus Width: 3.2809
Entry
ampl
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2
3
4
5
6
7
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0
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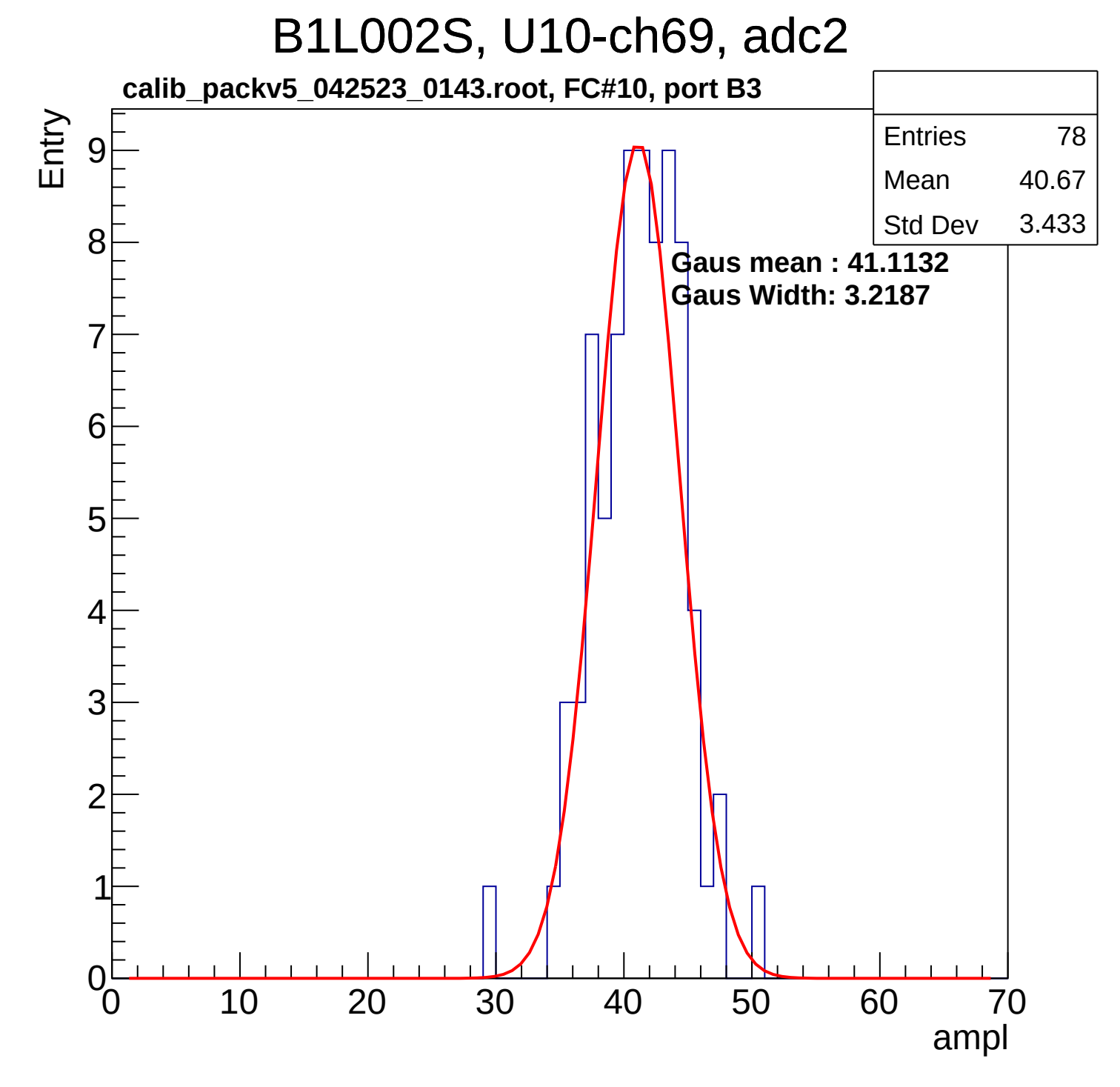
B1L002S, U10-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 40.67
Std Dev 3.433
Gaus mean : 41.1132
Gaus Width: 3.2187
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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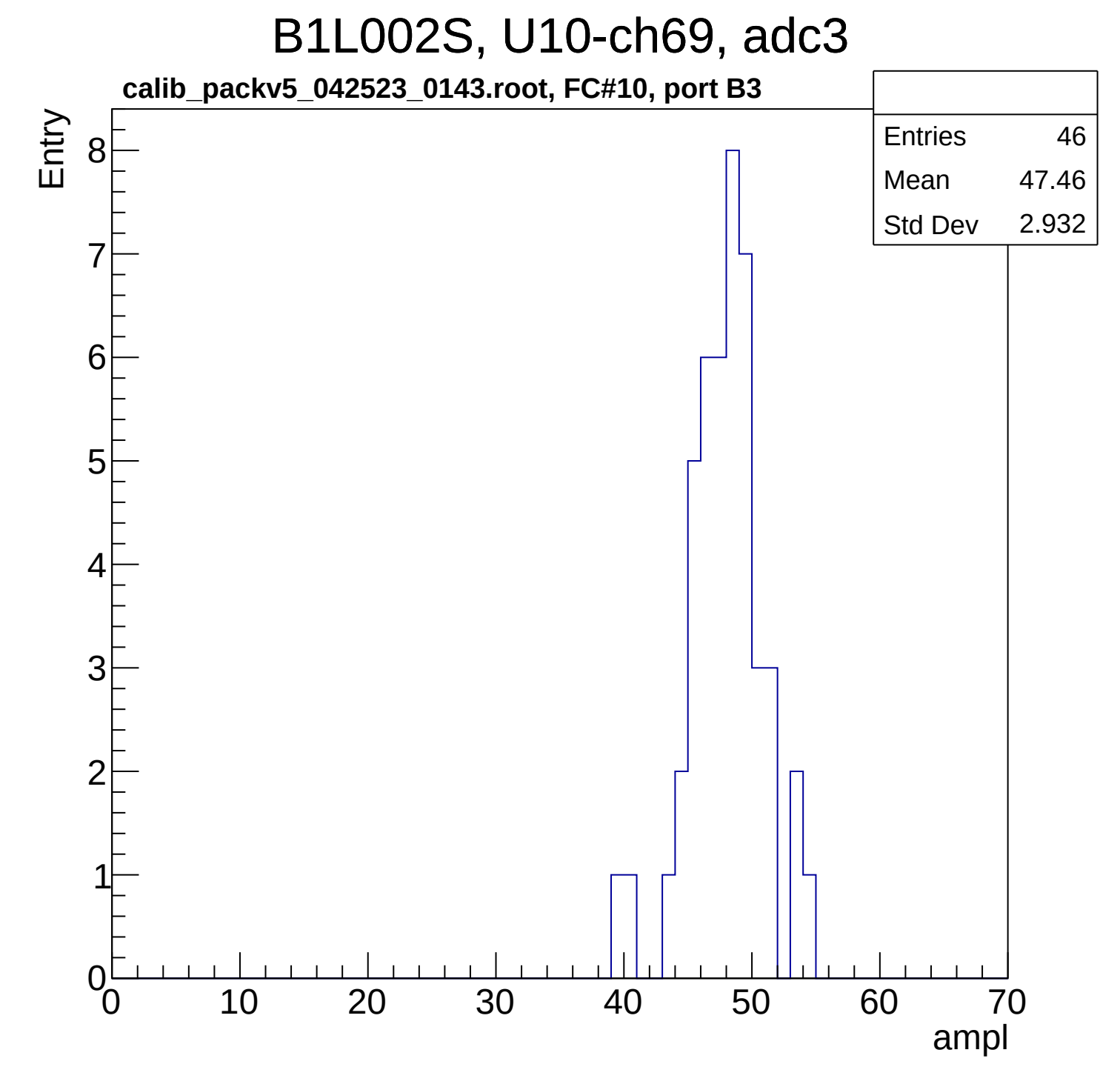
B1L002S, U10-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 47.46
Std Dev 2.932
Entry
ampl
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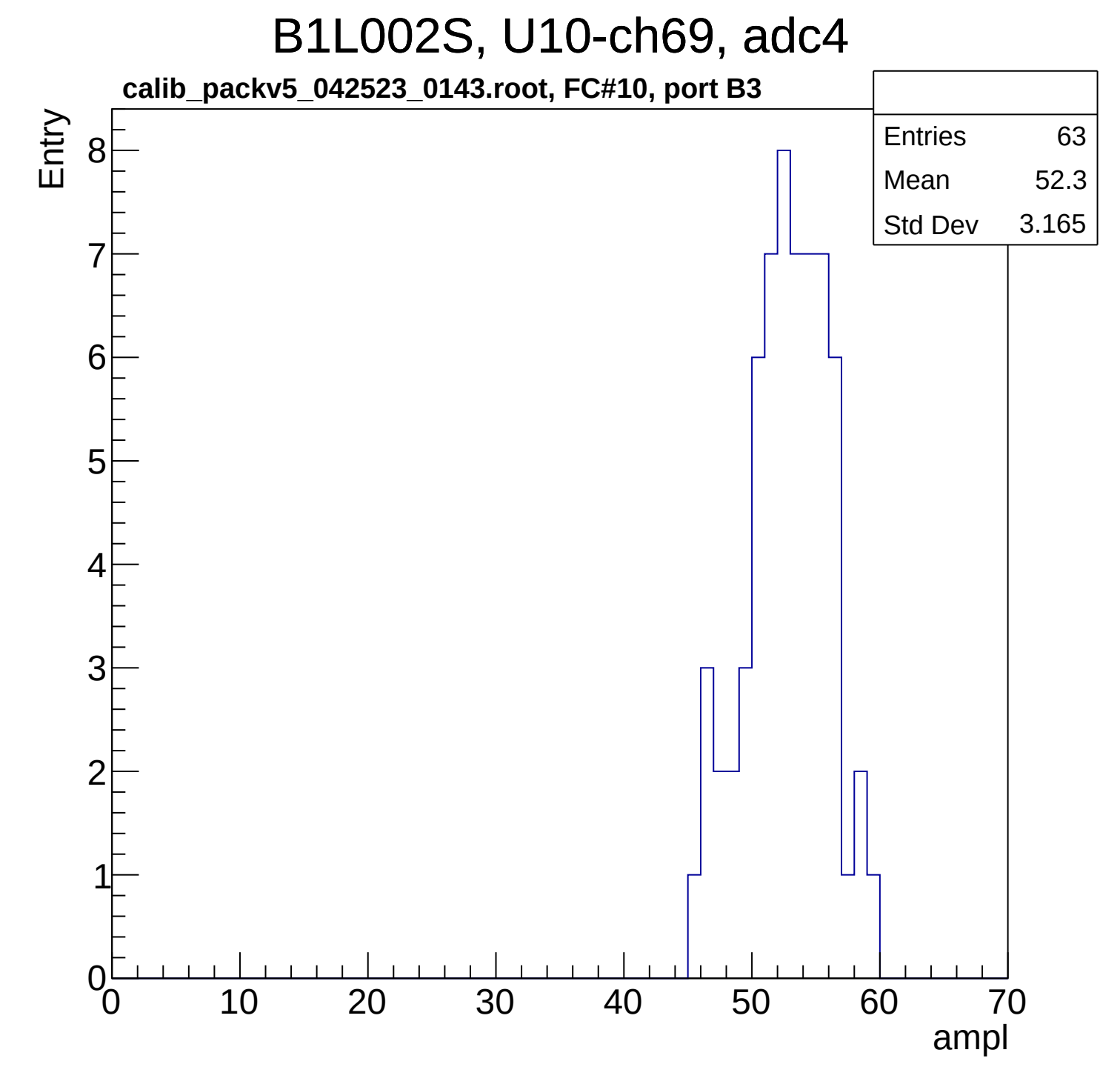
B1L002S, U10-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.3
Std Dev 3.165
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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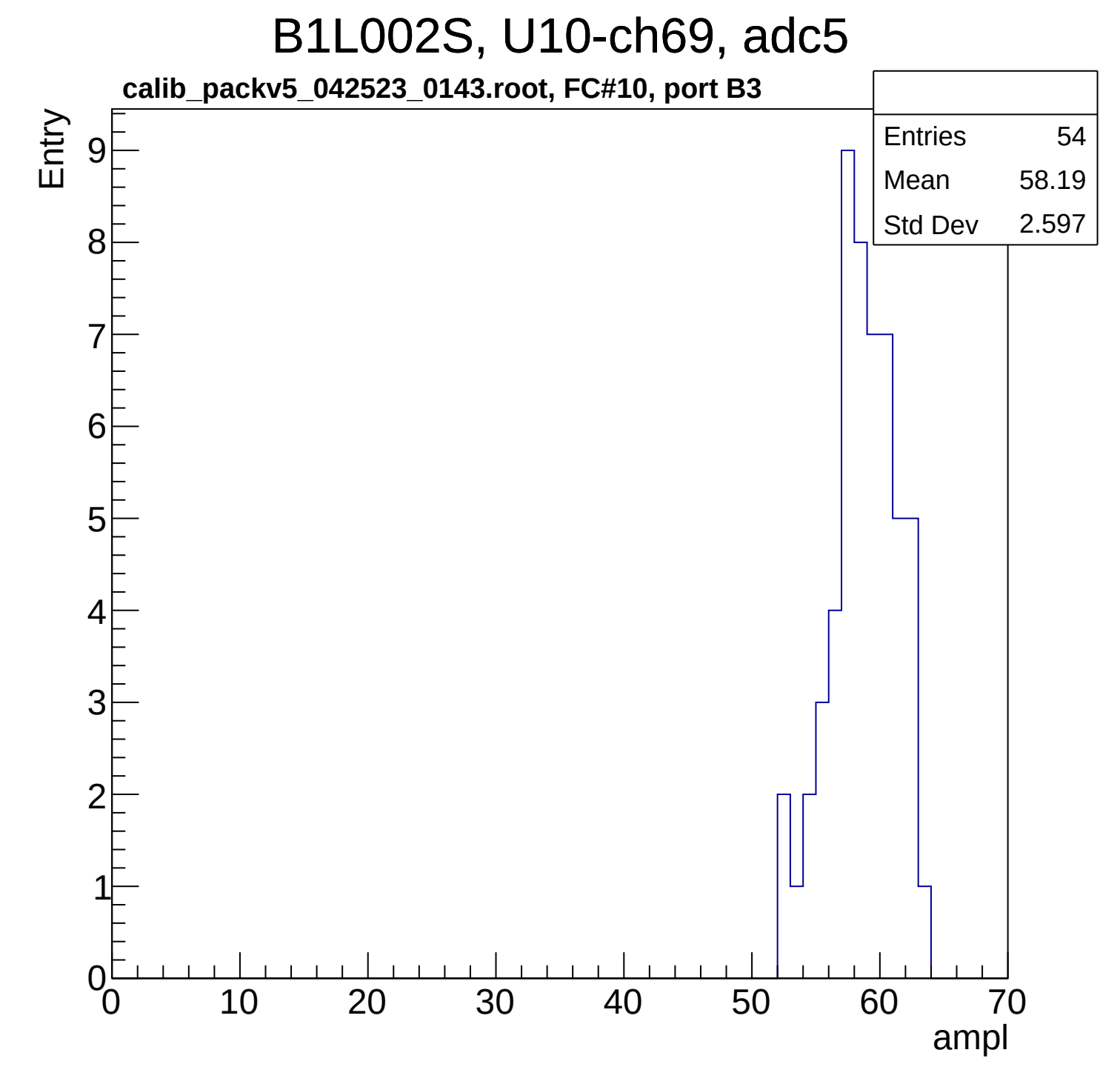
B1L002S, U10-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.19
Std Dev 2.597
Entry
ampl
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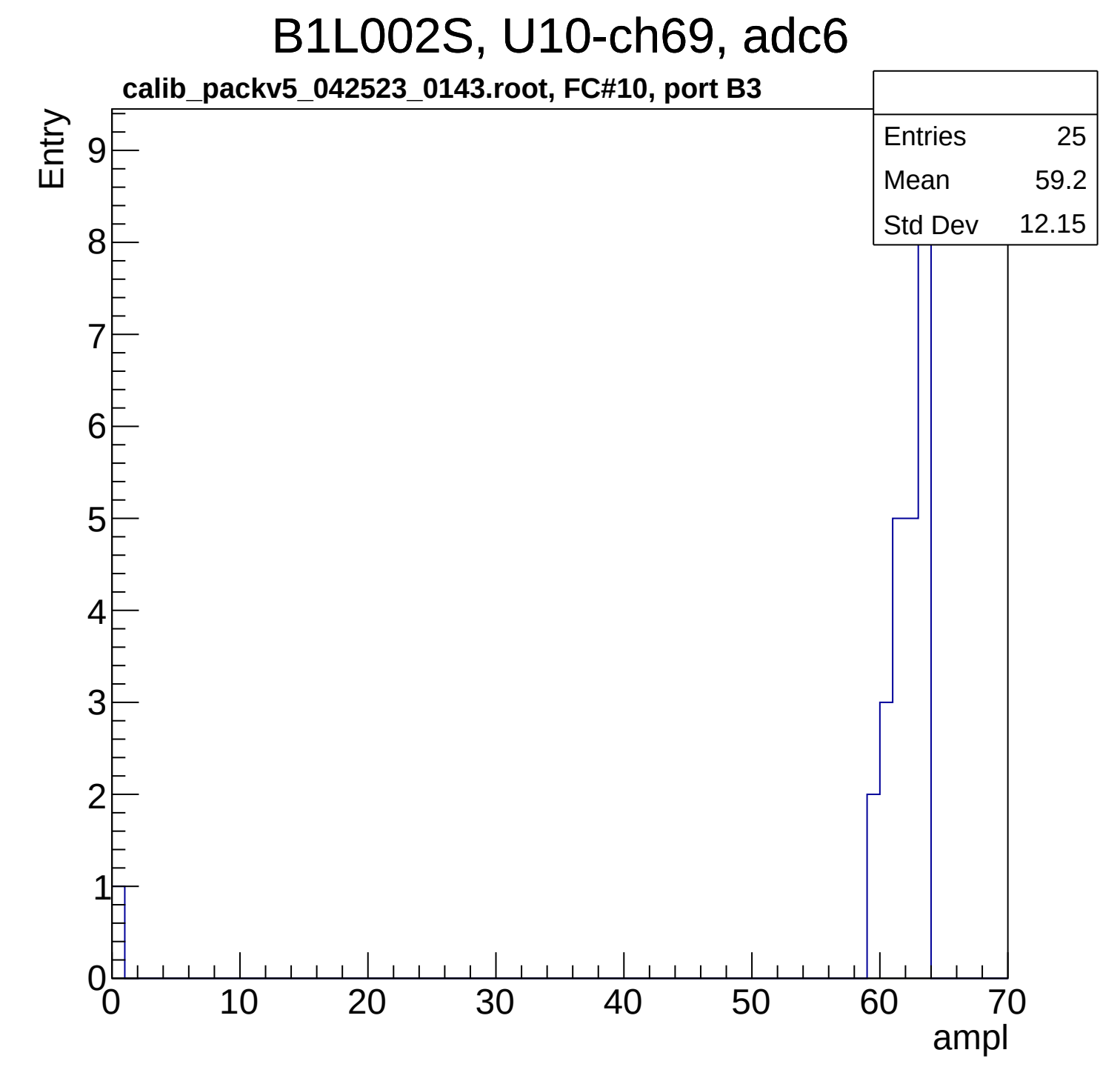
B1L002S, U10-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59.2
Std Dev 12.15
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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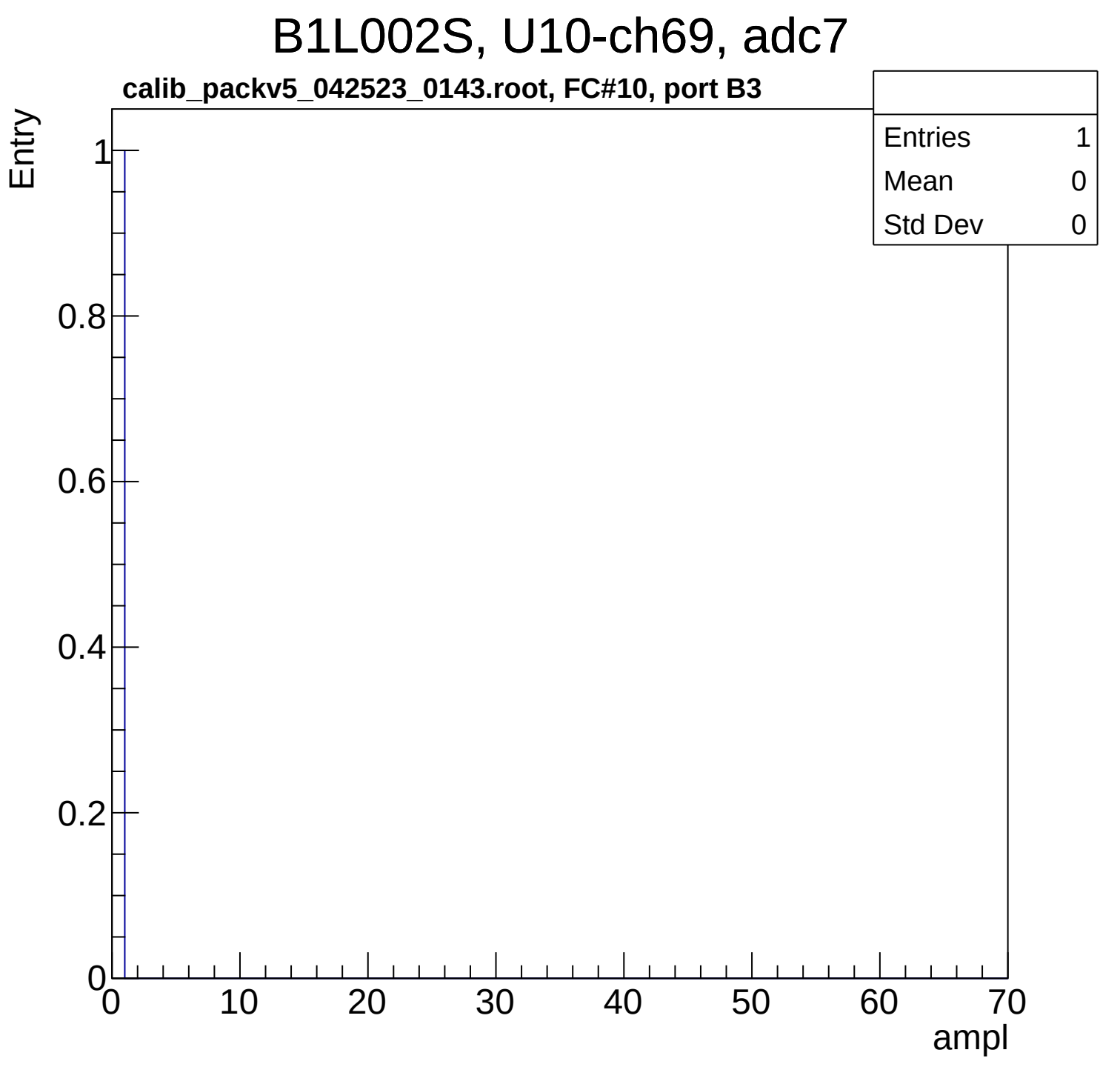
B1L002S, U10-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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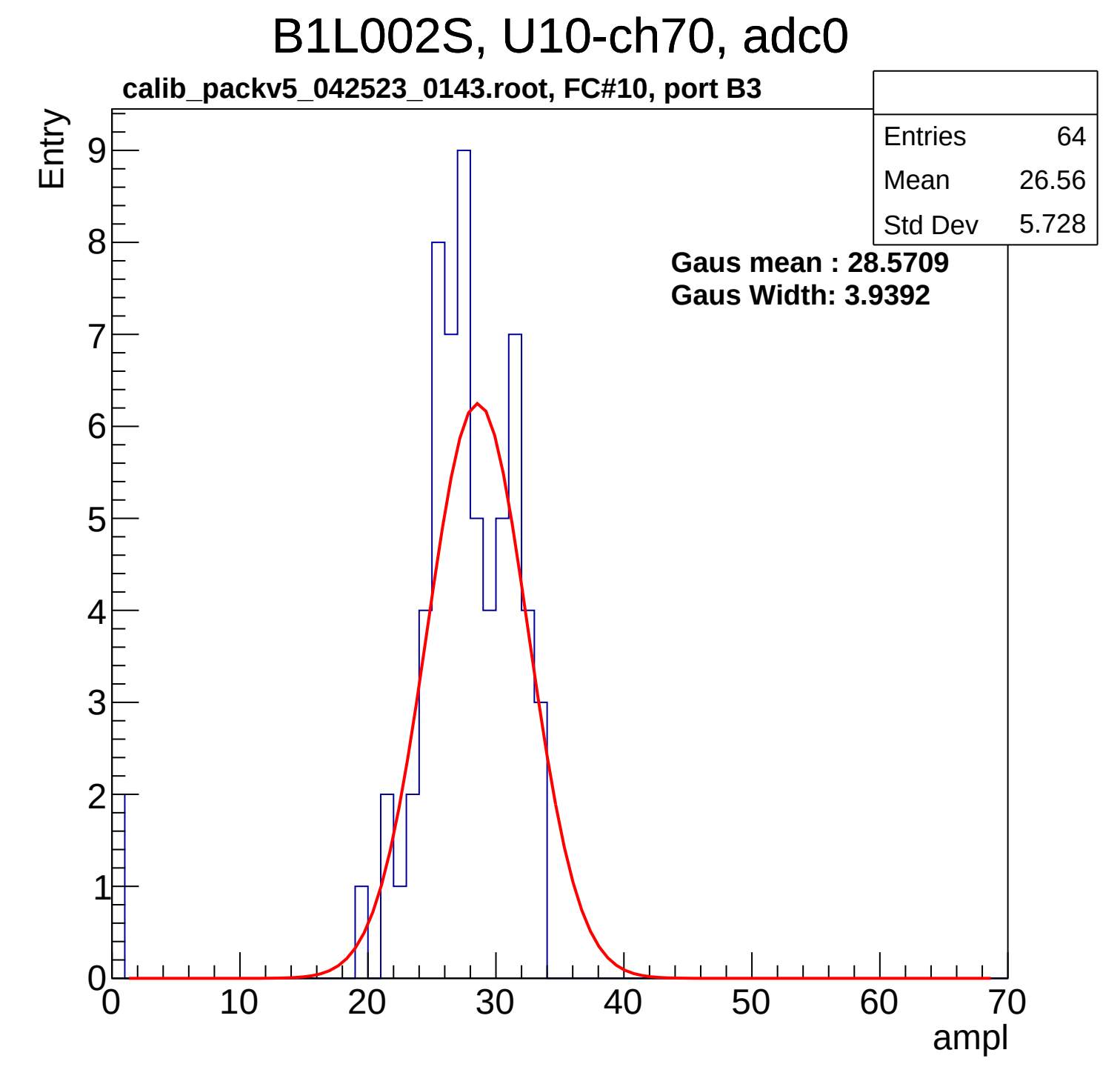
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.56
Std Dev 5.728
Gaus mean : 28.5709
Gaus Width: 3.9392
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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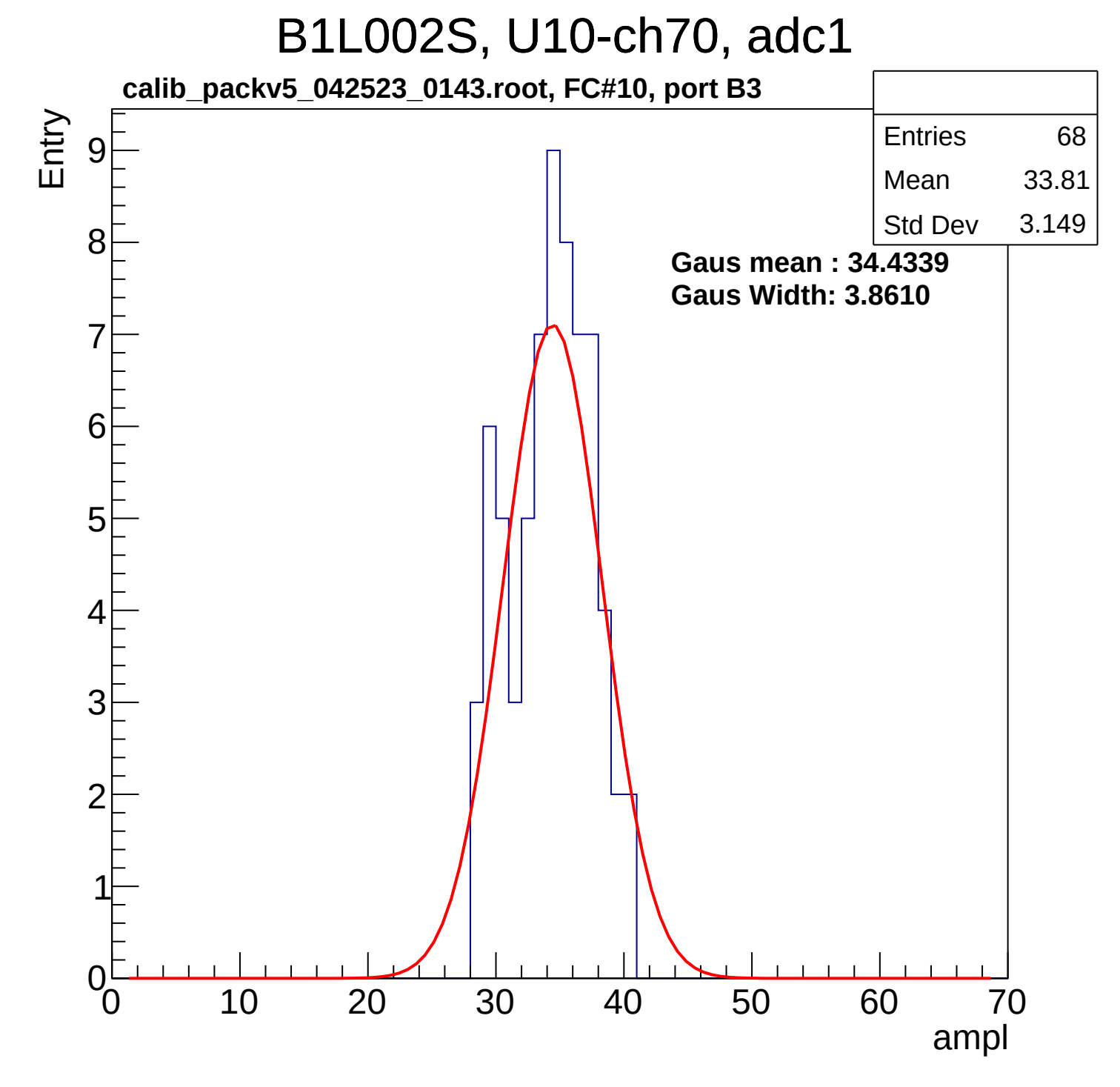
B1L002S, U10-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.81
Std Dev 3.149
Gaus mean : 34.4339
Gaus Width: 3.8610
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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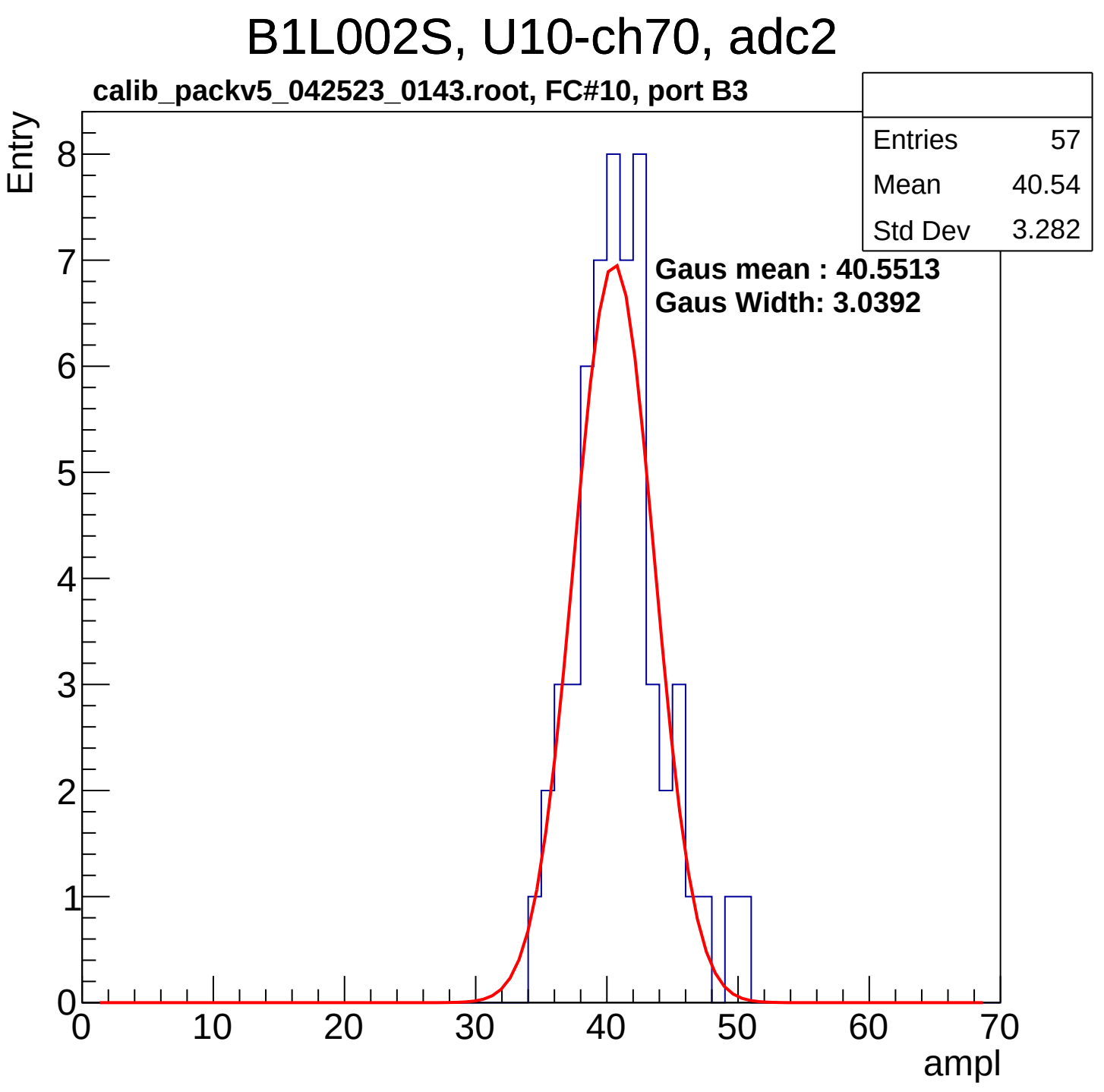
B1L002S, U10-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.54
Std Dev 3.282
Gaus mean : 40.5513
Gaus Width: 3.0392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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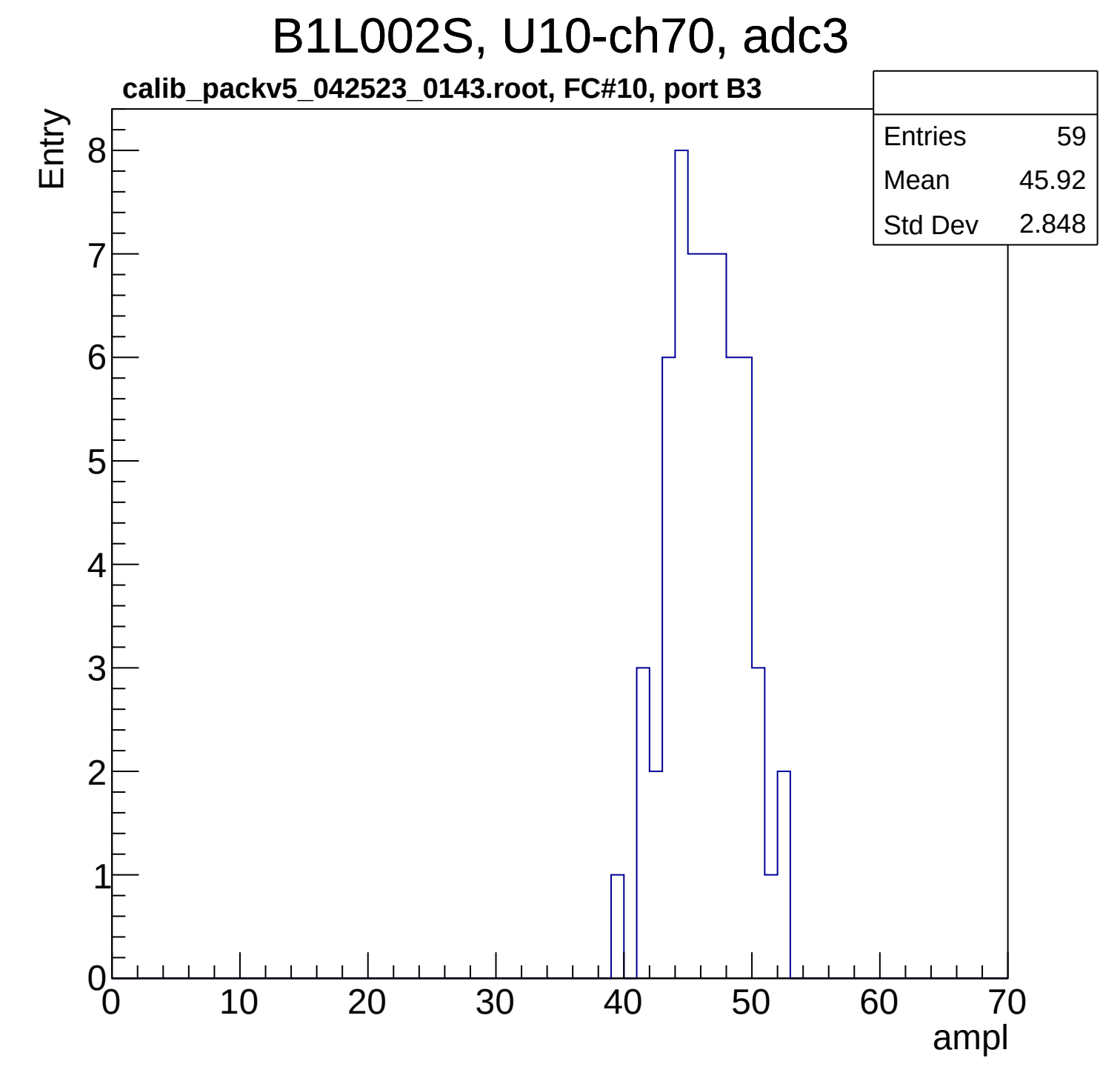
B1L002S, U10-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.92
Std Dev 2.848
Entry
ampl
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1
2
3
4
5
6
7
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0
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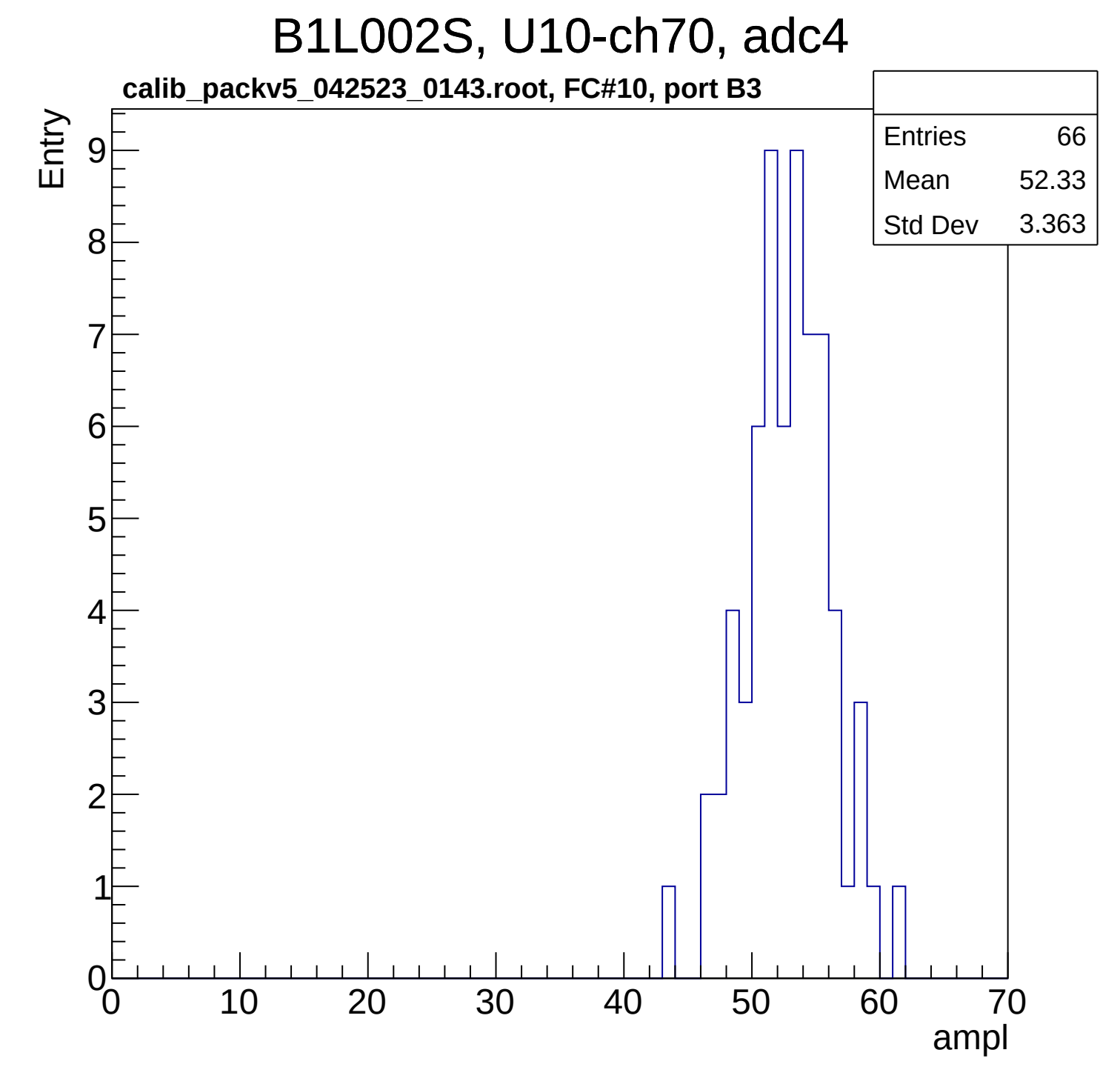
B1L002S, U10-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.33
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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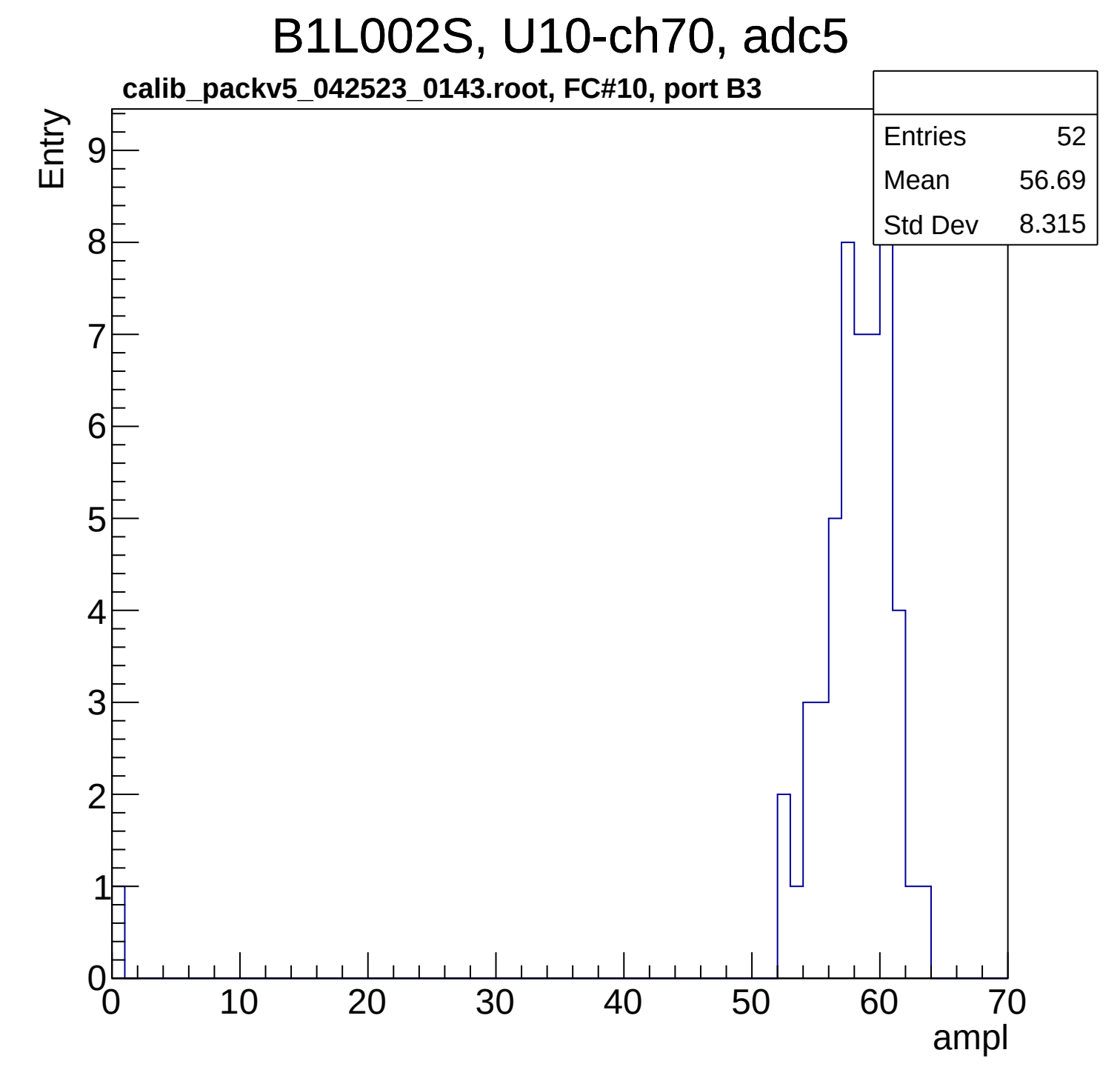
B1L002S, U10-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 56.69
Std Dev 8.315
Entry
ampl
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2
3
4
5
6
7
8
9
0
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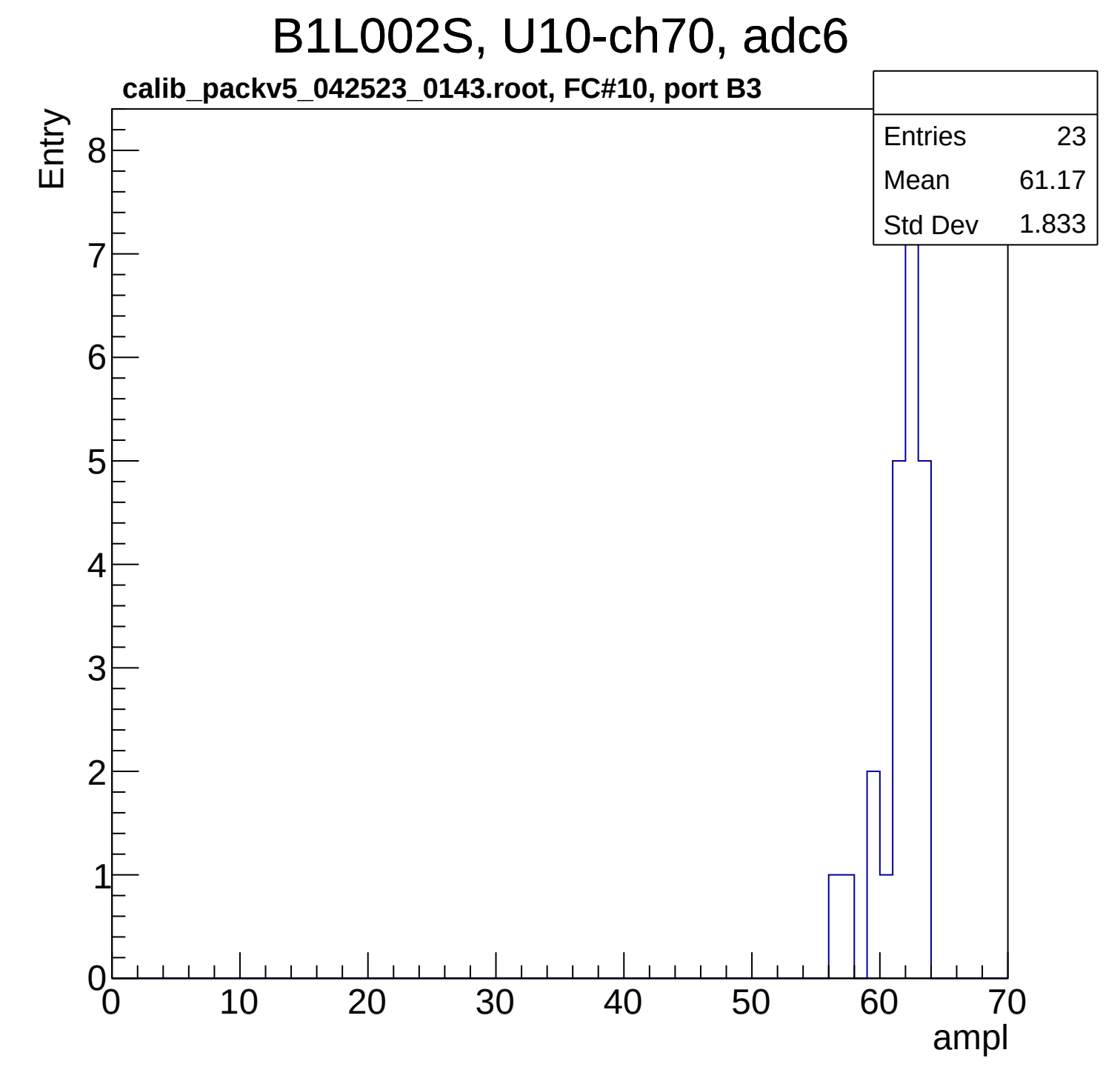
B1L002S, U10-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.17
Std Dev 1.833
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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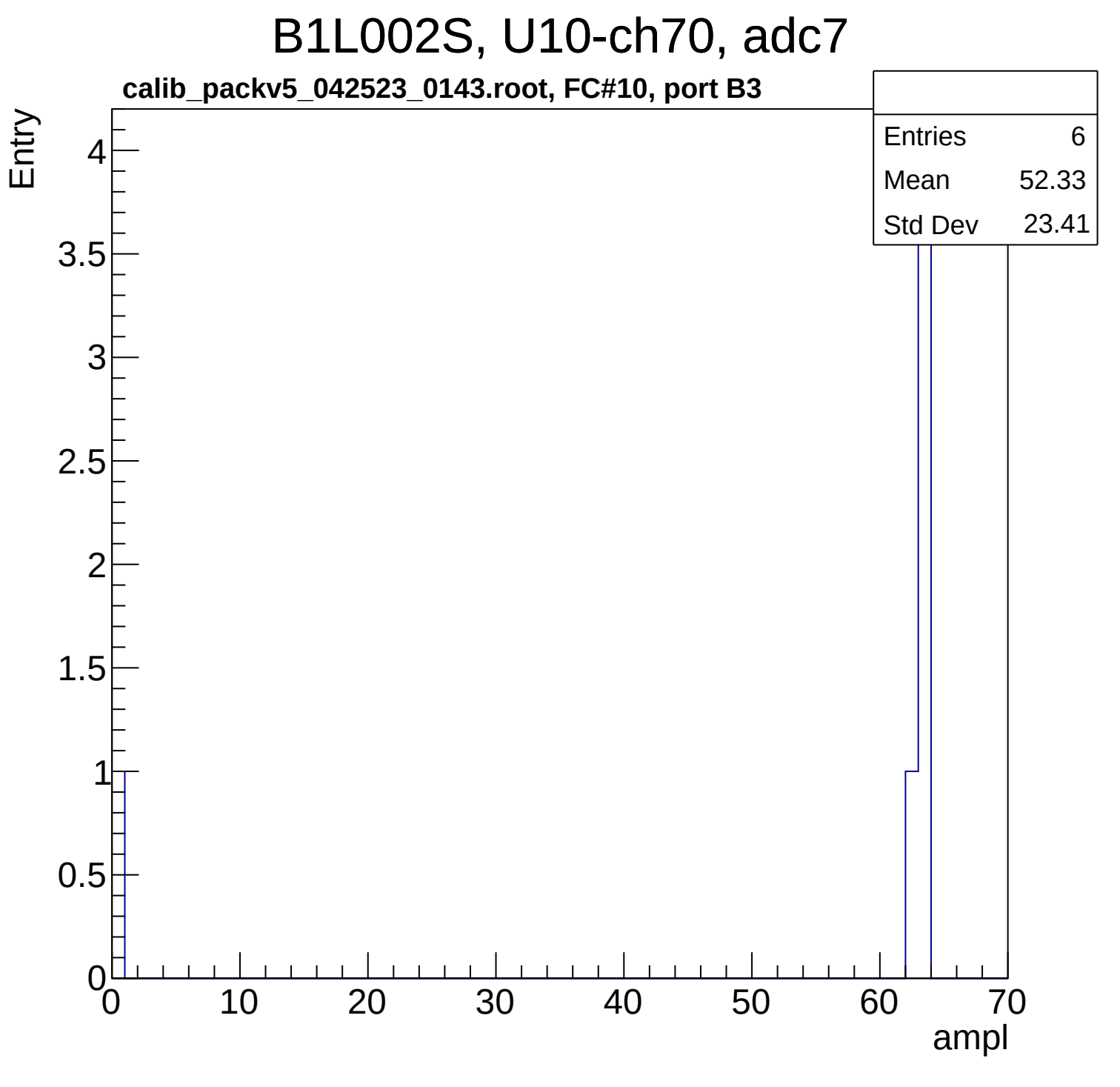
B1L002S, U10-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52.33
Std Dev 23.41
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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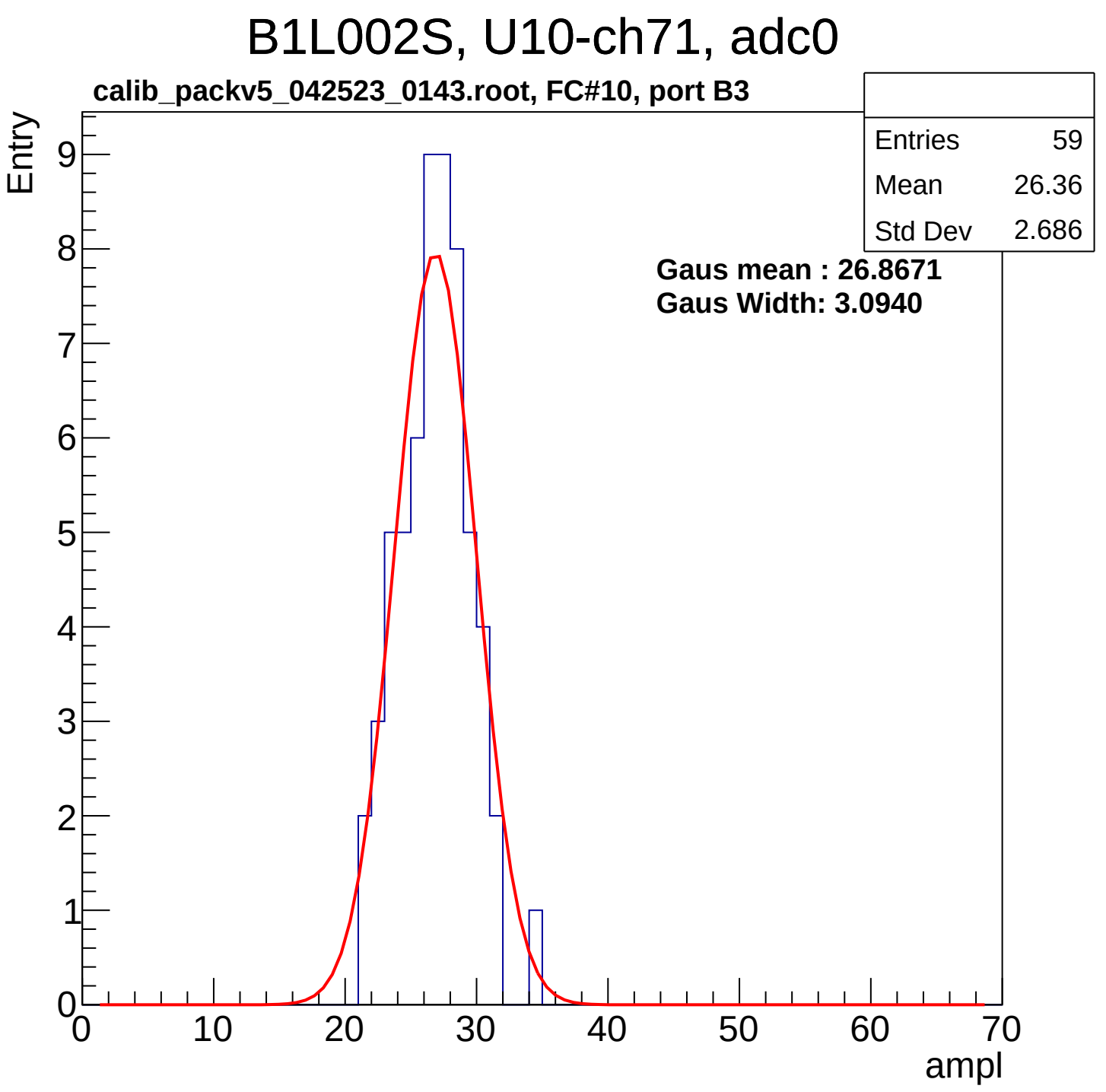
B1L002S, U10-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.36
Std Dev 2.686
Gaus mean : 26.8671
Gaus Width: 3.0940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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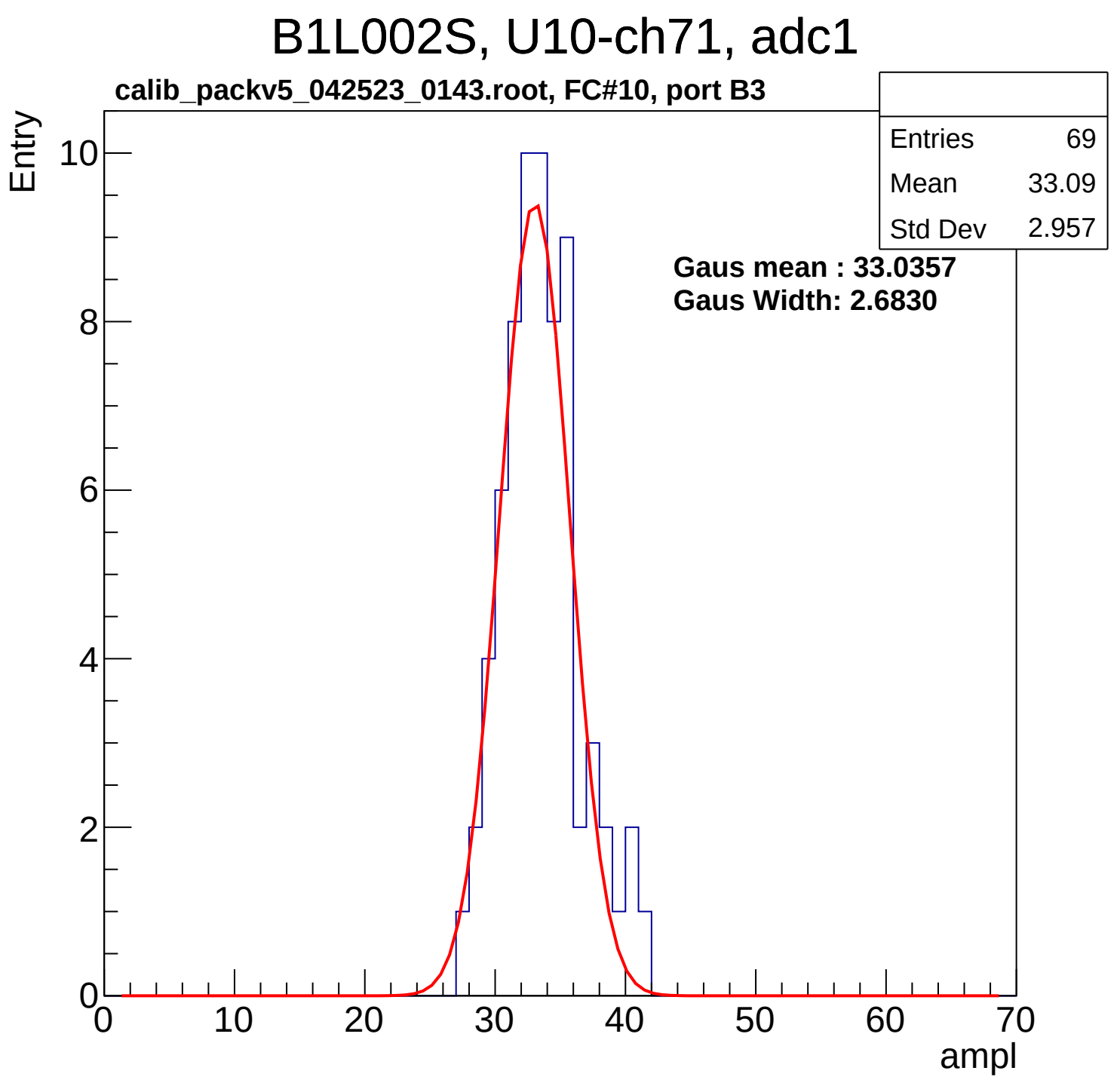
B1L002S, U10-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.09
Std Dev 2.957
Gaus mean : 33.0357
Gaus Width: 2.6830
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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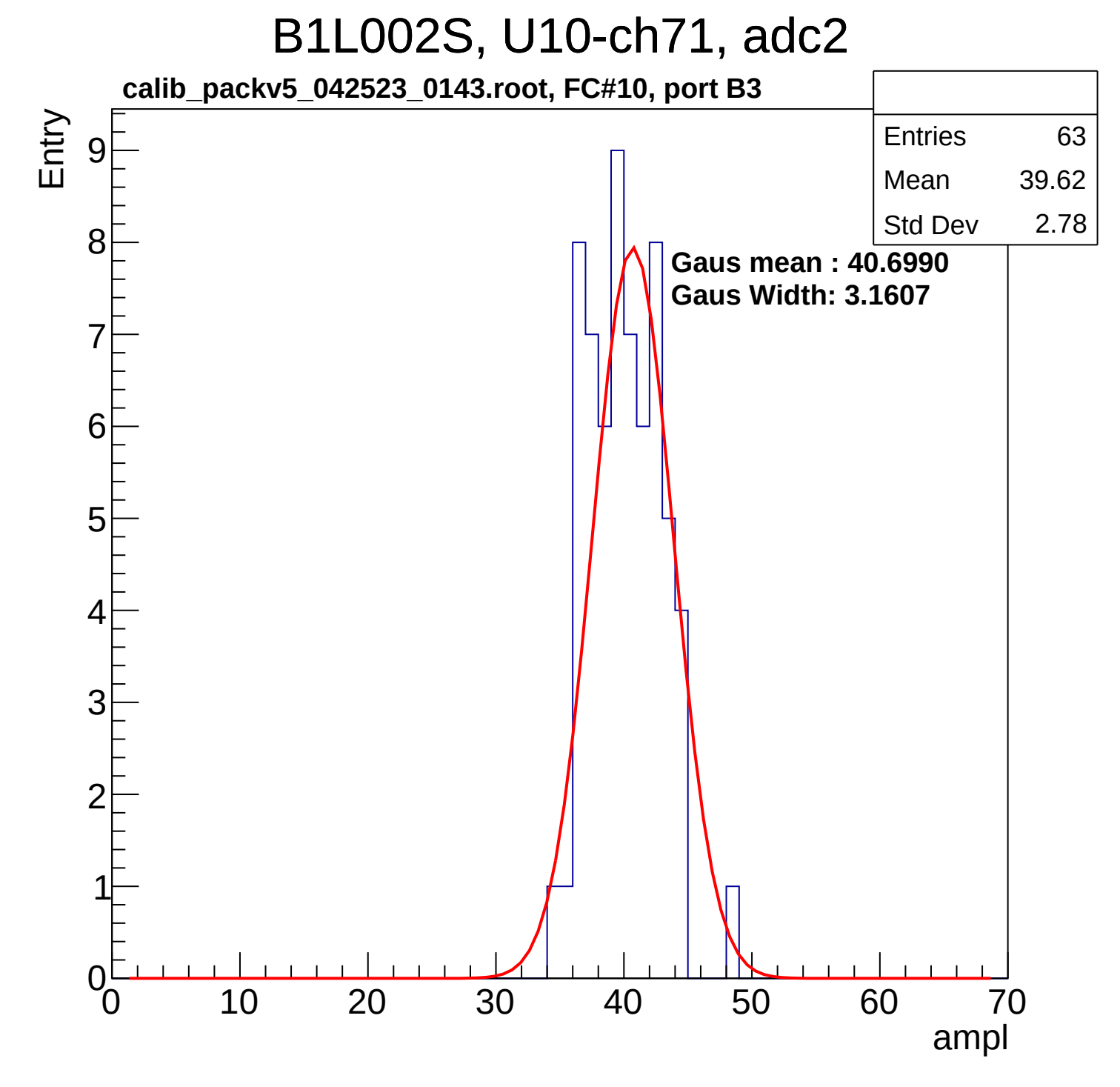
B1L002S, U10-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.62
Std Dev 2.78
Gaus mean : 40.6990
Gaus Width: 3.1607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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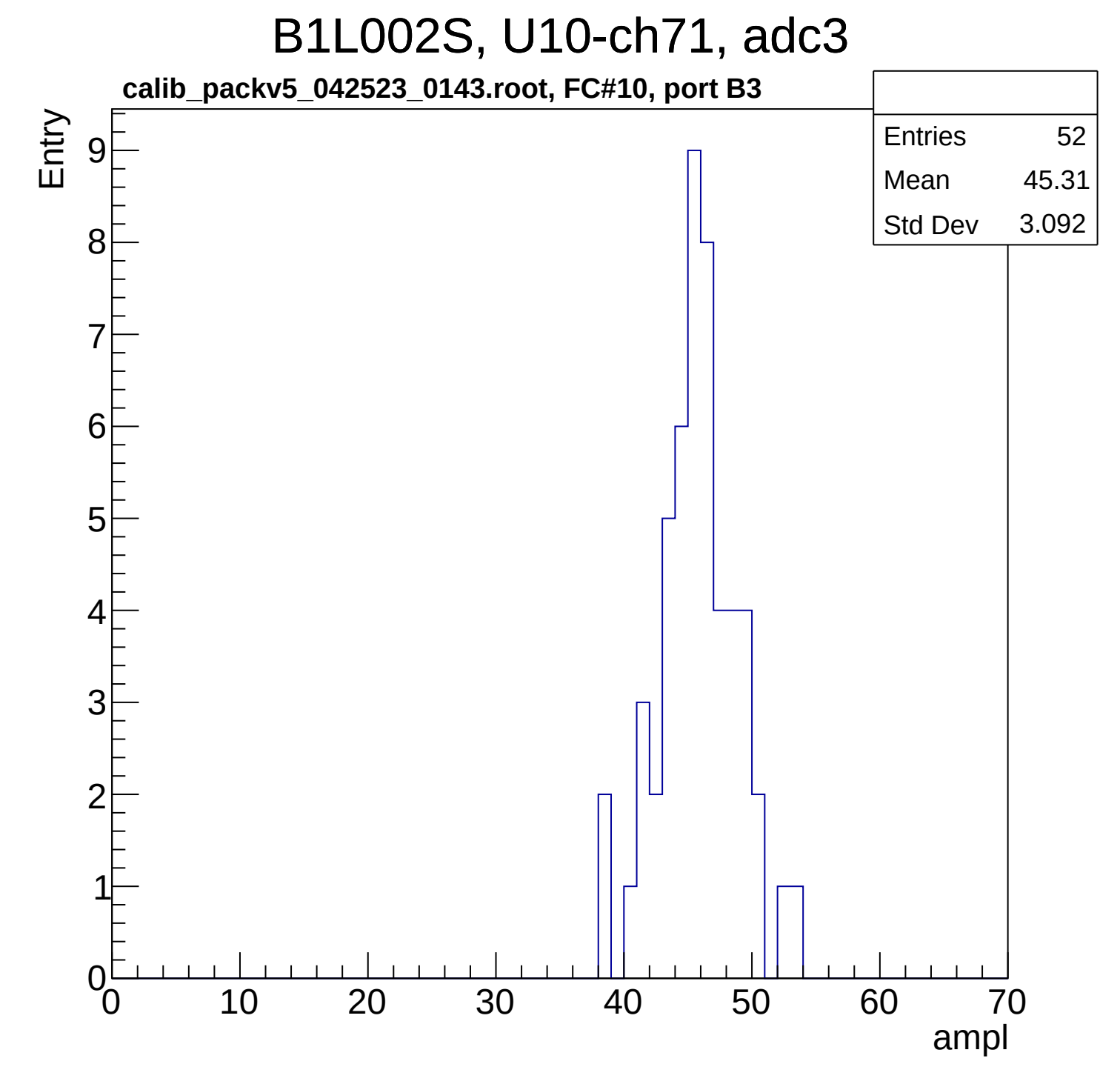
B1L002S, U10-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 45.31
Std Dev 3.092
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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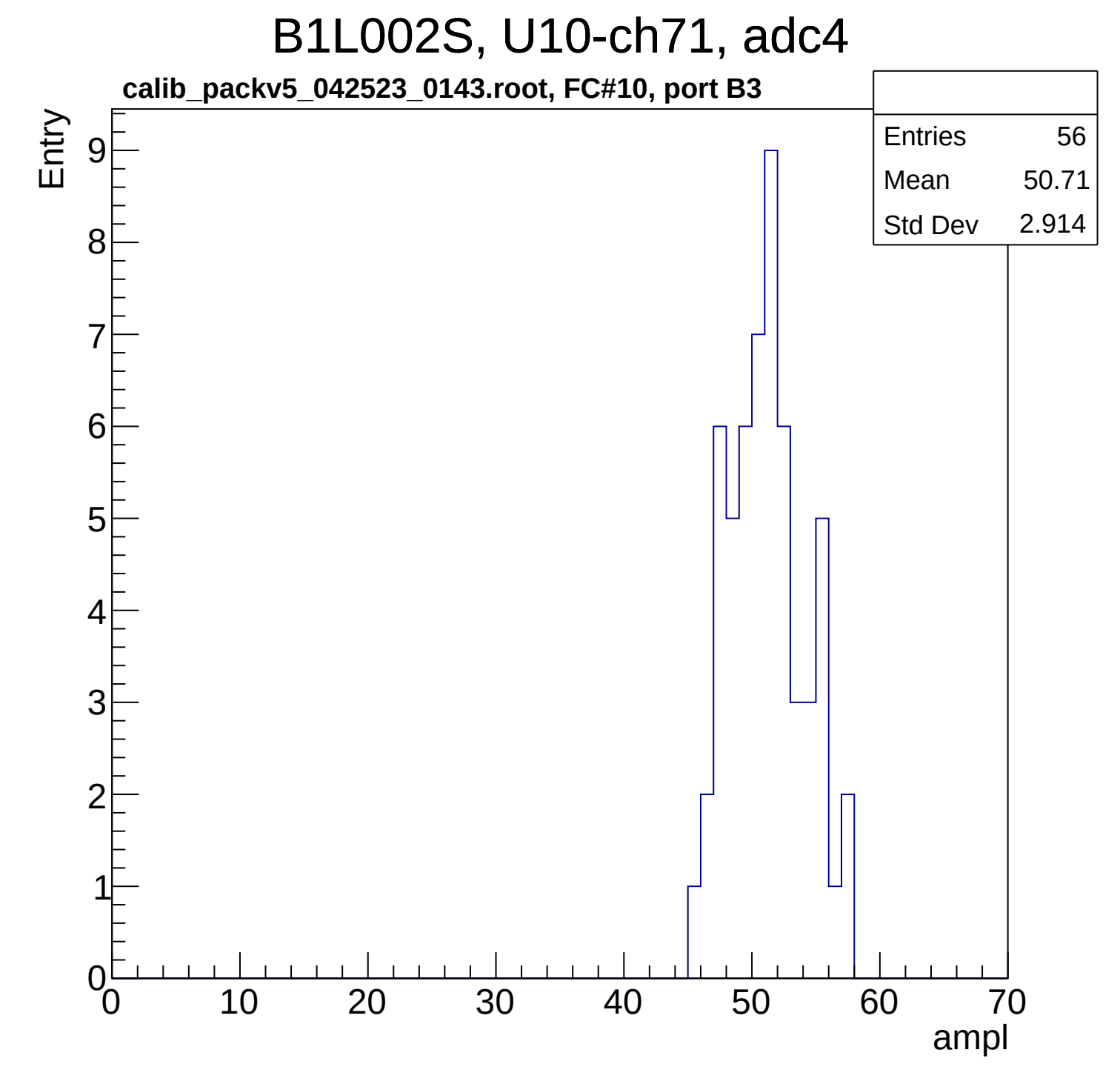
B1L002S, U10-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 50.71
Std Dev 2.914
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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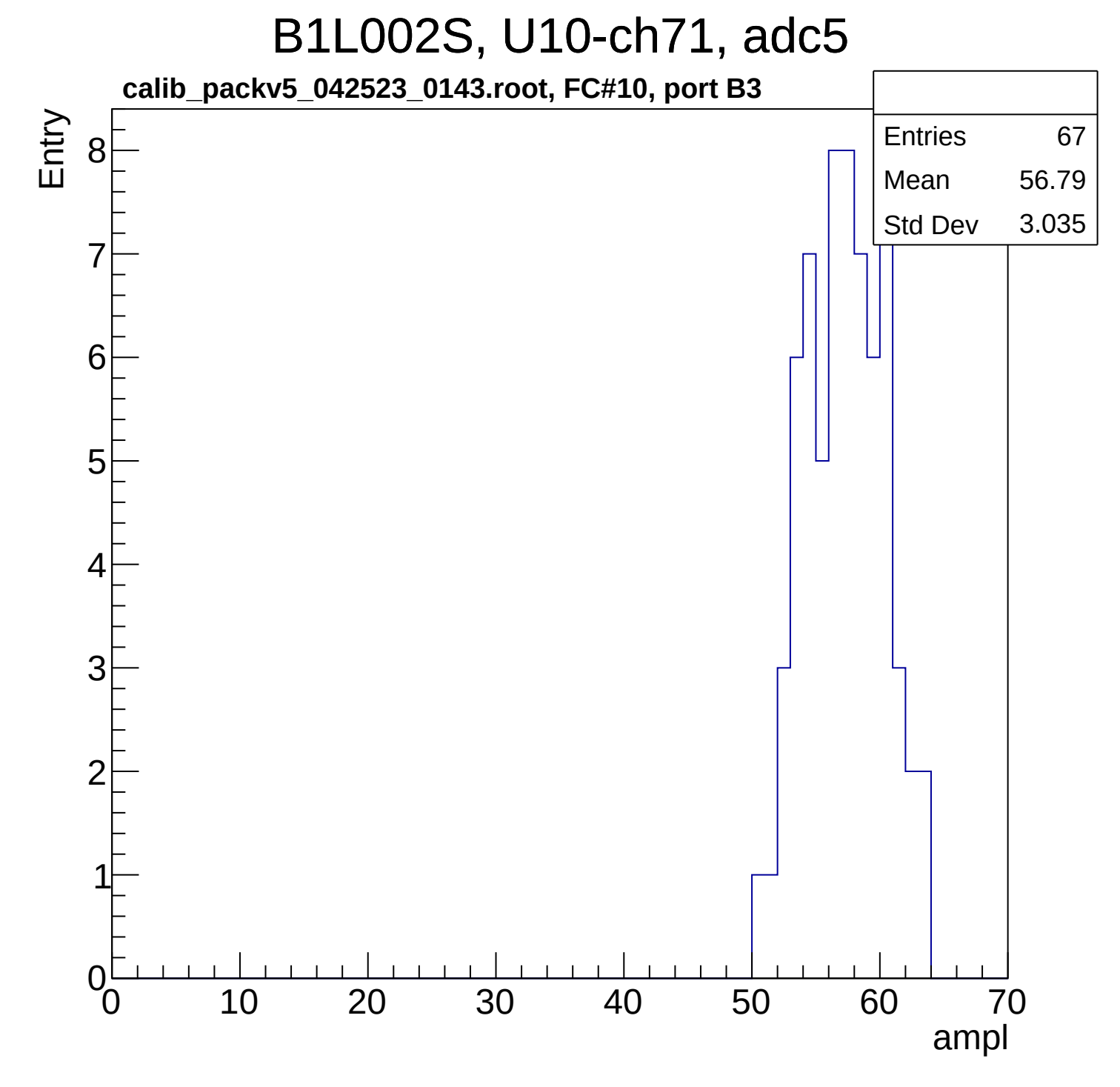
B1L002S, U10-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.79
Std Dev 3.035
Entry
ampl
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0
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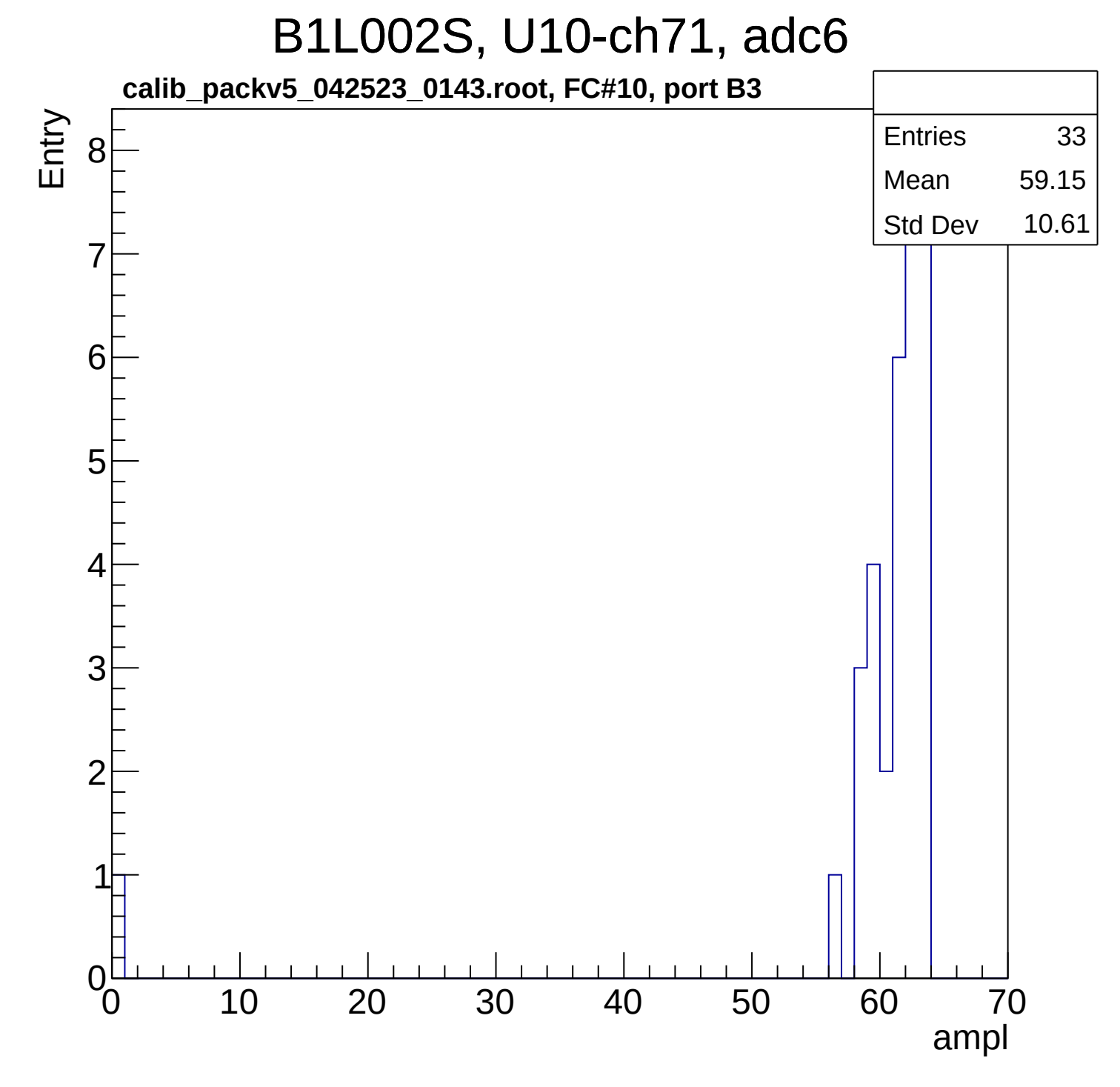
B1L002S, U10-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 59.15
Std Dev 10.61
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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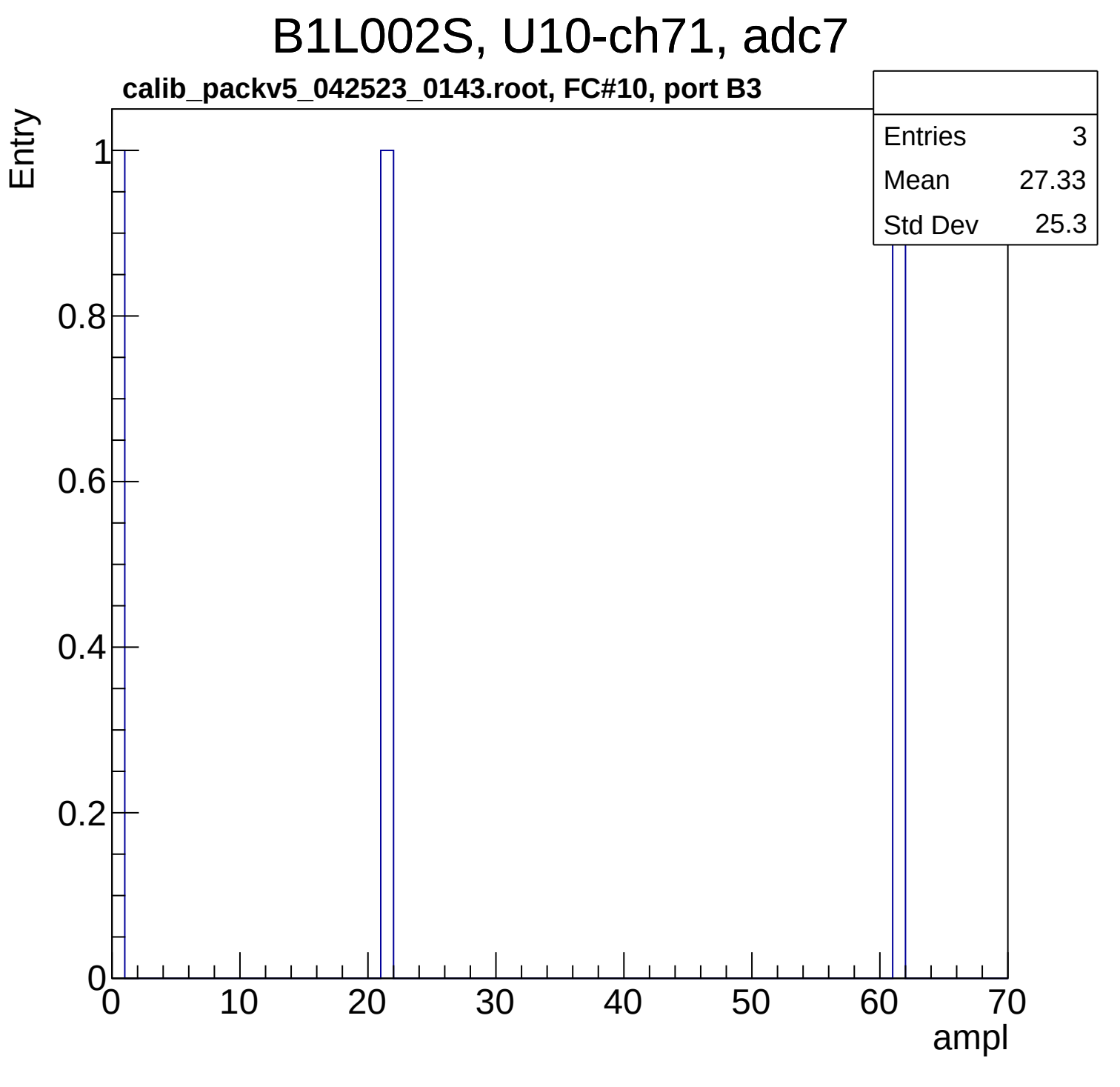
B1L002S, U10-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 25.3
Entry
ampl
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0.2
0.4
0.6
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1
0
10
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60
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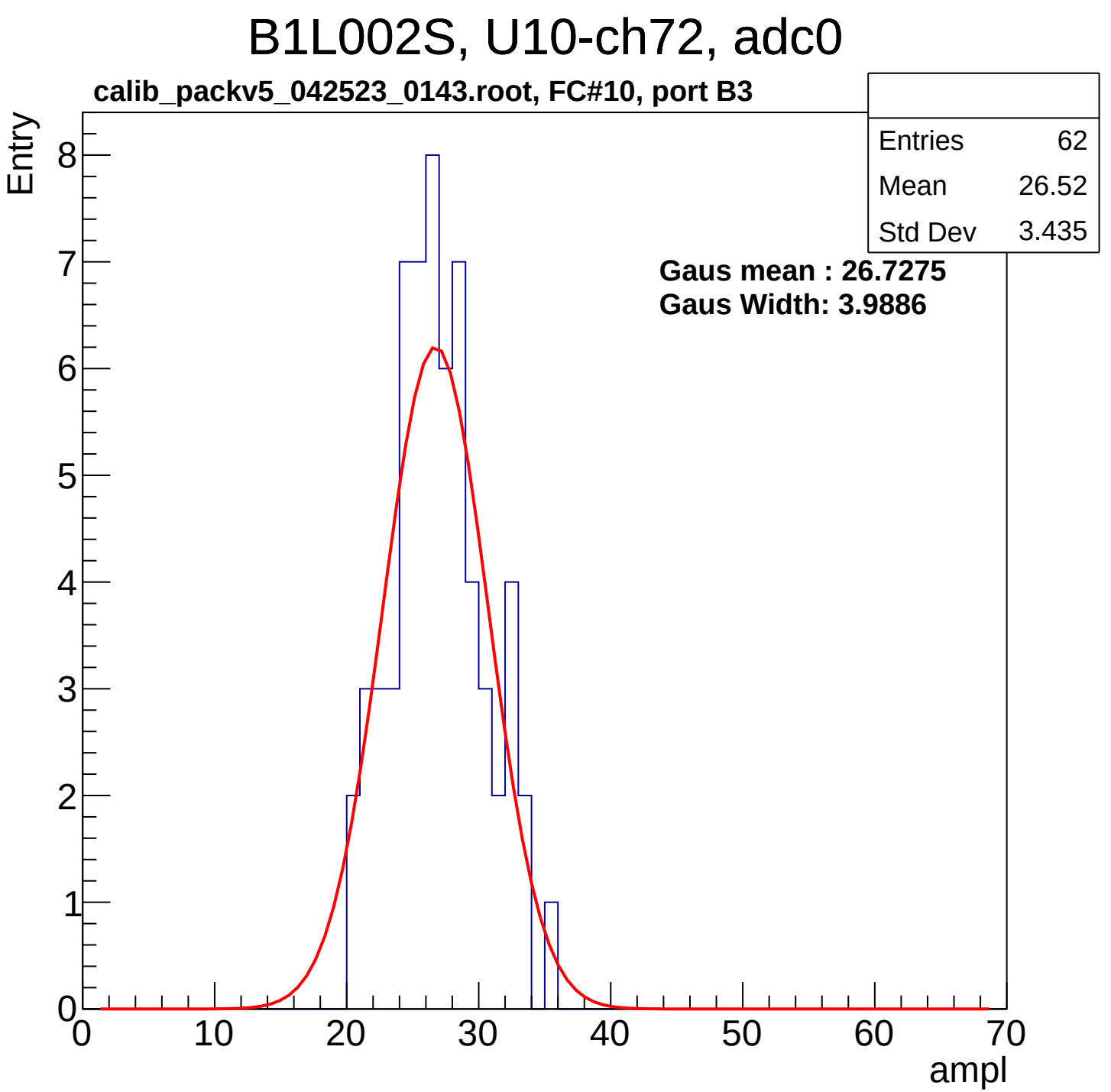
B1L002S, U10-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.52
Std Dev 3.435
Gaus mean : 26.7275
Gaus Width: 3.9886
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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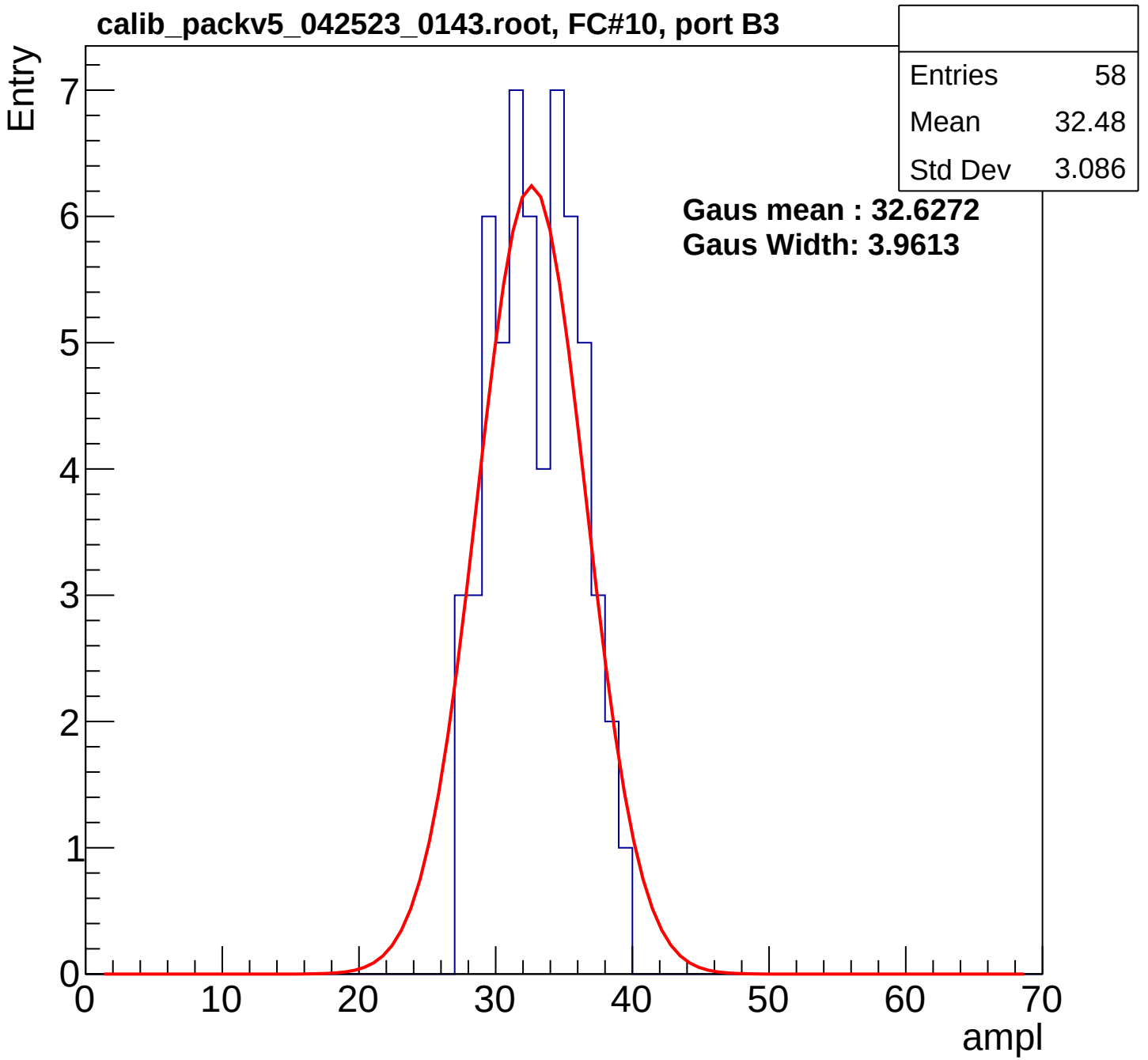
B1L002S, U10-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 32.48
Std Dev 3.086
Gaus mean : 32.6272
Gaus Width: 3.9613
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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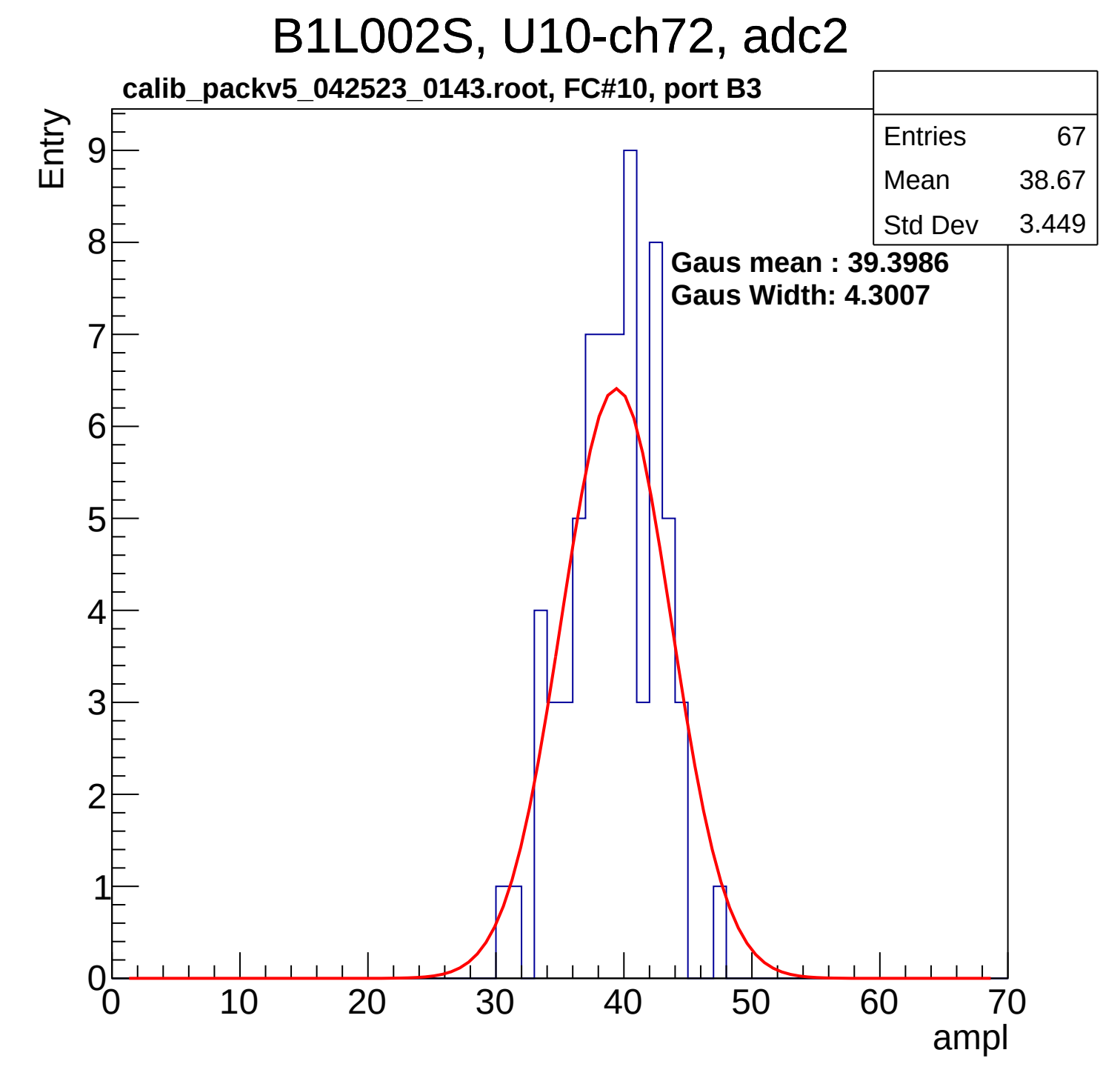
B1L002S, U10-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 38.67
Std Dev 3.449
Gaus mean : 39.3986
Gaus Width: 4.3007
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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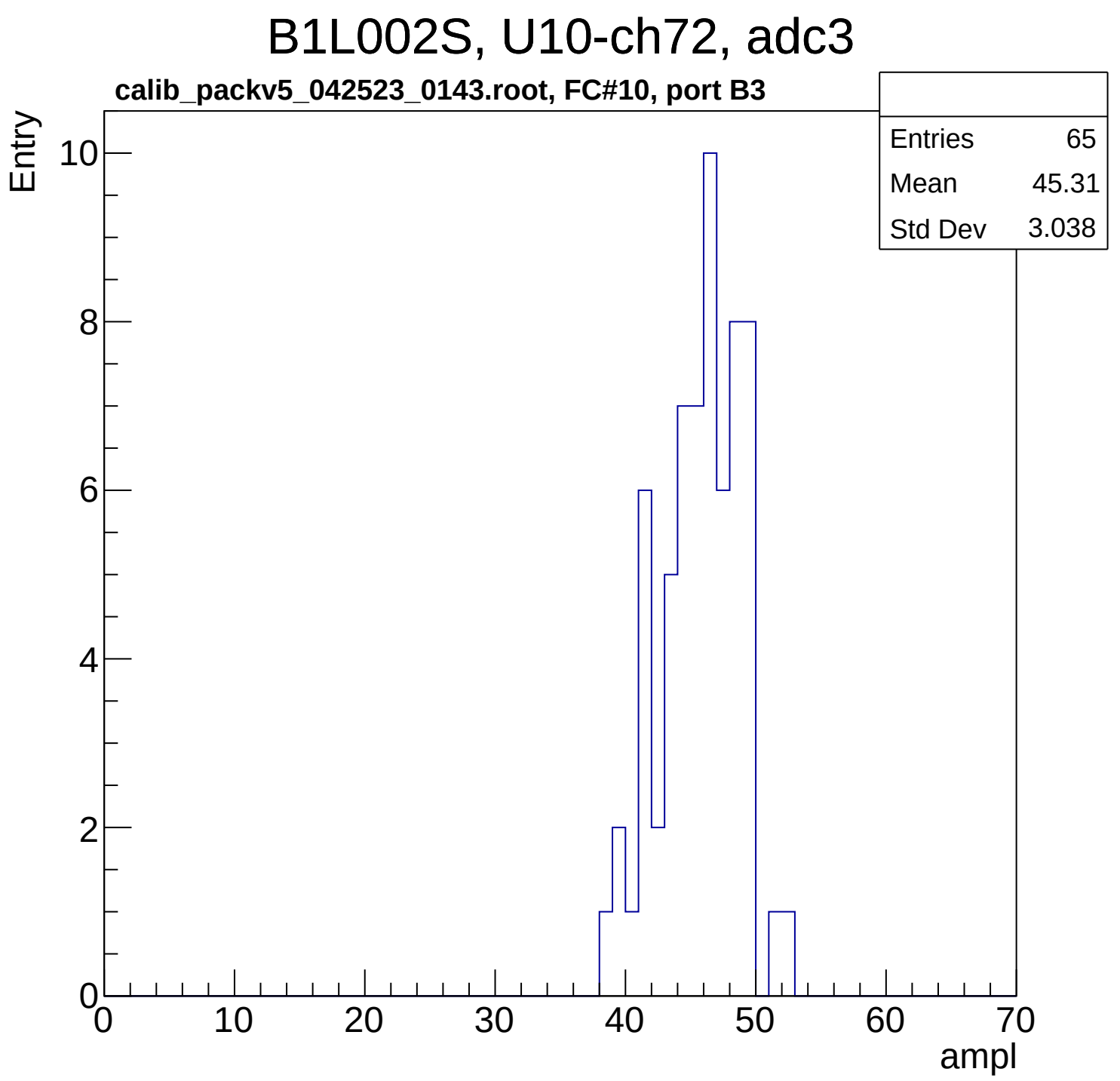
B1L002S, U10-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.31
Std Dev 3.038
Entry
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4
6
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10
0
10
20
30
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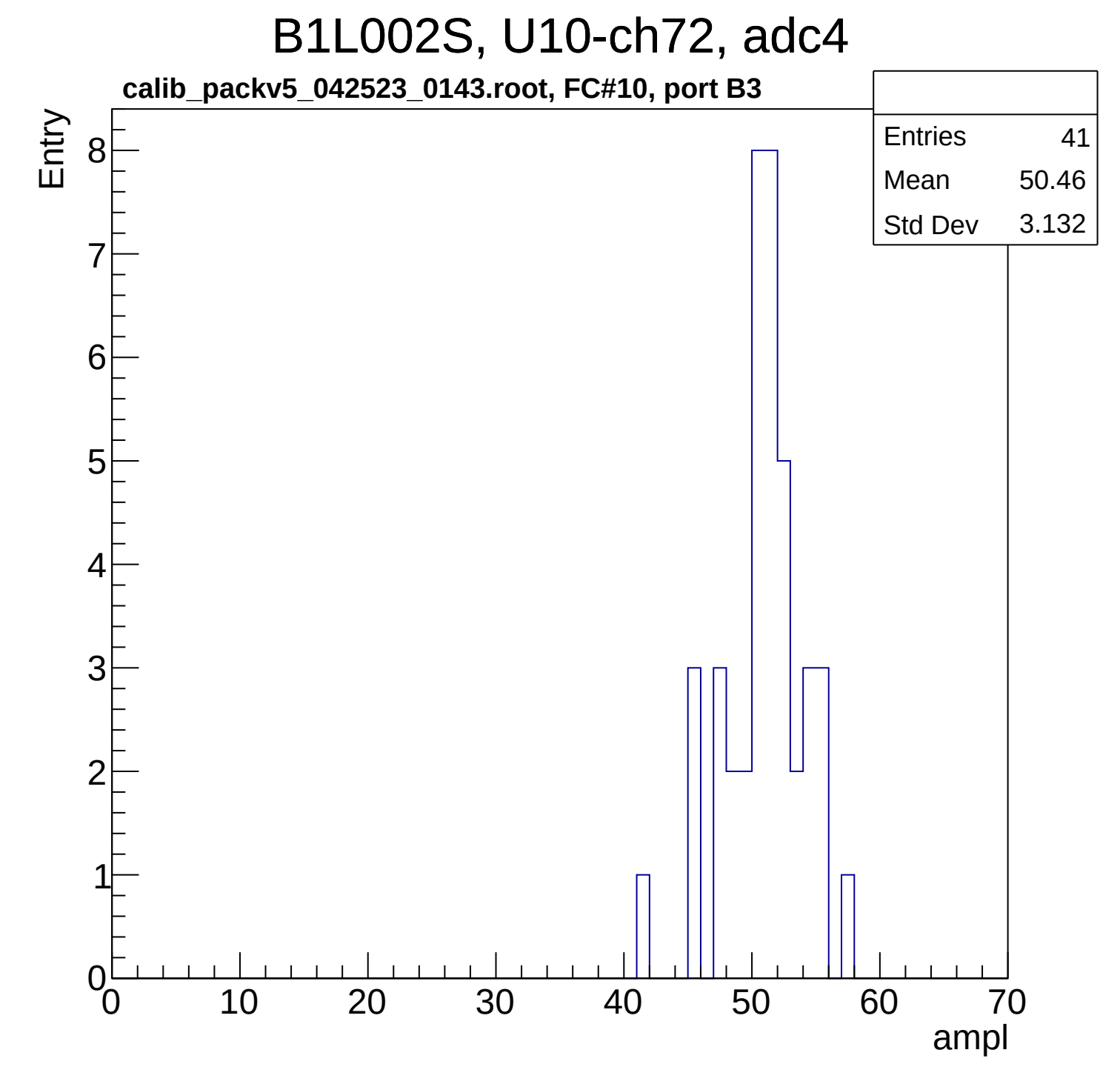
50
60
70
ampl


B1L002S, U10-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 50.46
Std Dev 3.132
Entry
ampl
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3
4
5
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7
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0
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20
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60
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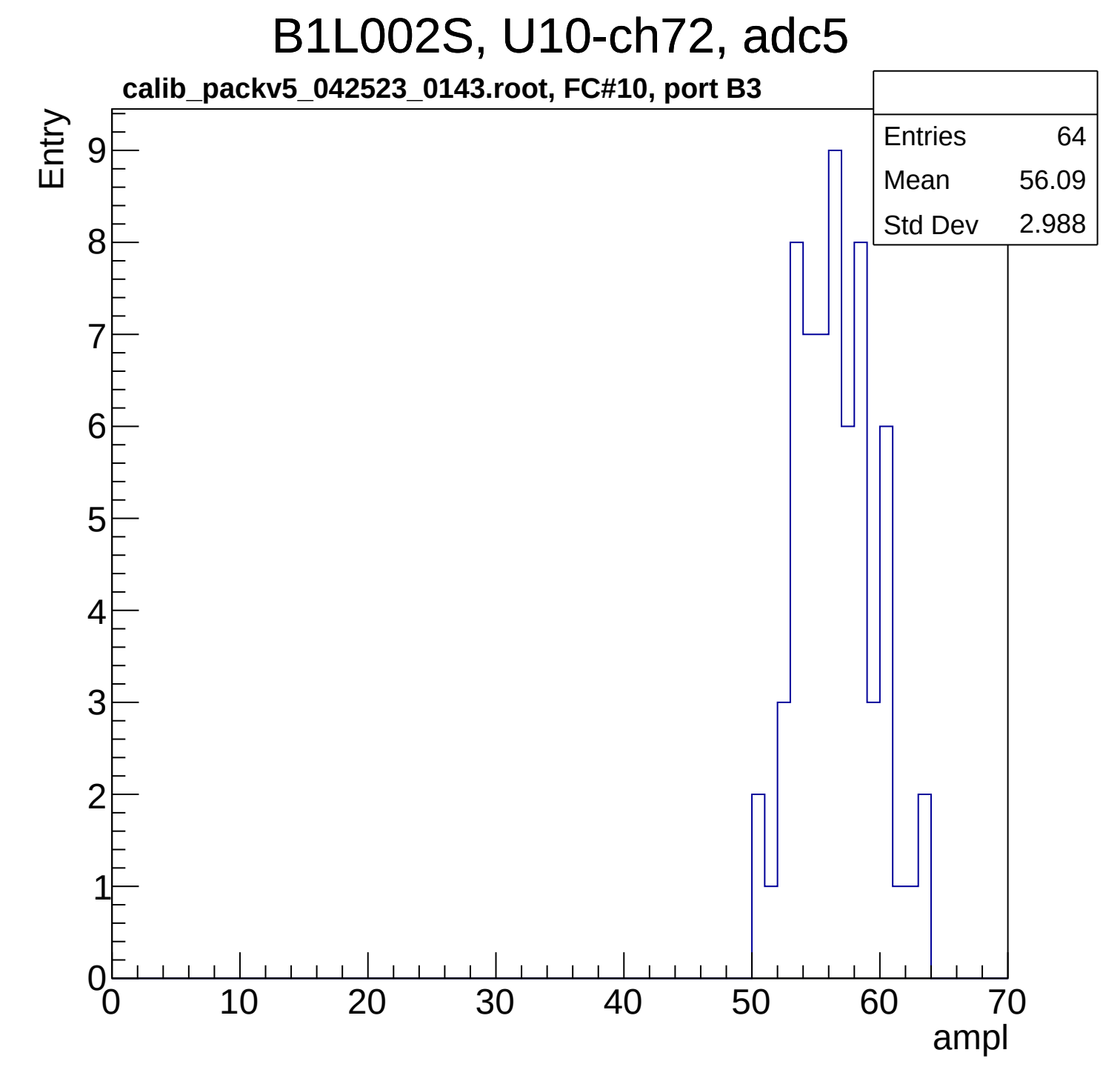
B1L002S, U10-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 56.09
Std Dev 2.988
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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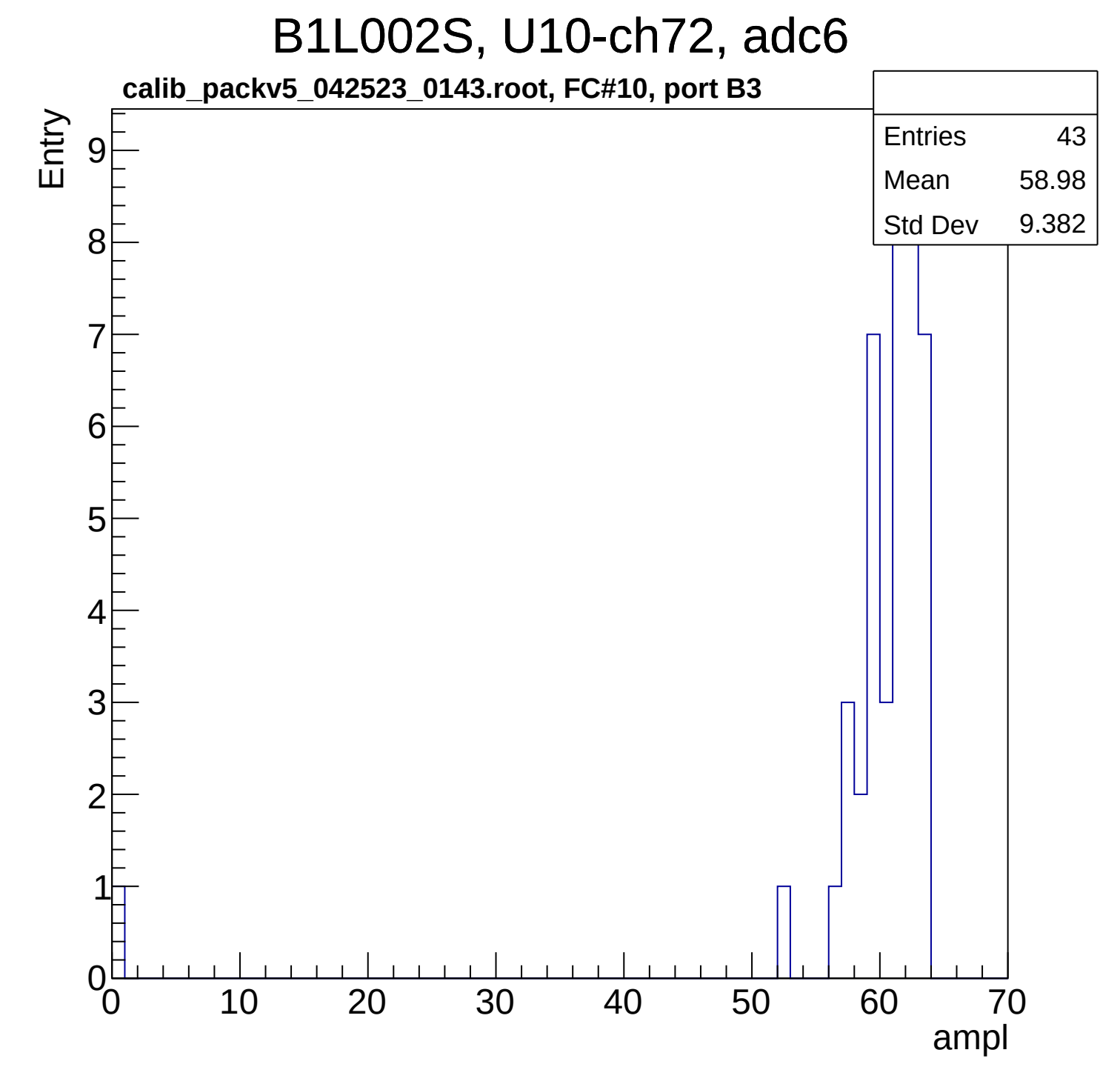
B1L002S, U10-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.98
Std Dev 9.382
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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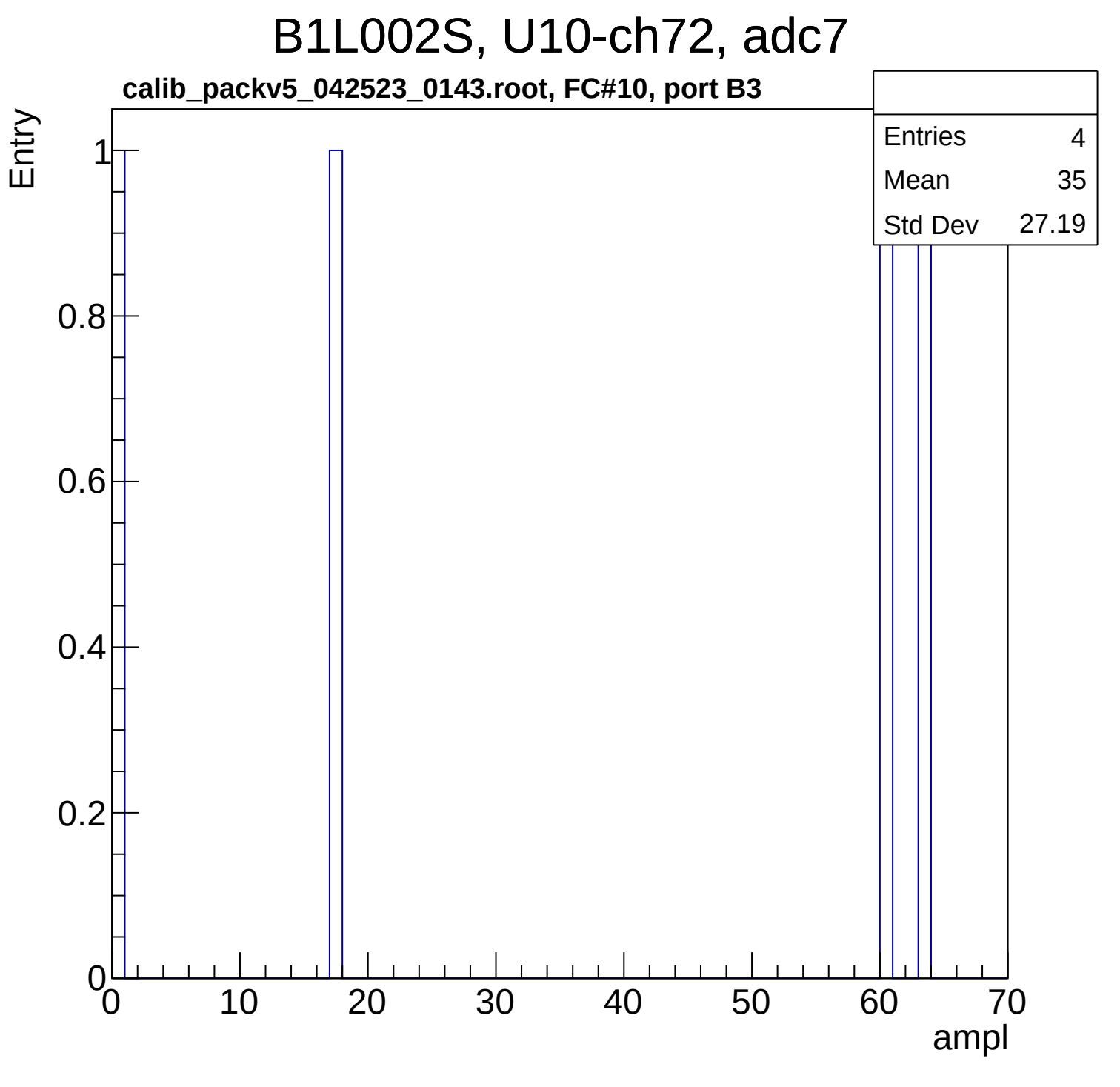
B1L002S, U10-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 35
Std Dev 27.19
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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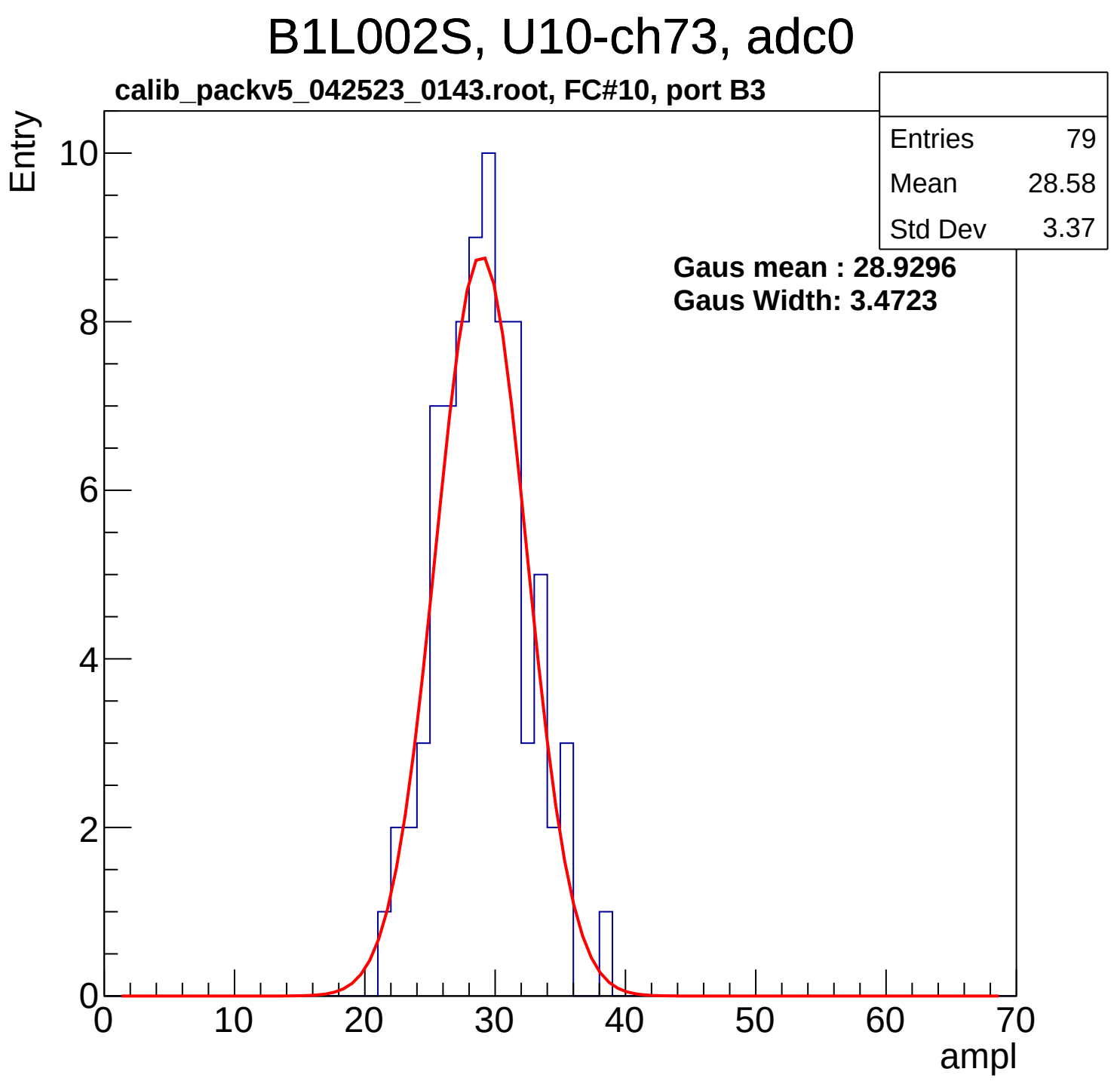
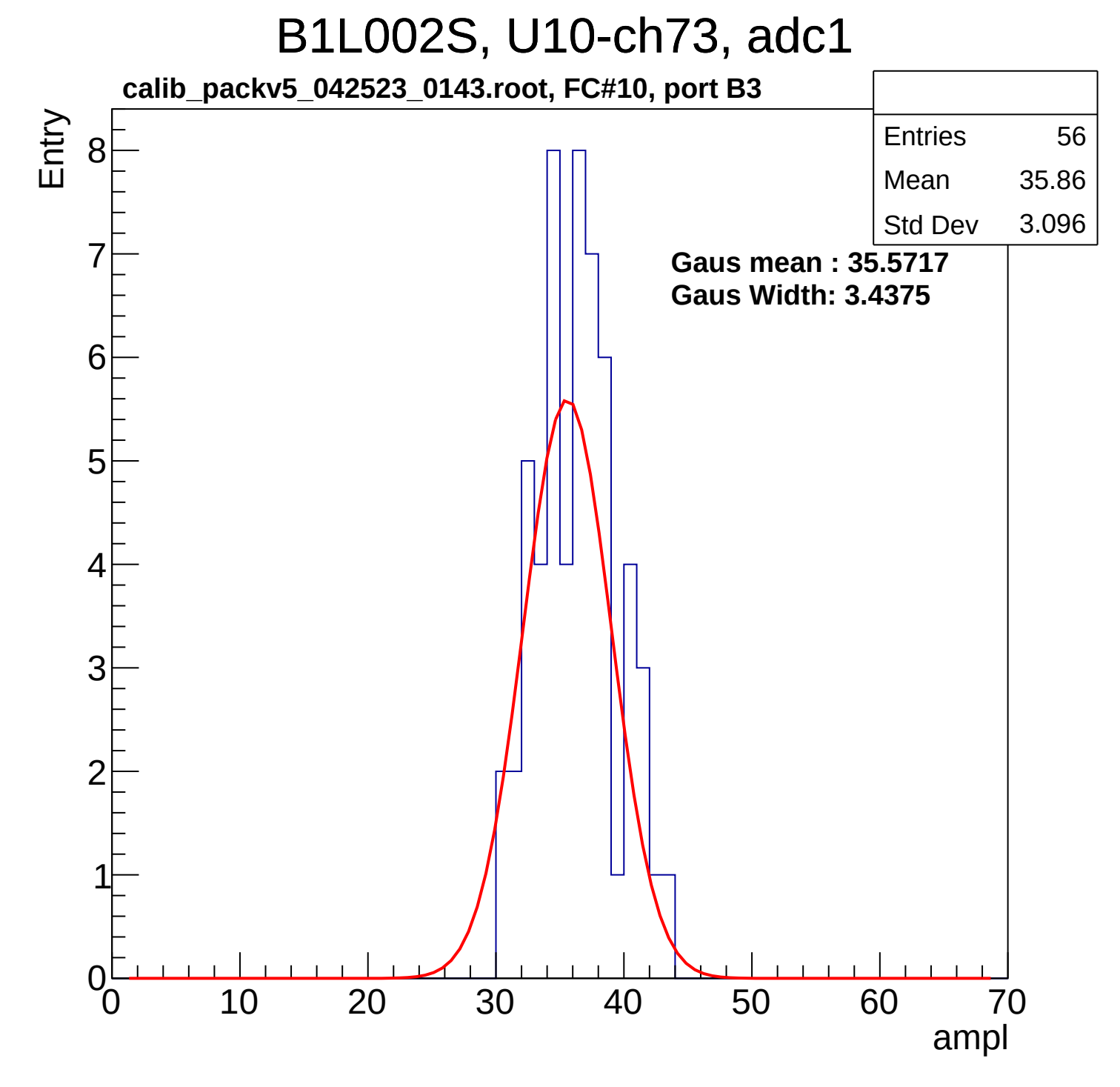

B1L002S, U10-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 28.58
Std Dev 3.37
Gaus mean : 28.9296
Gaus Width: 3.4723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.86
Std Dev 3.096
Gaus mean : 35.5717
Gaus Width: 3.4375
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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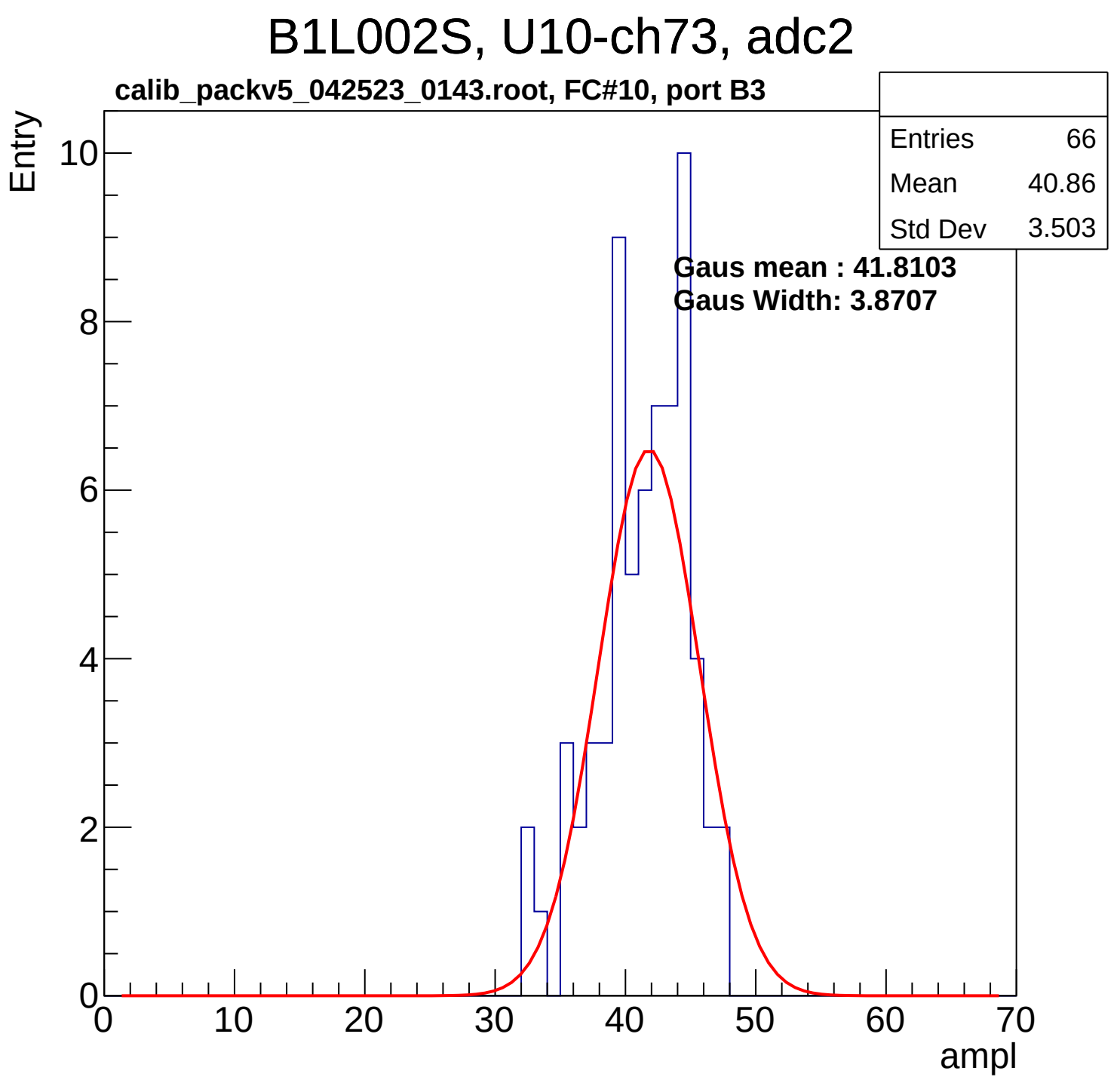
B1L002S, U10-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.86
Std Dev 3.503
Gaus mean : 41.8103
Gaus Width: 3.8707
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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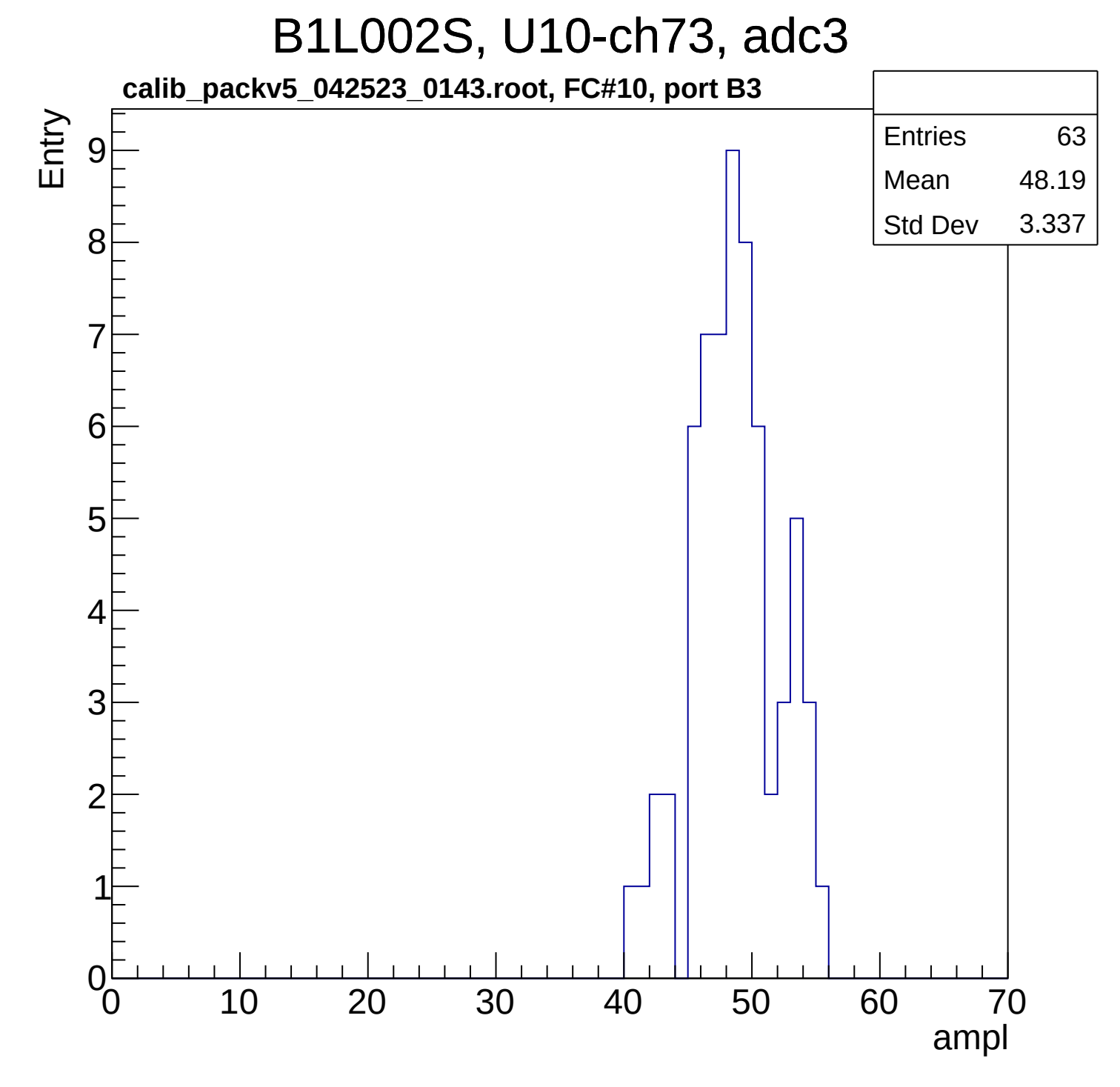
B1L002S, U10-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.19
Std Dev 3.337
Entry
ampl
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6
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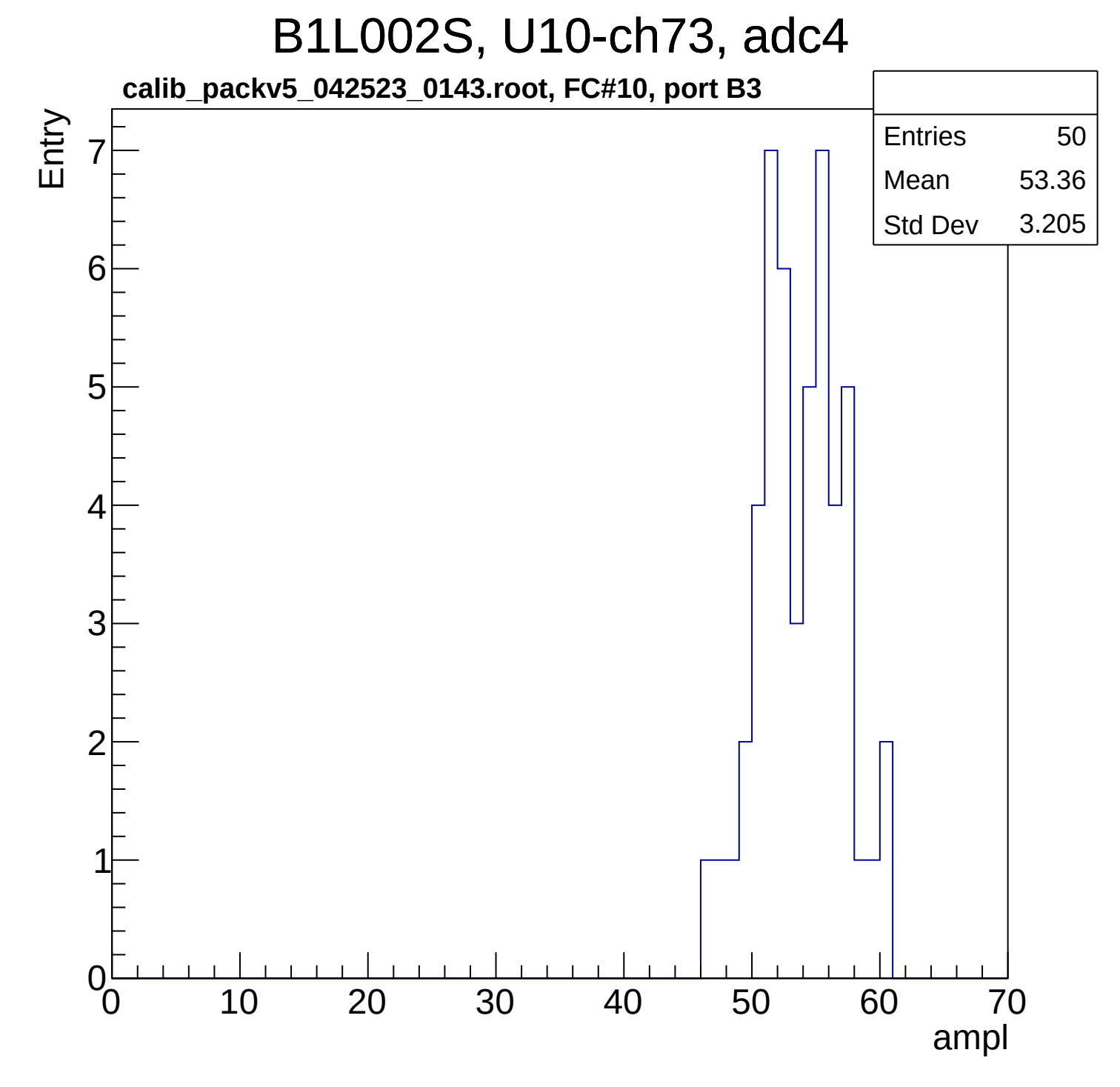
B1L002S, U10-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.36
Std Dev 3.205
Entry
ampl
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4
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0
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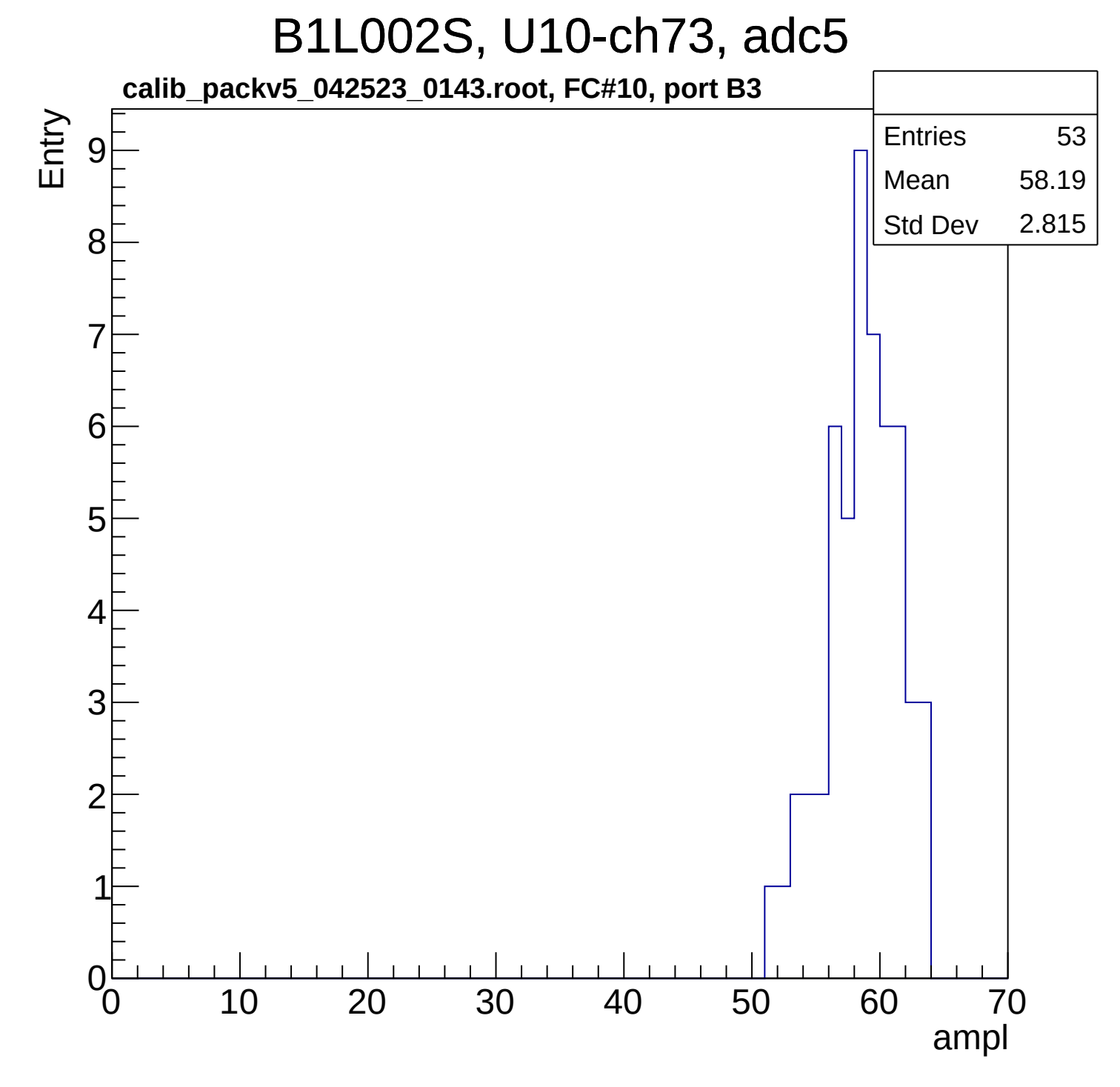
B1L002S, U10-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.19
Std Dev 2.815
Entry
ampl
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3
4
5
6
7
8
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0
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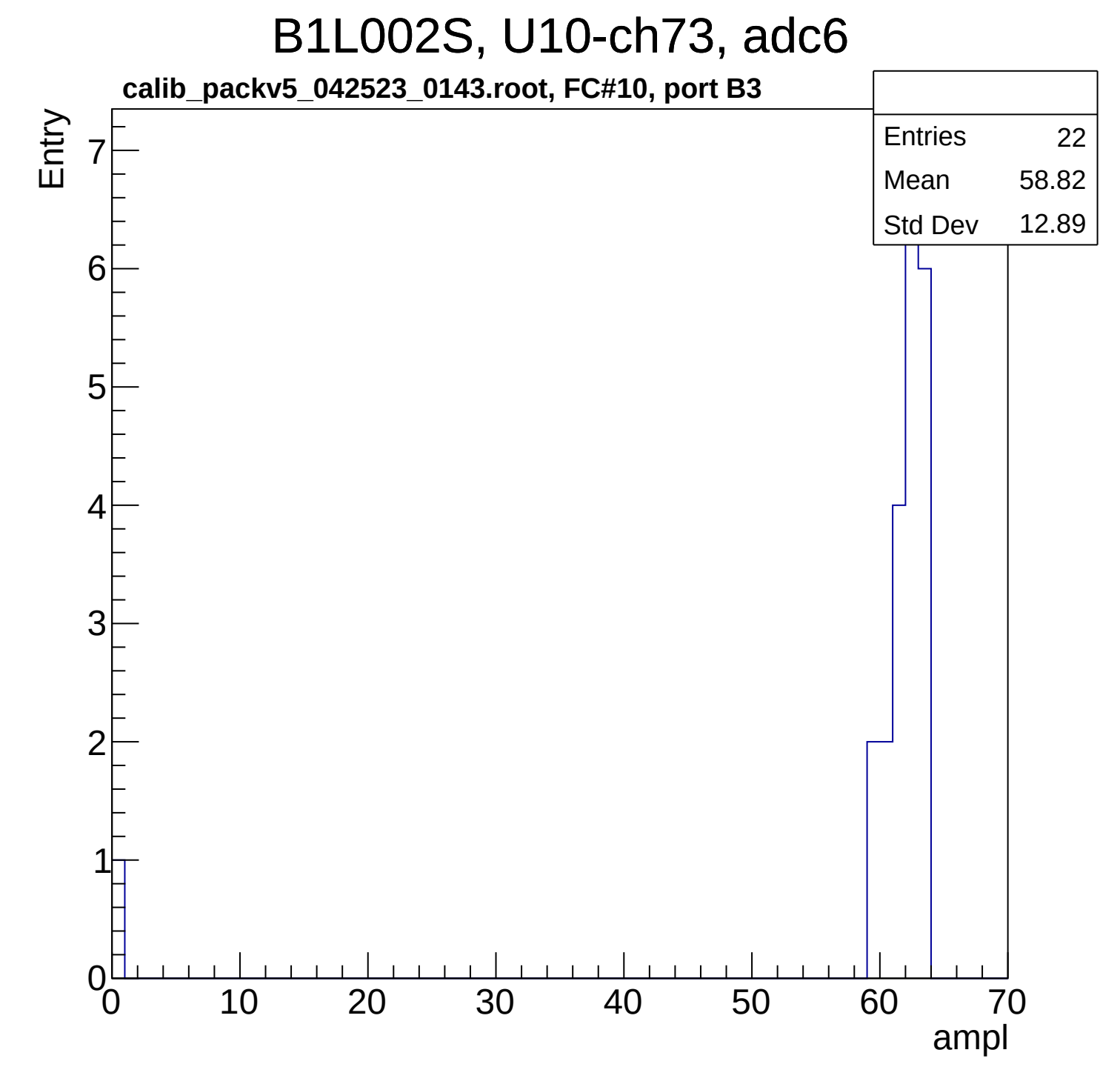
B1L002S, U10-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.82
Std Dev 12.89
Entry
0
1
2
3
4
5
6
7
0
10
20
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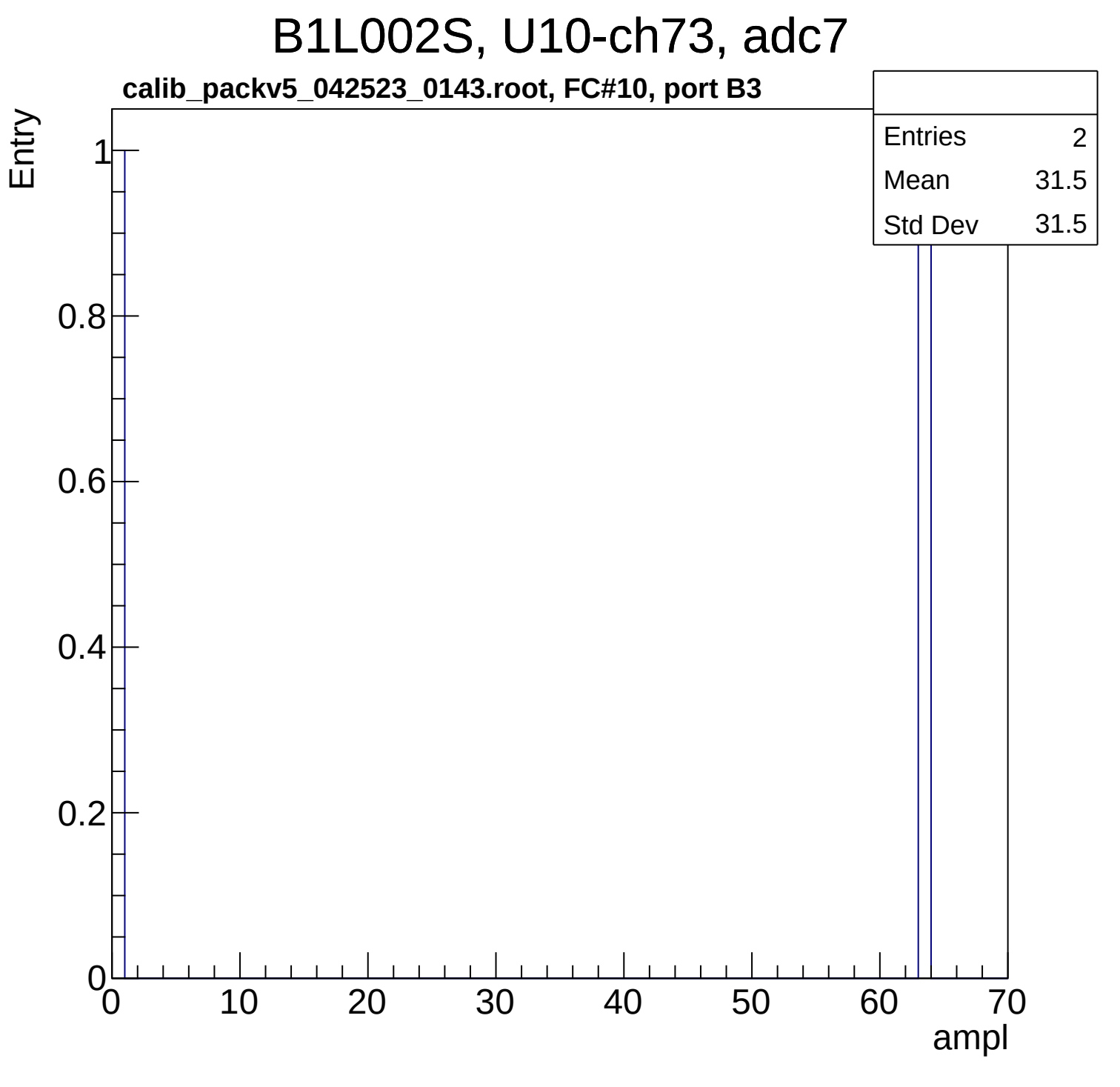
50
60
70
ampl


B1L002S, U10-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
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0.6
0.4
0.2
0
0
10
20
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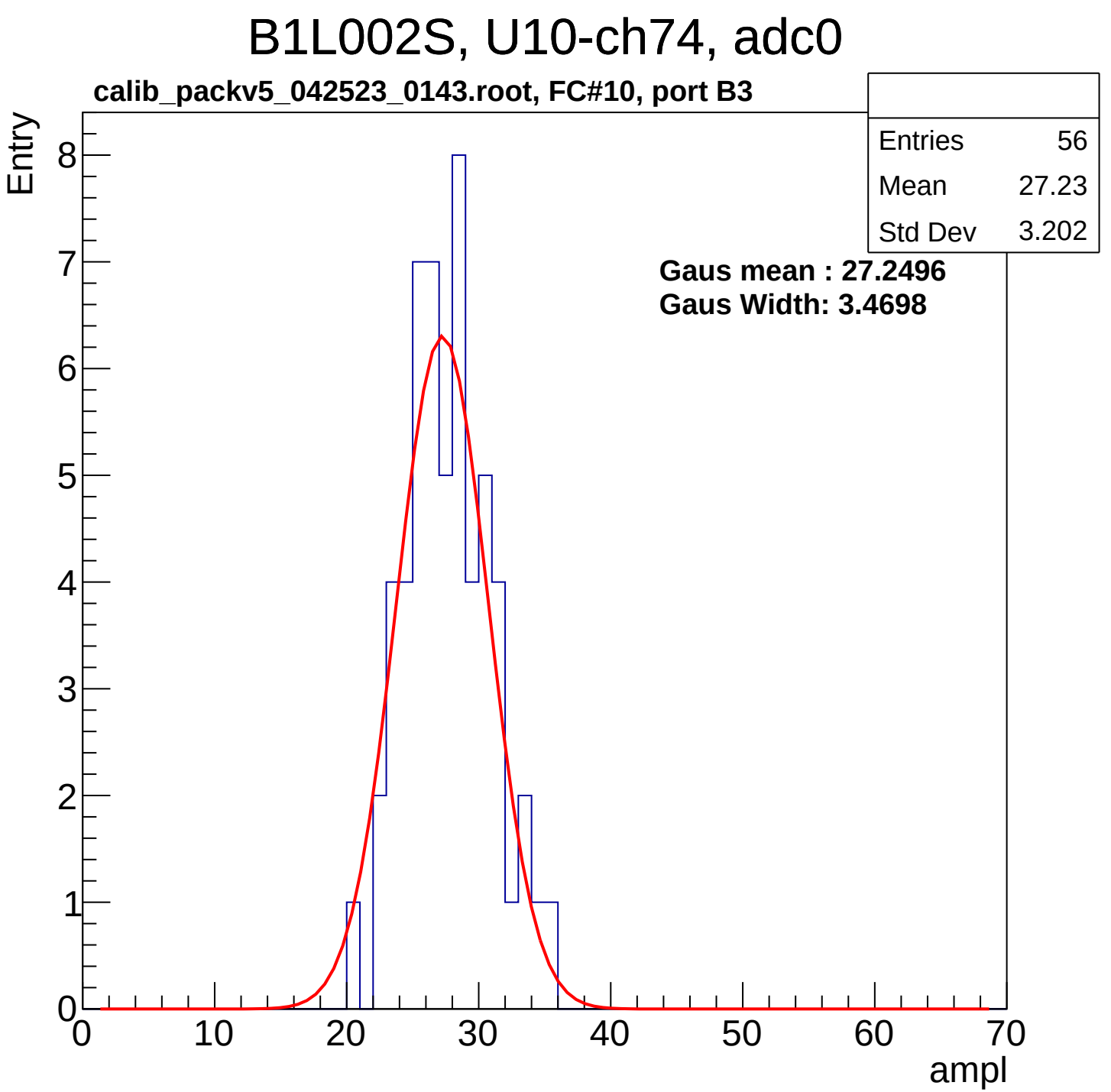
40
50
60
70
ampl


B1L002S, U10-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.23
Std Dev 3.202
Gaus mean : 27.2496
Gaus Width: 3.4698
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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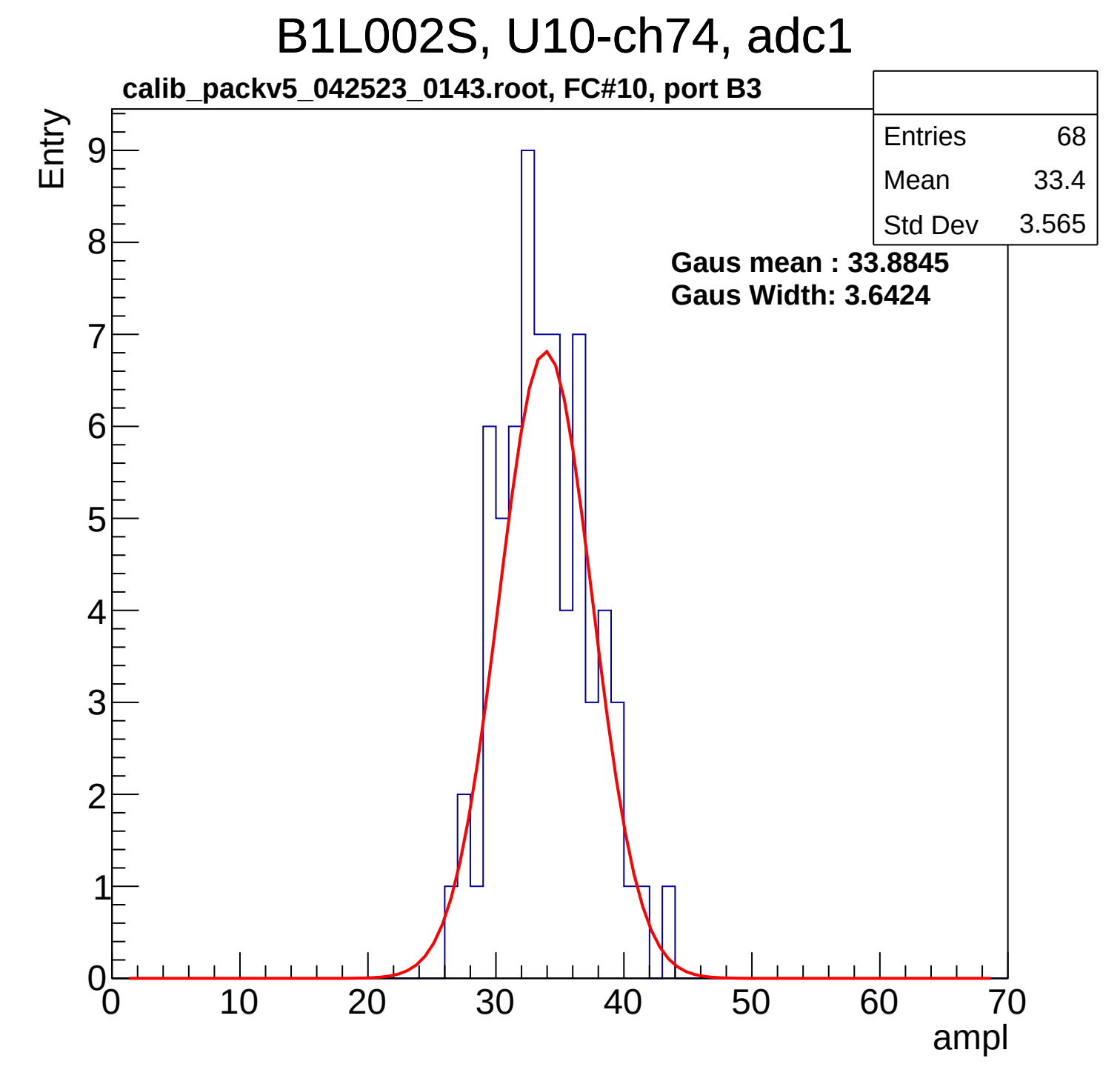
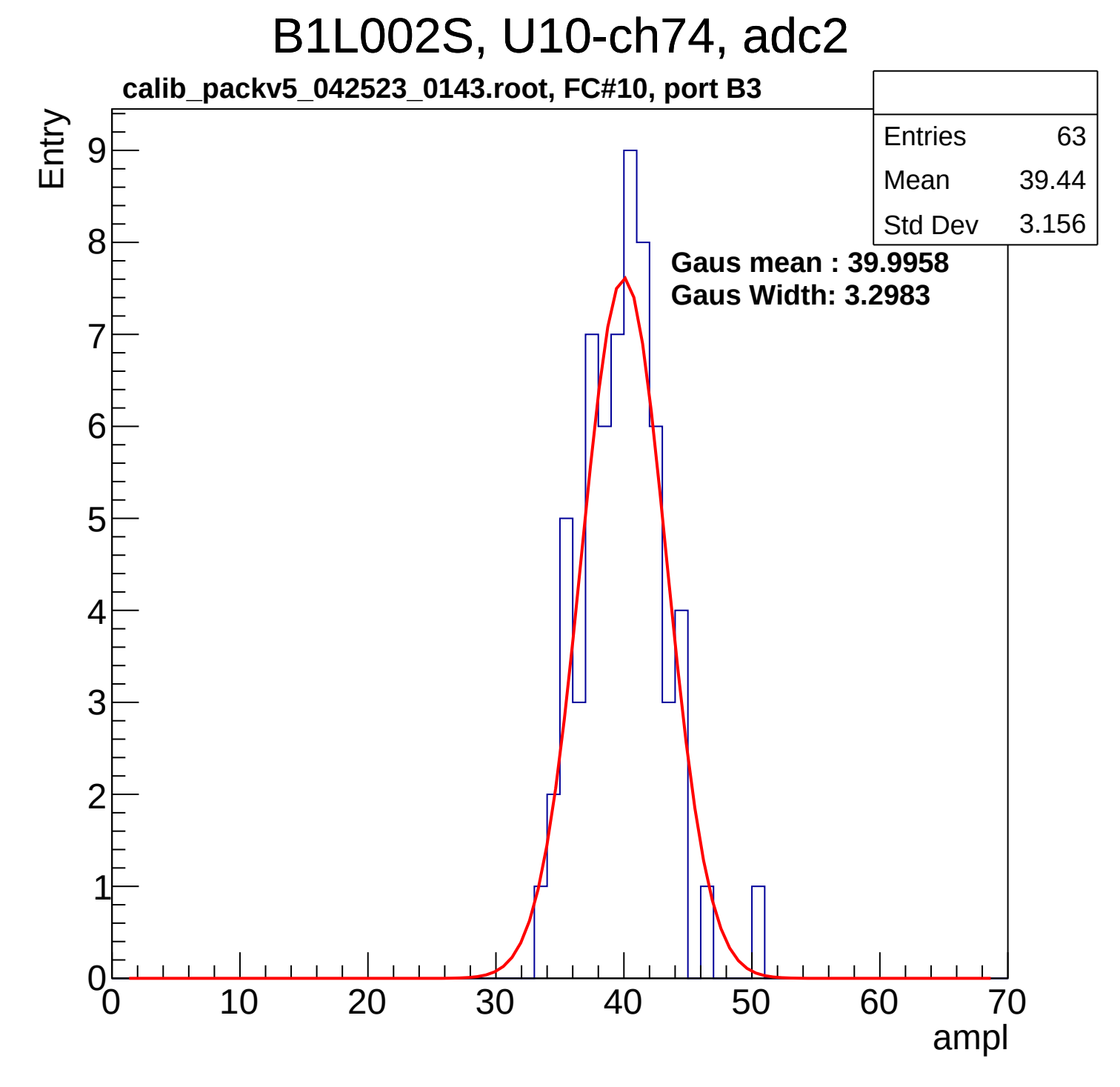

B1L002S, U10-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.4
Std Dev 3.565
Gaus mean : 33.8845
Gaus Width: 3.6424
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.44
Std Dev 3.156
Gaus mean : 39.9958
Gaus Width: 3.2983
Entry
ampl
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2
3
4
5
6
7
8
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0
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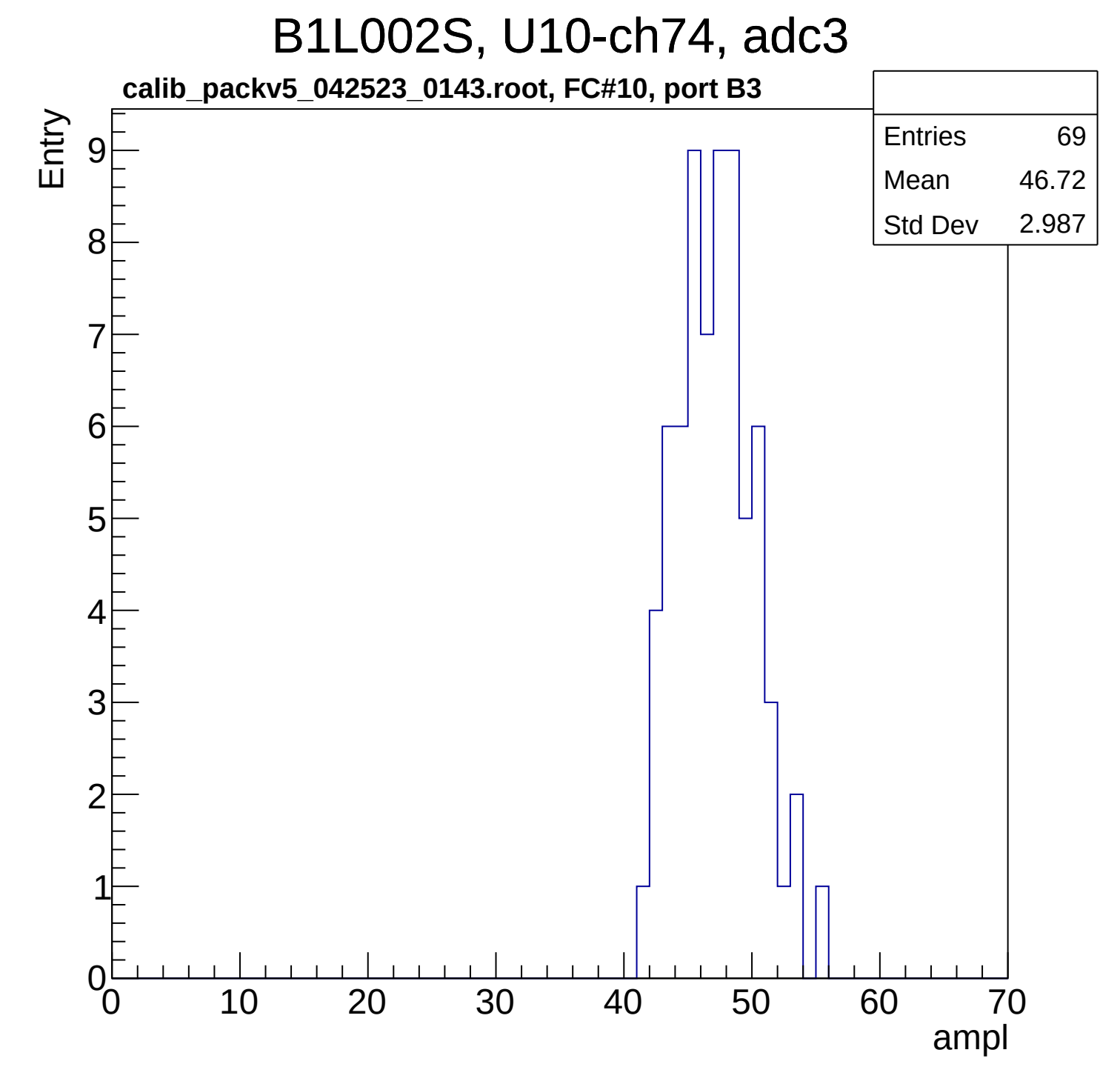
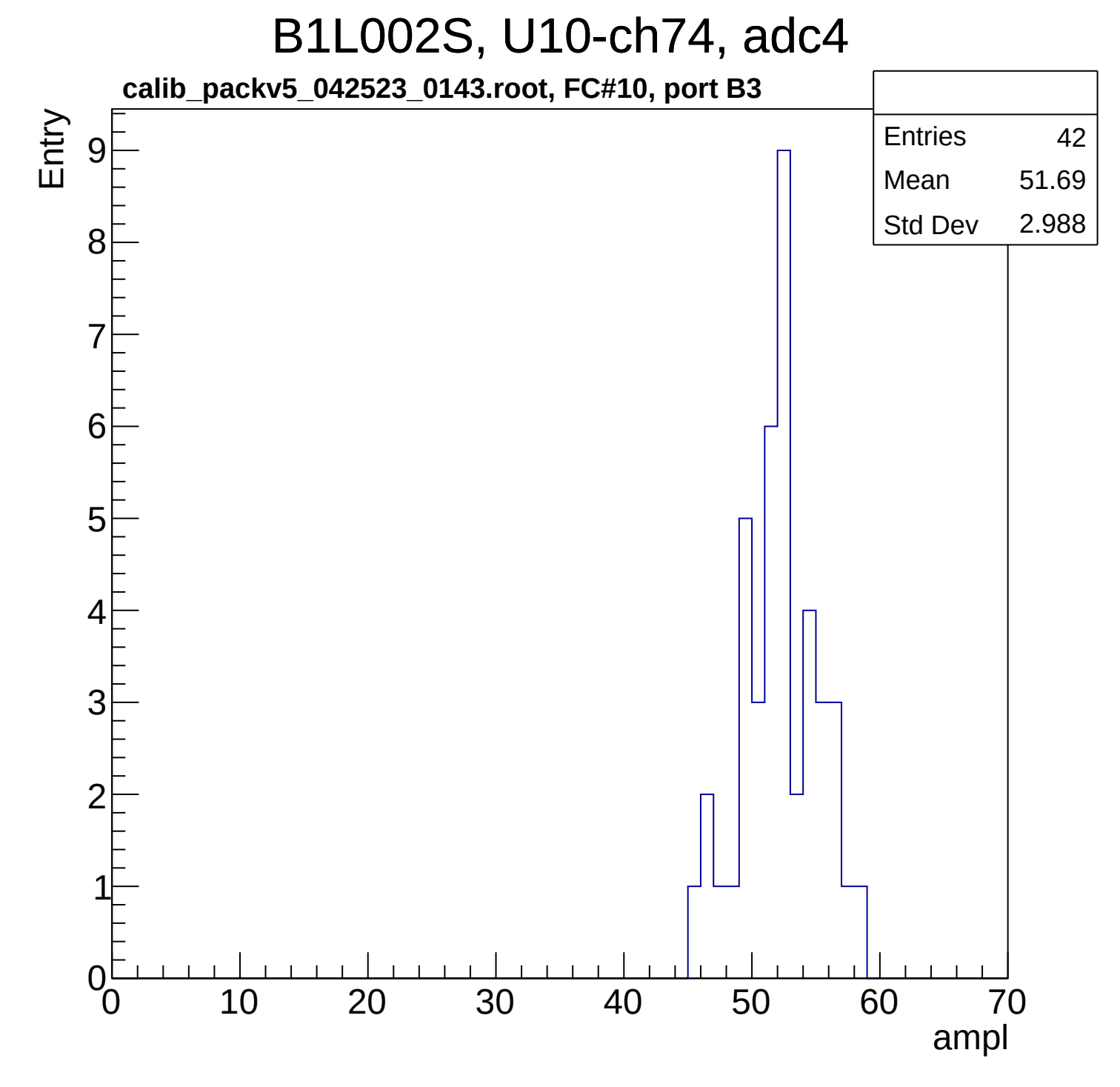

B1L002S, U10-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.72
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 51.69
Std Dev 2.988
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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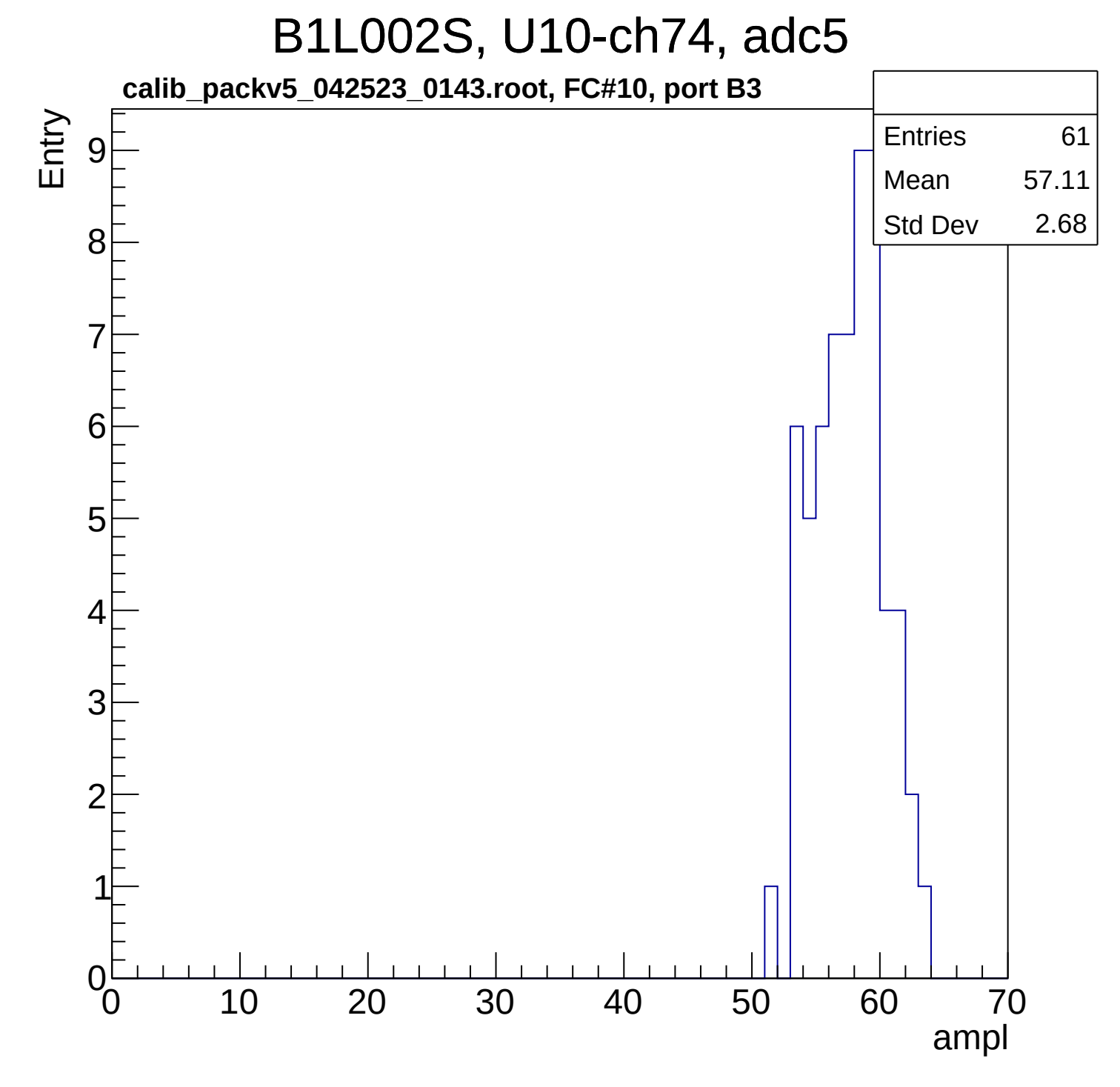
B1L002S, U10-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.11
Std Dev 2.68
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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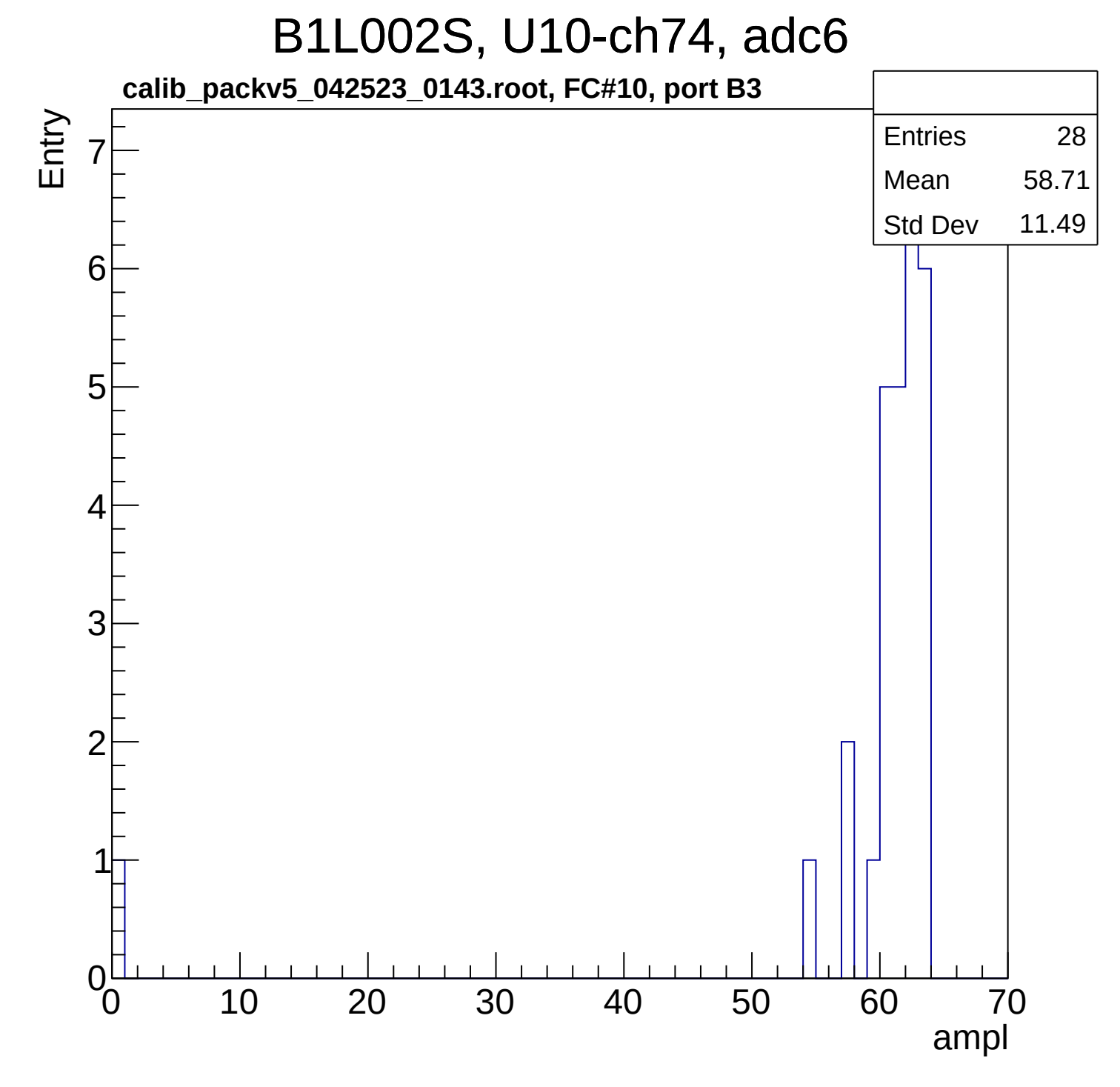
B1L002S, U10-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.71
Std Dev 11.49
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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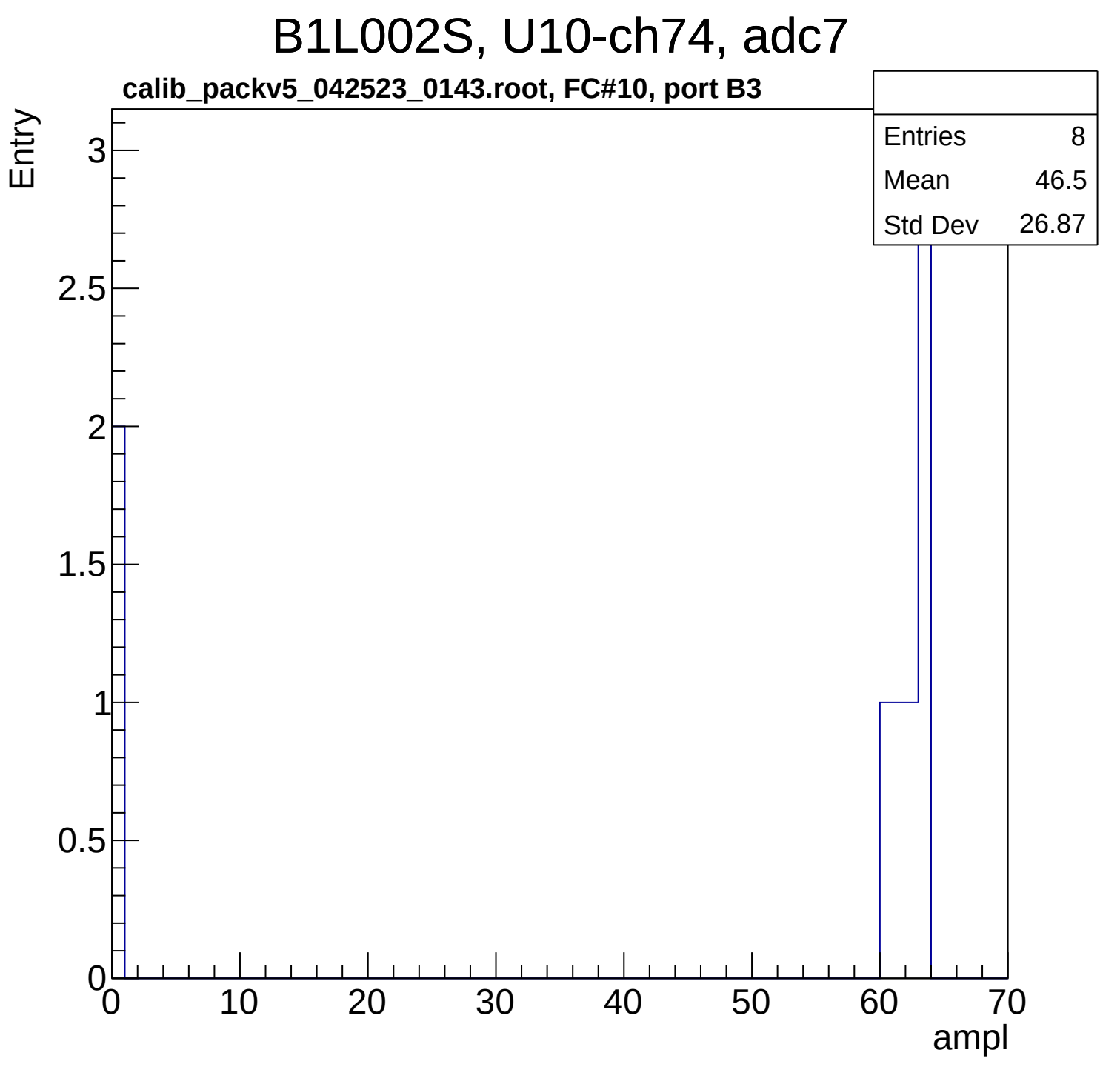
B1L002S, U10-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 46.5
Std Dev 26.87
Entry
ampl
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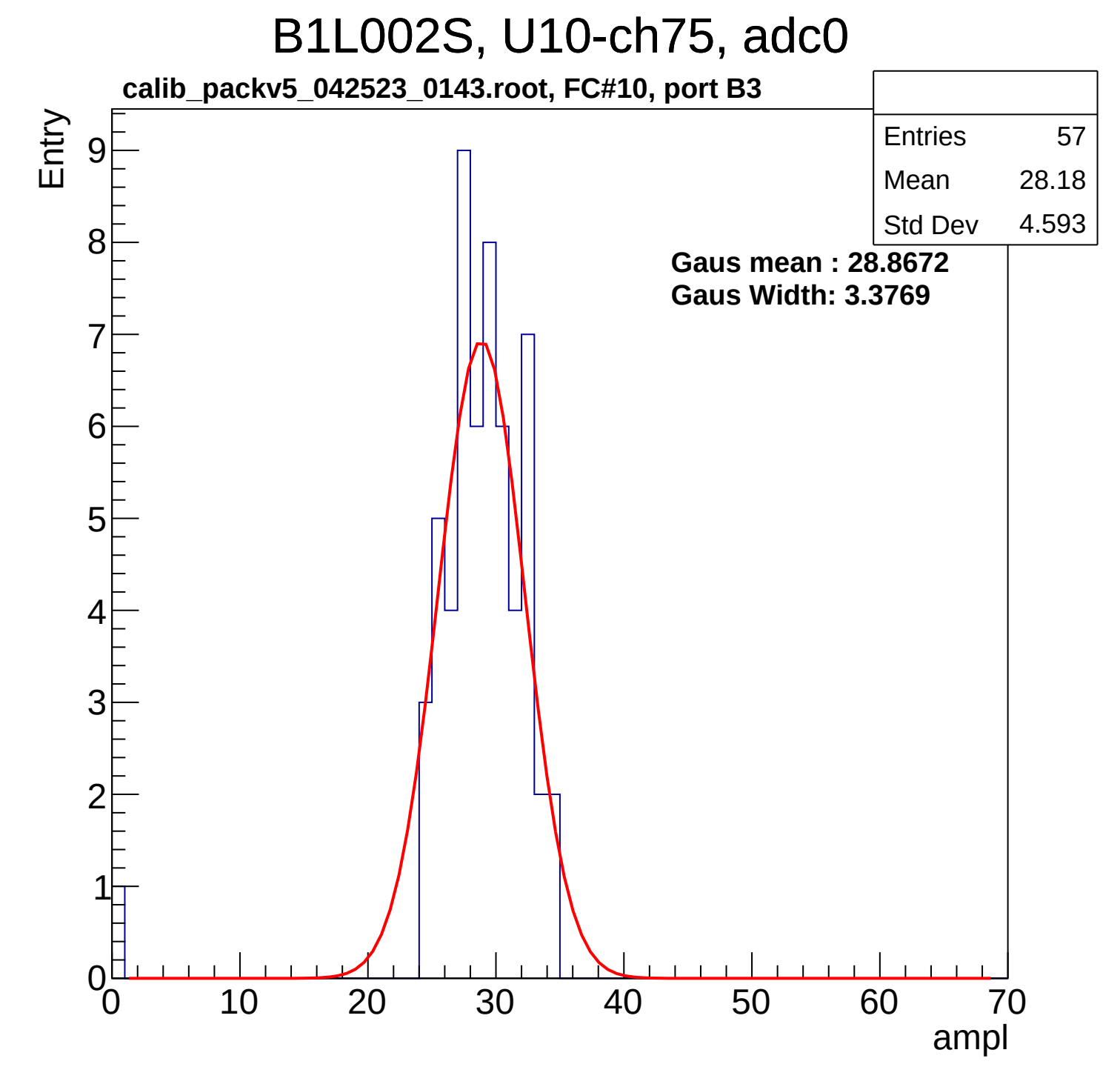
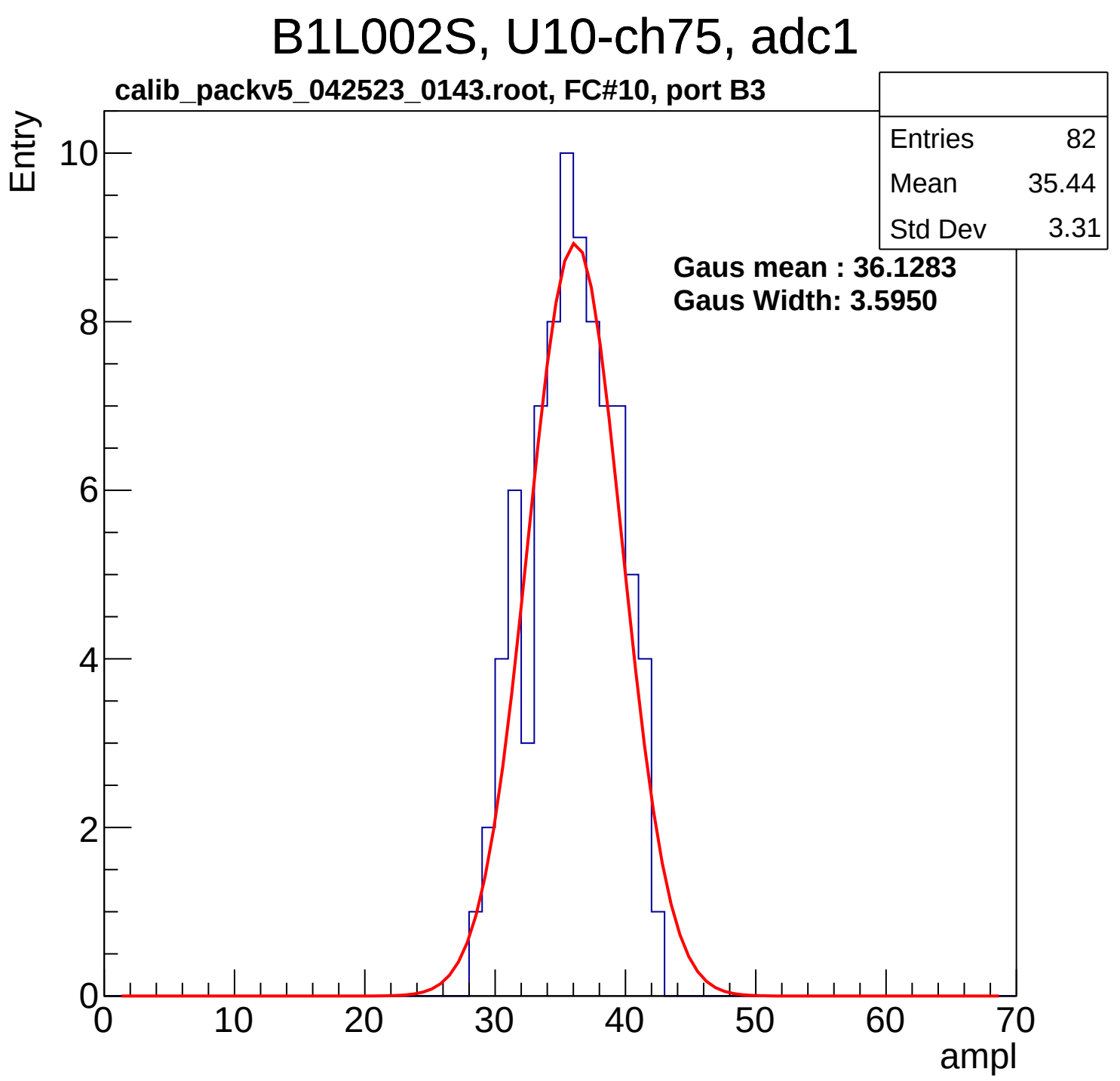
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.18
Std Dev 4.593
Gaus mean : 28.8672
Gaus Width: 3.3769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 35.44
Std Dev 3.31
Gaus mean : 36.1283
Gaus Width: 3.5950
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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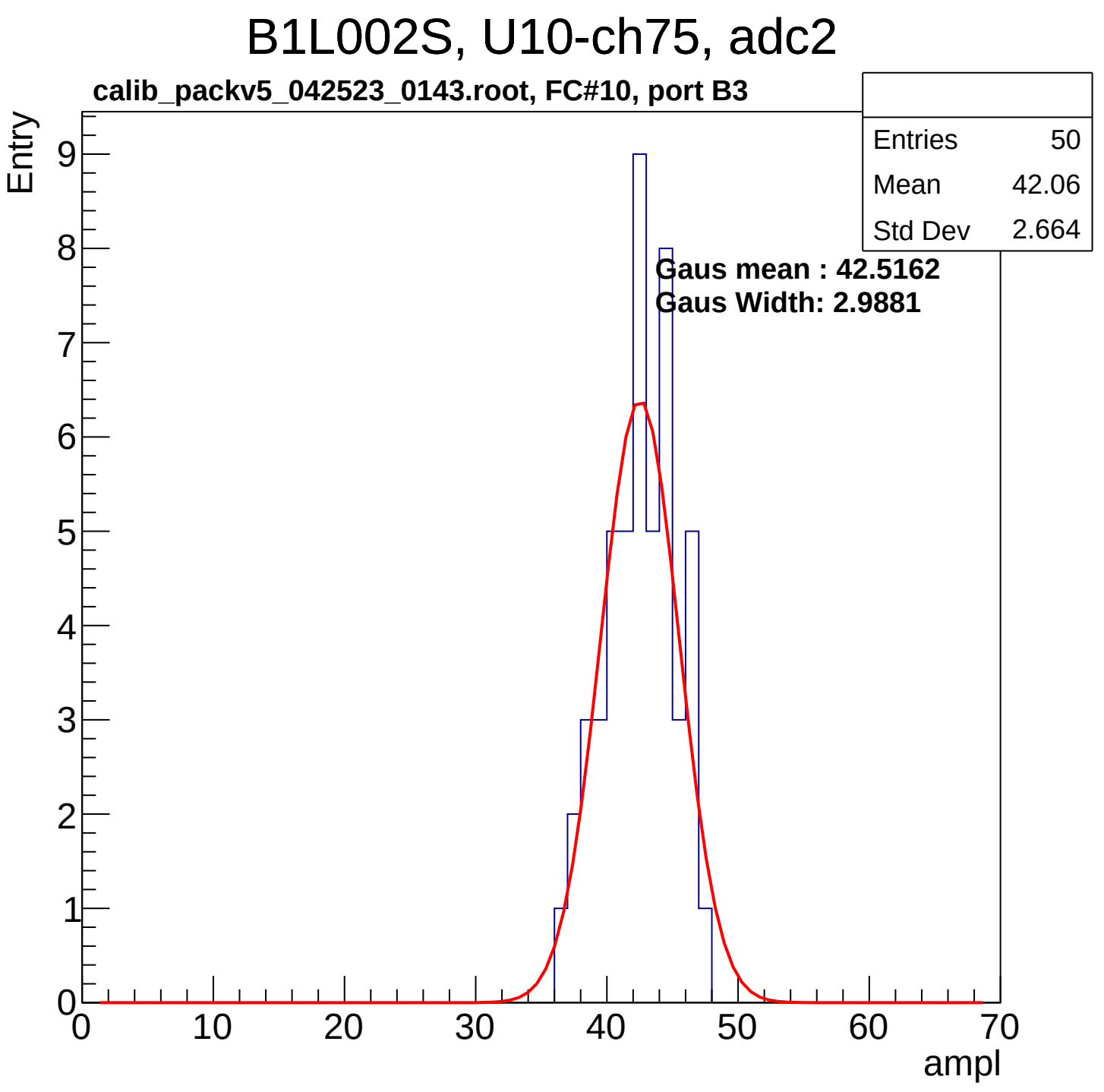
B1L002S, U10-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.06
Std Dev 2.664
Gaus mean : 42.5162
Gaus Width: 2.9881
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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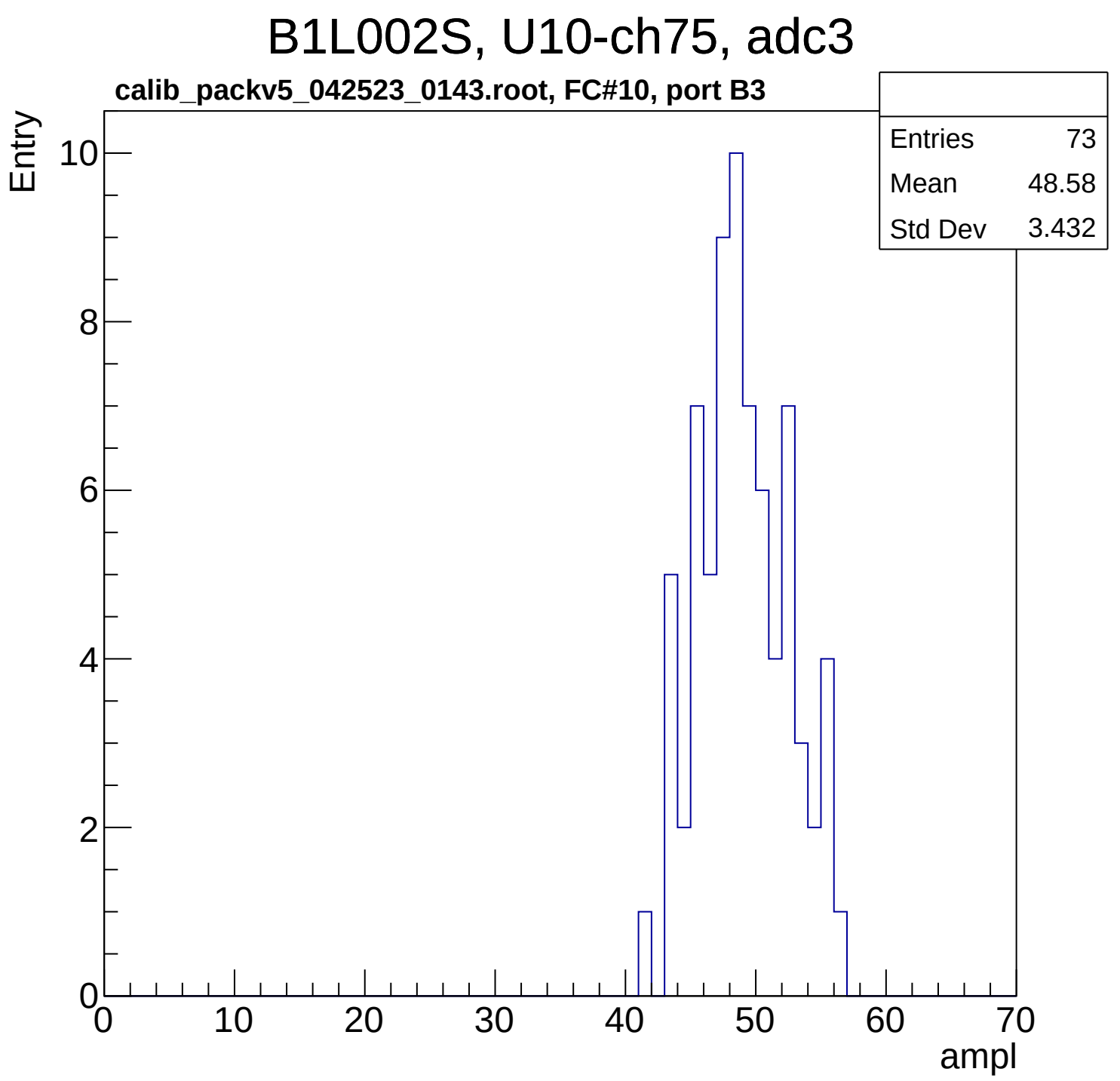
B1L002S, U10-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.58
Std Dev 3.432
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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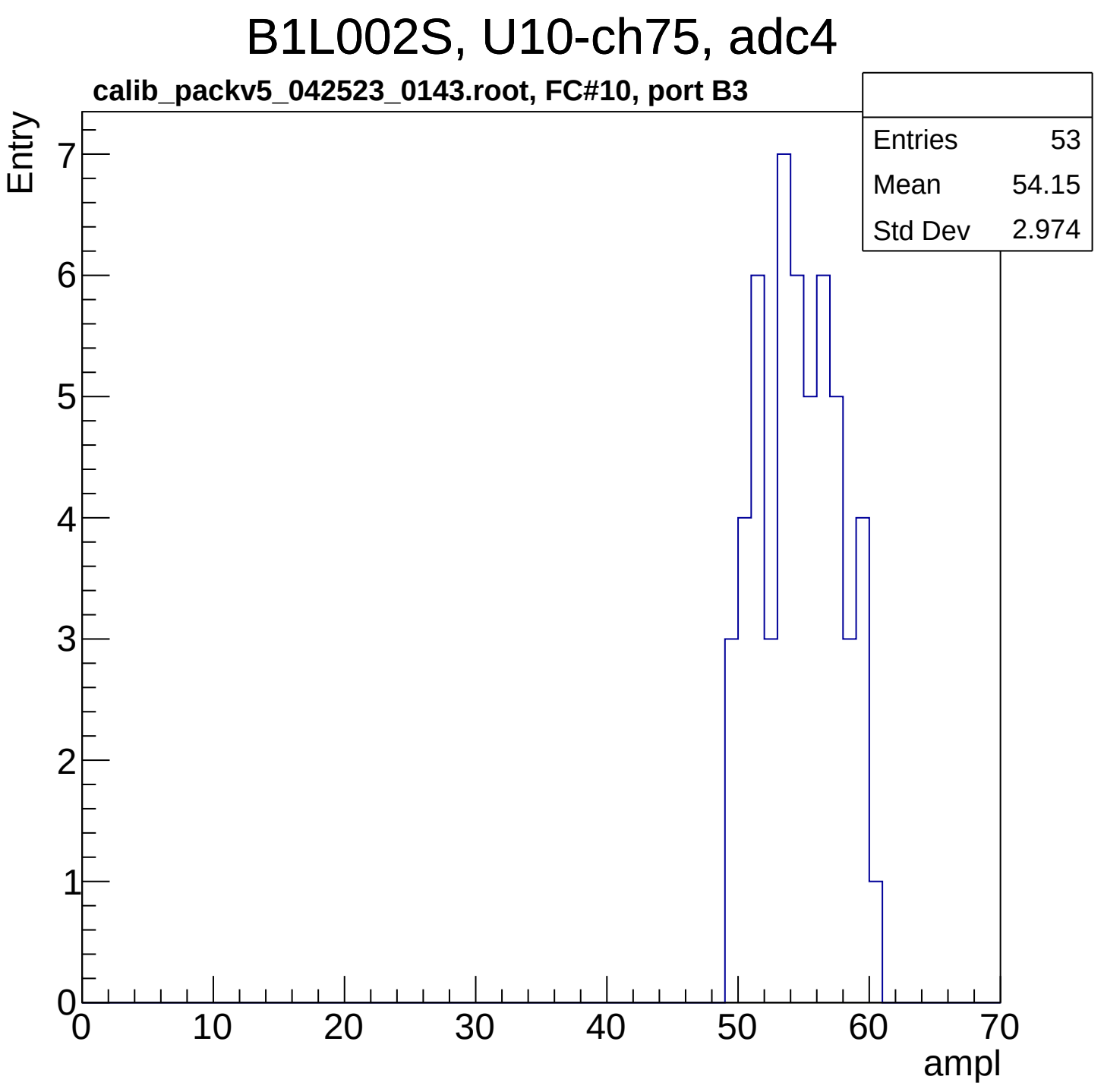
B1L002S, U10-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.15
Std Dev 2.974
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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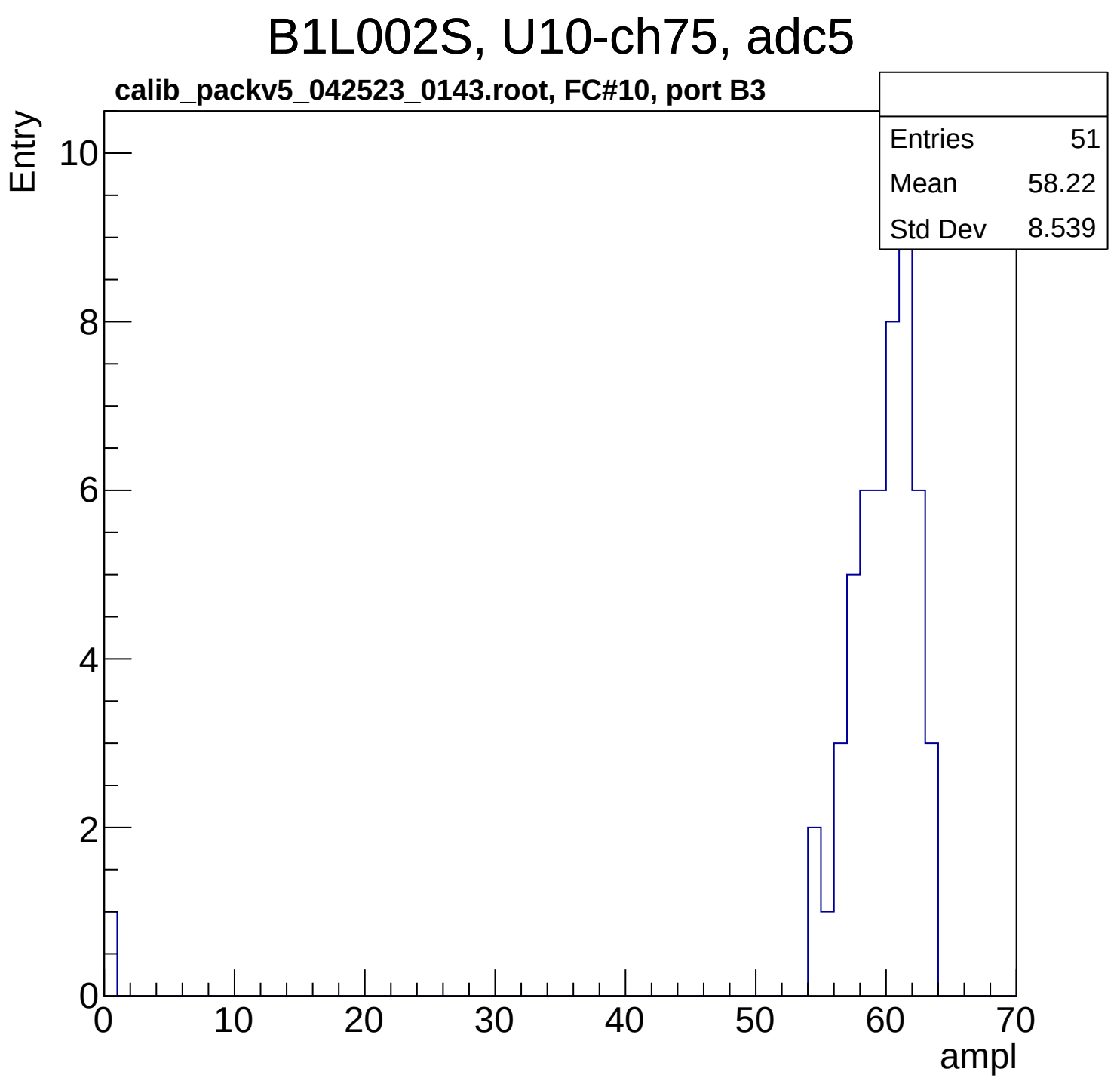
B1L002S, U10-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.22
Std Dev 8.539
Entry
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30
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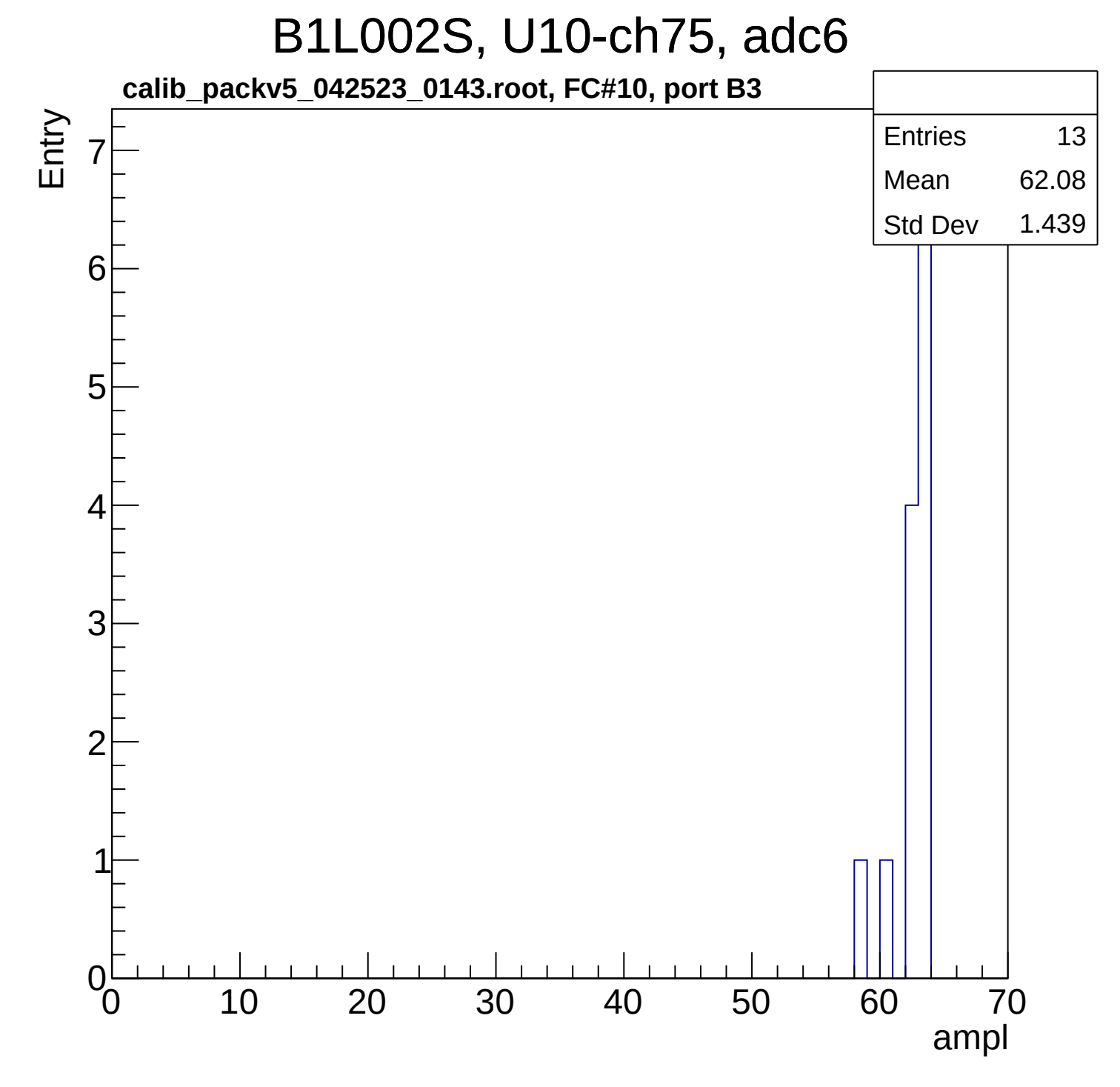
60
70
ampl


B1L002S, U10-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 62.08
Std Dev 1.439
Entry
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10
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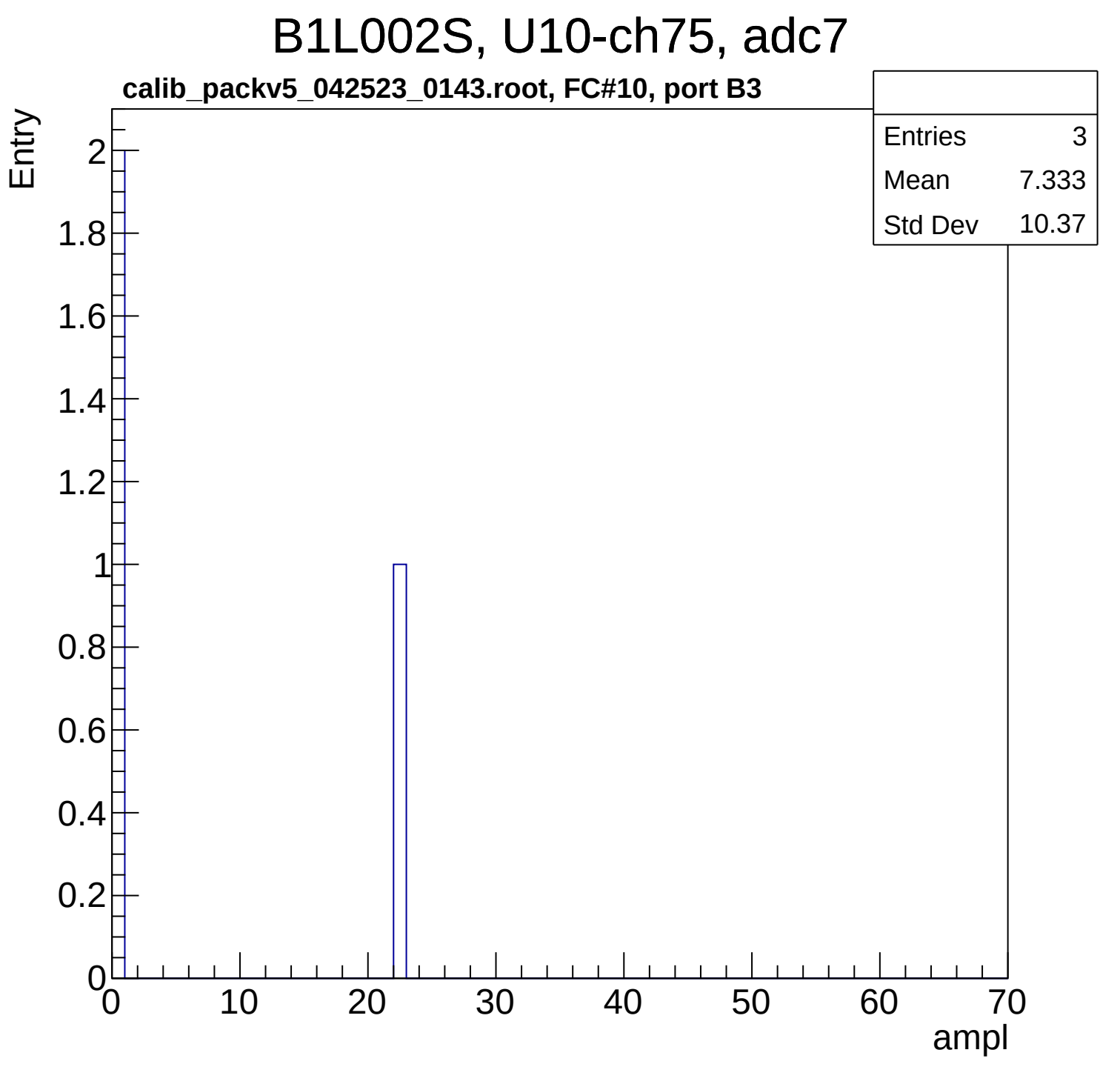
70
ampl


B1L002S, U10-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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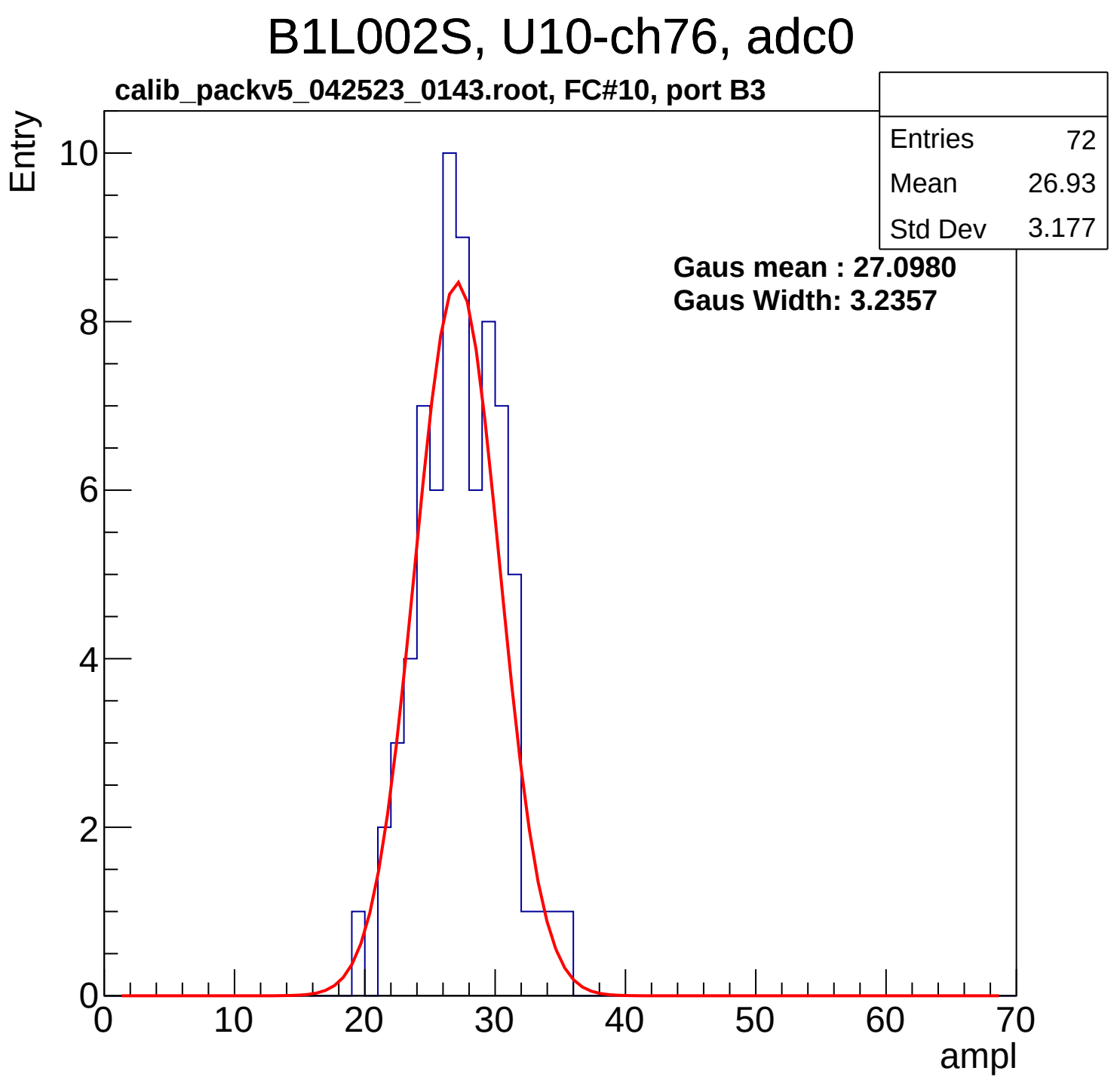
B1L002S, U10-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.93
Std Dev 3.177
Gaus mean : 27.0980
Gaus Width: 3.2357
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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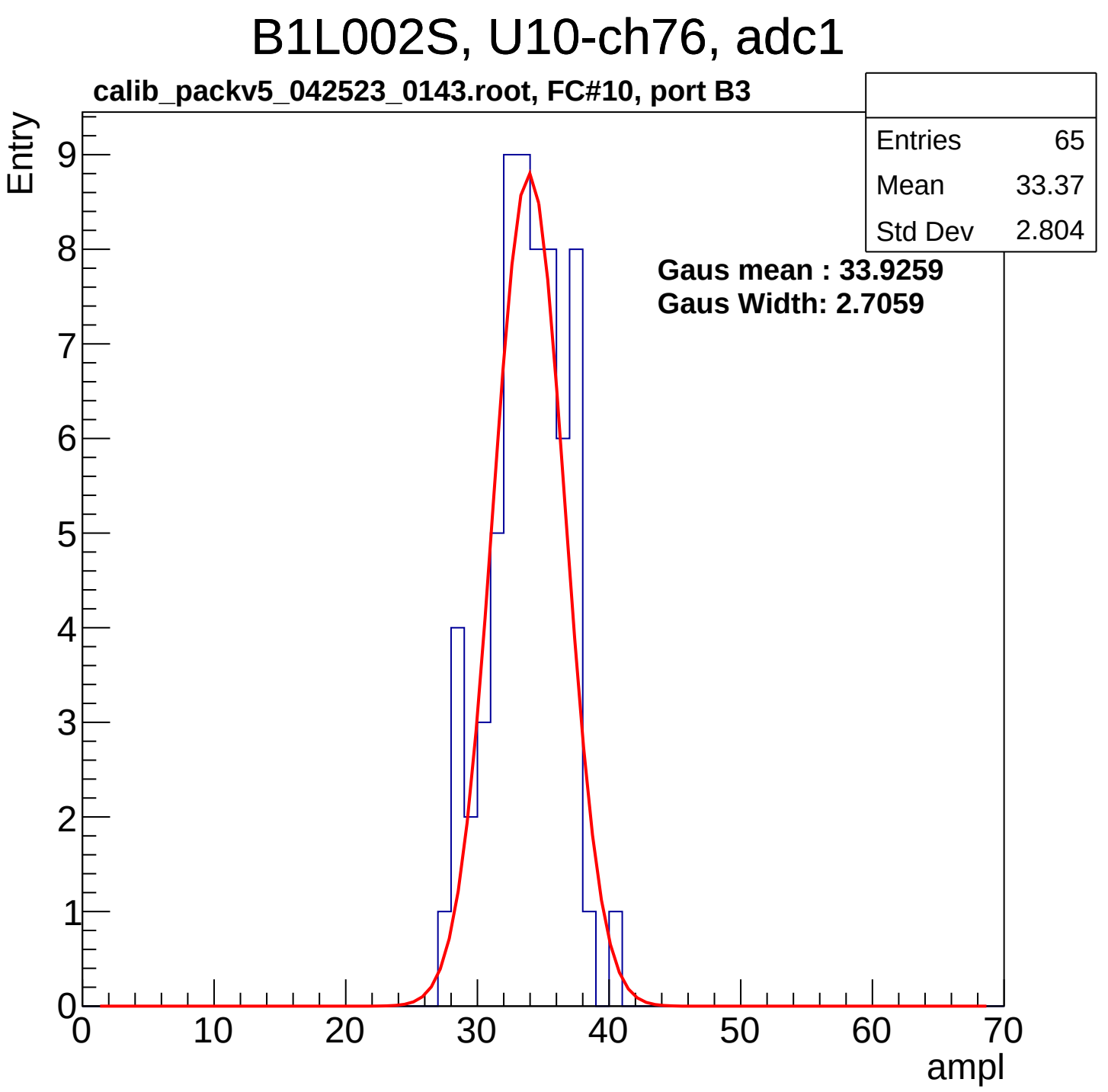
B1L002S, U10-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.37
Std Dev 2.804
Gaus mean : 33.9259
Gaus Width: 2.7059
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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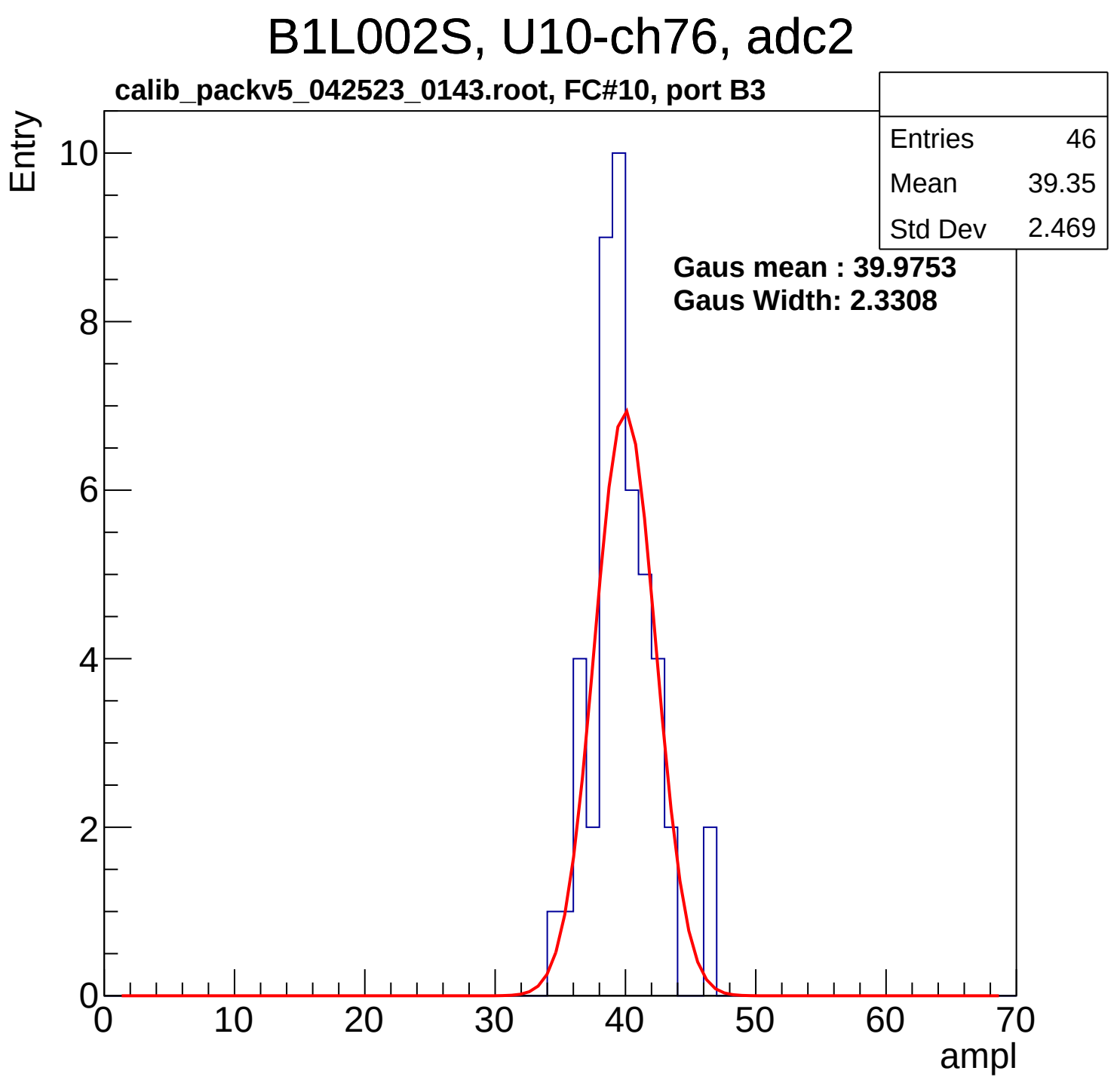
60
70


B1L002S, U10-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 39.35
Std Dev 2.469
Gaus mean : 39.9753
Gaus Width: 2.3308
Entry
ampl
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8
10
0
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20
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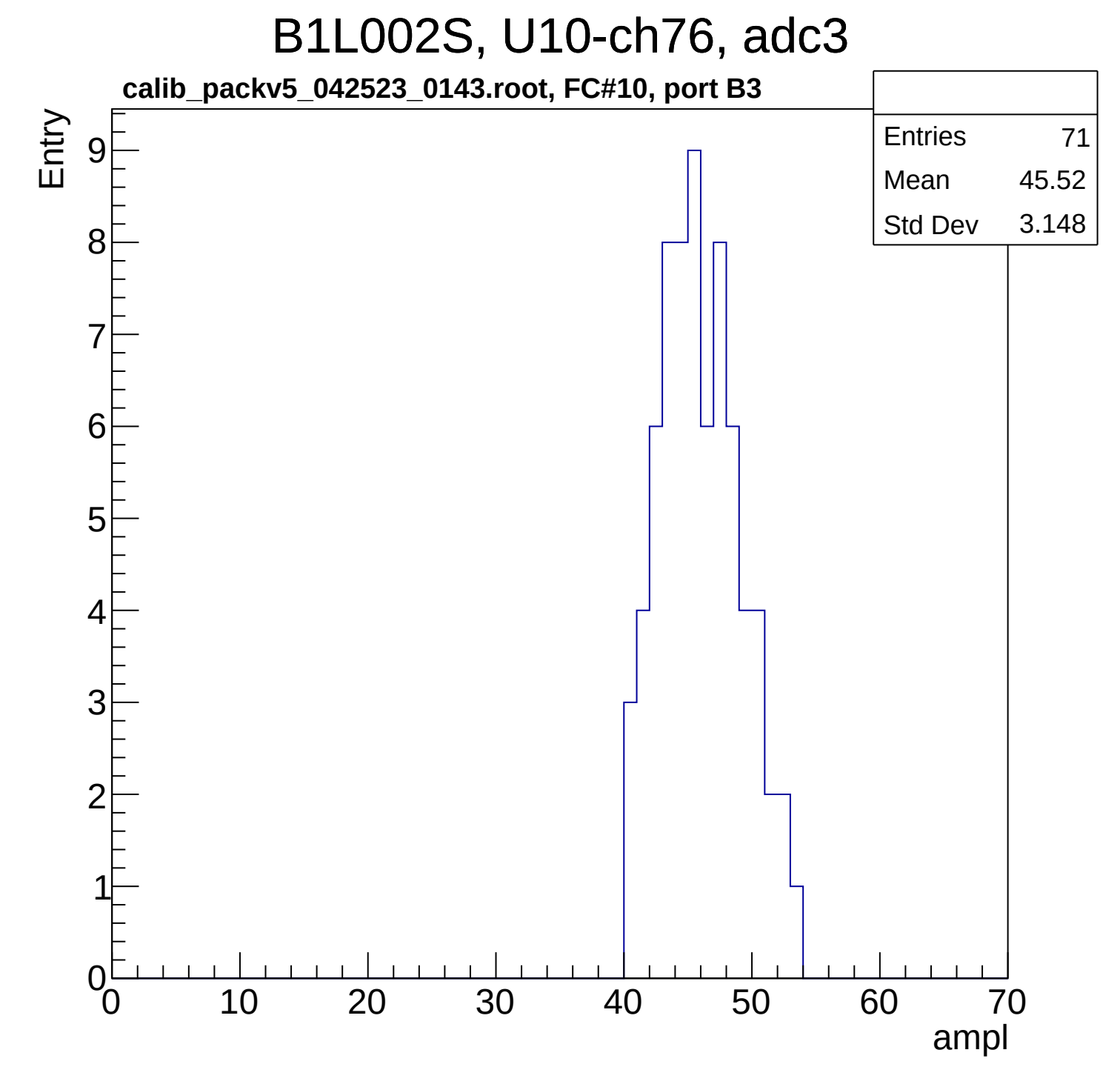
40
50
60
70


B1L002S, U10-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 45.52
Std Dev 3.148
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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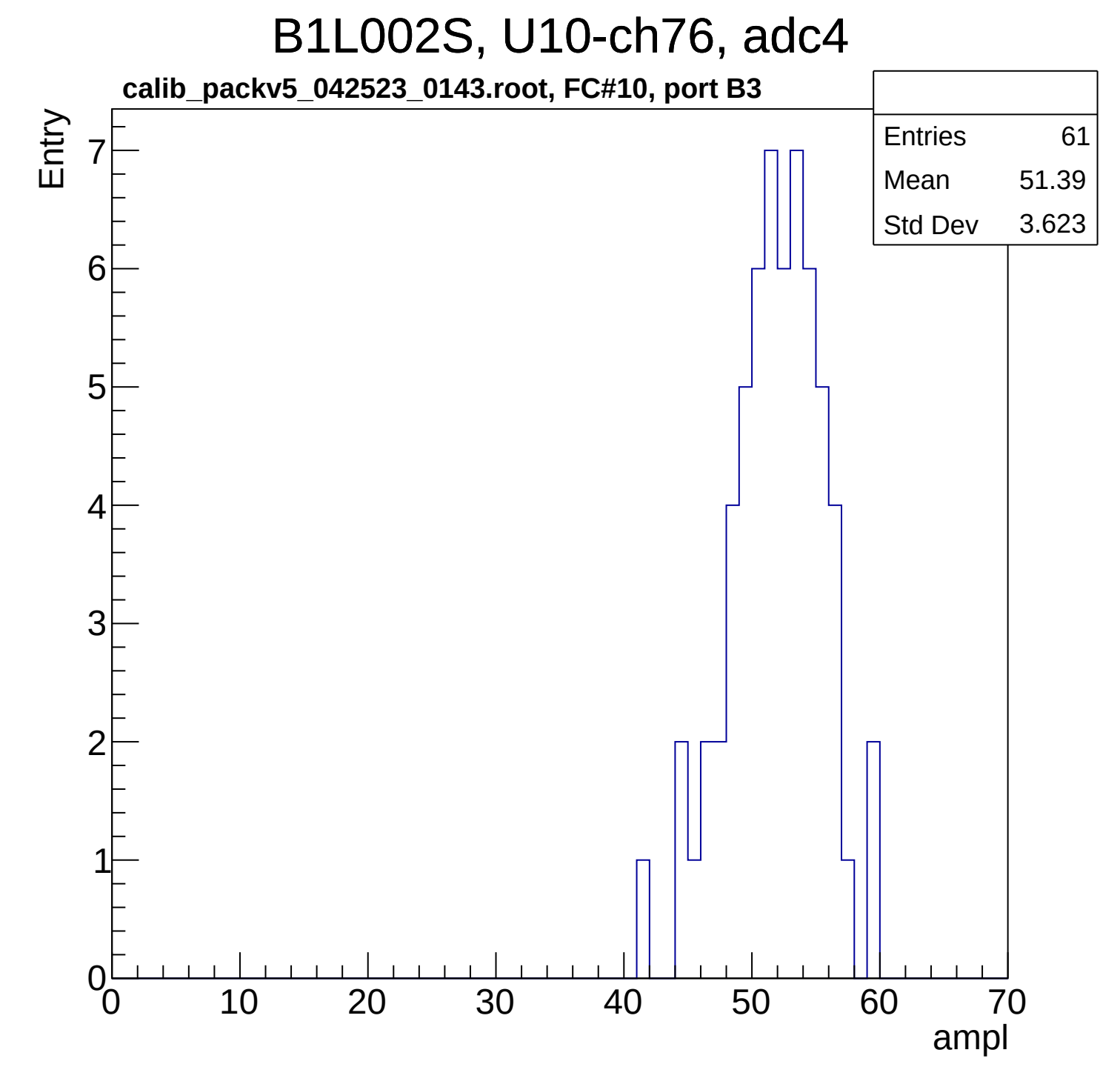
B1L002S, U10-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 51.39
Std Dev 3.623
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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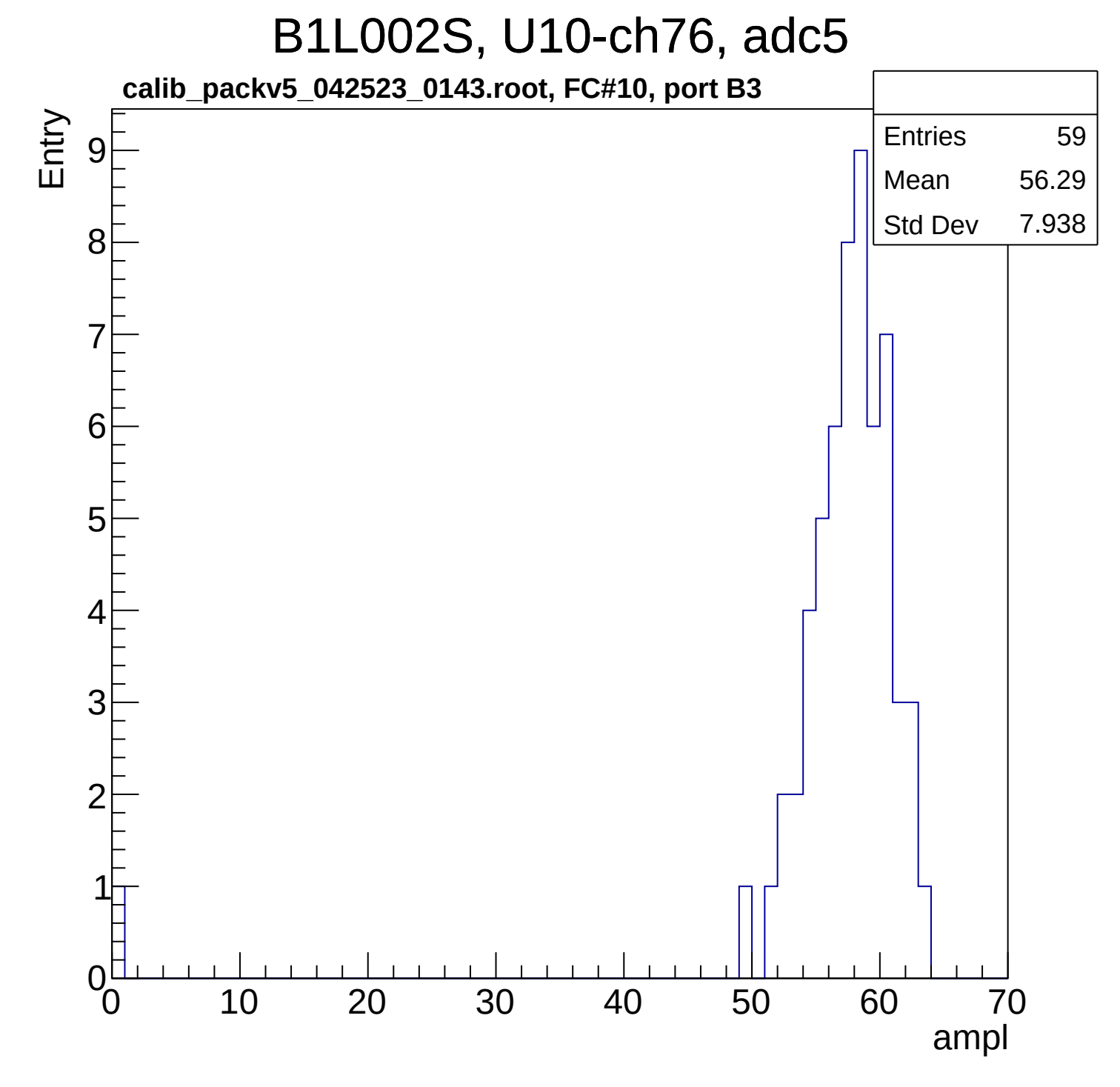
B1L002S, U10-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 56.29
Std Dev 7.938
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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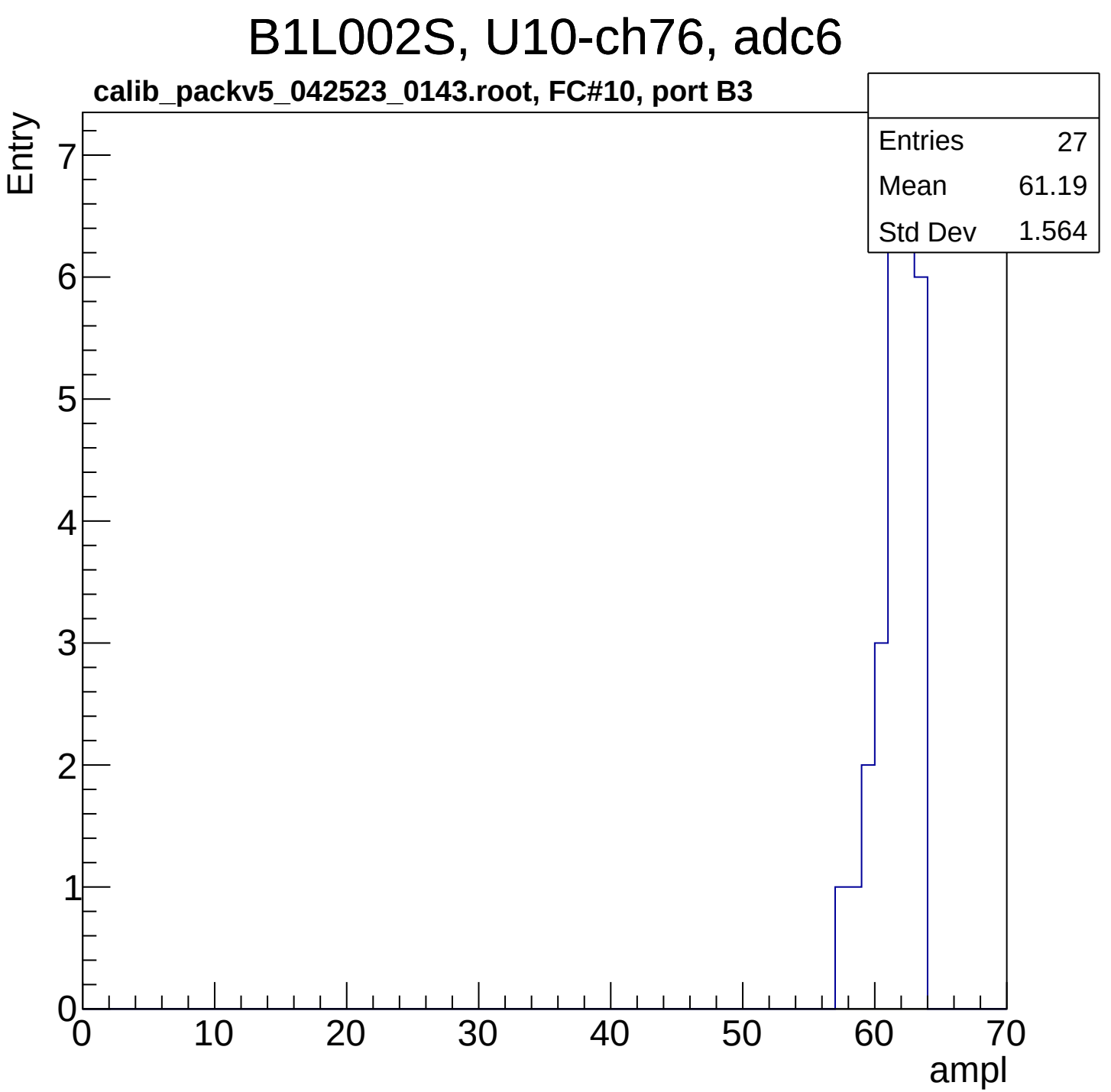
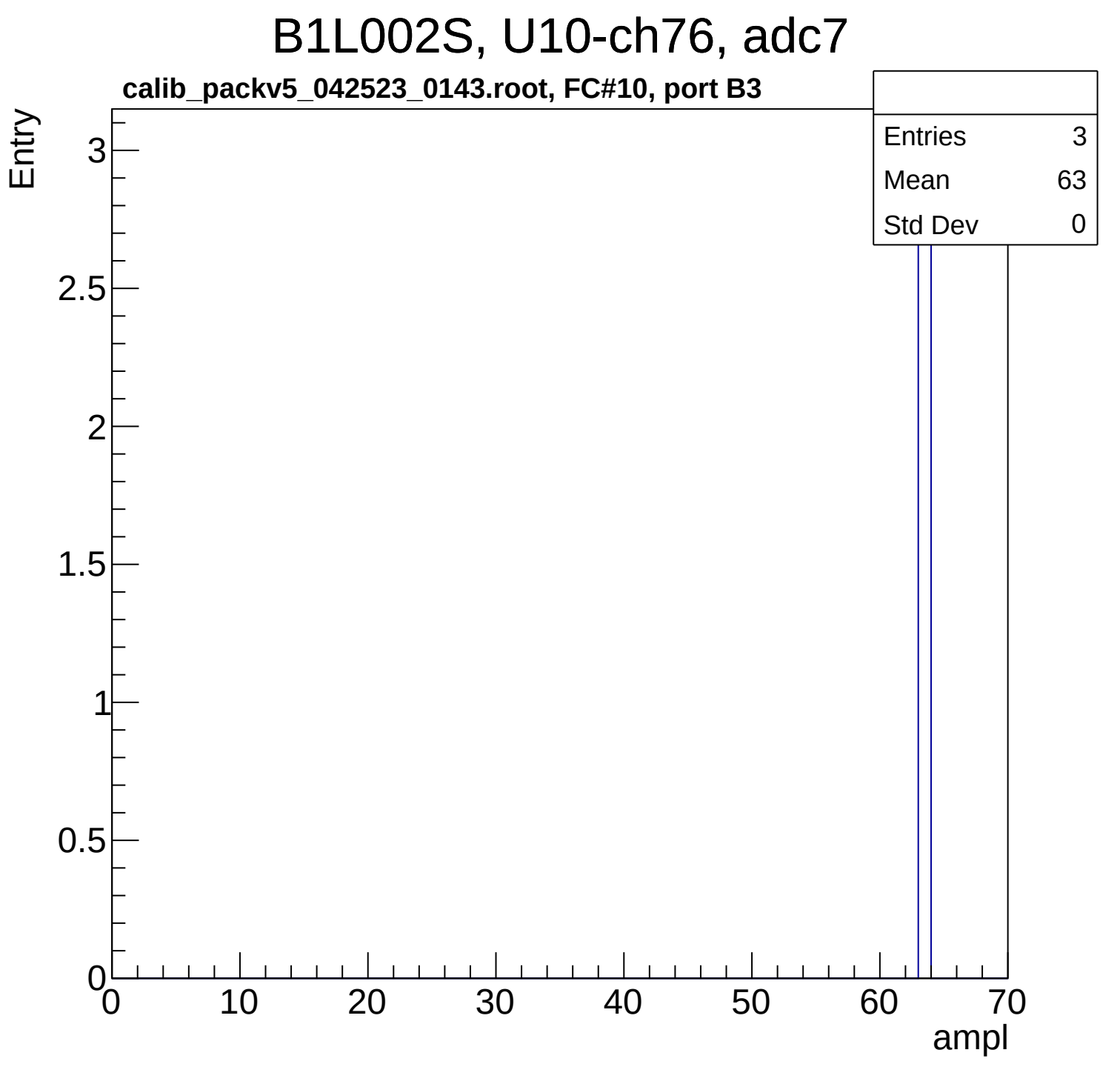

B1L002S, U10-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 61.19
Std Dev 1.564
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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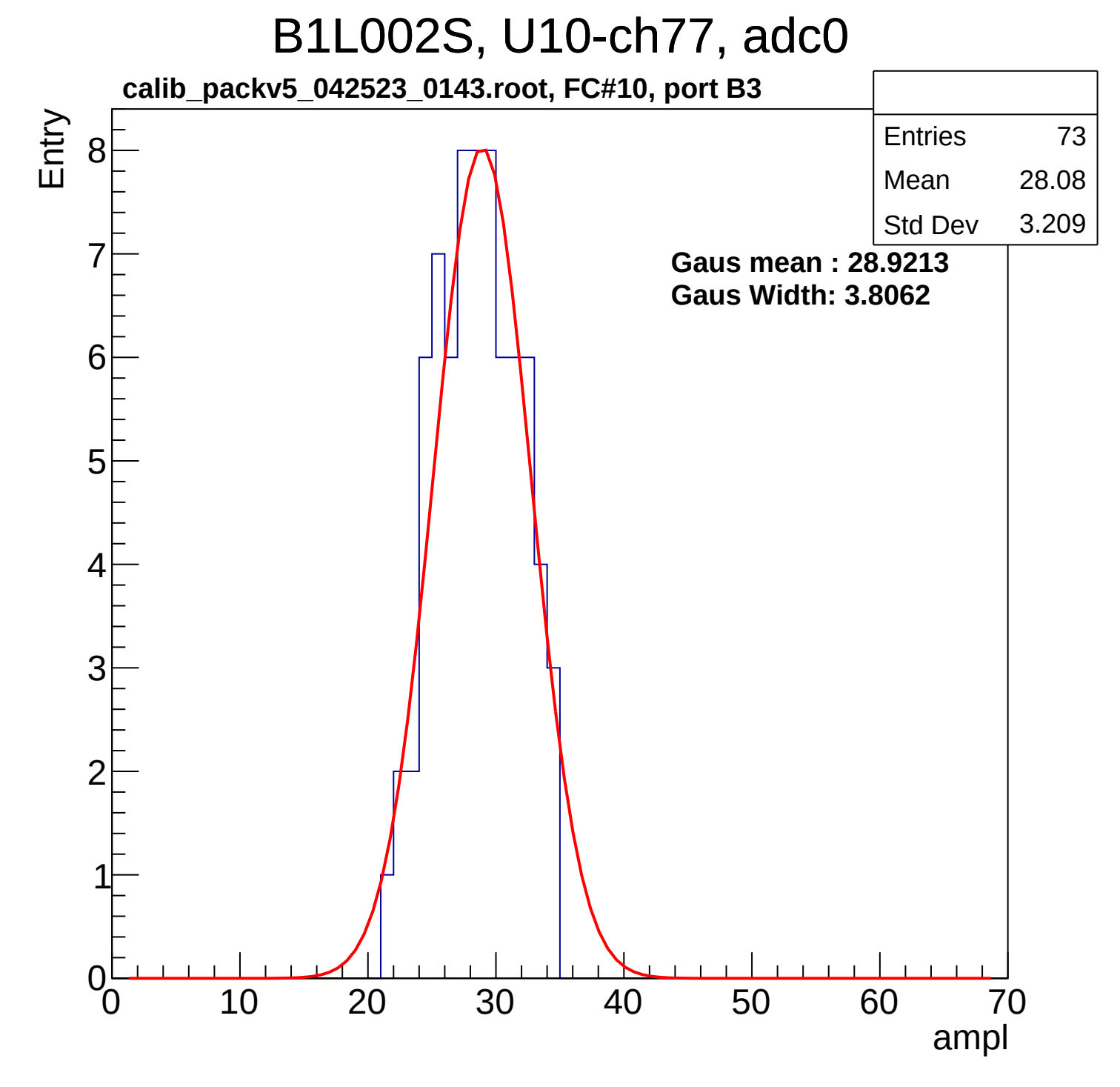
B1L002S, U10-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.08
Std Dev 3.209
Gaus mean : 28.9213
Gaus Width: 3.8062
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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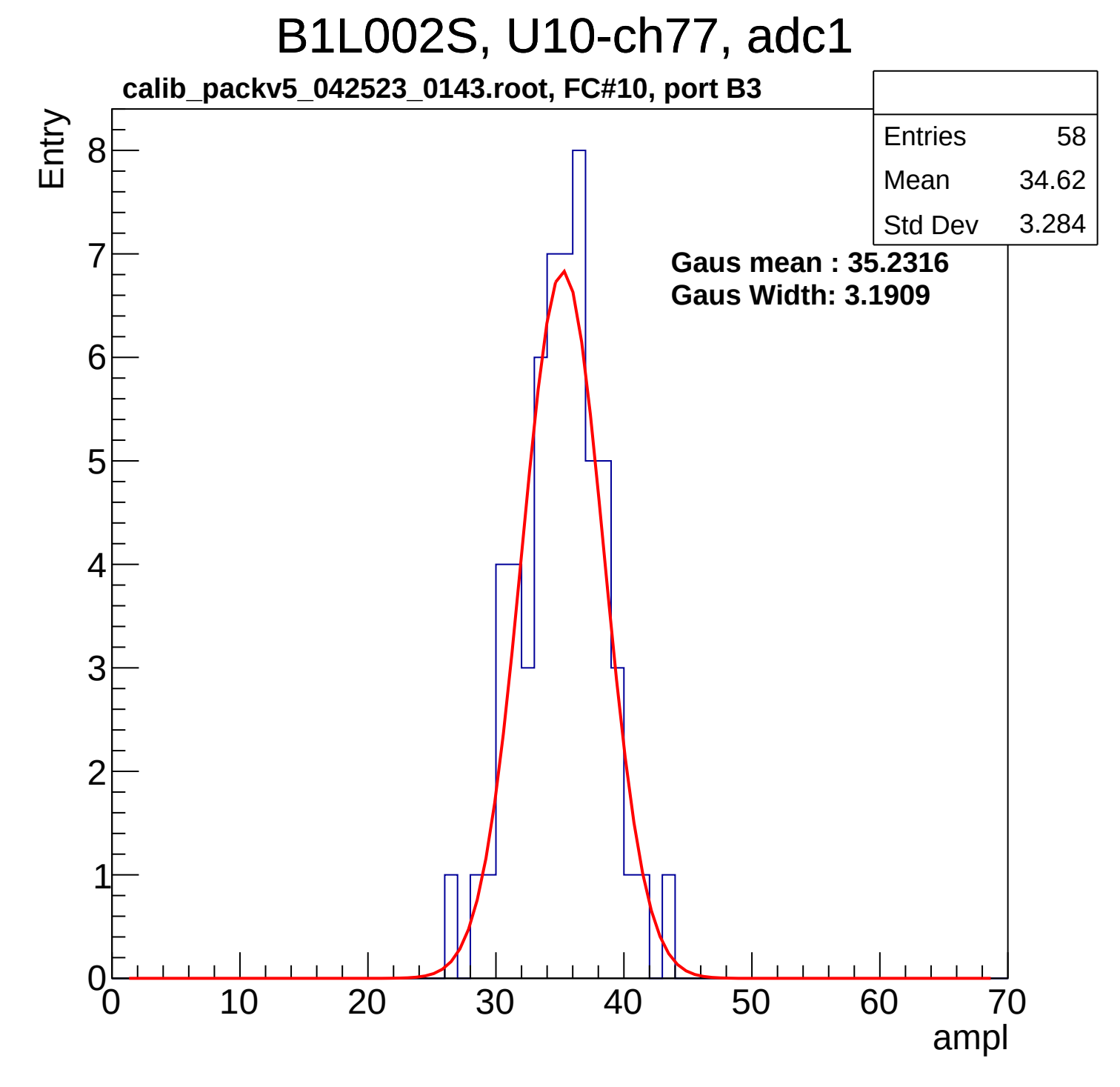
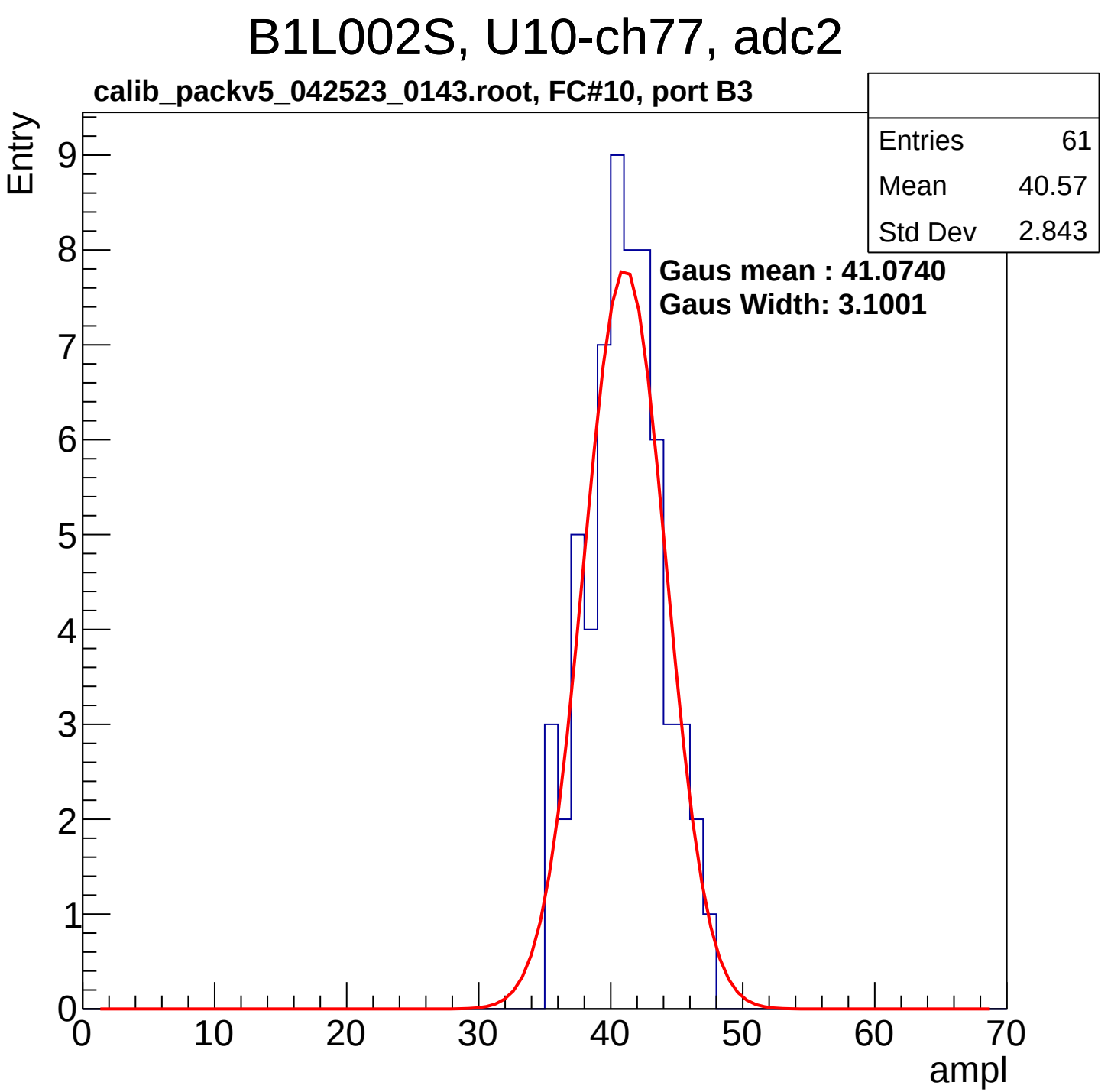

B1L002S, U10-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.62
Std Dev 3.284
Gaus mean : 35.2316
Gaus Width: 3.1909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.57
Std Dev 2.843
Gaus mean : 41.0740
Gaus Width: 3.1001
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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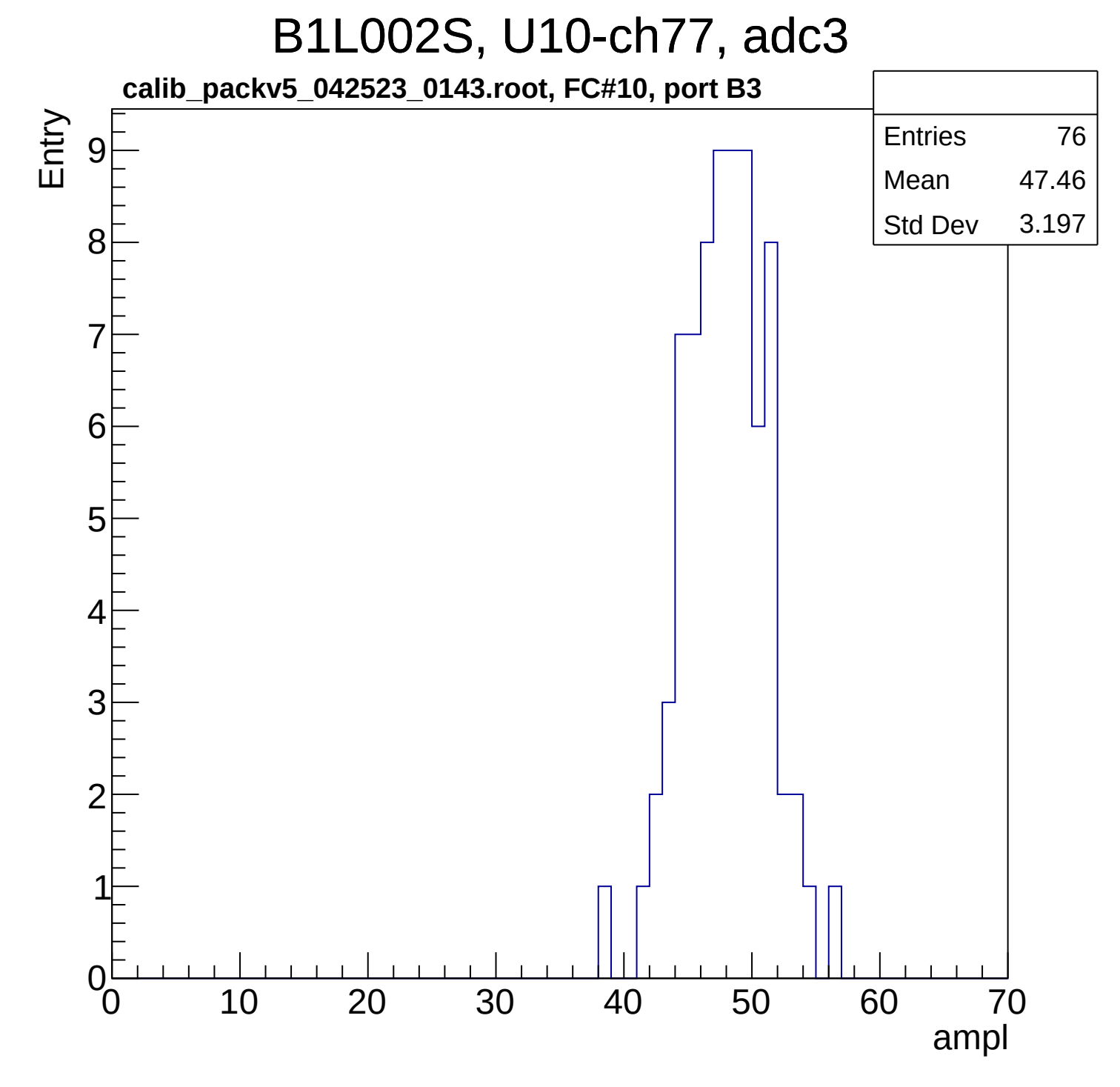
B1L002S, U10-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.46
Std Dev 3.197
Entry
ampl
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2
3
4
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6
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0
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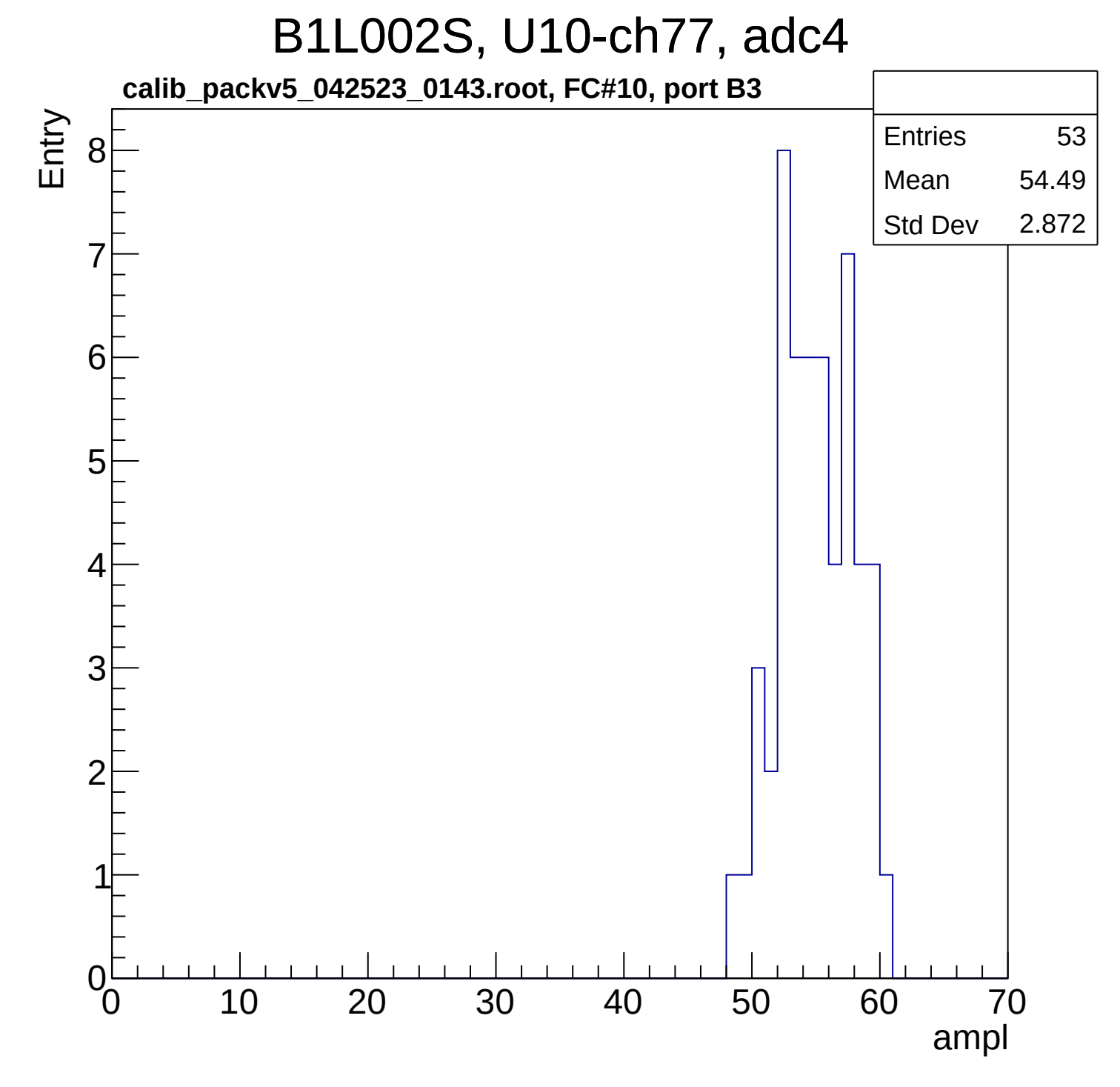
B1L002S, U10-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.49
Std Dev 2.872
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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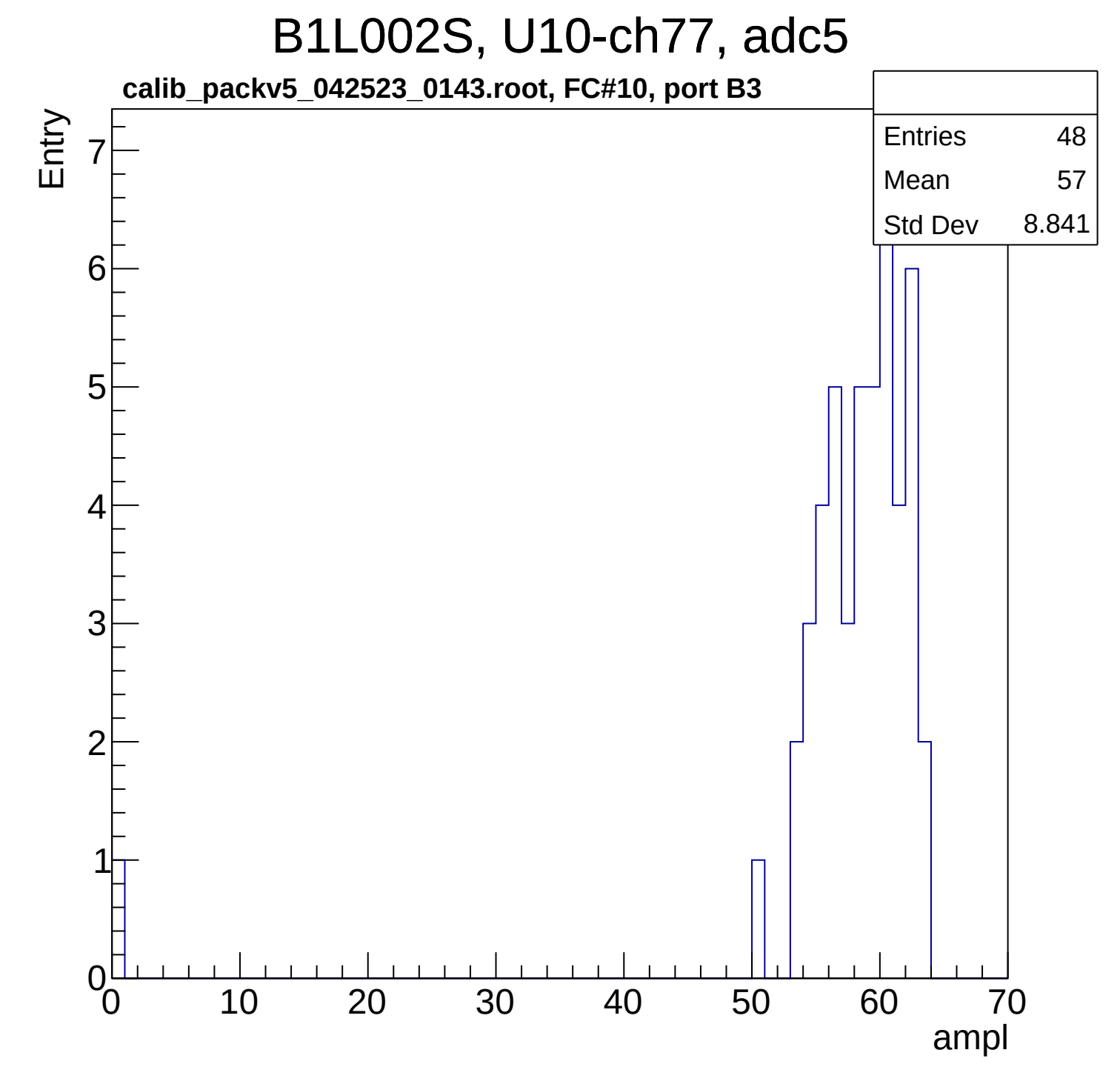
B1L002S, U10-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57
Std Dev 8.841
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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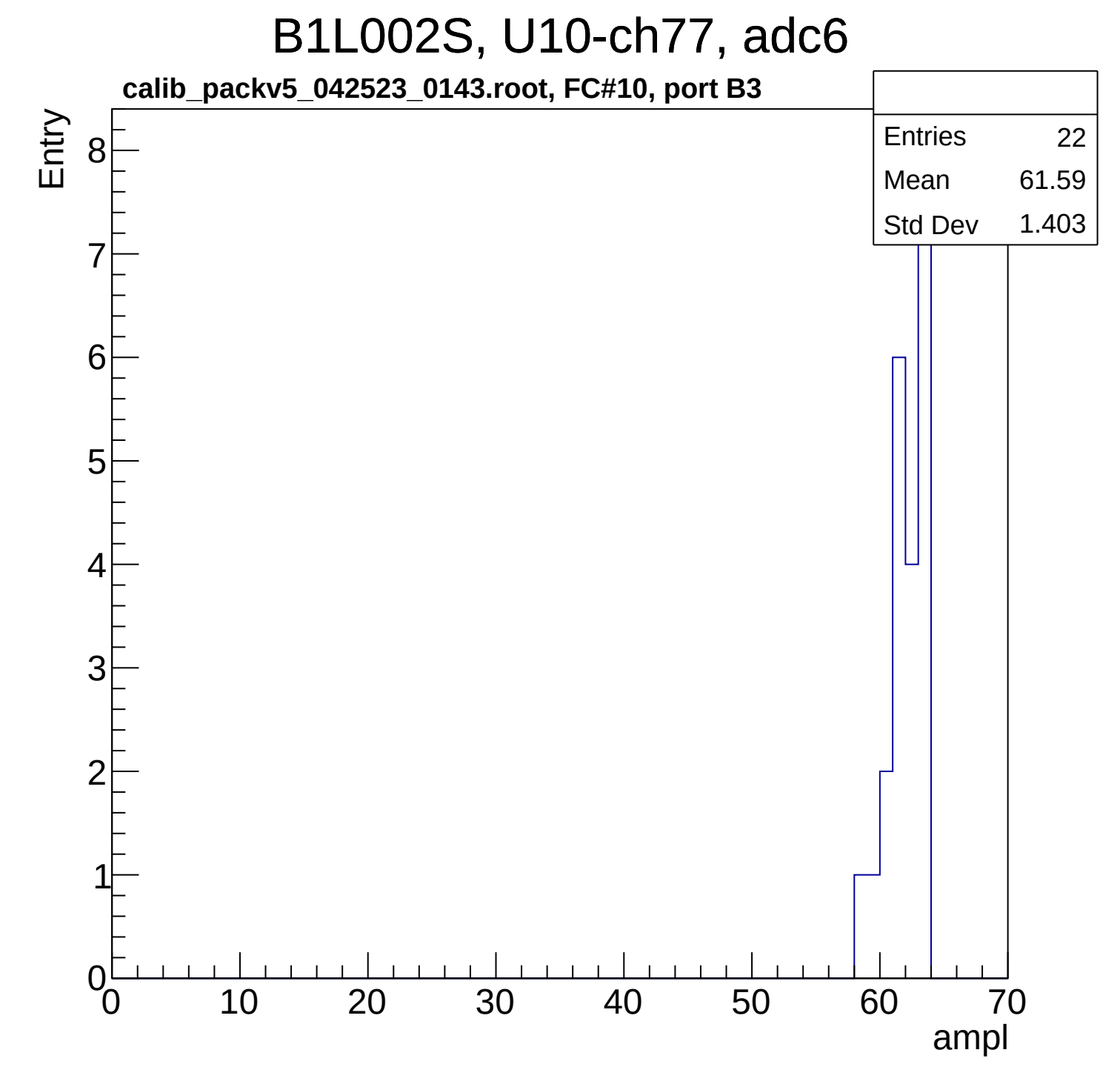
B1L002S, U10-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.59
Std Dev 1.403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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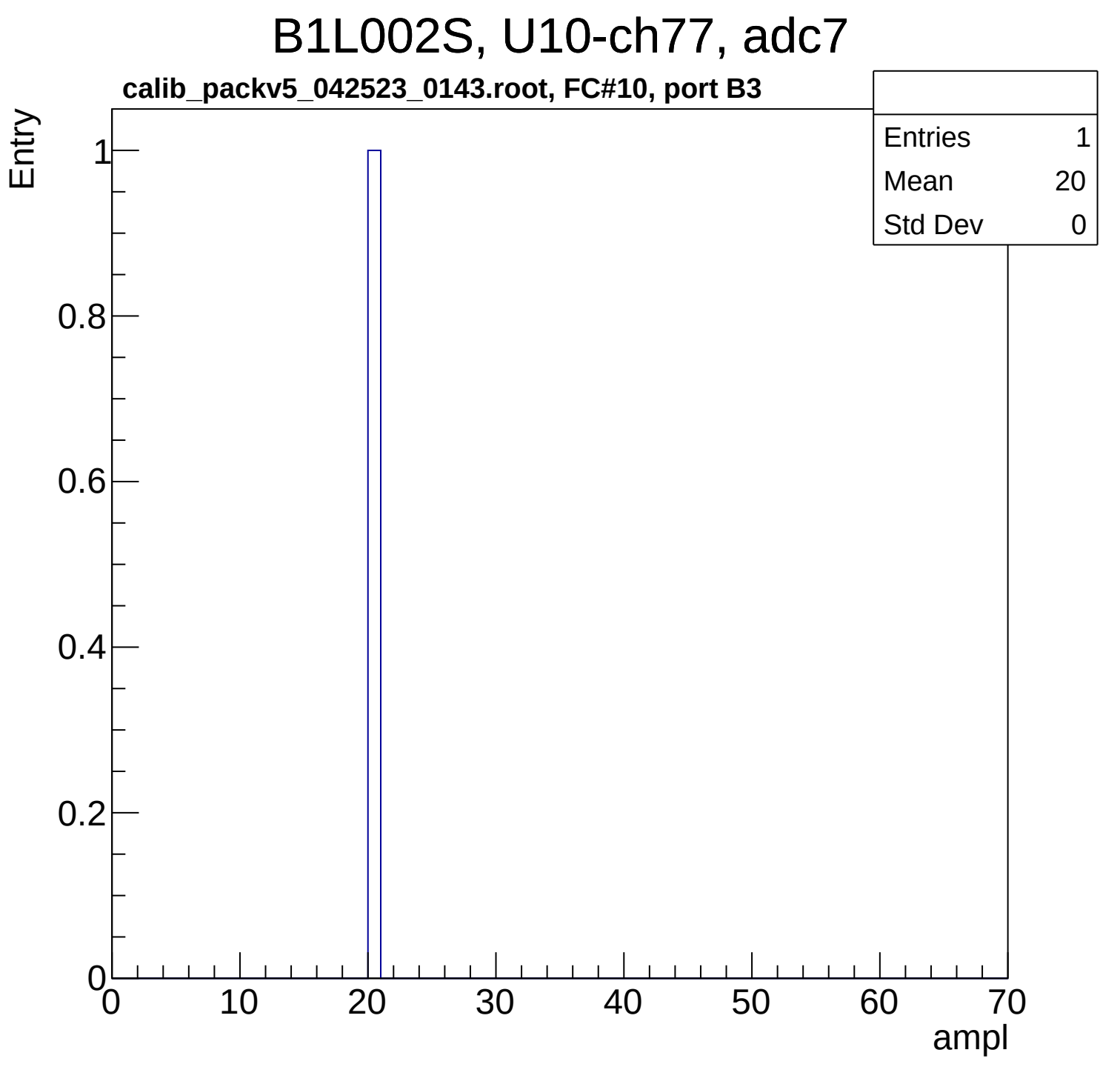
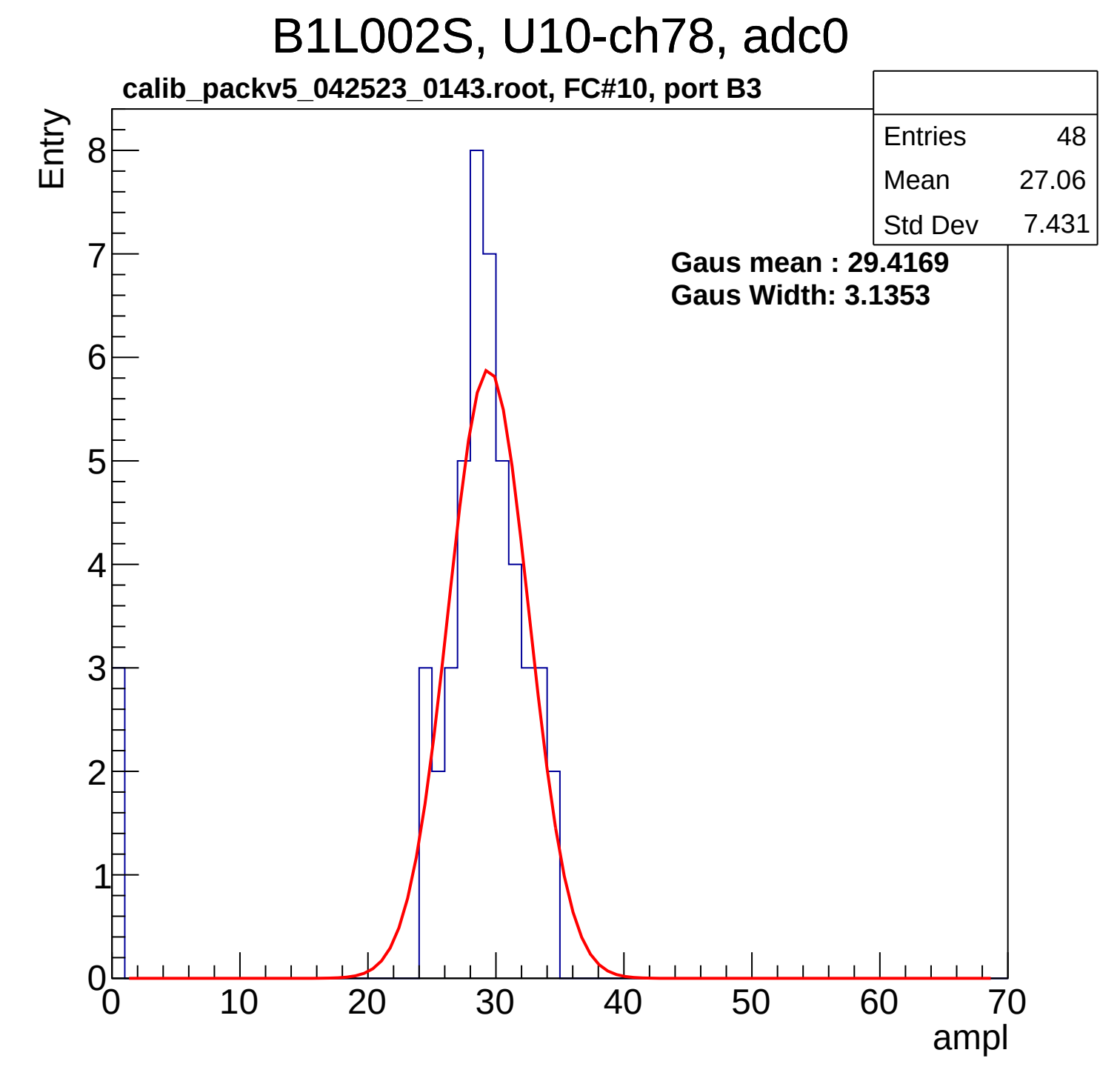
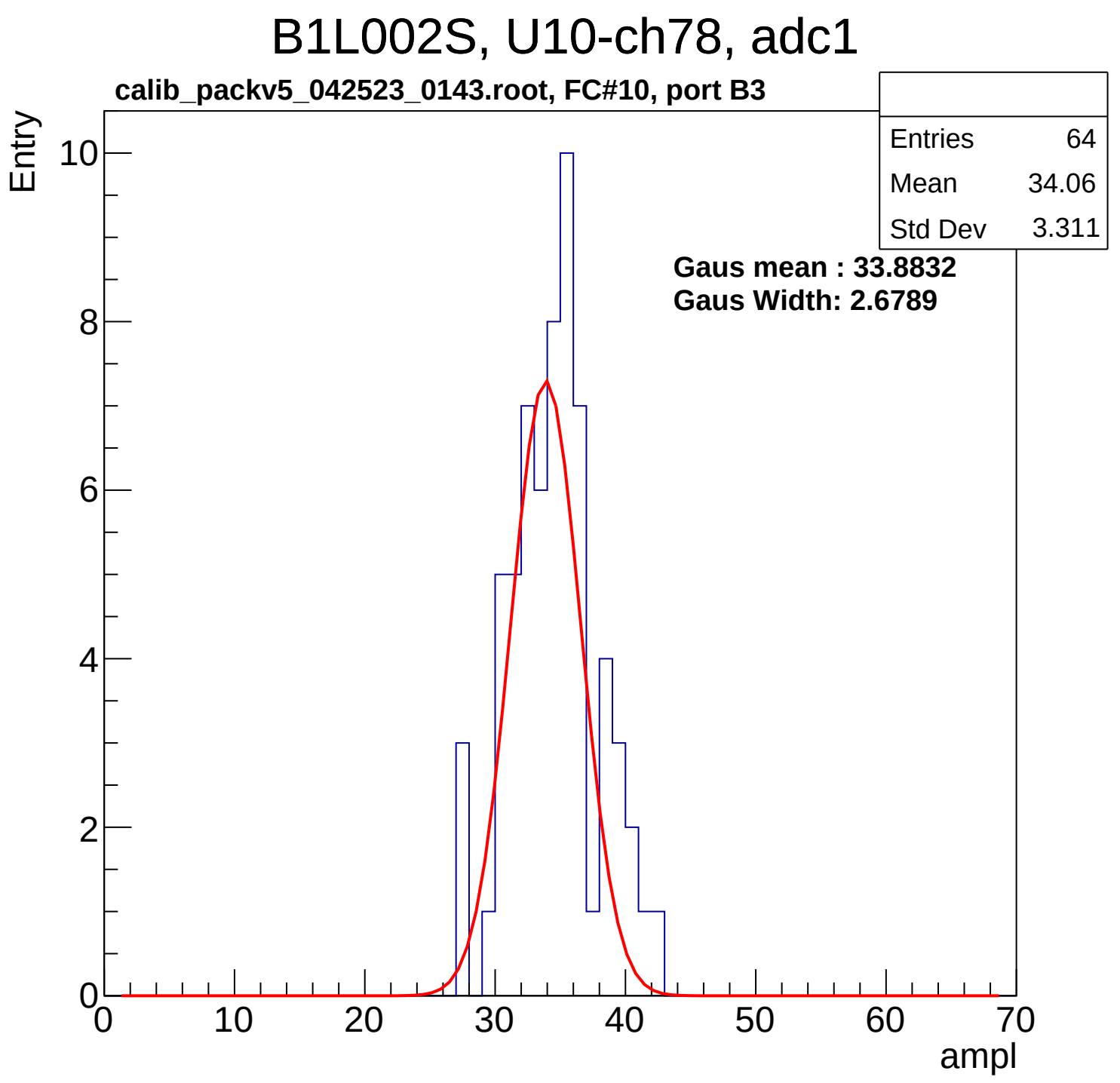

B1L002S, U10-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 27.06
Std Dev 7.431
Gaus mean : 29.4169
Gaus Width: 3.1353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.06
Std Dev 3.311
Gaus mean : 33.8832
Gaus Width: 2.6789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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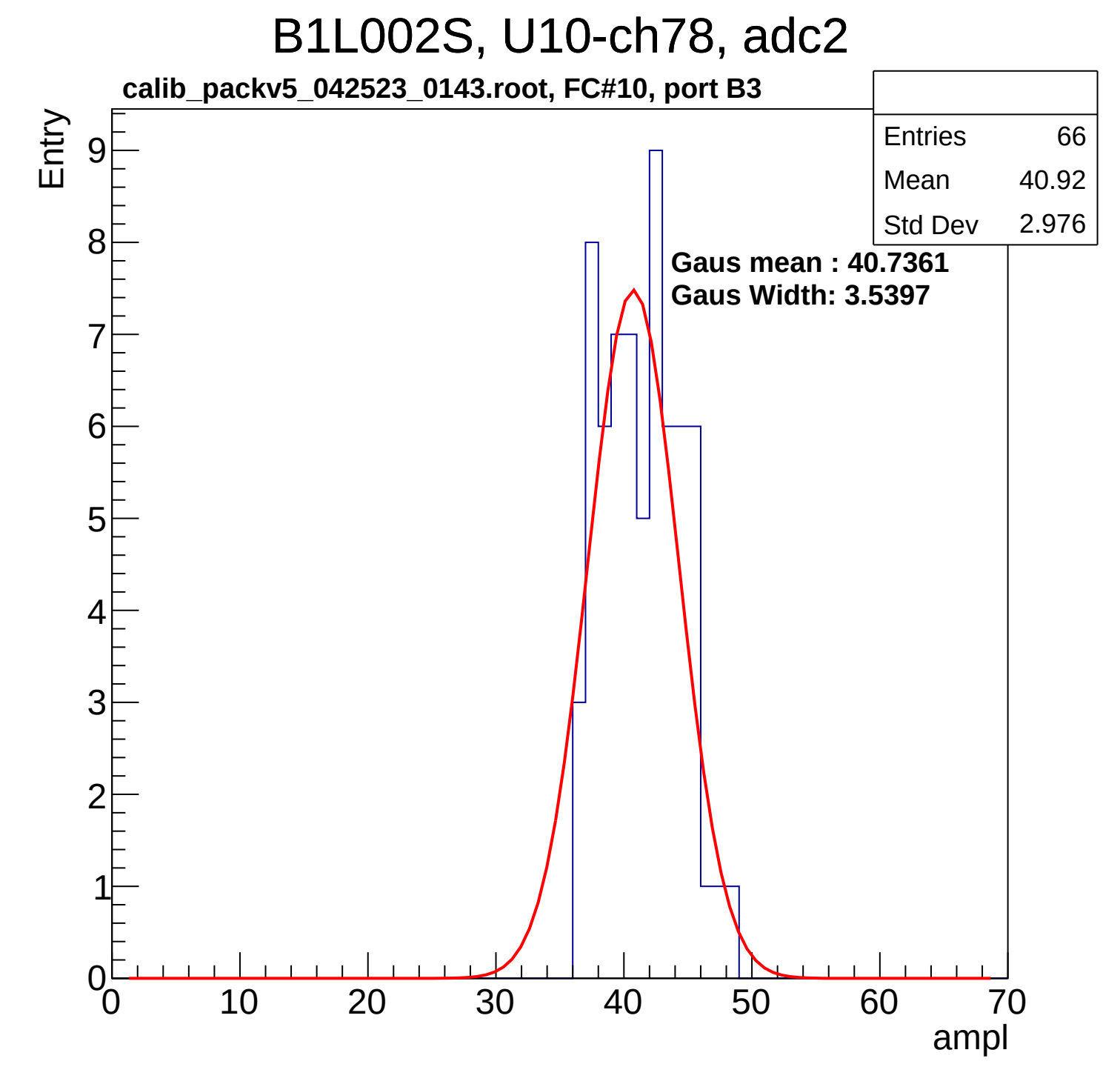
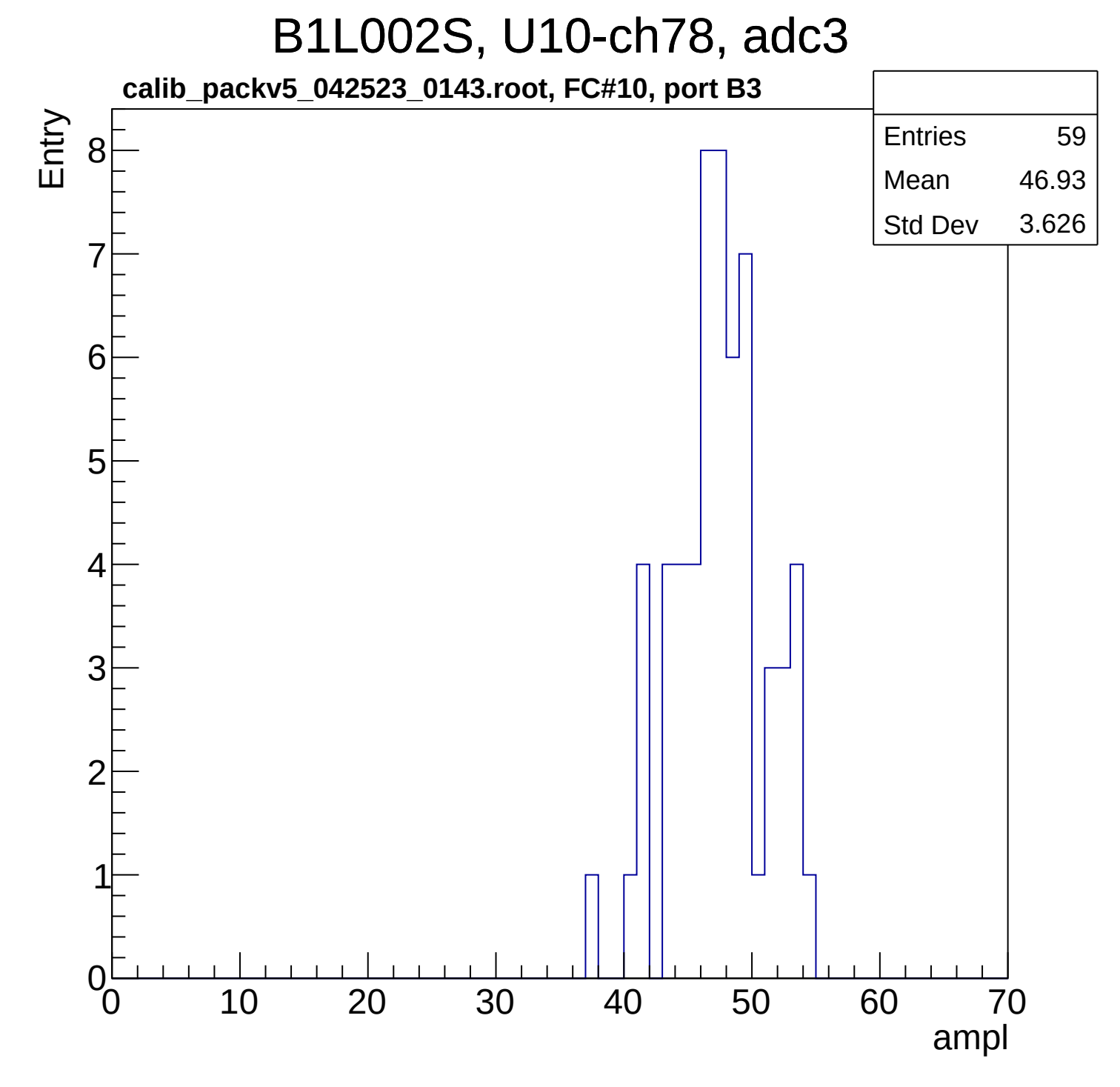

B1L002S, U10-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.92
Std Dev 2.976
Gaus mean : 40.7361
Gaus Width: 3.5397
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.93
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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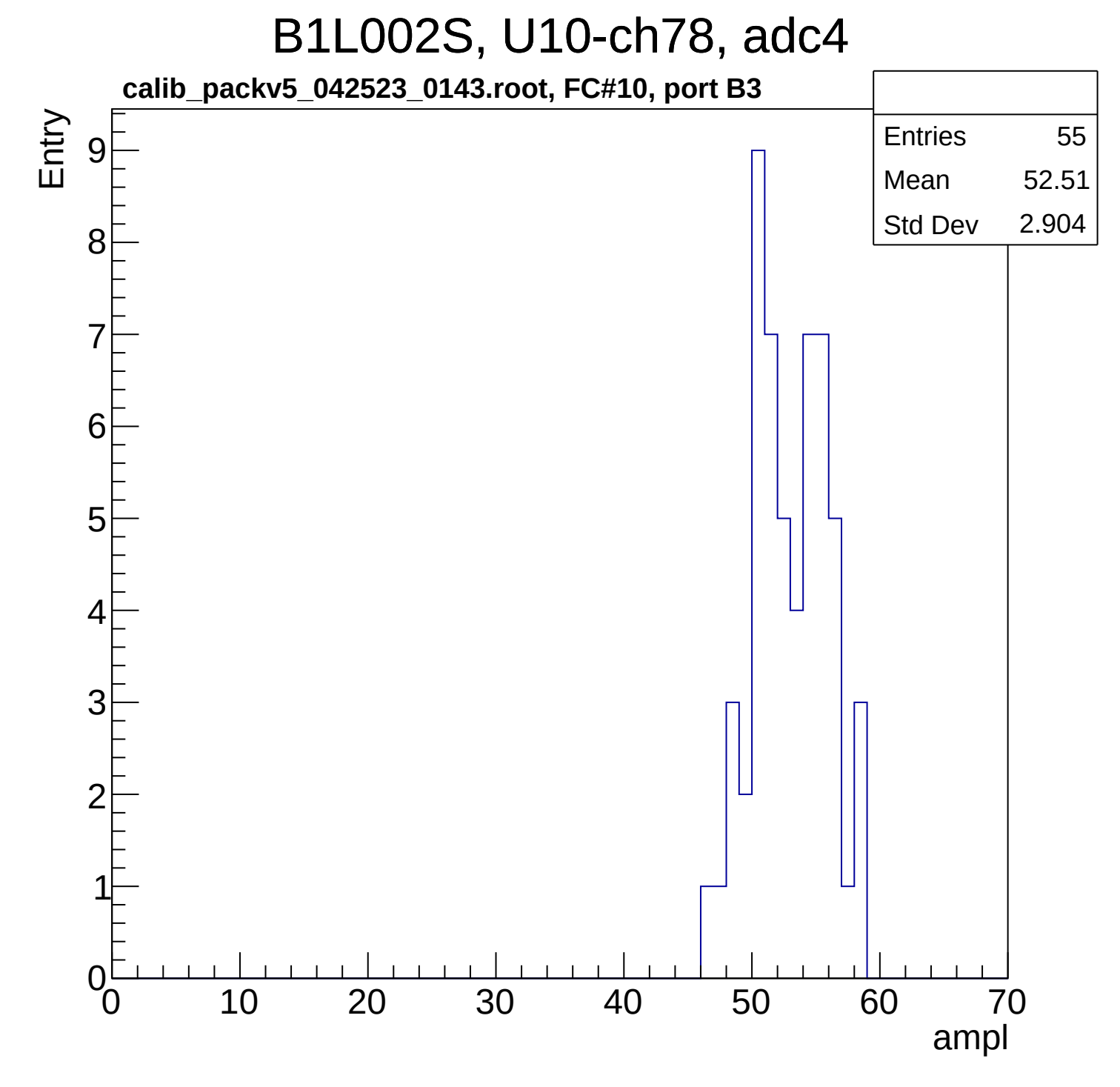
B1L002S, U10-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.51
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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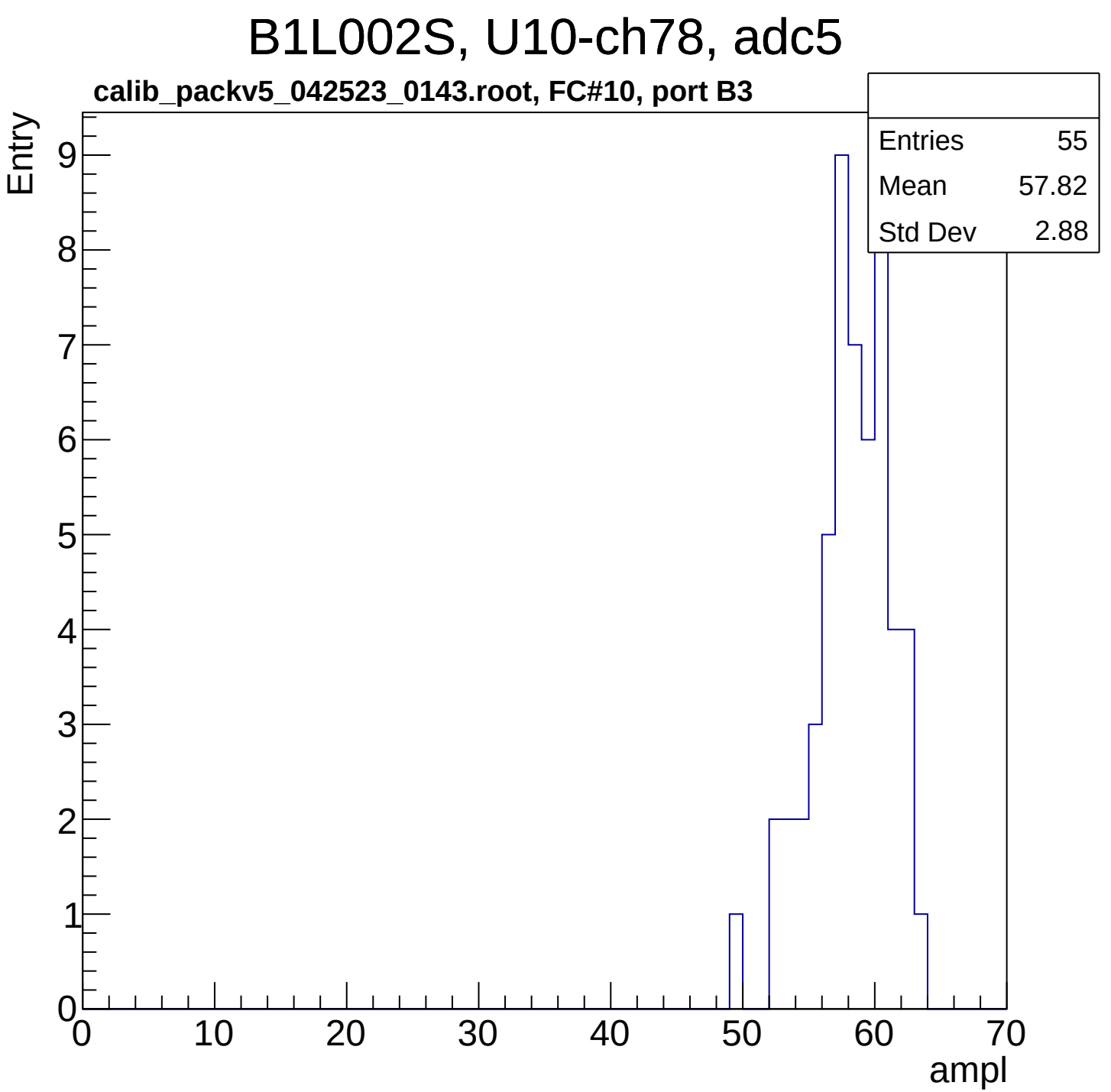
B1L002S, U10-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.82
Std Dev 2.88
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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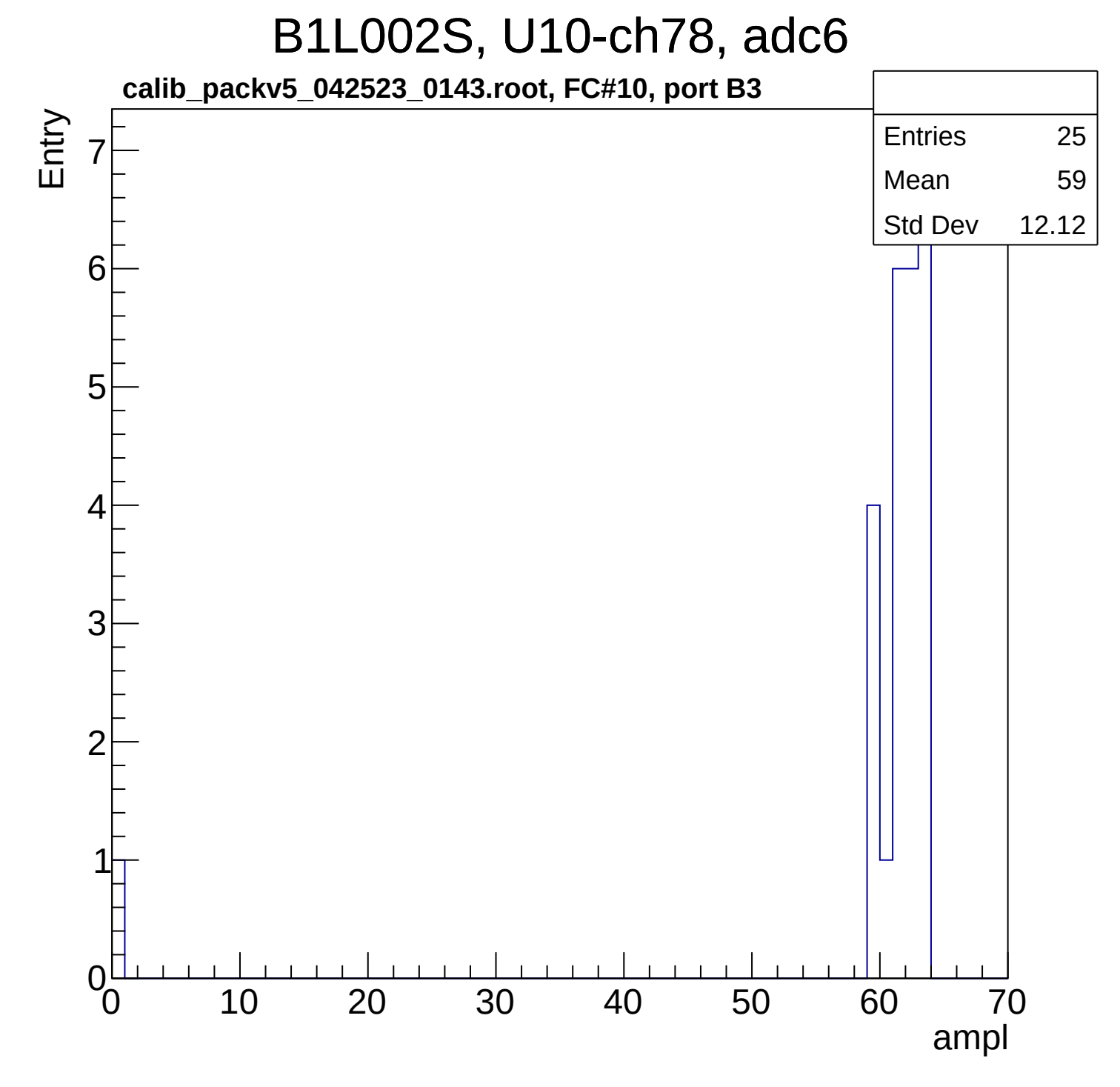
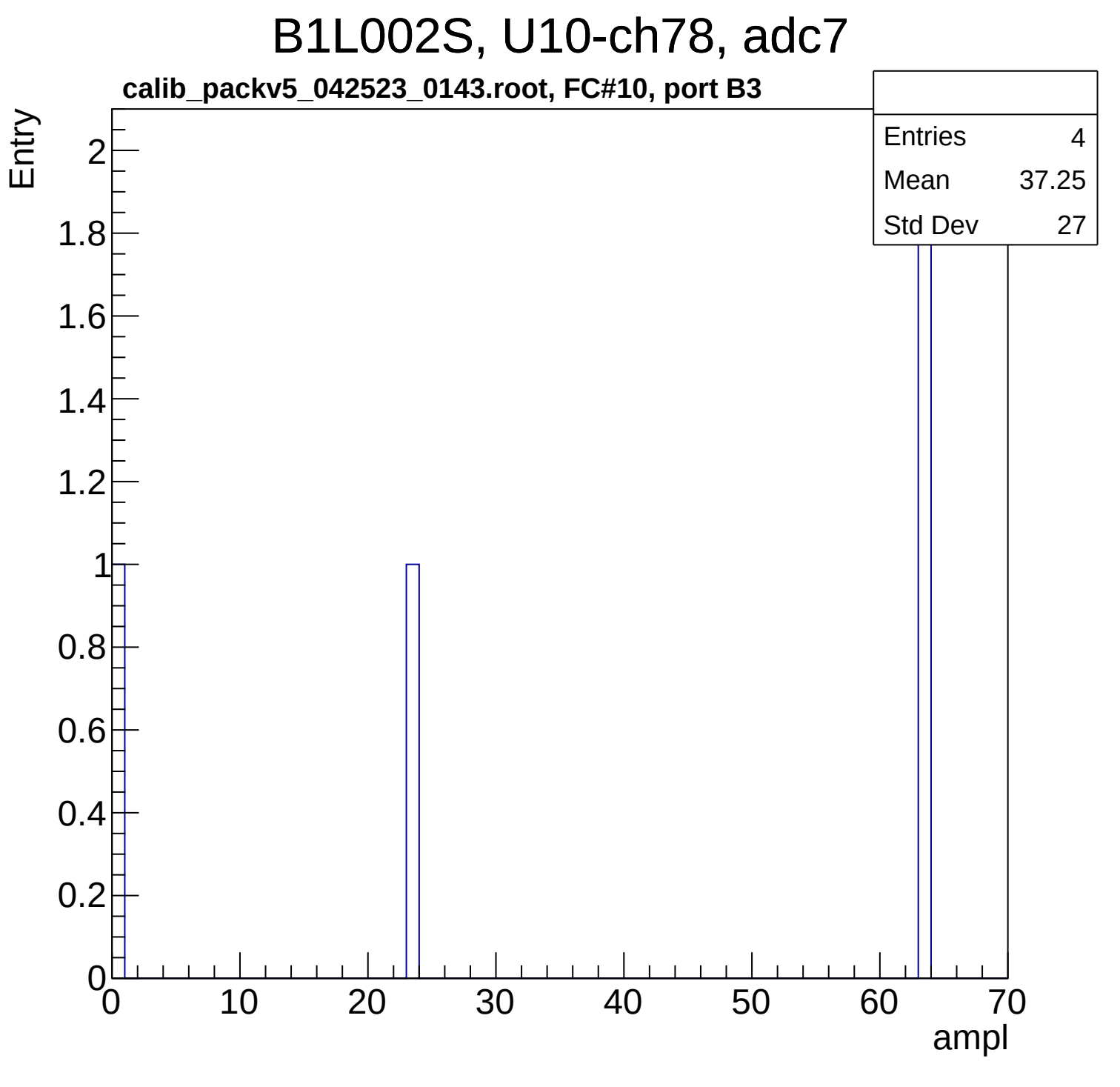
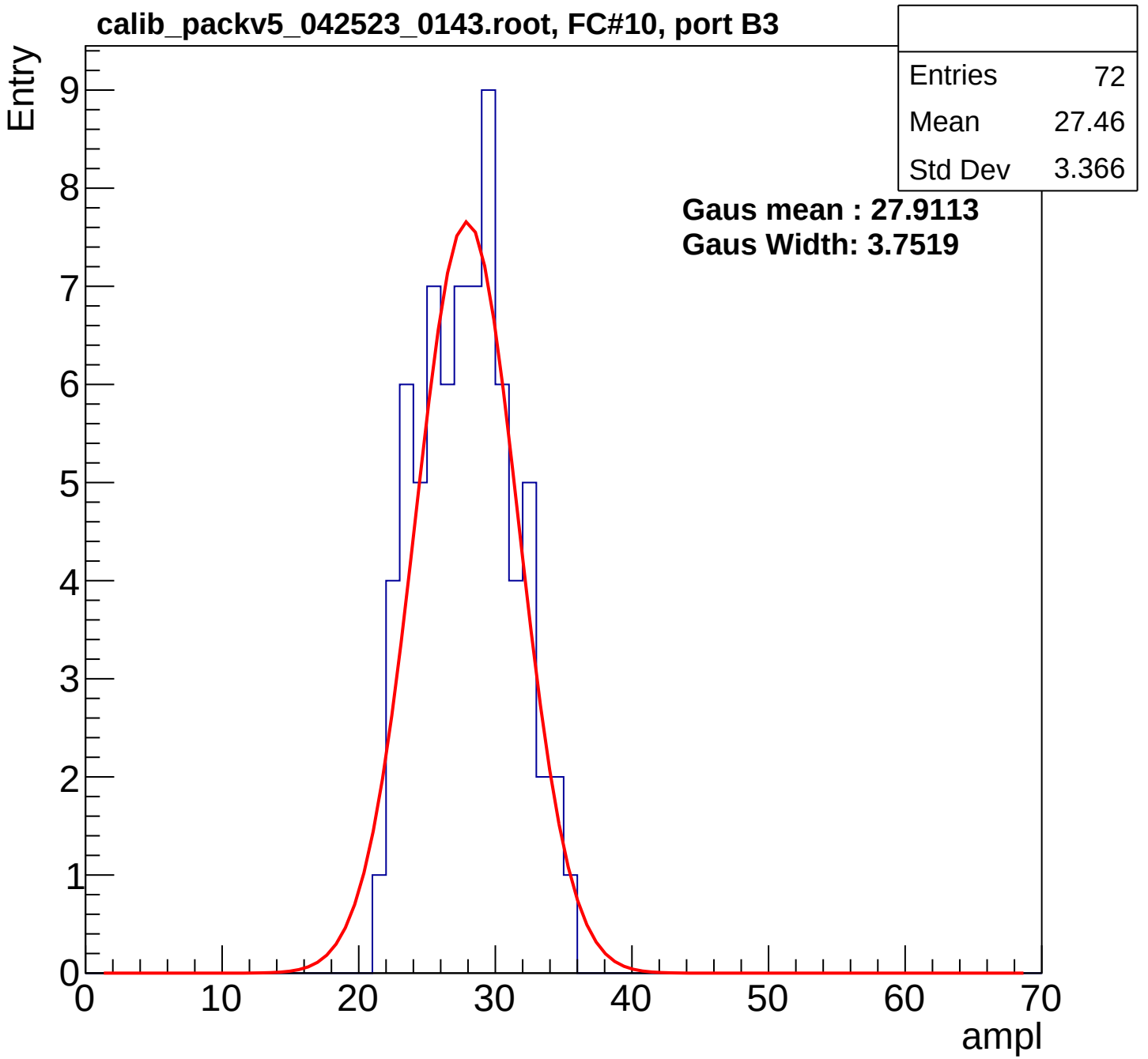

B1L002S, U10-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59
Std Dev 12.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 37.25
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.46
Std Dev 3.366
Gaus mean : 27.9113
Gaus Width: 3.7519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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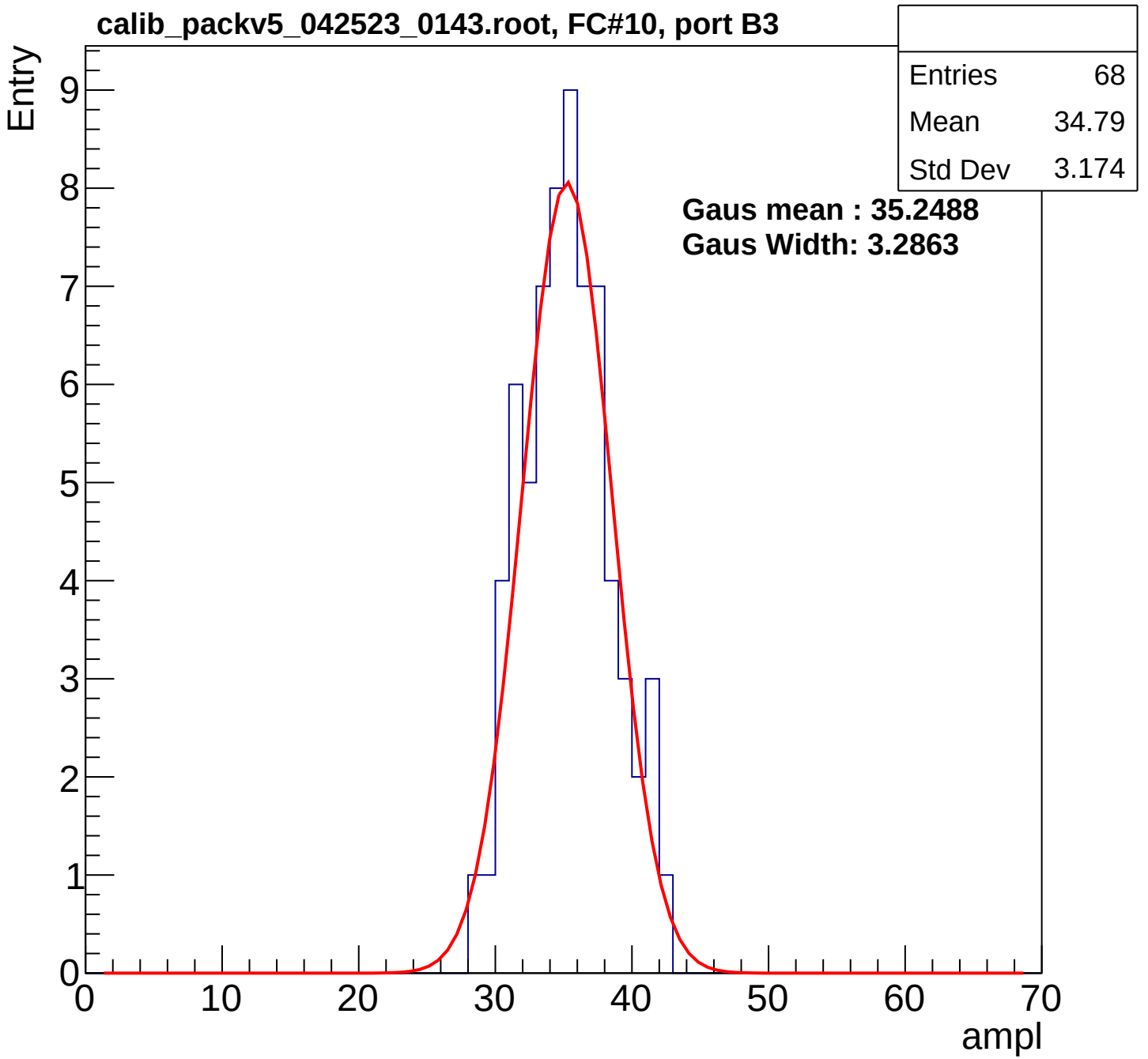
B1L002S, U10-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.79
Std Dev 3.174
Gaus mean : 35.2488
Gaus Width: 3.2863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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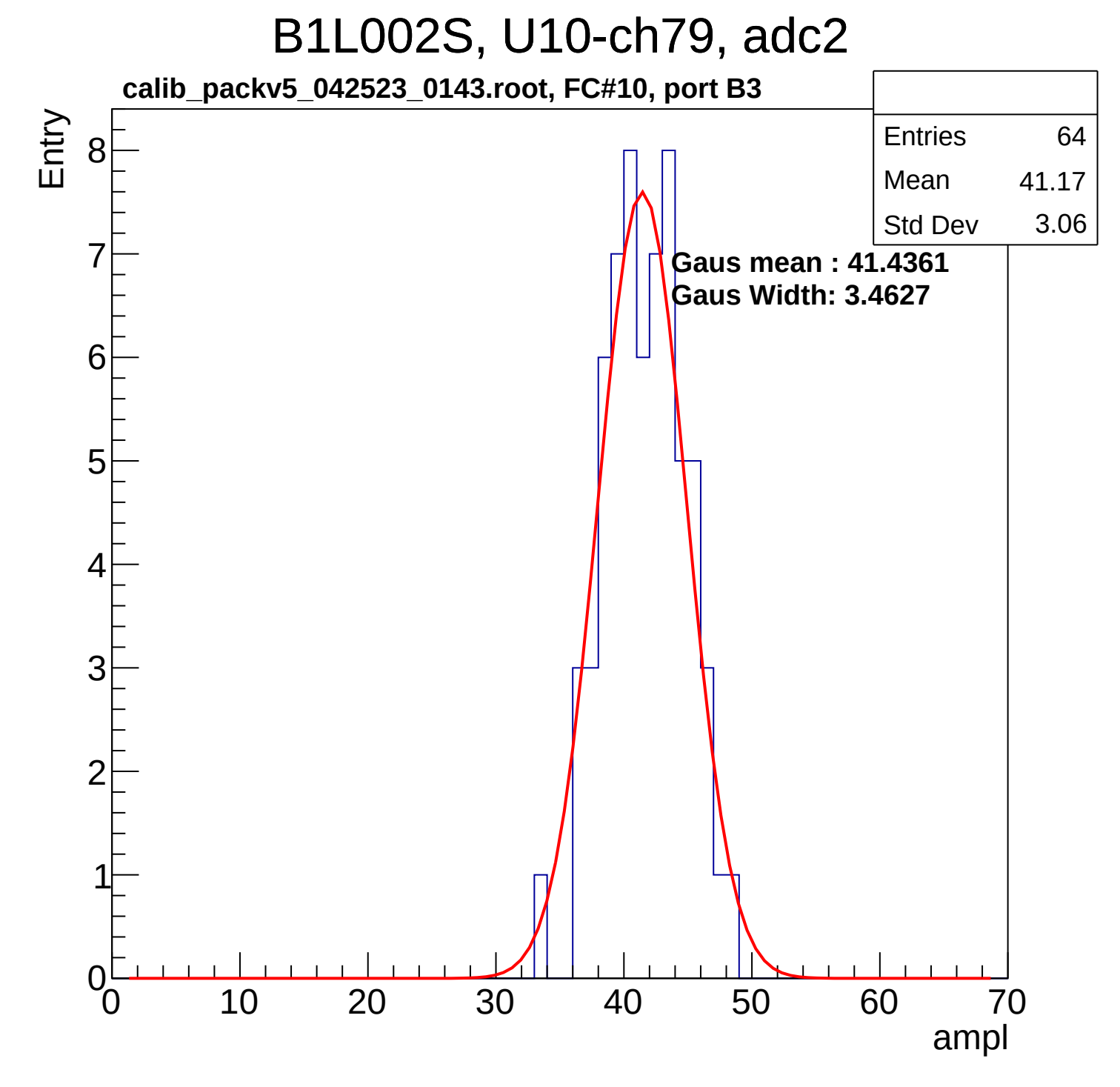
B1L002S, U10-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.17
Std Dev 3.06
Gaus mean : 41.4361
Gaus Width: 3.4627
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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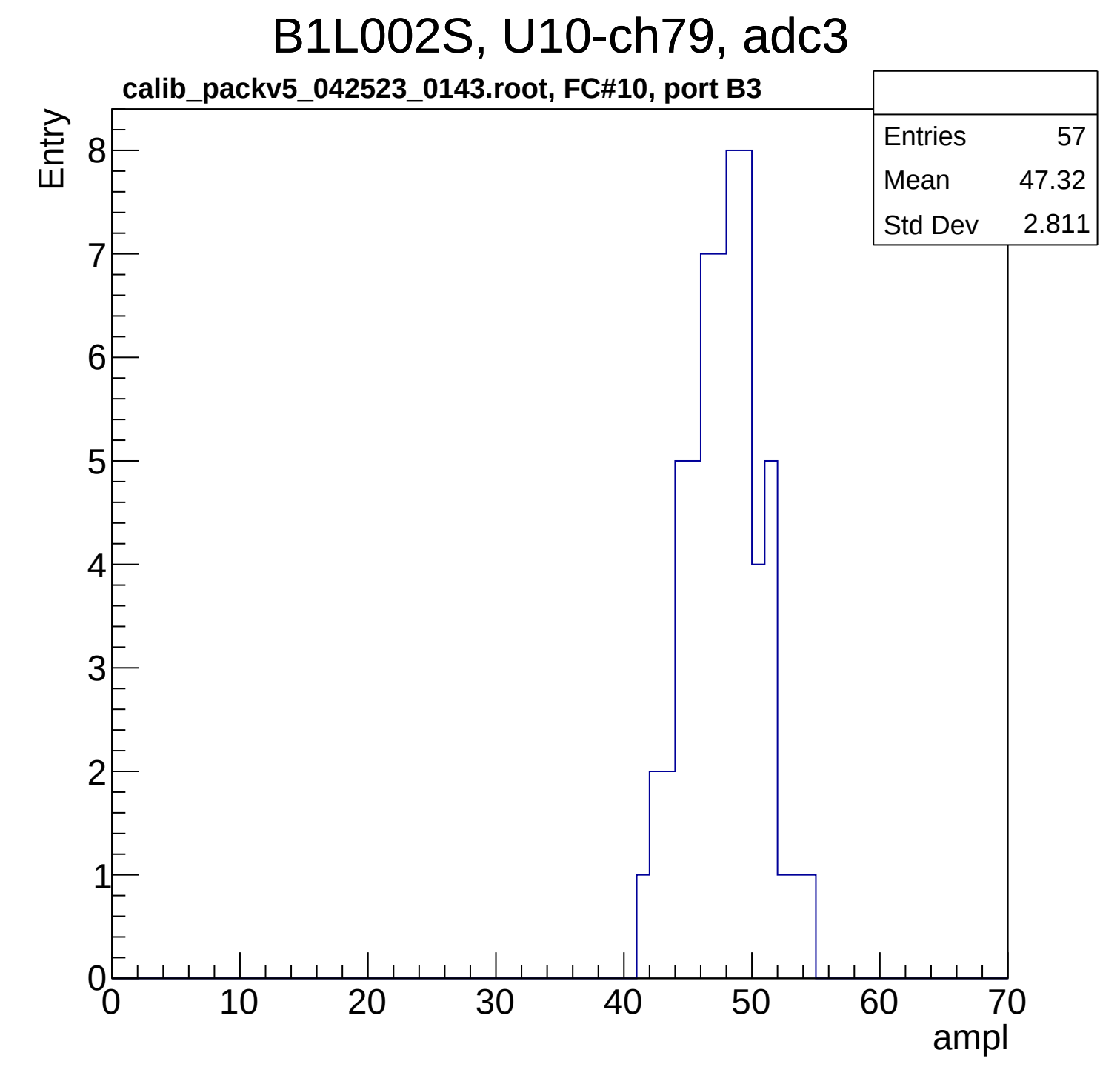
40
50
60
70


B1L002S, U10-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.32
Std Dev 2.811
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
30
40
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60
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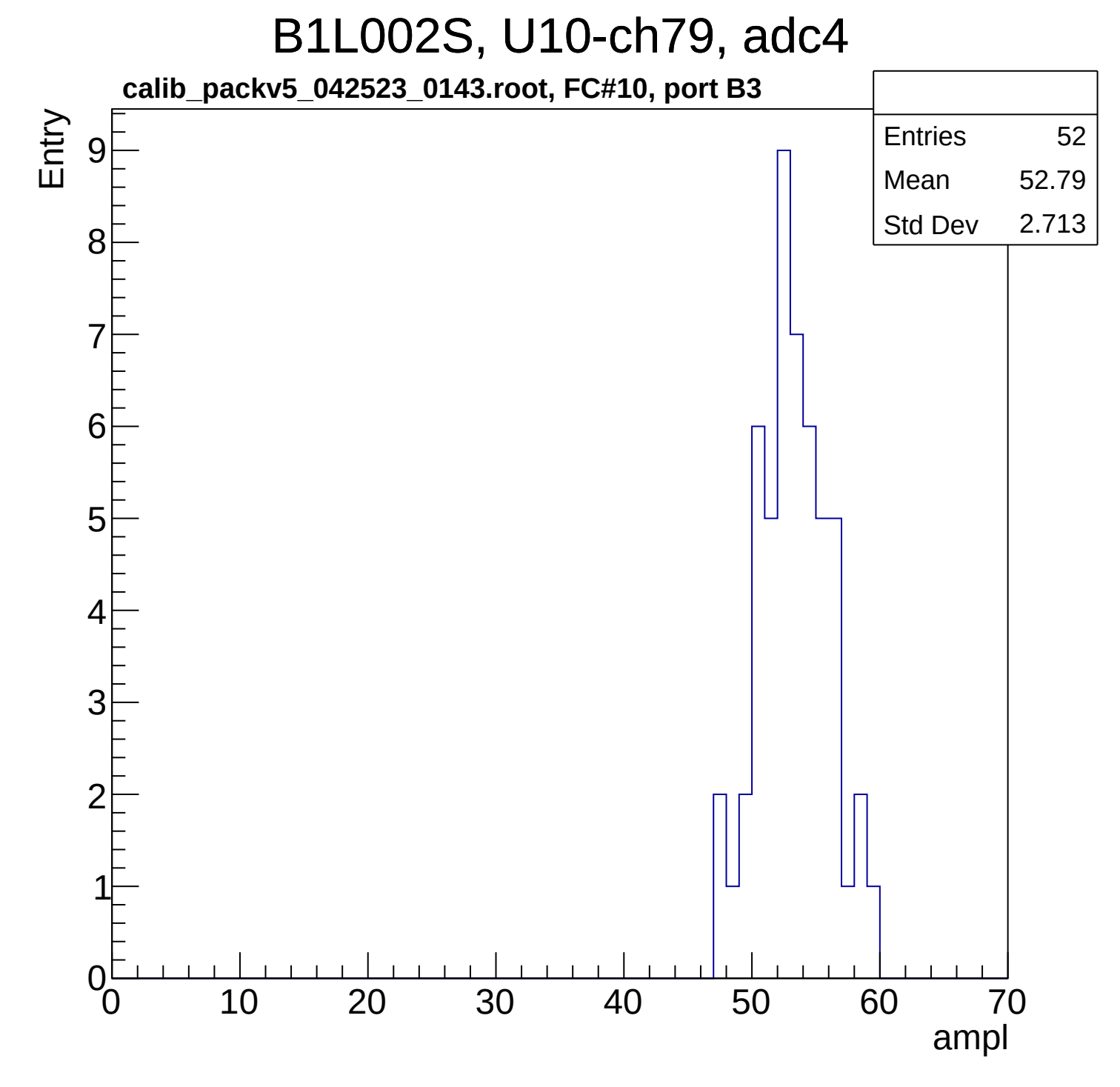
B1L002S, U10-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.79
Std Dev 2.713
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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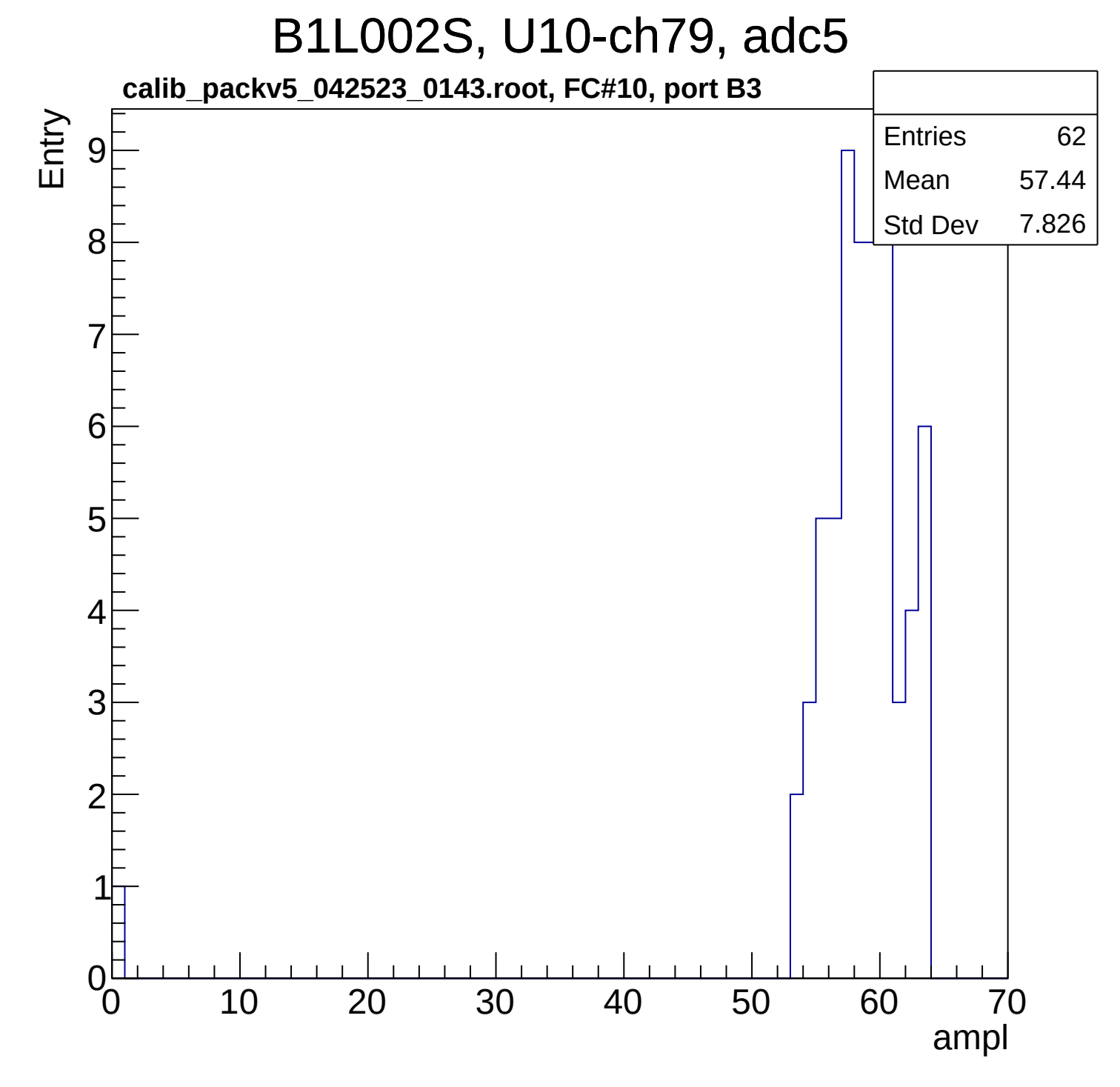
B1L002S, U10-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.44
Std Dev 7.826
Entry
ampl
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3
4
5
6
7
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0
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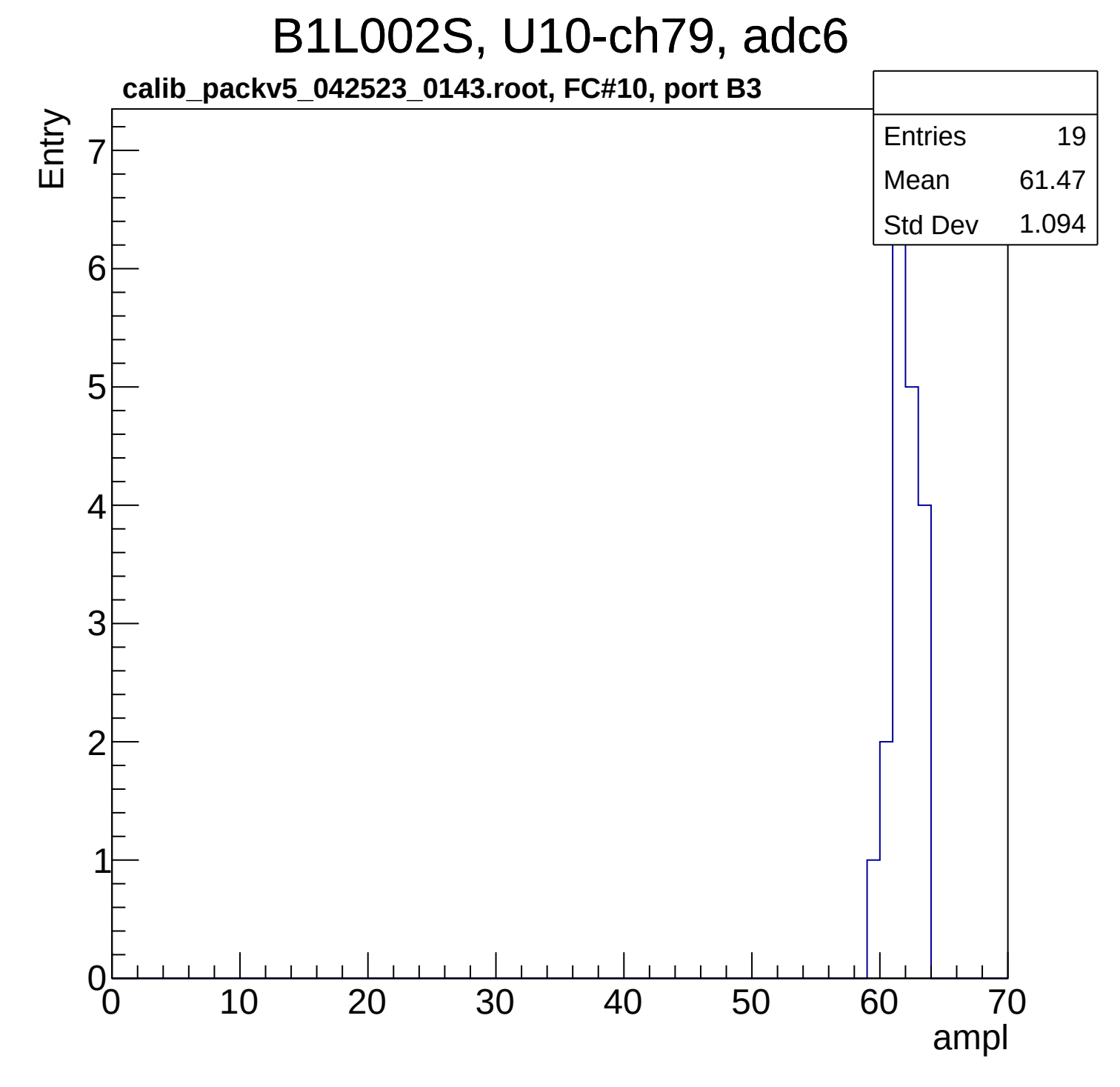
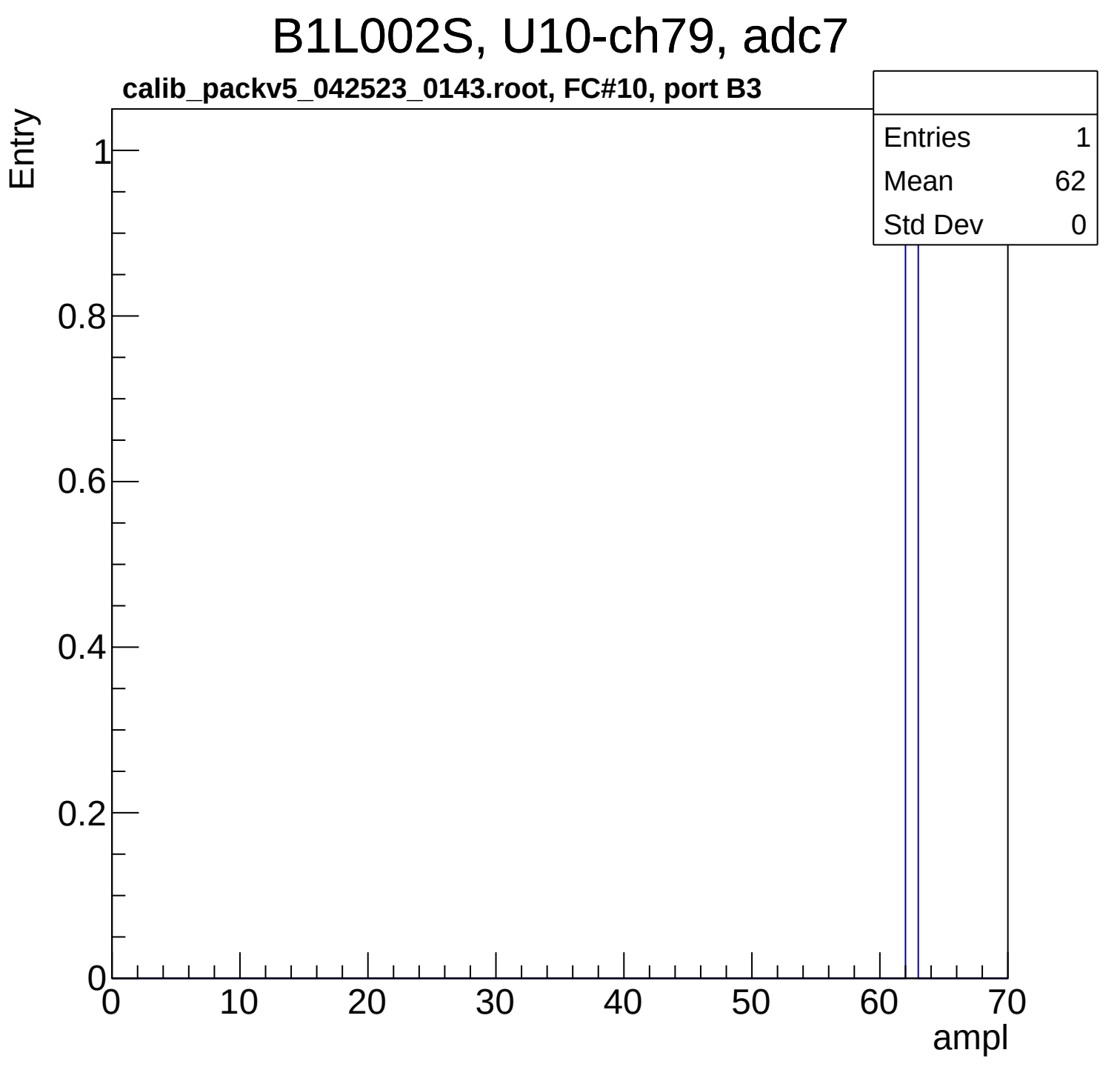

B1L002S, U10-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.47
Std Dev 1.094
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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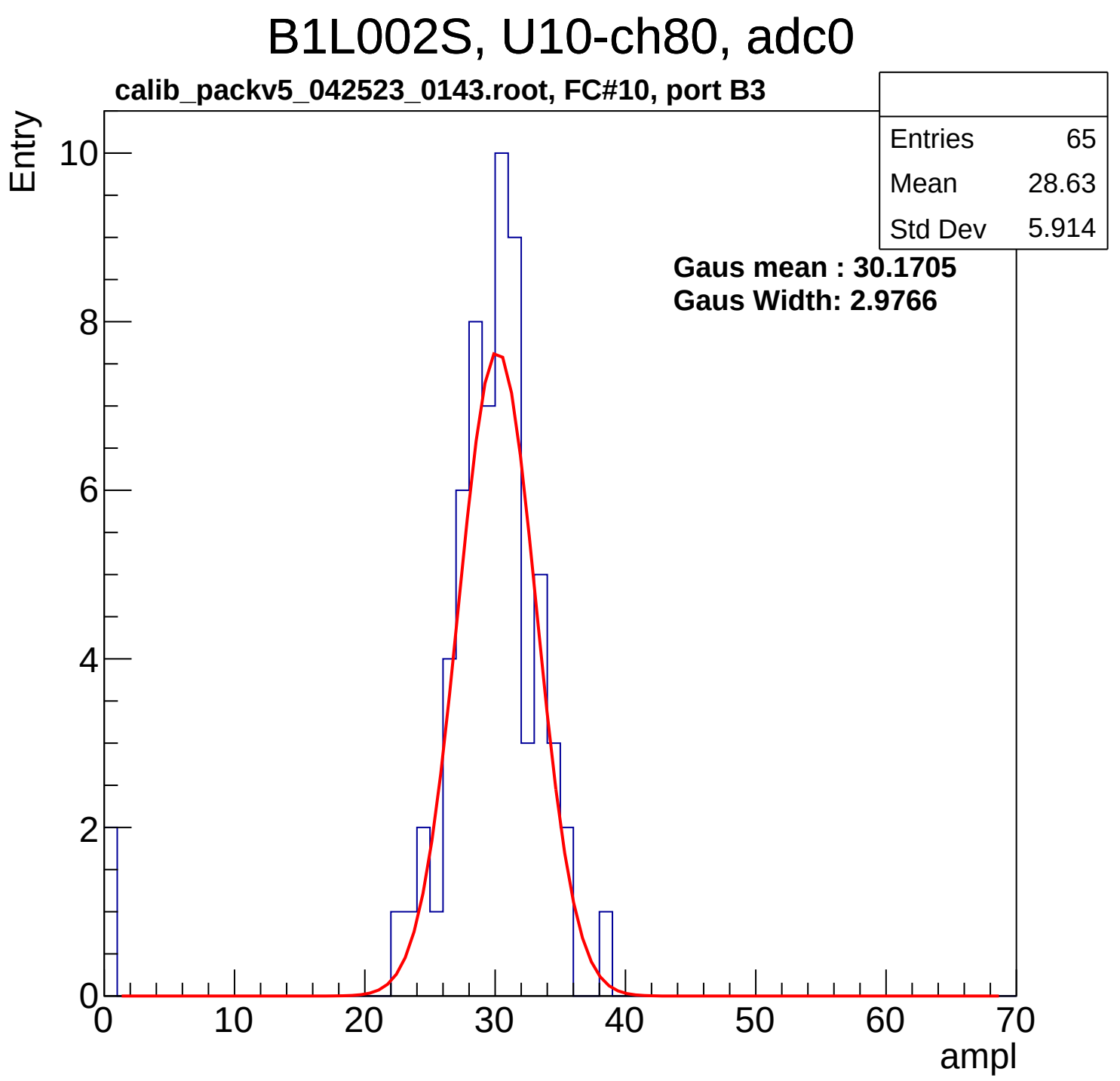
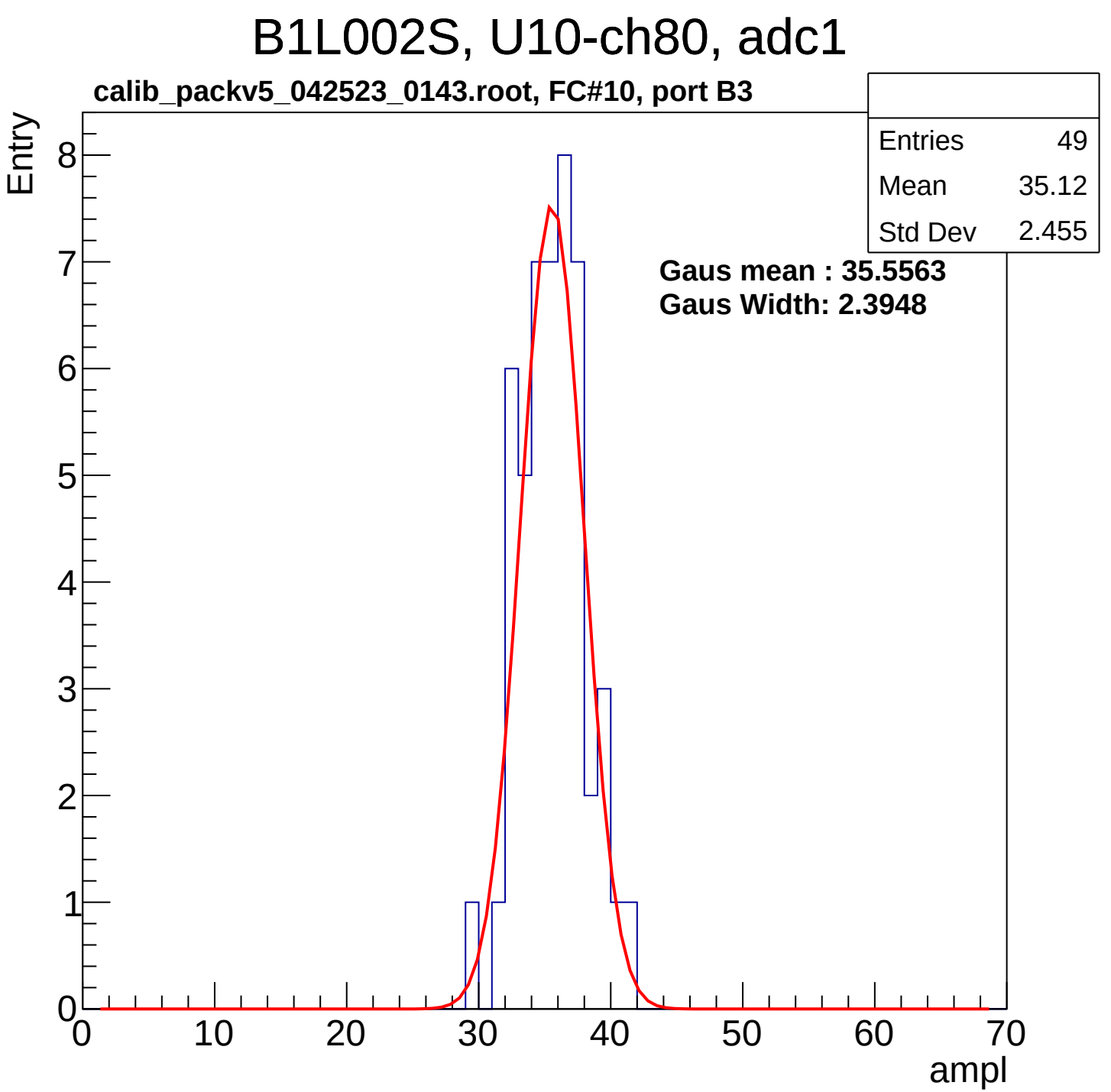
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.63
Std Dev 5.914
Gaus mean : 30.1705
Gaus Width: 2.9766
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 35.12
Std Dev 2.455
Gaus mean : 35.5563
Gaus Width: 2.3948
Entry
ampl
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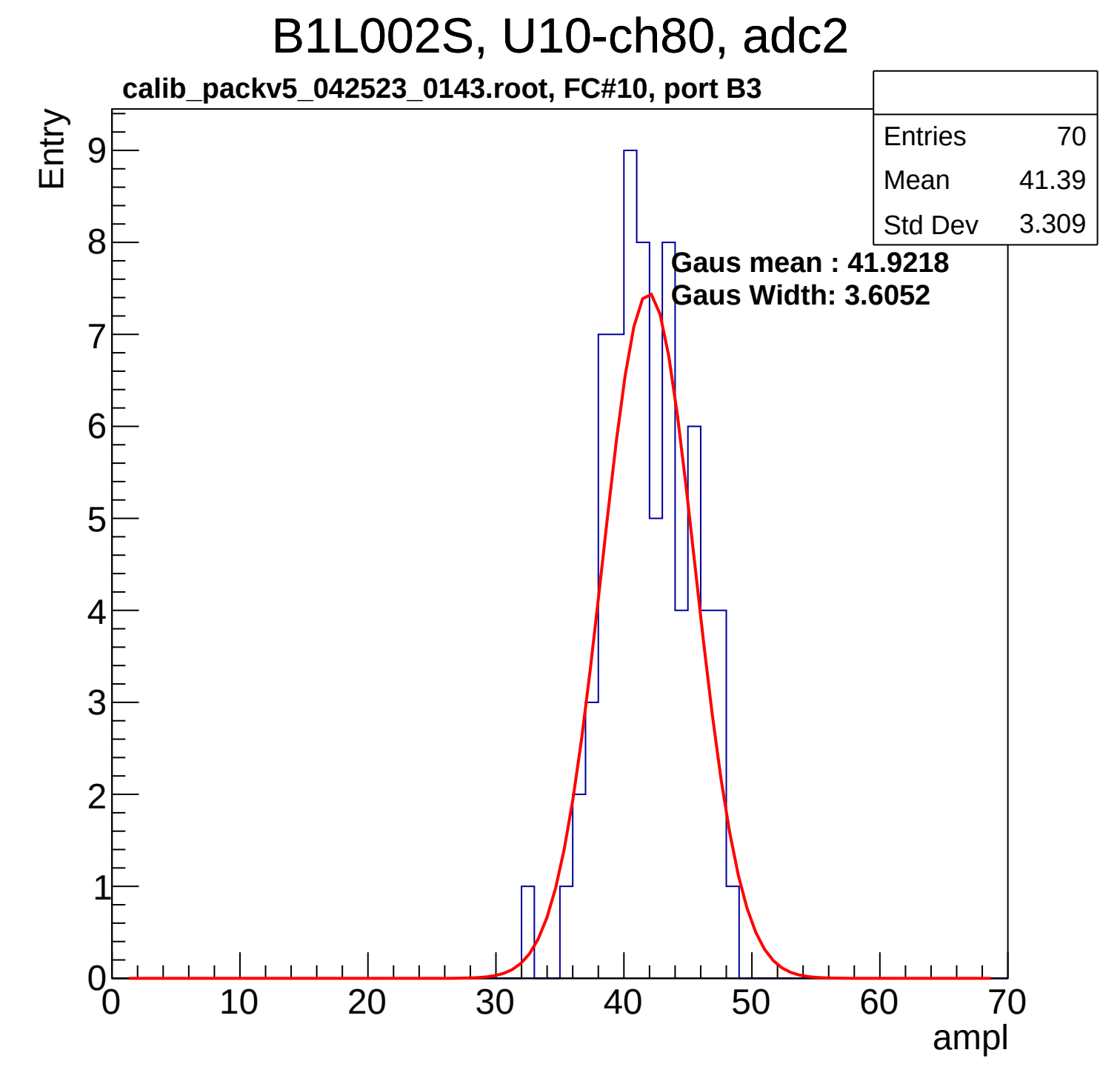
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.39
Std Dev 3.309
Gaus mean : 41.9218
Gaus Width: 3.6052
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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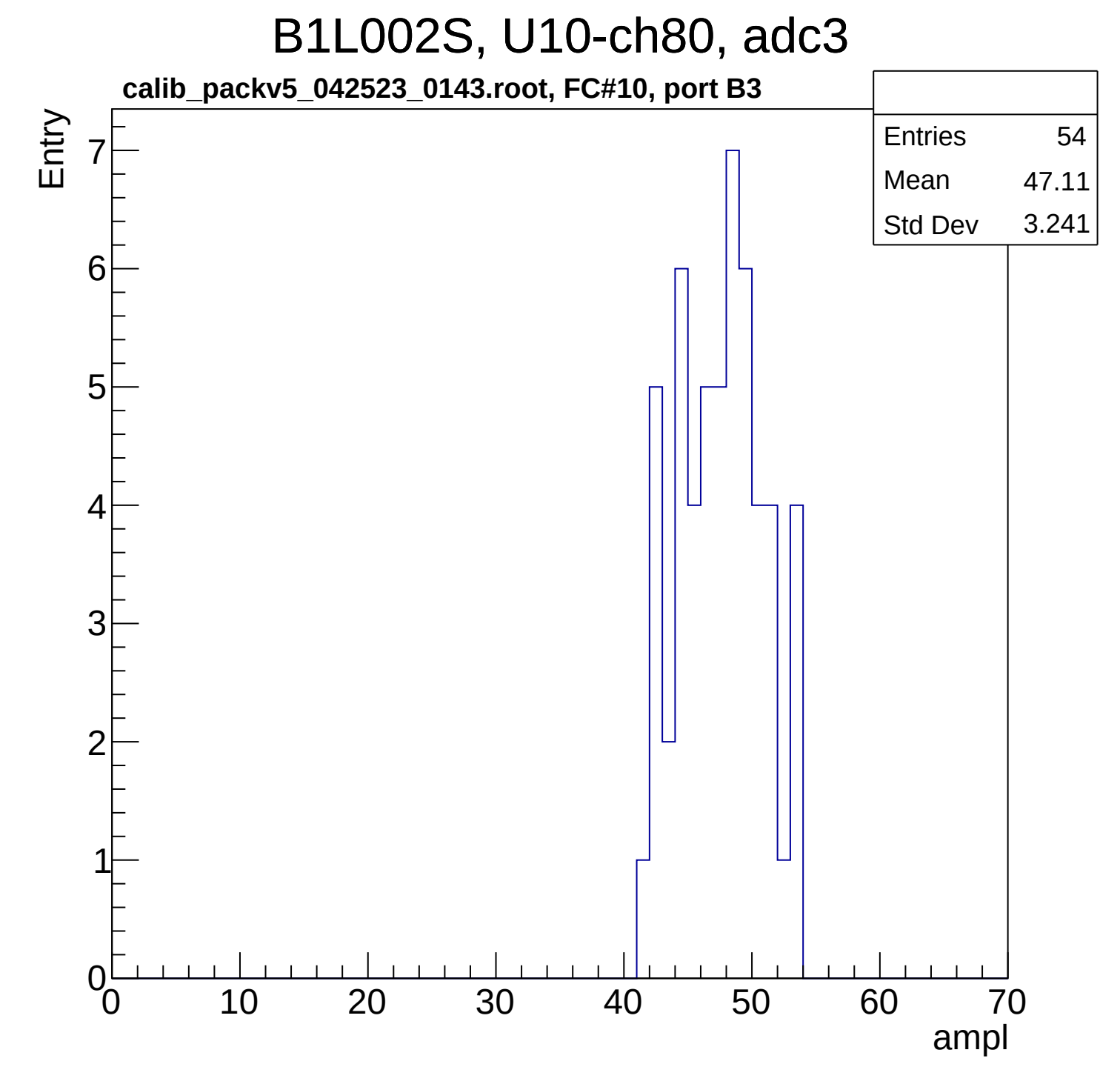
B1L002S, U10-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.11
Std Dev 3.241
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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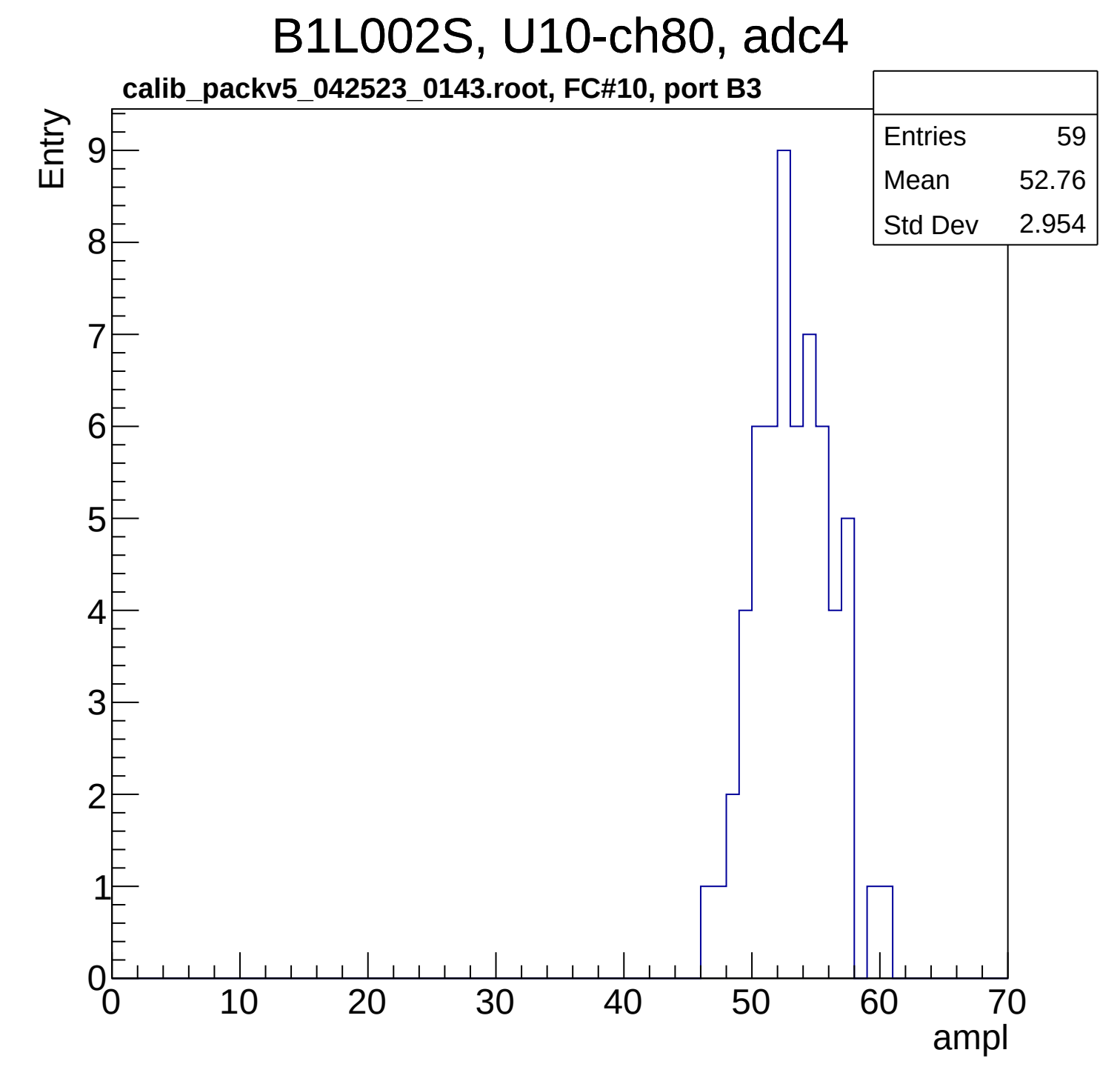
B1L002S, U10-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.76
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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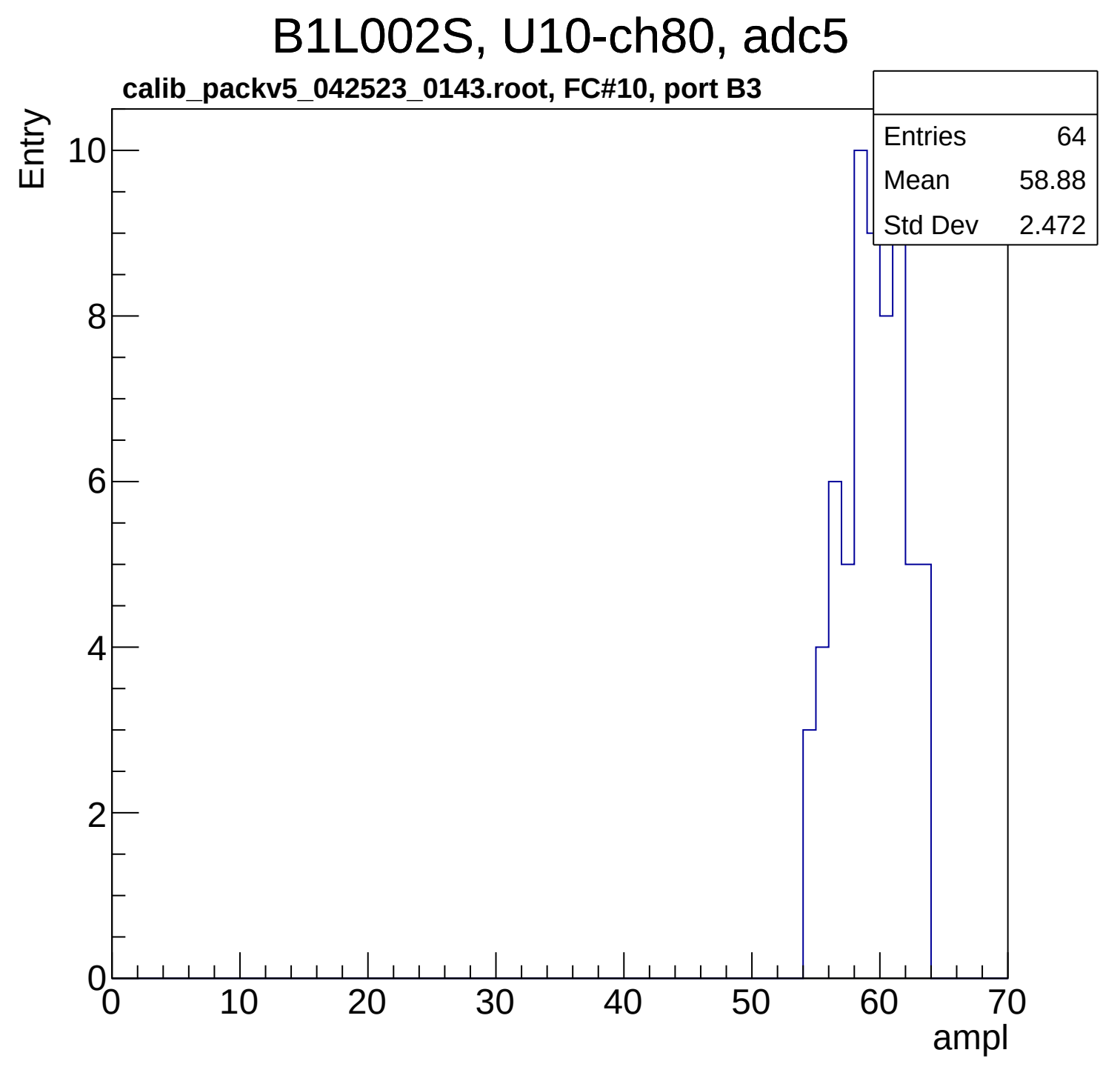
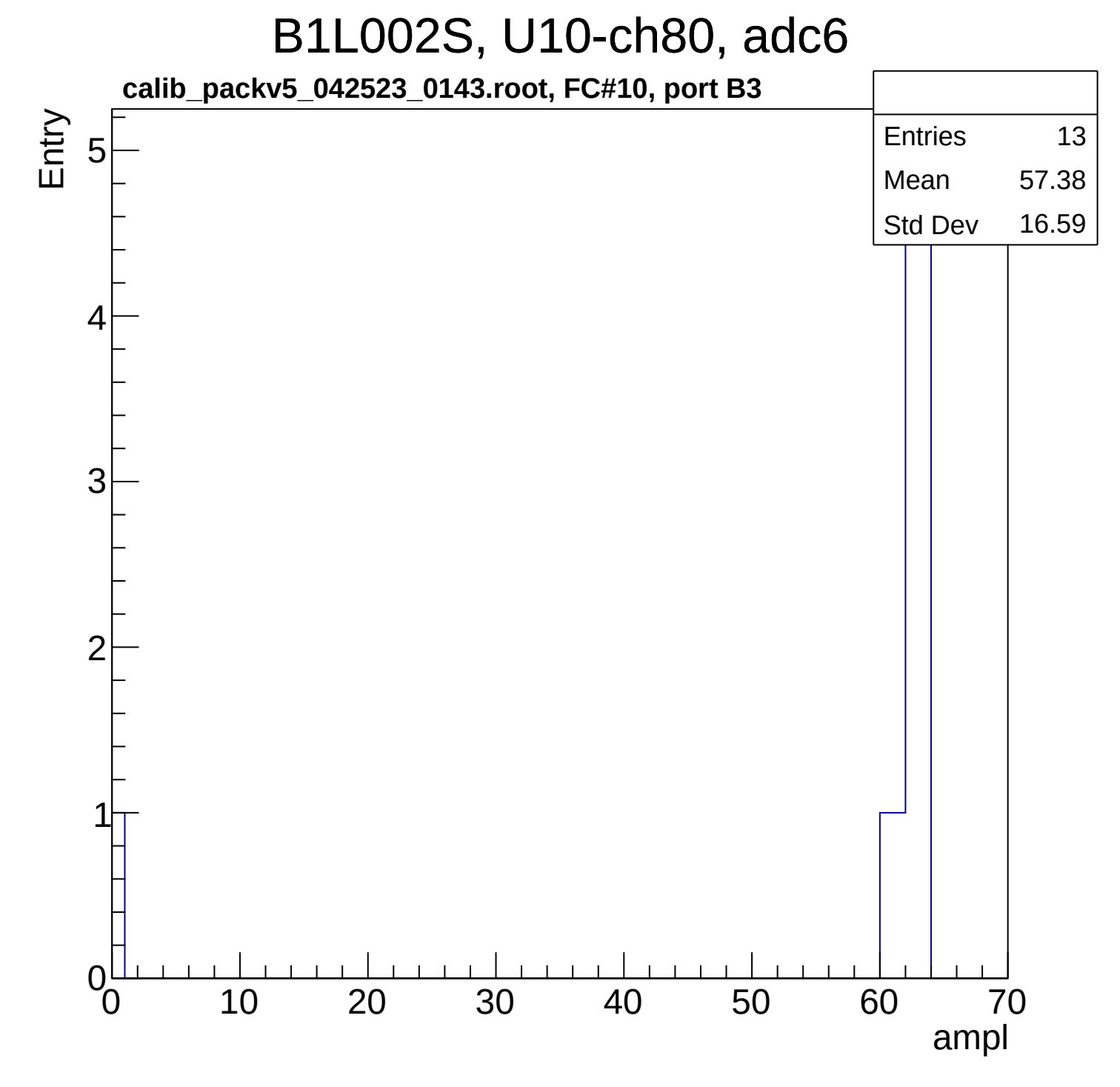
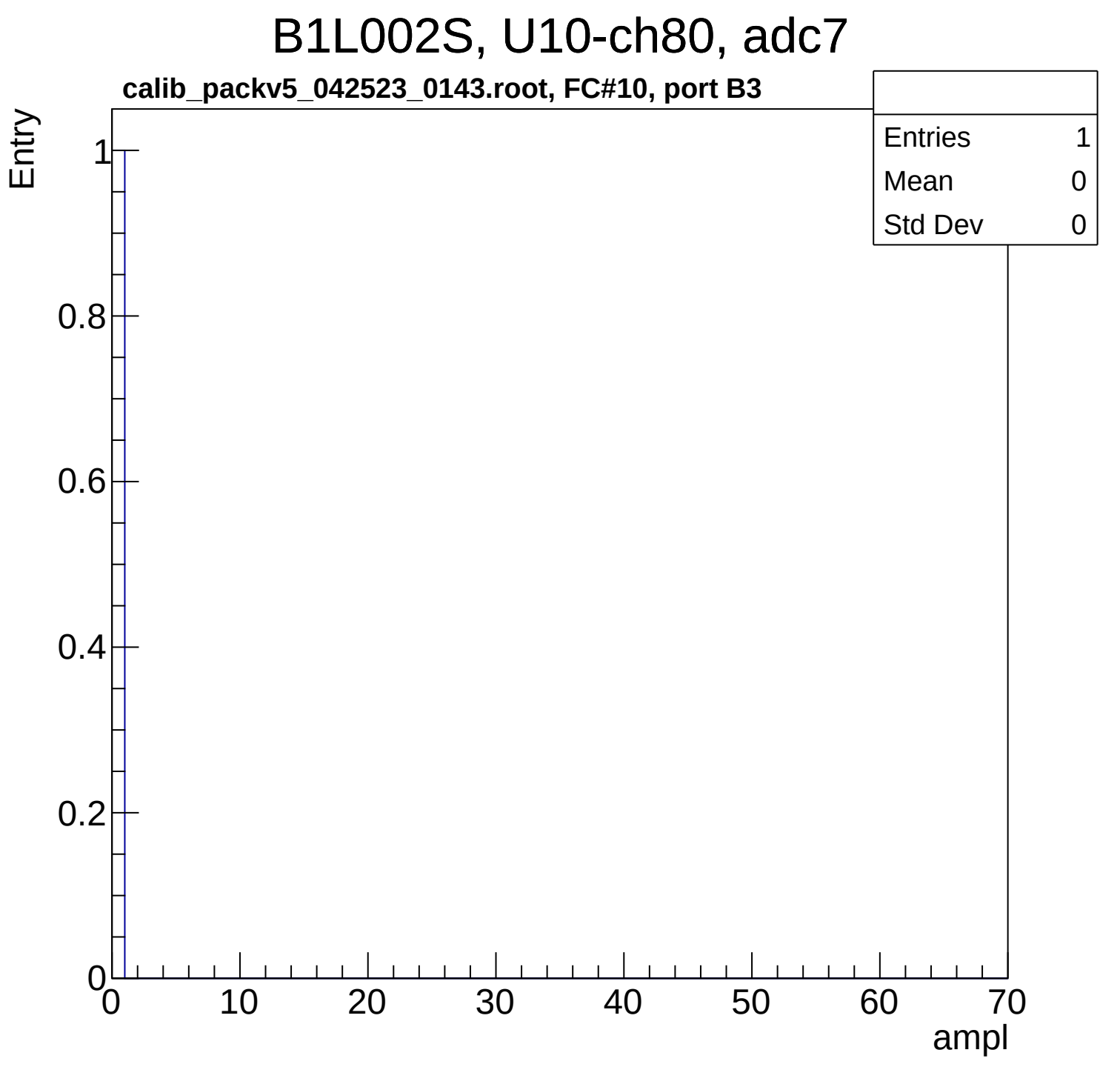

B1L002S, U10-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.88
Std Dev 2.472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.38
Std Dev 16.59
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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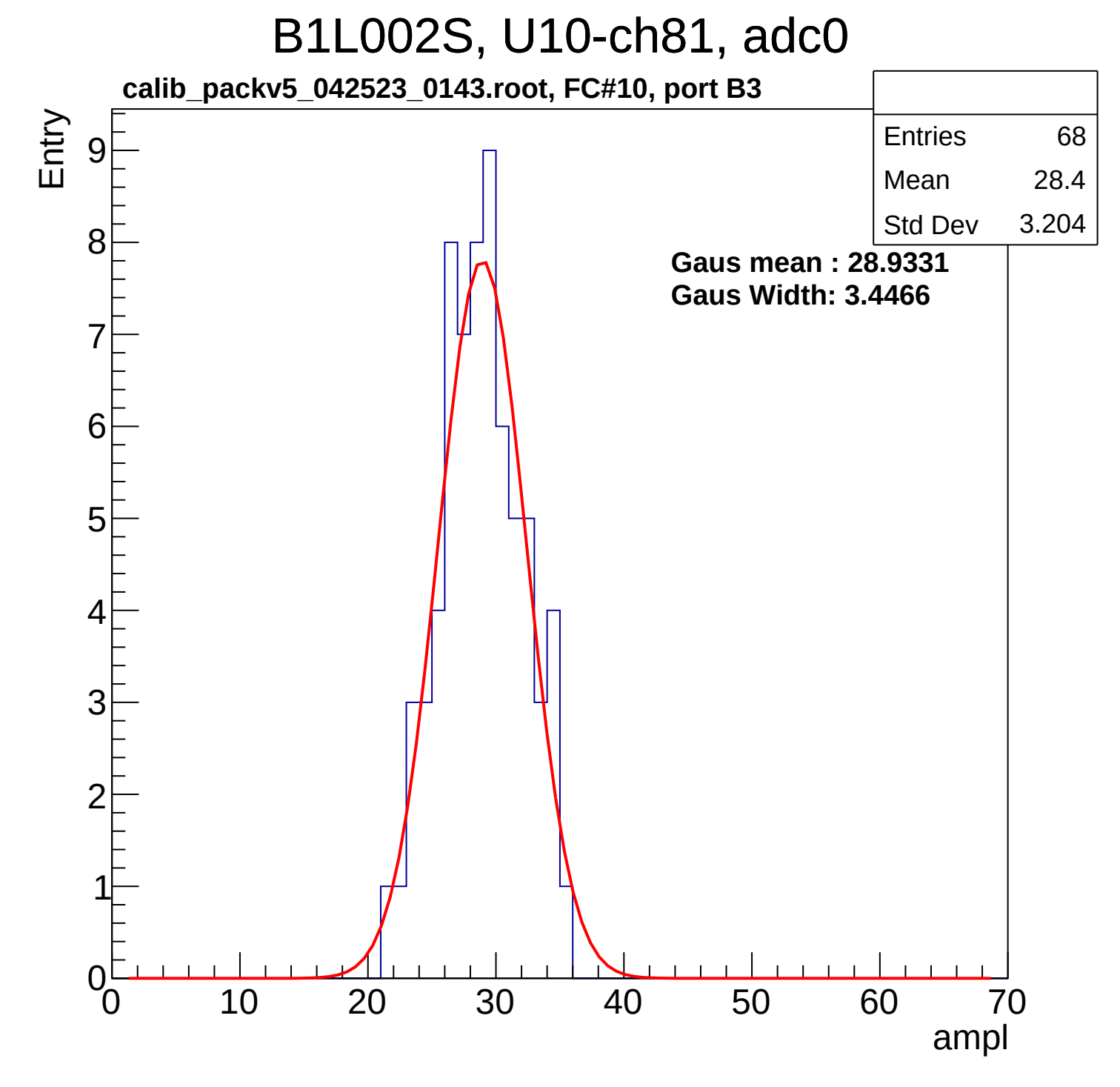
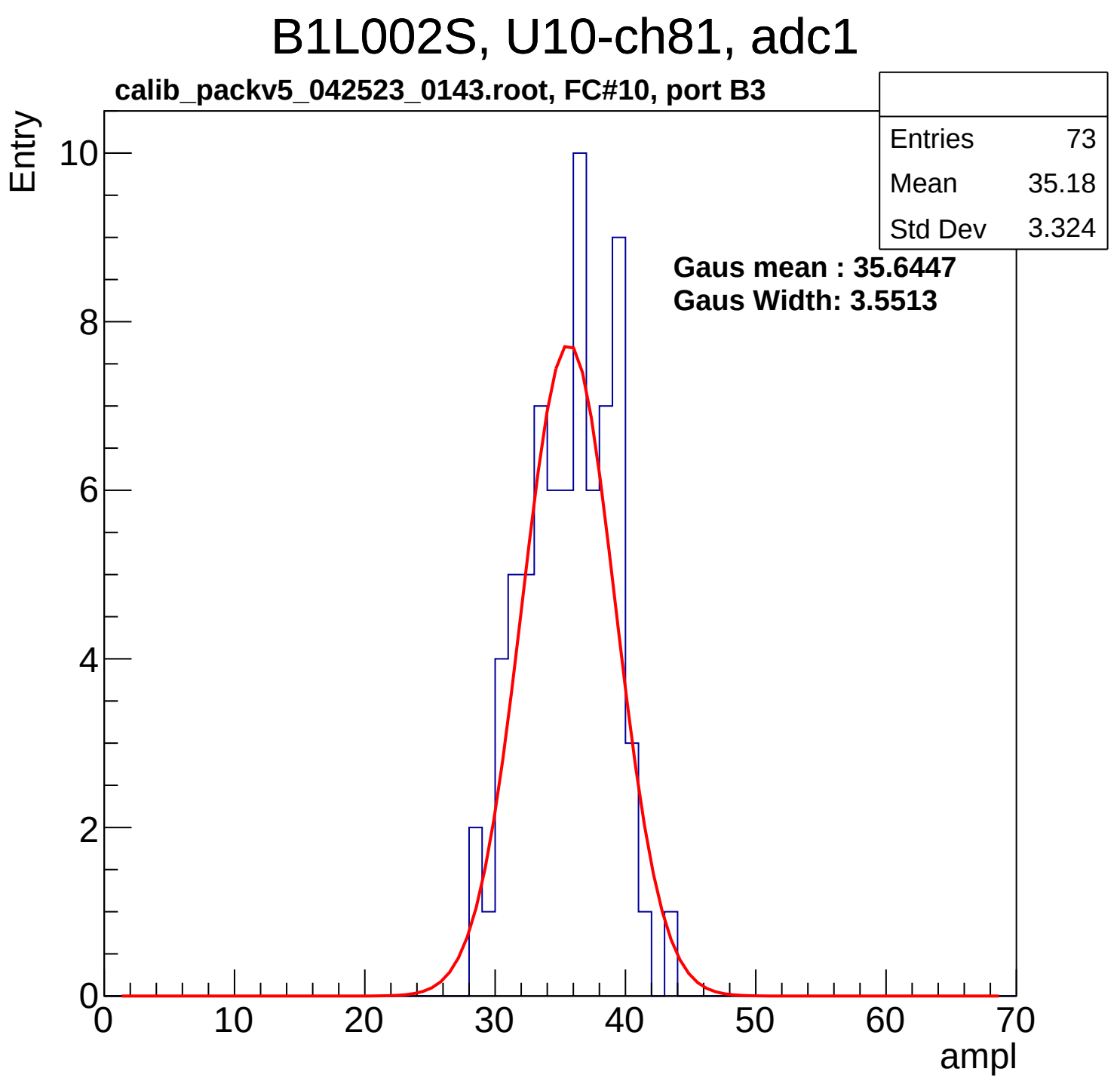
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.4
Std Dev 3.204
Gaus mean : 28.9331
Gaus Width: 3.4466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.18
Std Dev 3.324
Gaus mean : 35.6447
Gaus Width: 3.5513
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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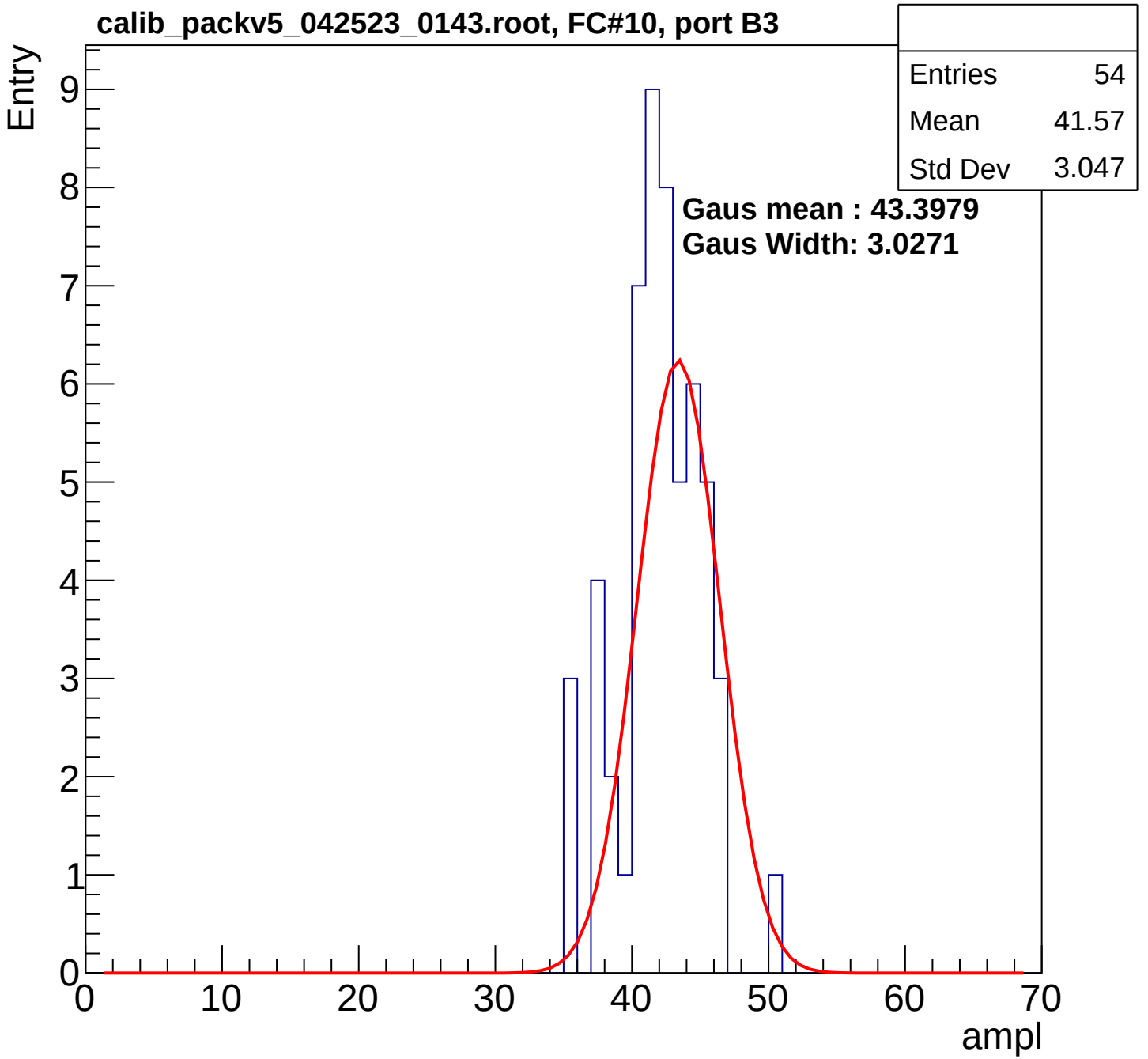
B1L002S, U10-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 41.57
Std Dev 3.047
Gaus mean : 43.3979
Gaus Width: 3.0271
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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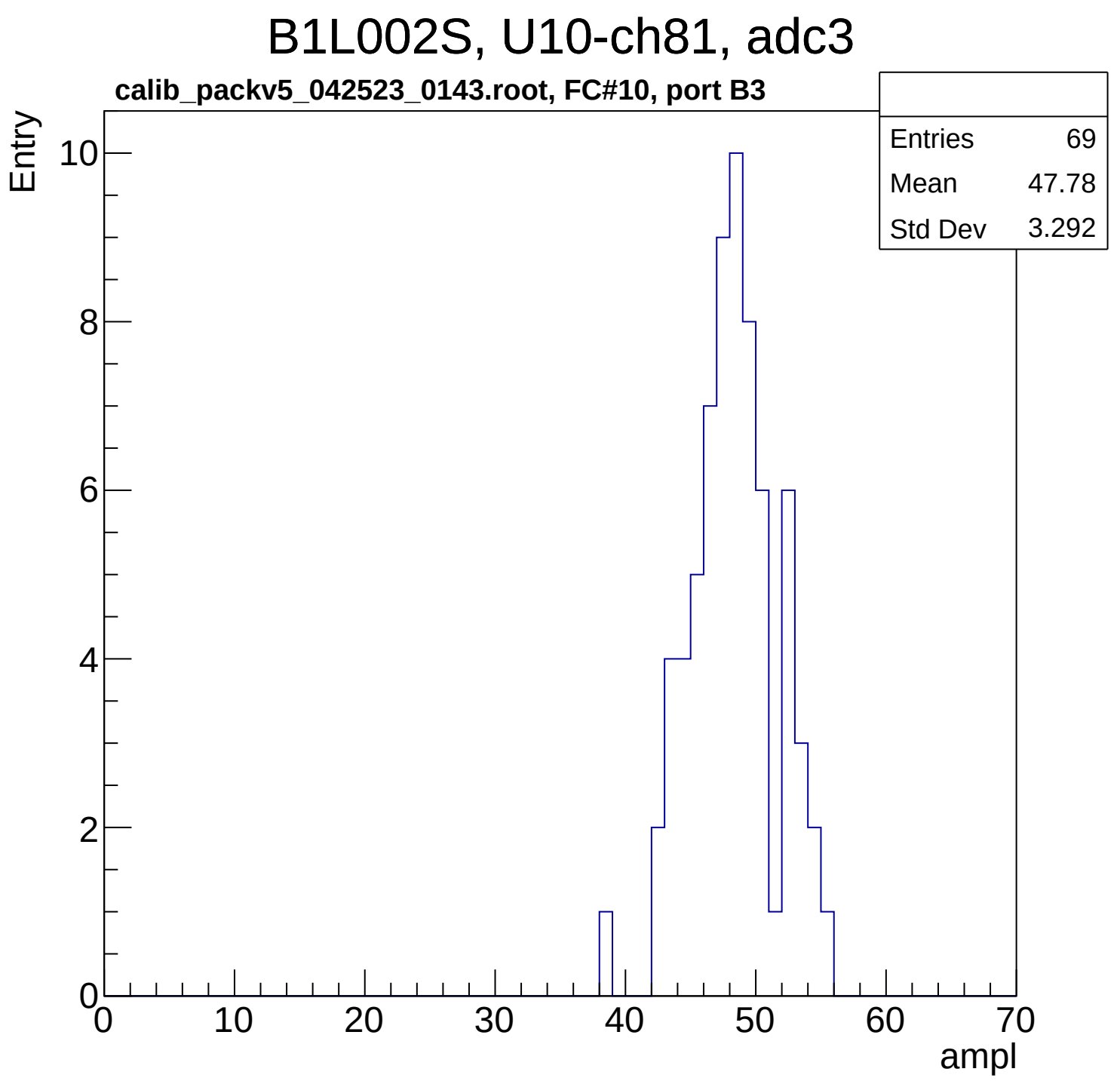
B1L002S, U10-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.78
Std Dev 3.292
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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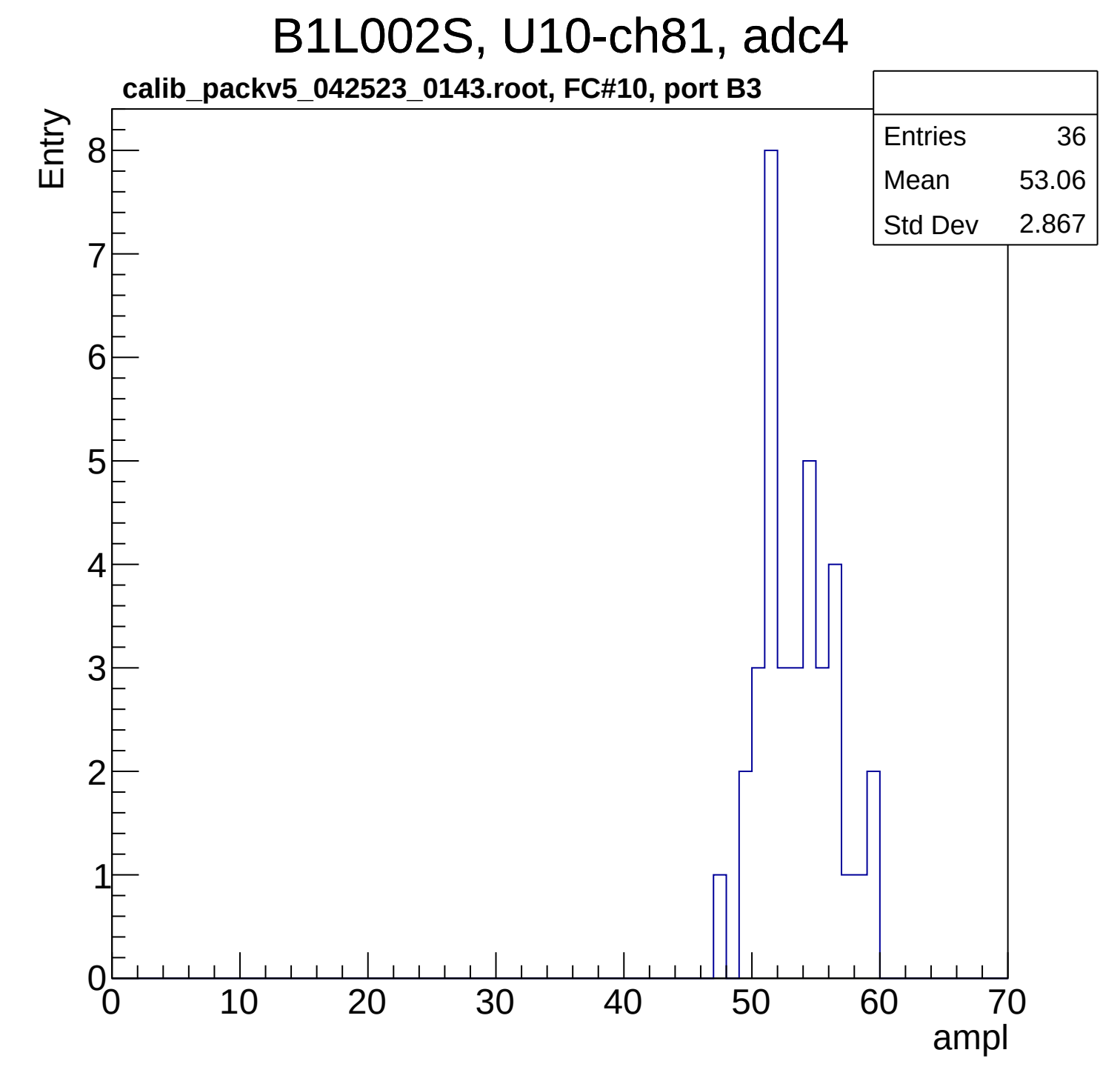
B1L002S, U10-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 53.06
Std Dev 2.867
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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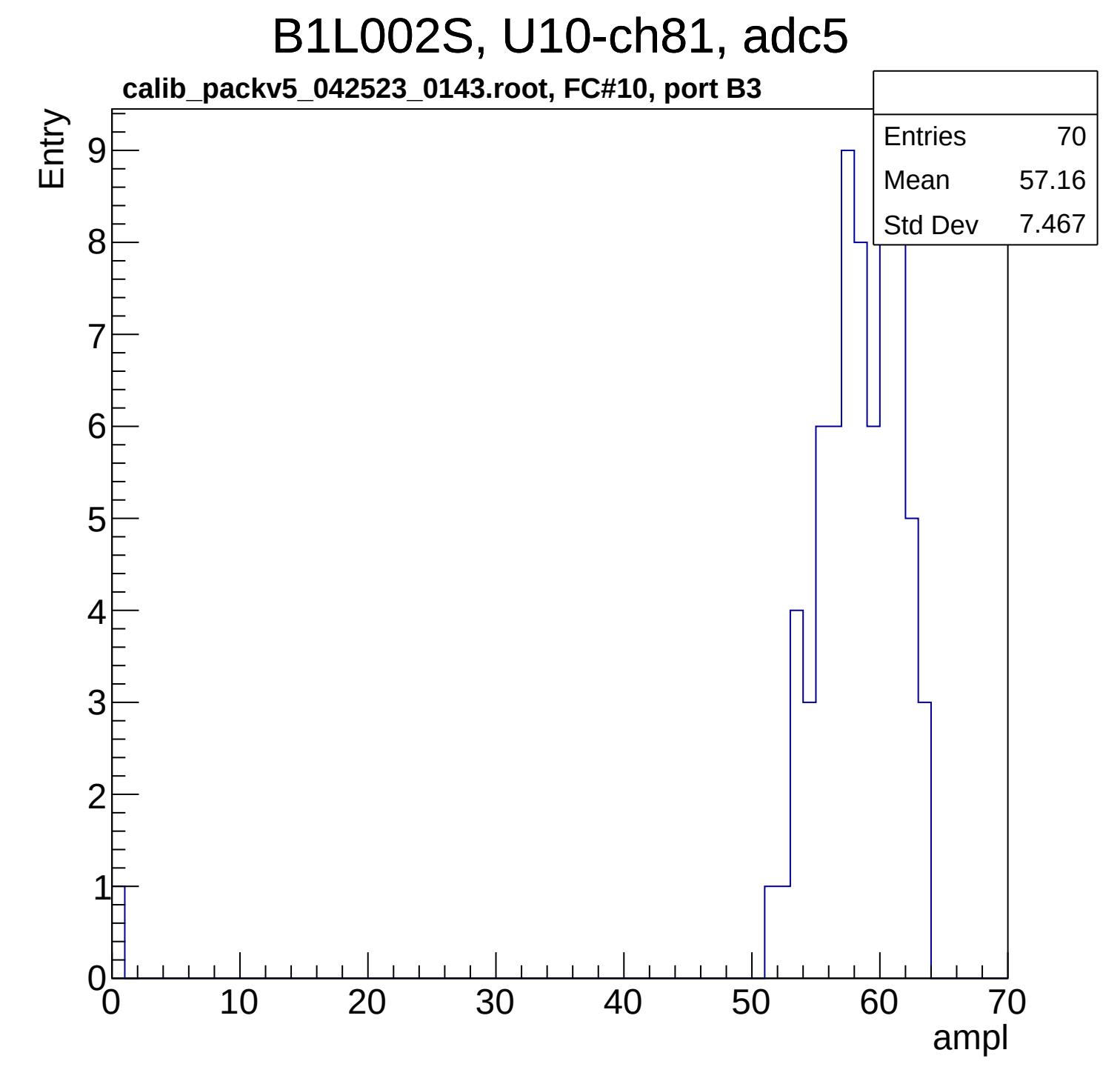
B1L002S, U10-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 57.16
Std Dev 7.467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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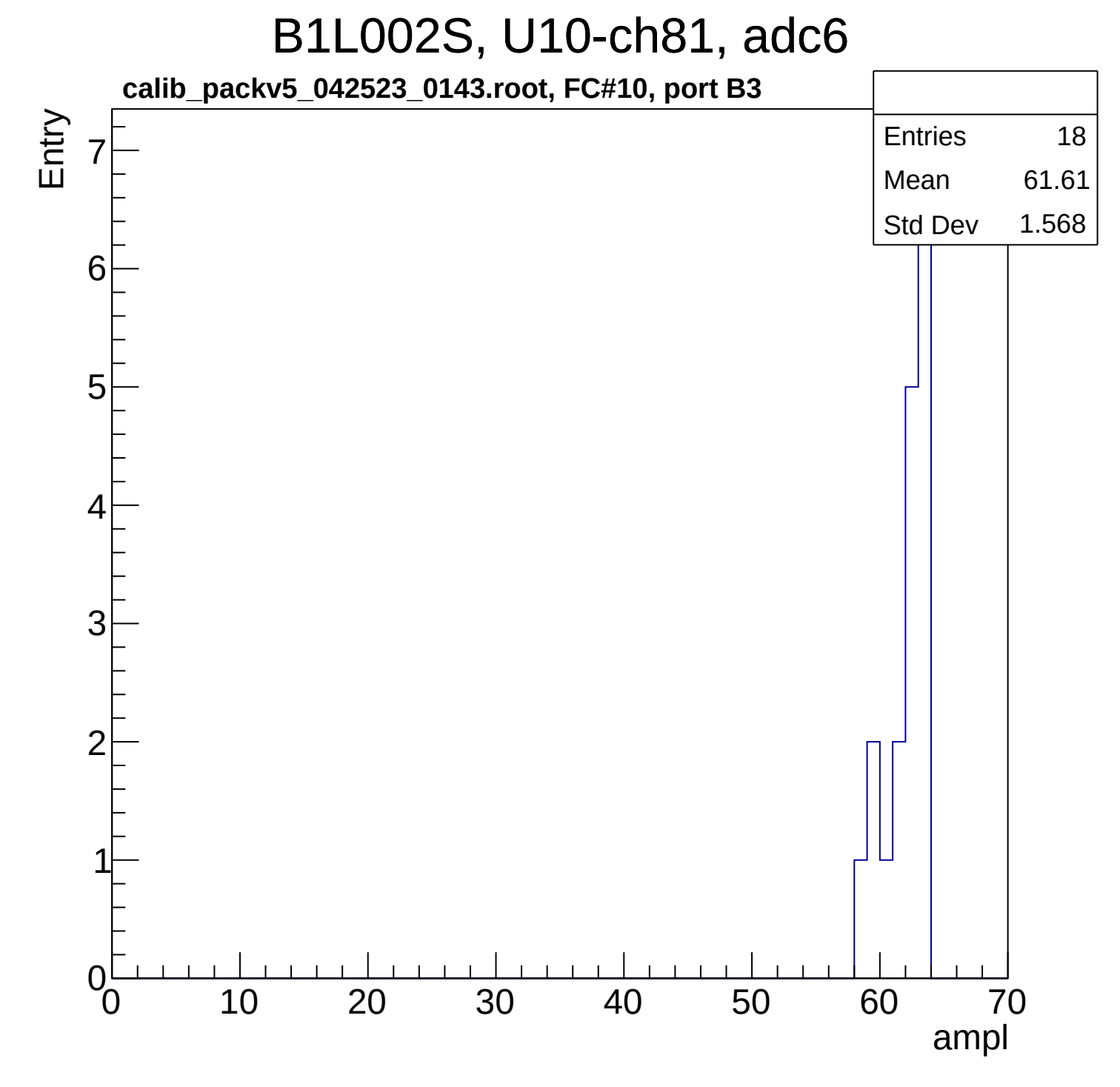
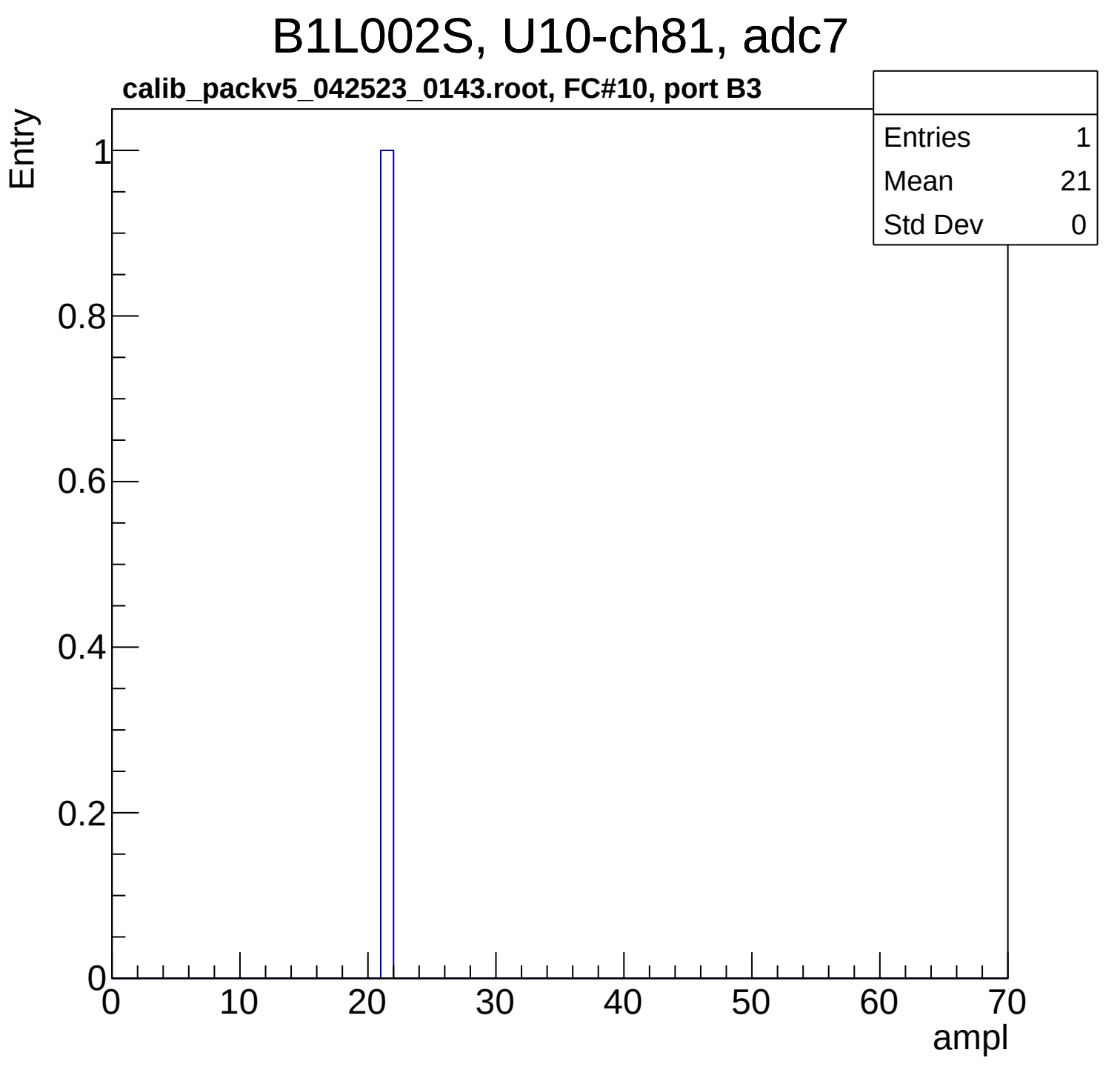
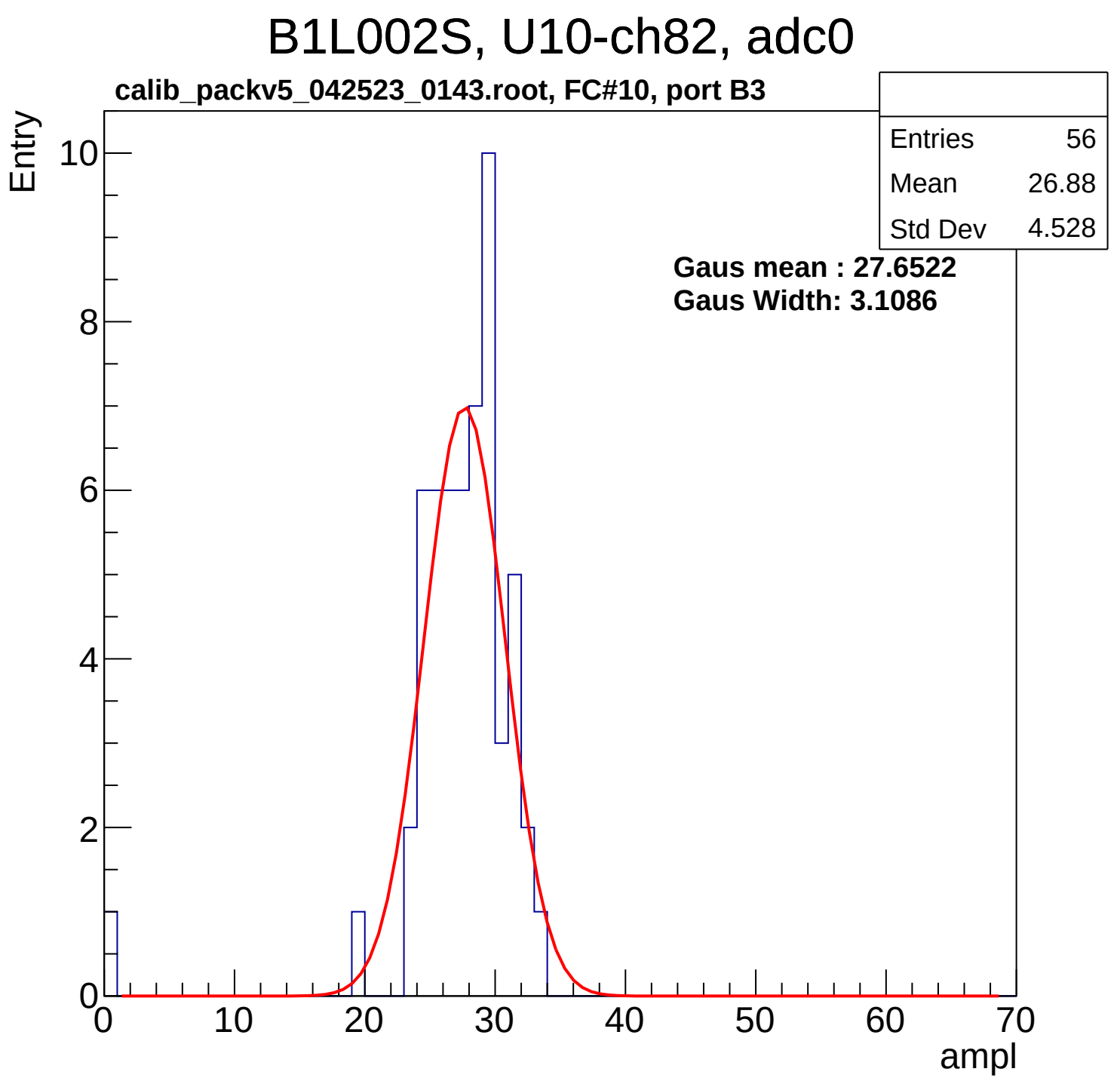
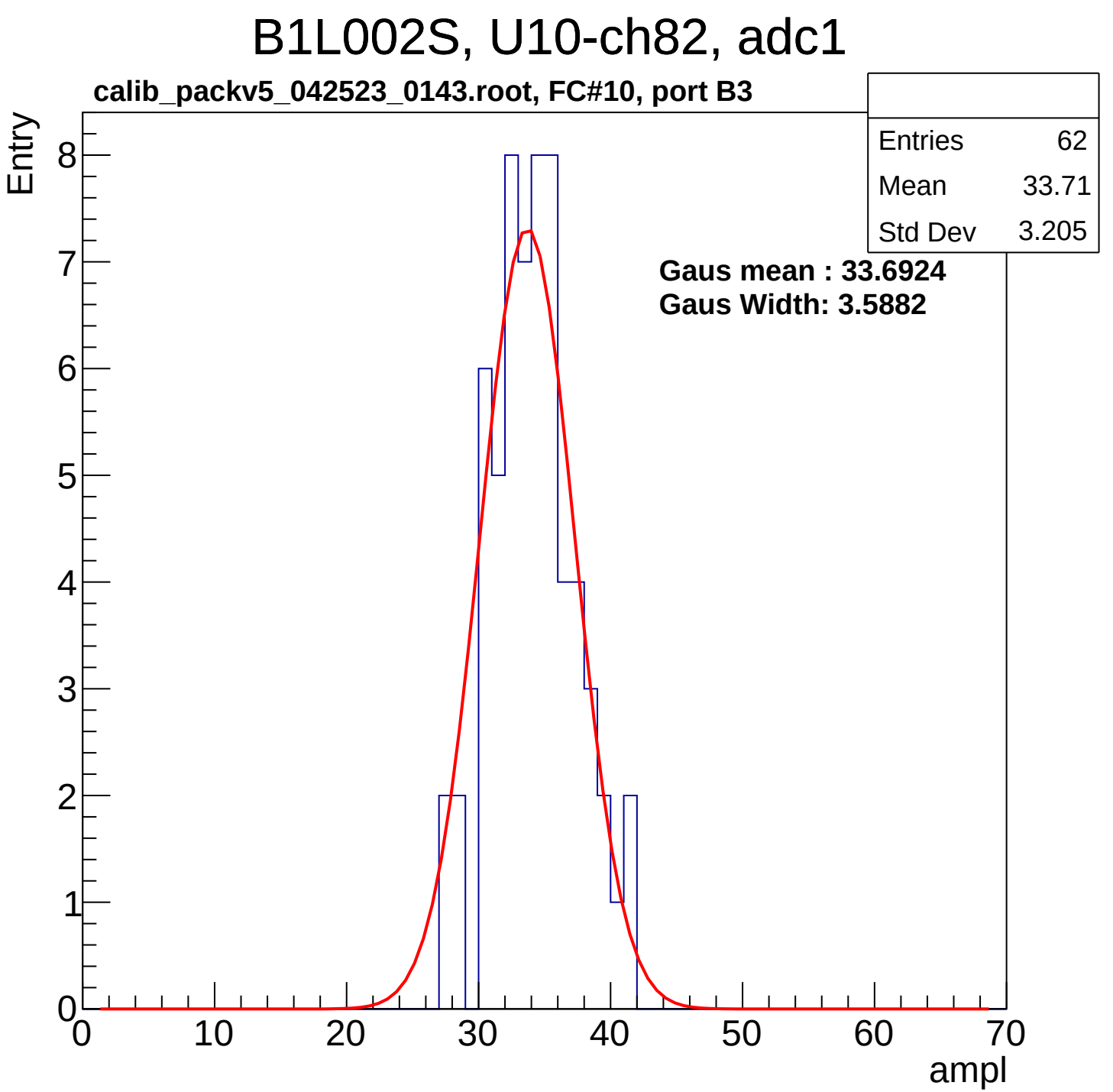
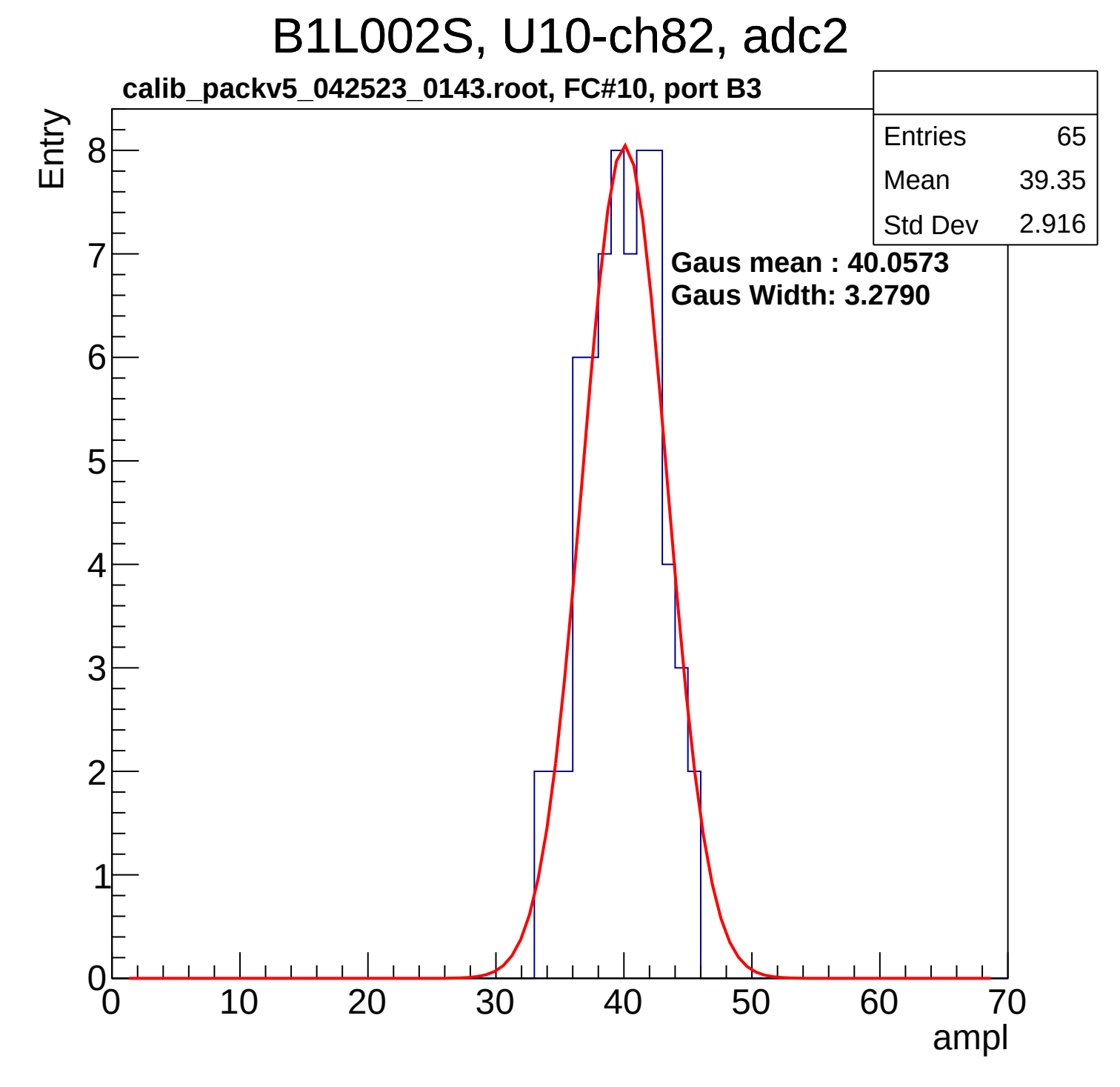
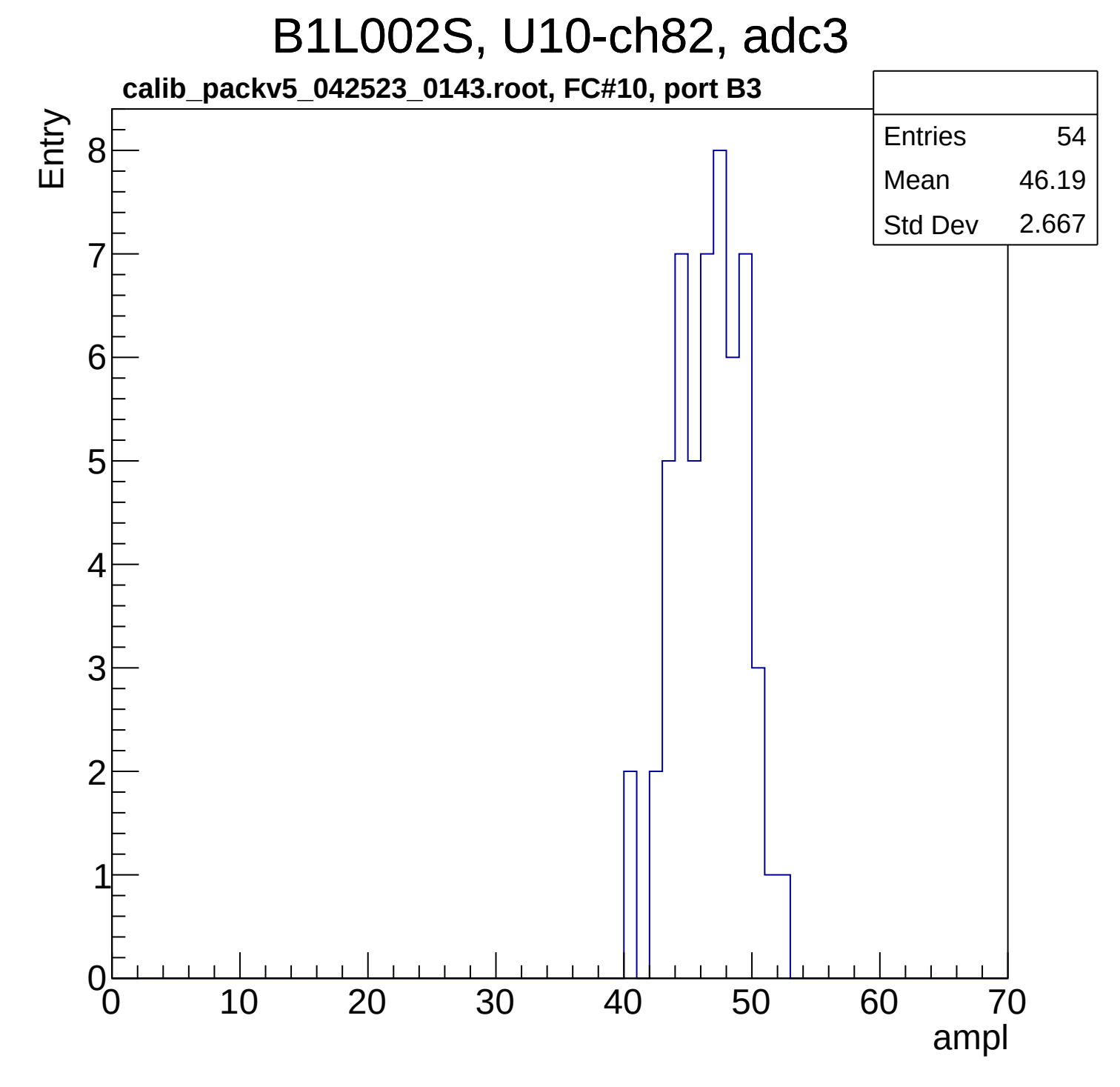

B1L002S, U10-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.61
Std Dev 1.568
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 26.88
Std Dev 4.528
Gaus mean : 27.6522
Gaus Width: 3.1086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.71
Std Dev 3.205
Gaus mean : 33.6924
Gaus Width: 3.5882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 39.35
Std Dev 2.916
Gaus mean : 40.0573
Gaus Width: 3.2790
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.19
Std Dev 2.667
Entry
ampl
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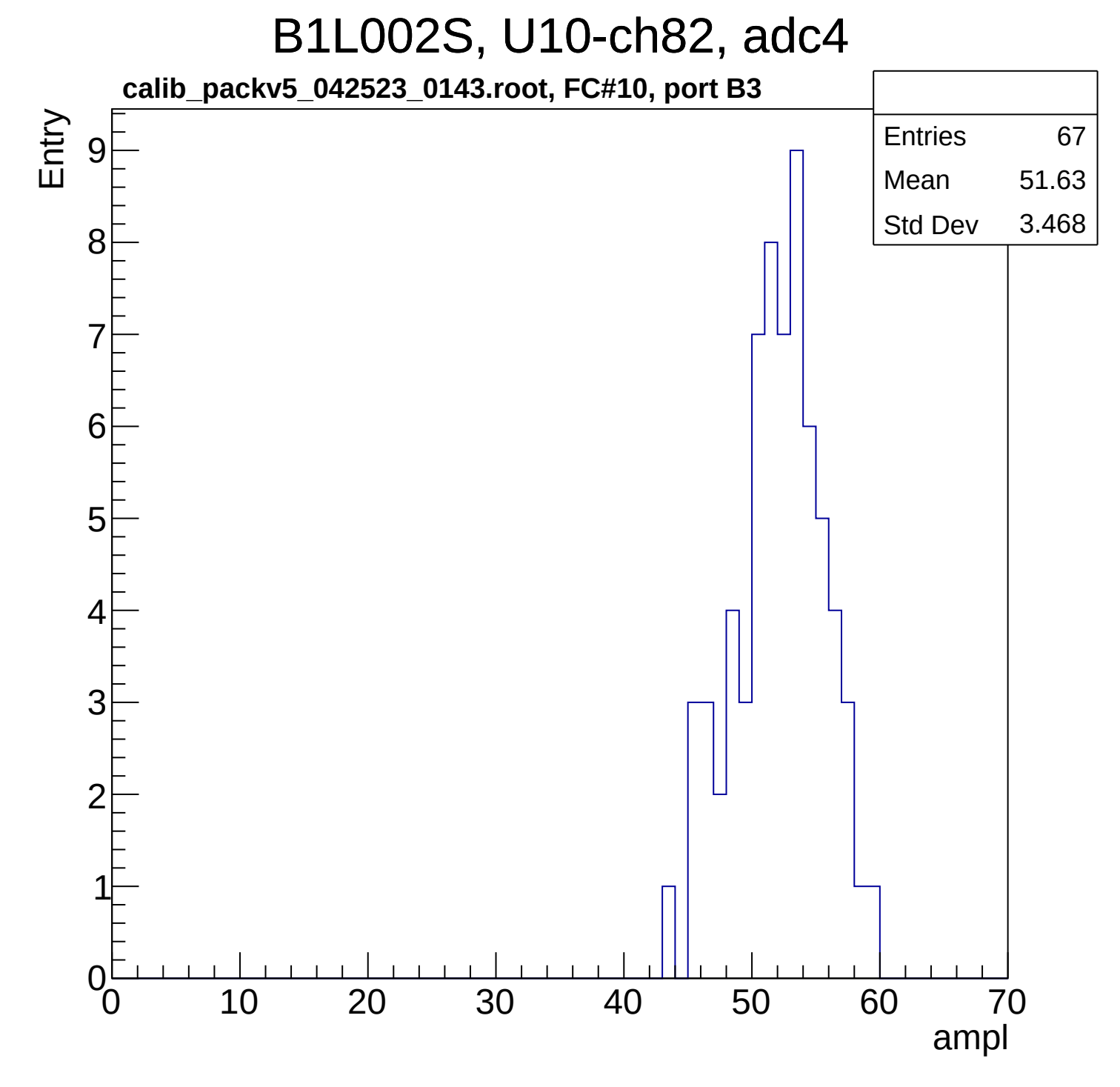
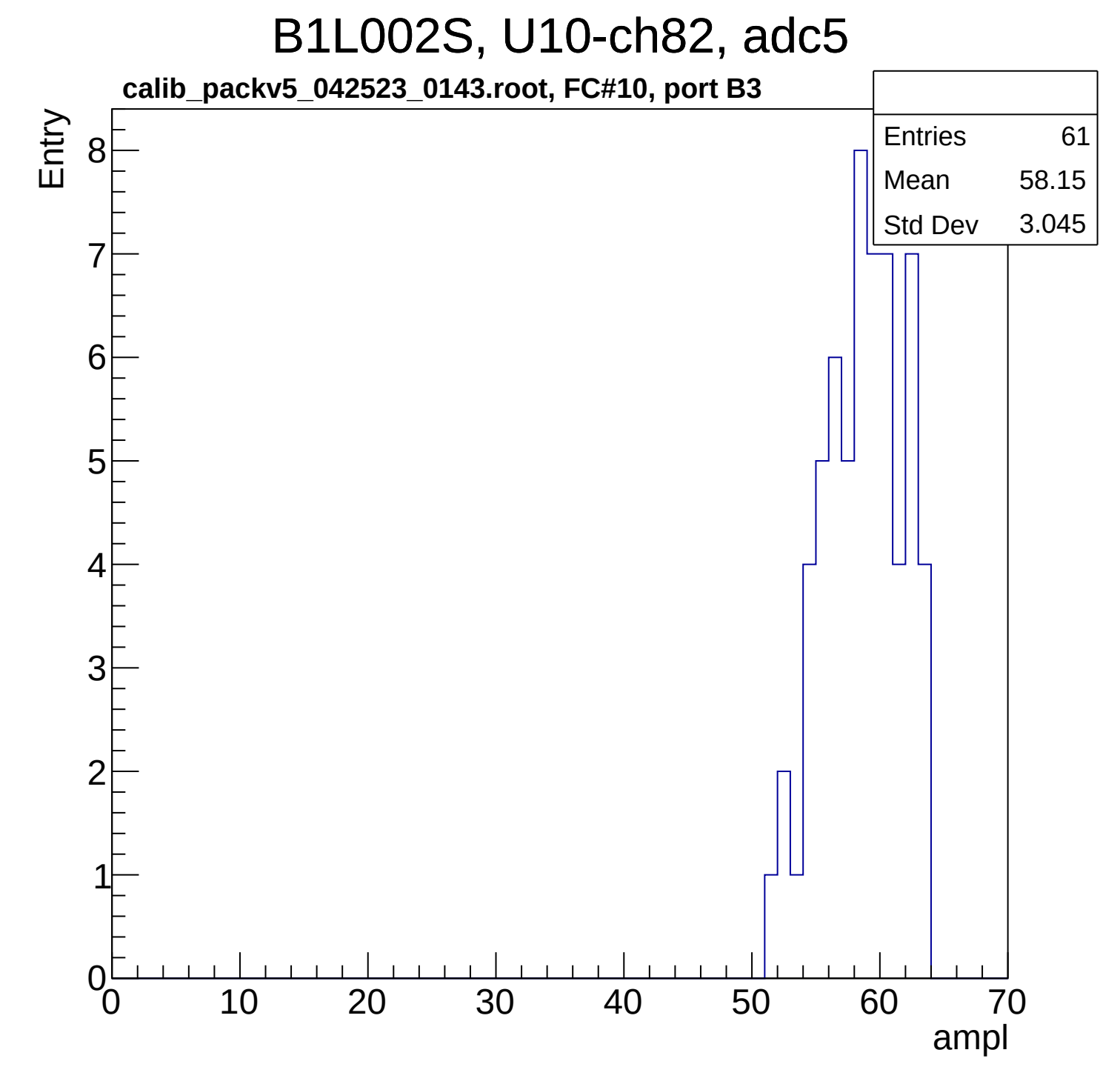
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 51.63
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.15
Std Dev 3.045
Entry
ampl
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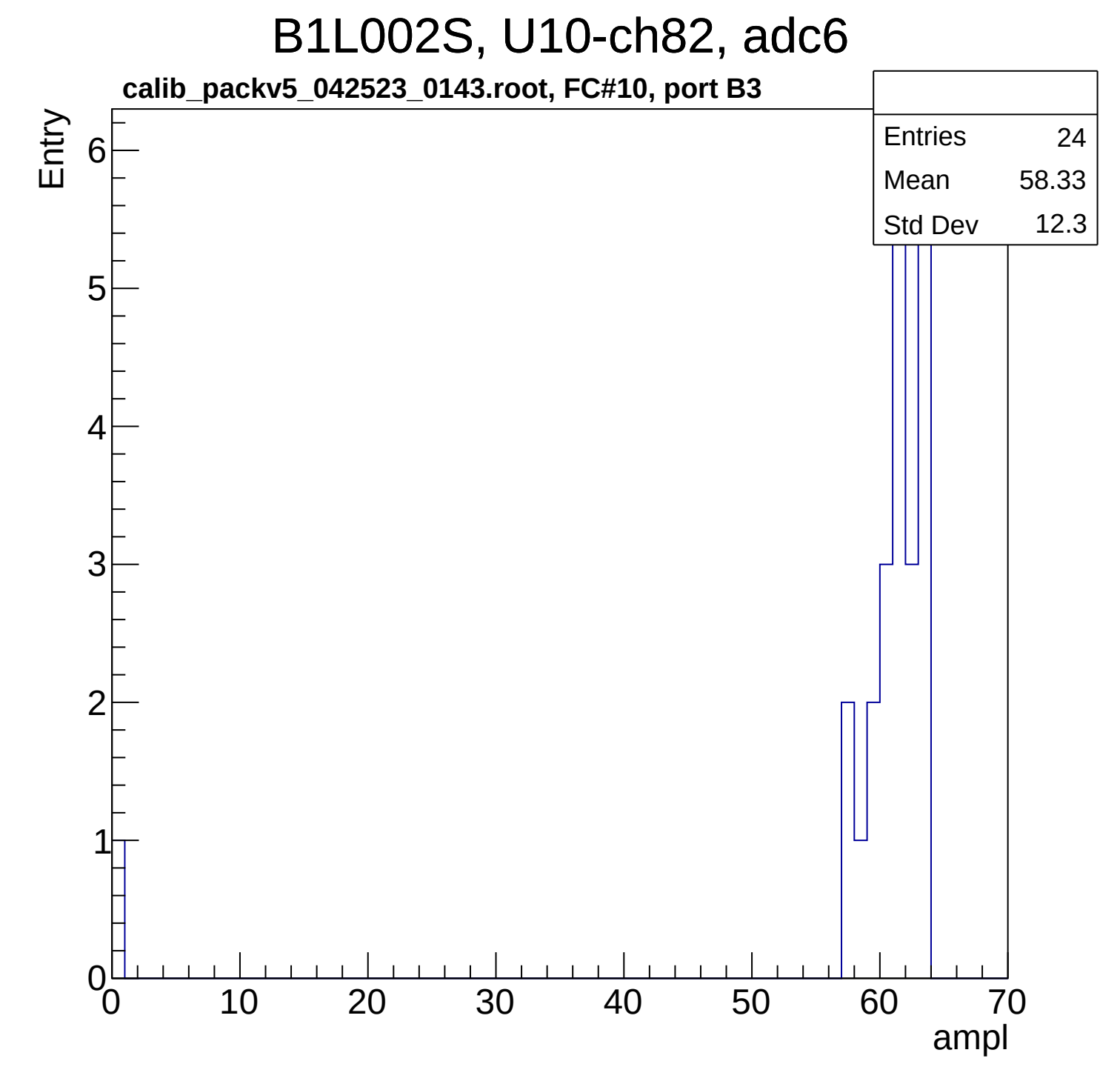
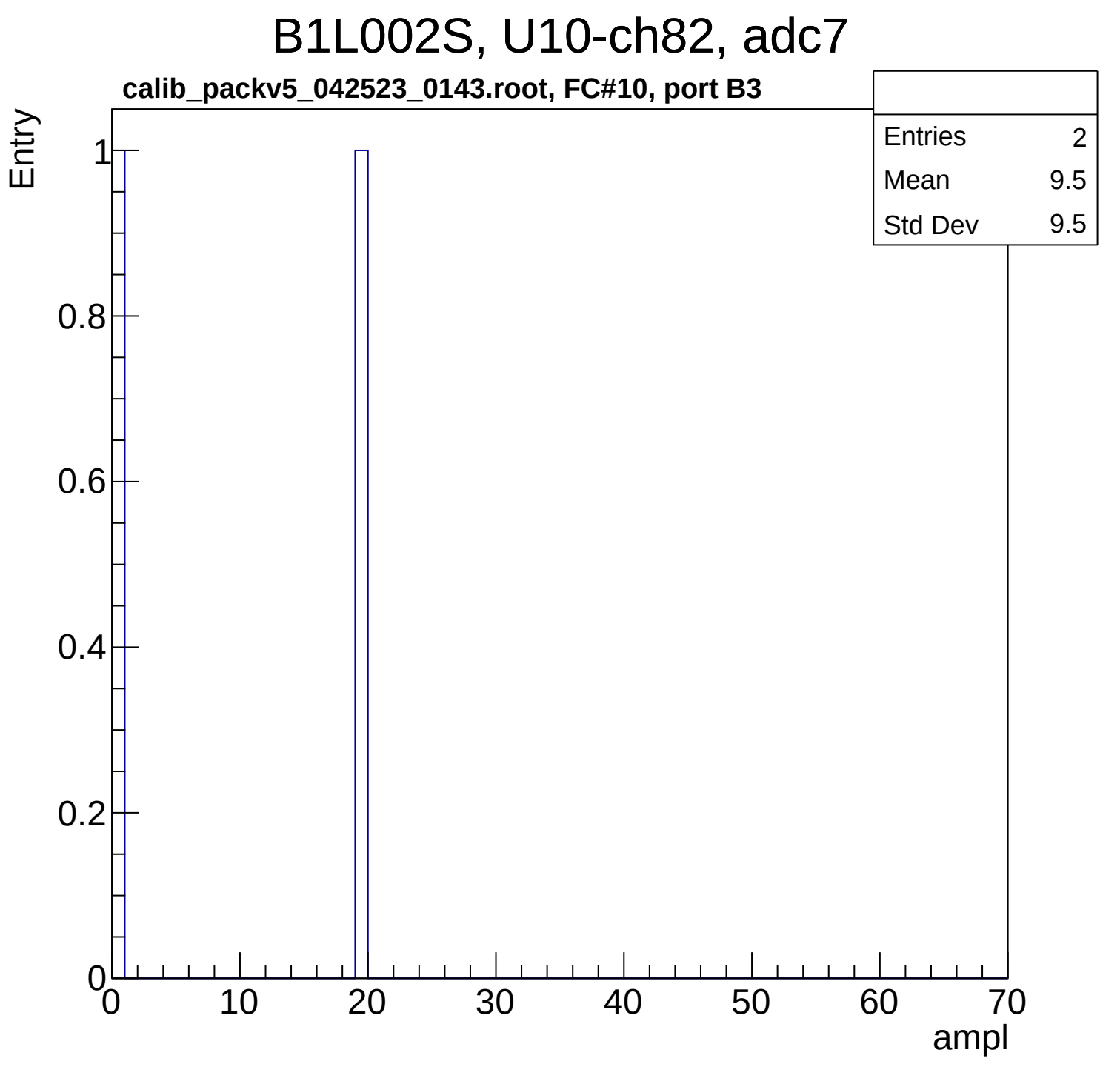
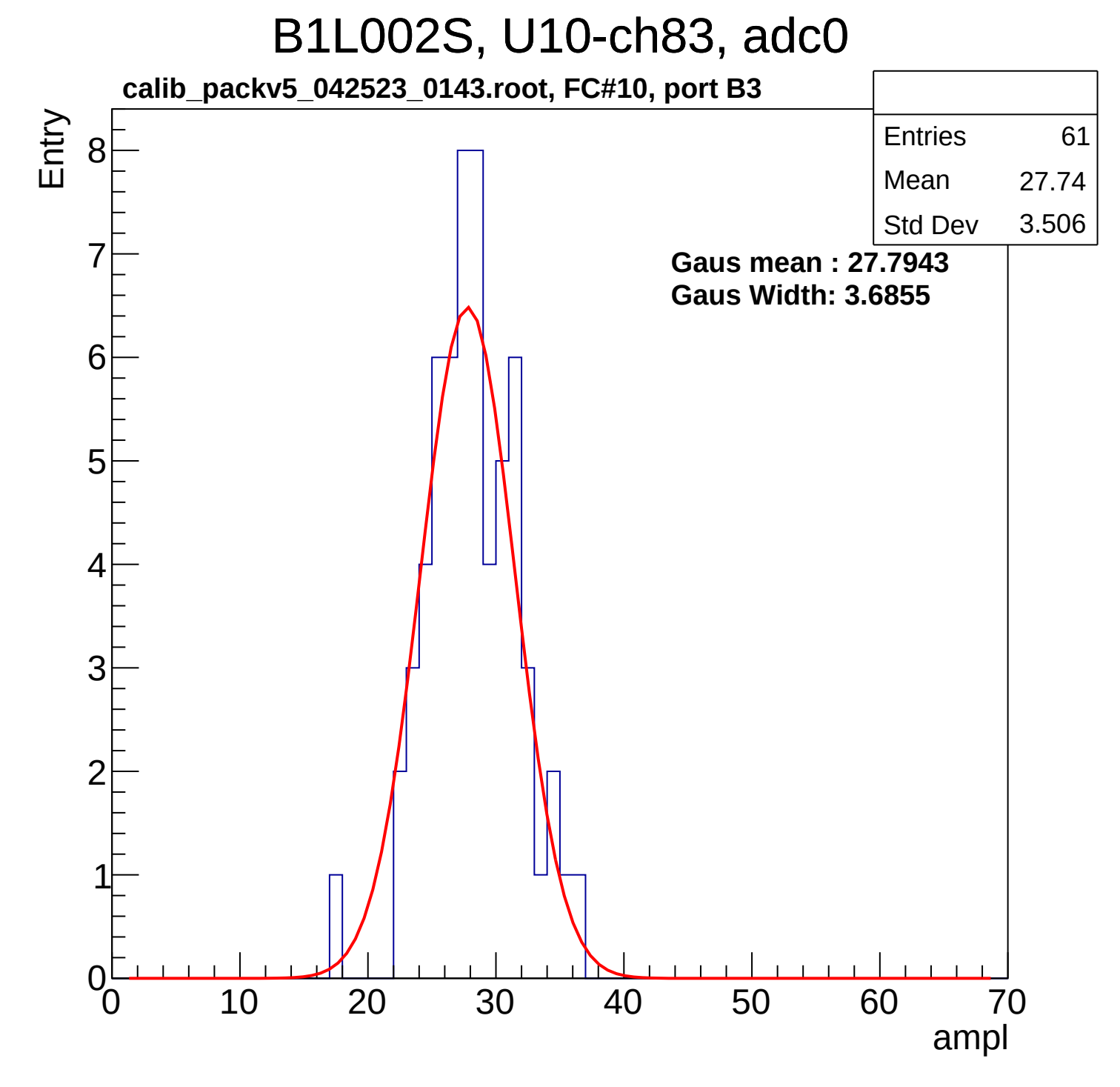
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.33
Std Dev 12.3
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.74
Std Dev 3.506
Gaus mean : 27.7943
Gaus Width: 3.6855
Entry
ampl
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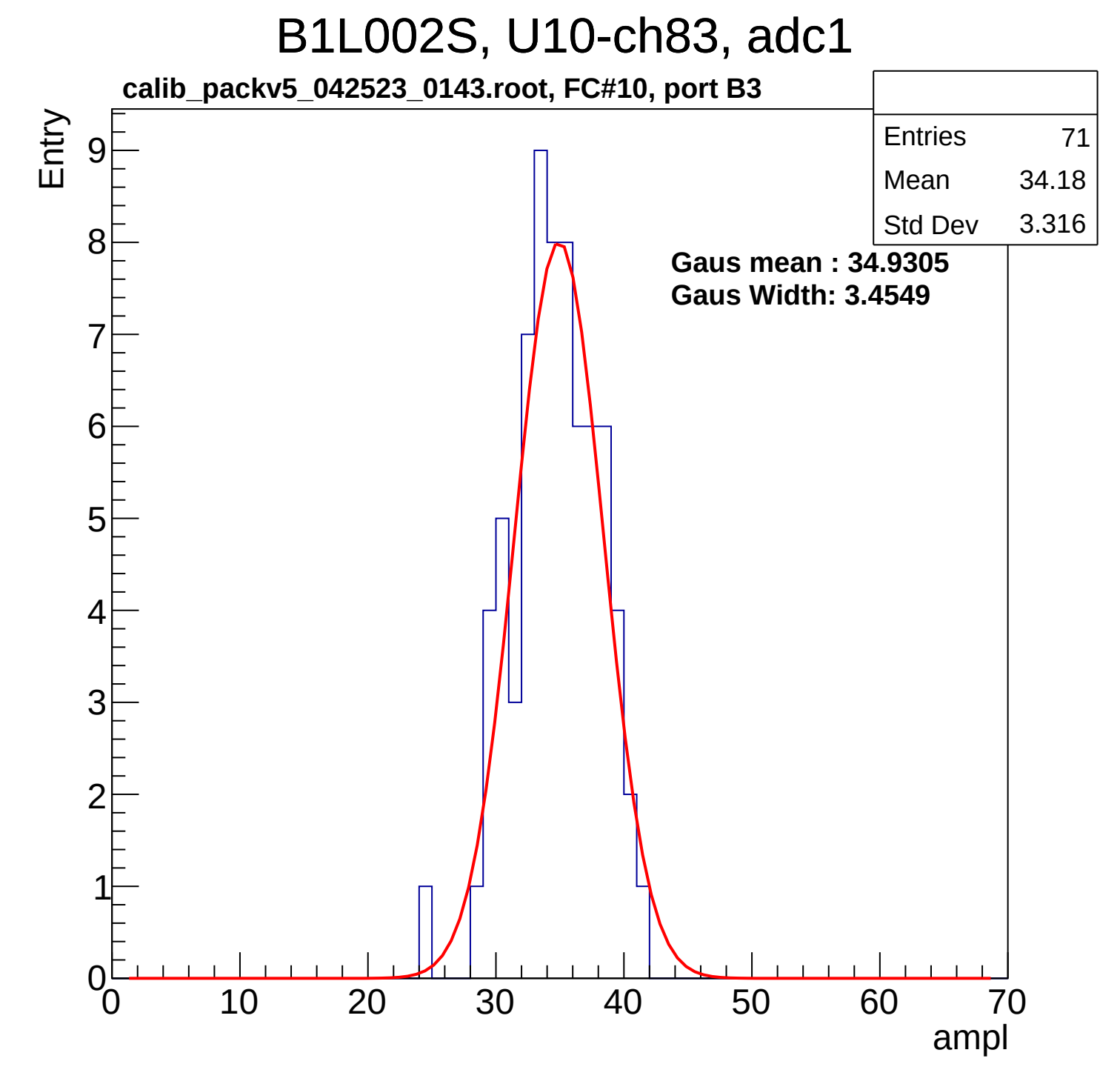
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.18
Std Dev 3.316
Gaus mean : 34.9305
Gaus Width: 3.4549
Entry
ampl
0
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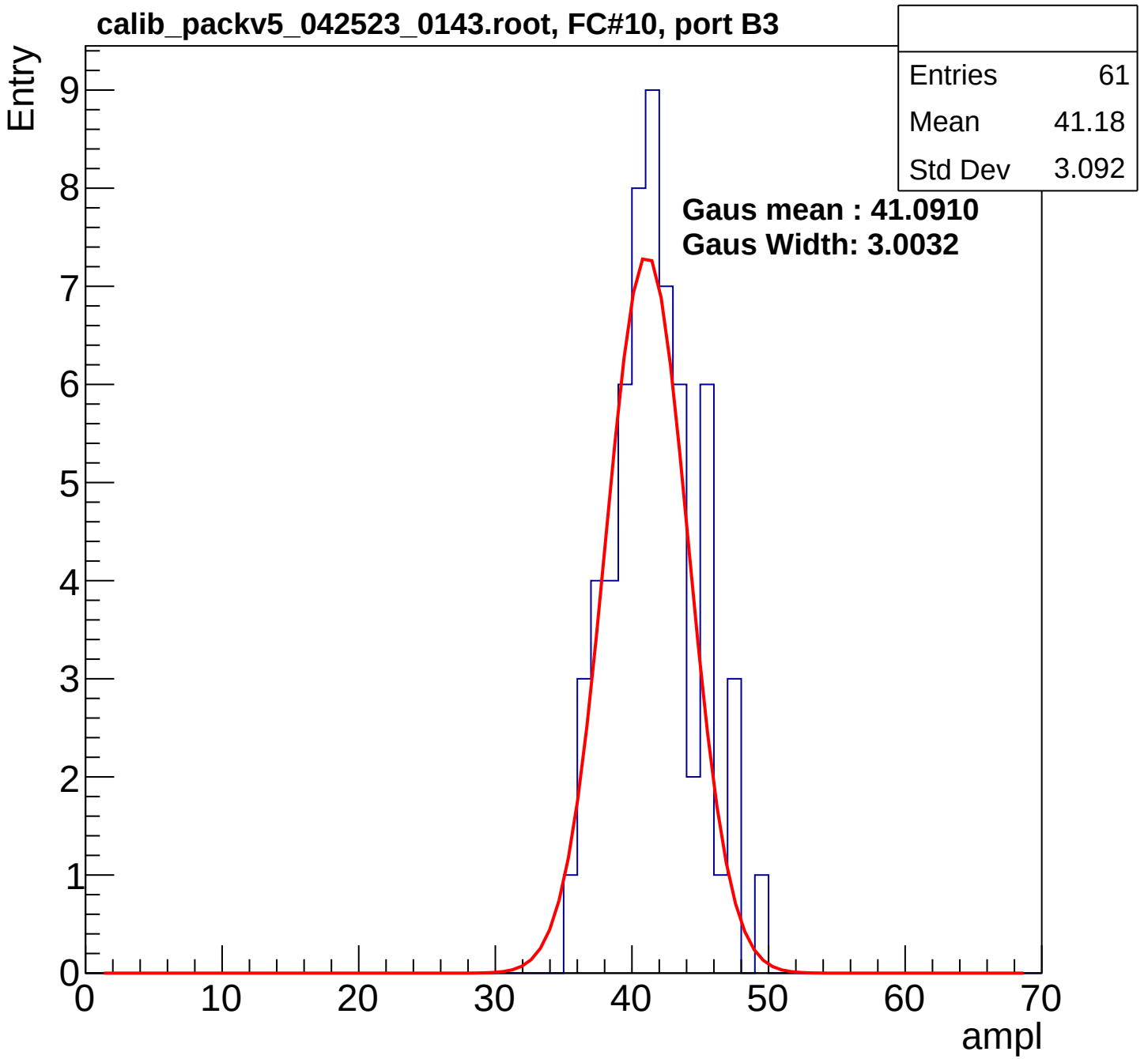
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.18
Std Dev 3.092
Gaus mean : 41.0910
Gaus Width: 3.0032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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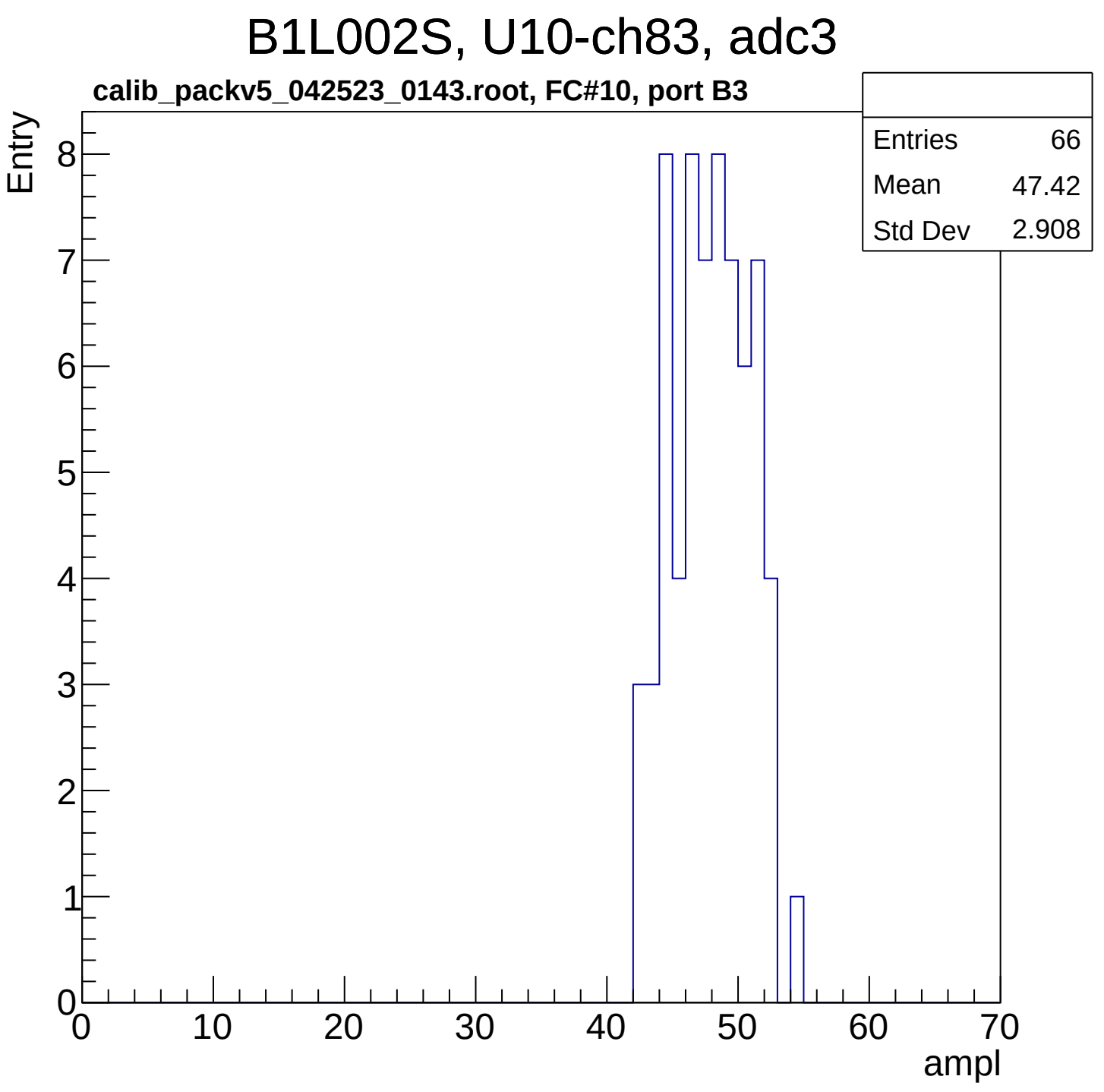
20
30
40
50
60
70


B1L002S, U10-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.42
Std Dev 2.908
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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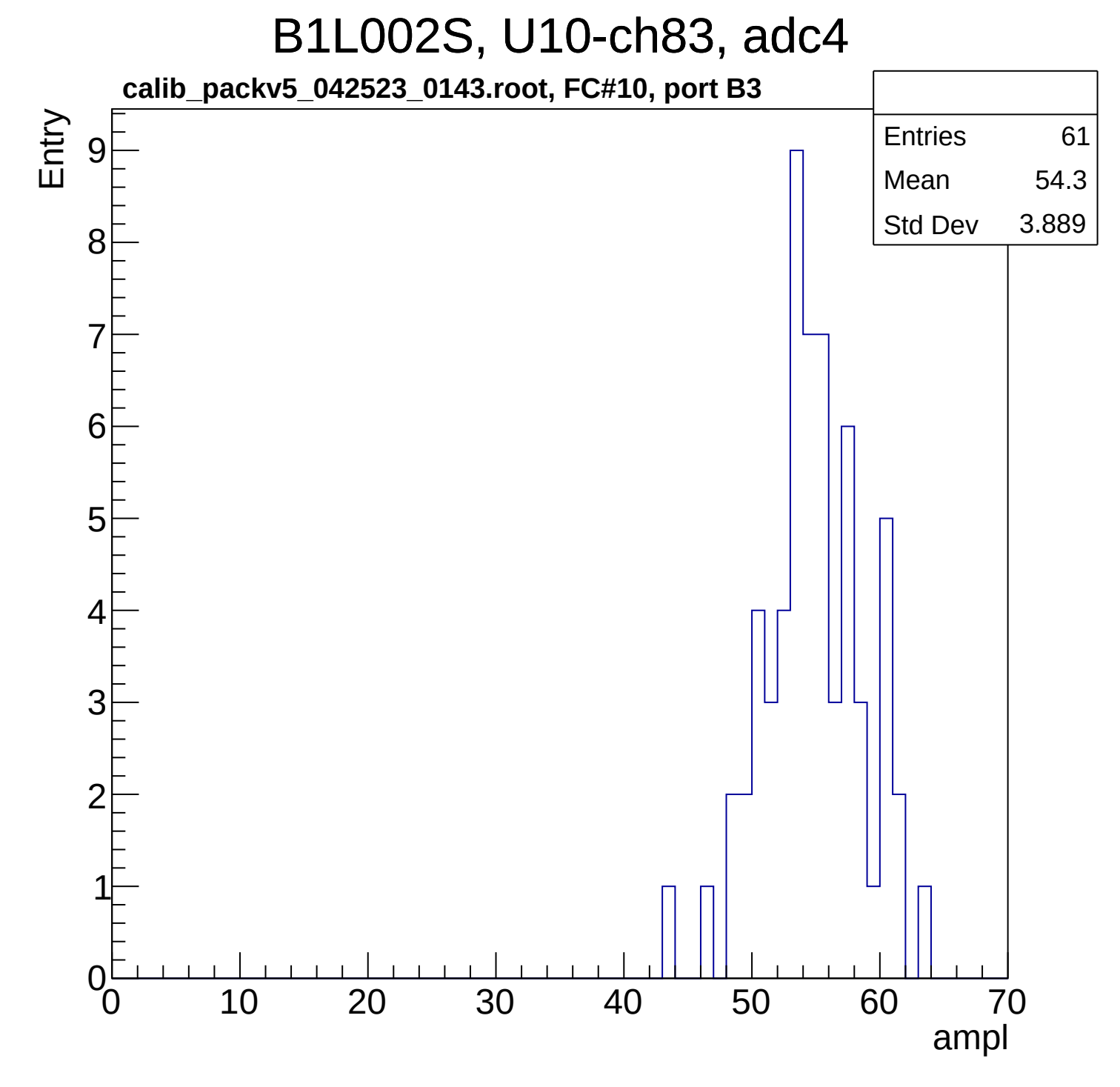
B1L002S, U10-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.3
Std Dev 3.889
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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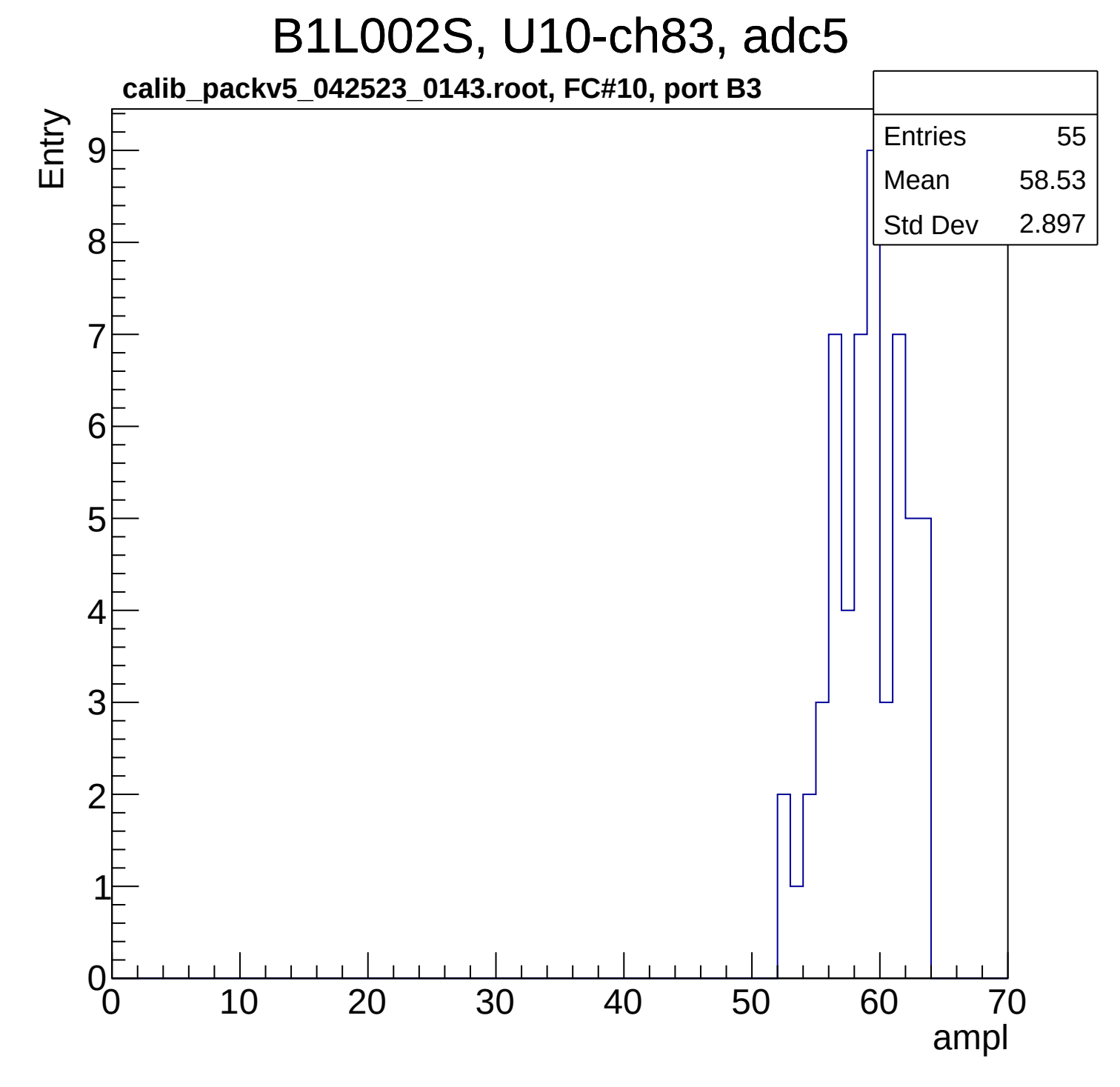
B1L002S, U10-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.53
Std Dev 2.897
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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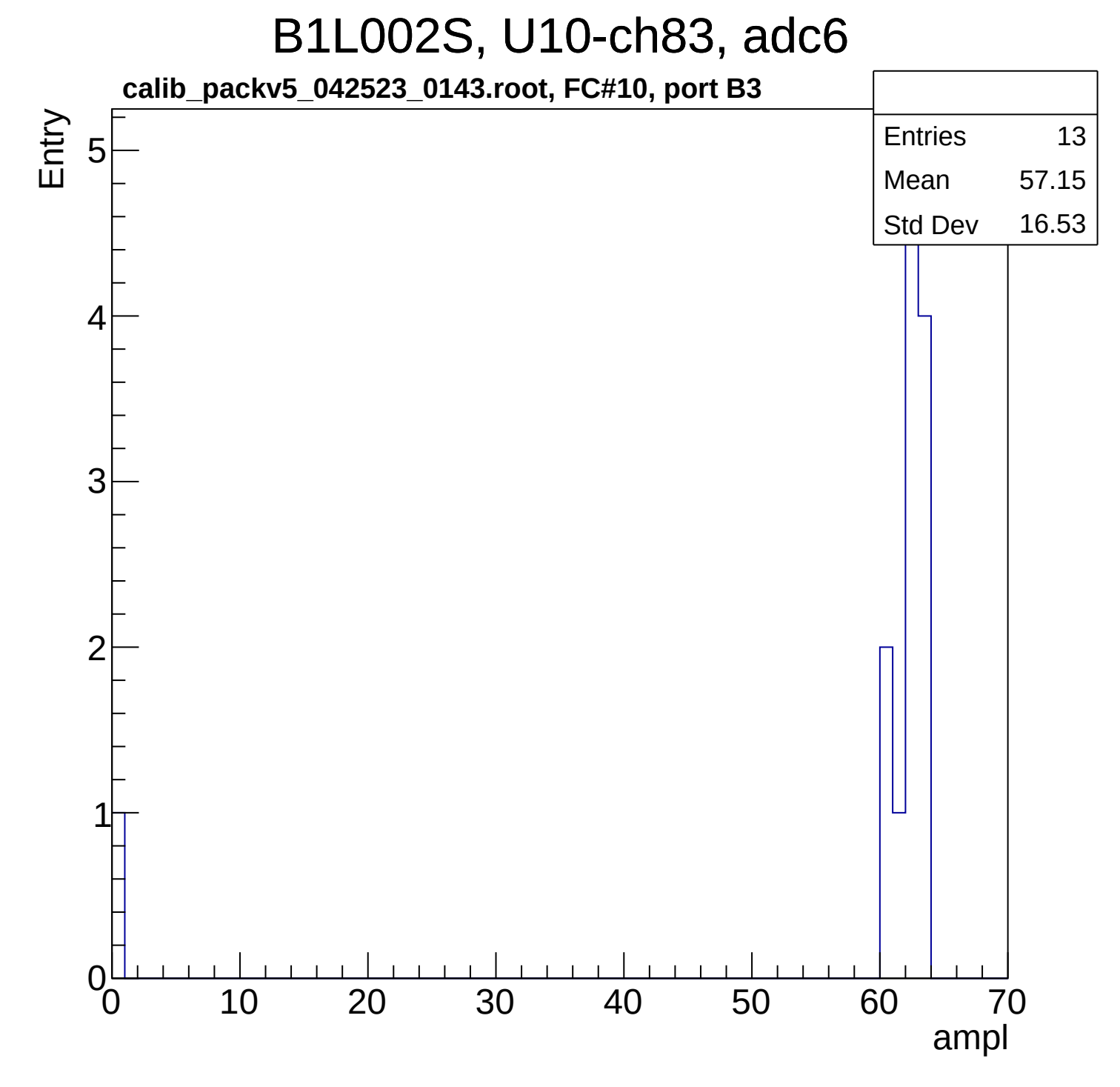
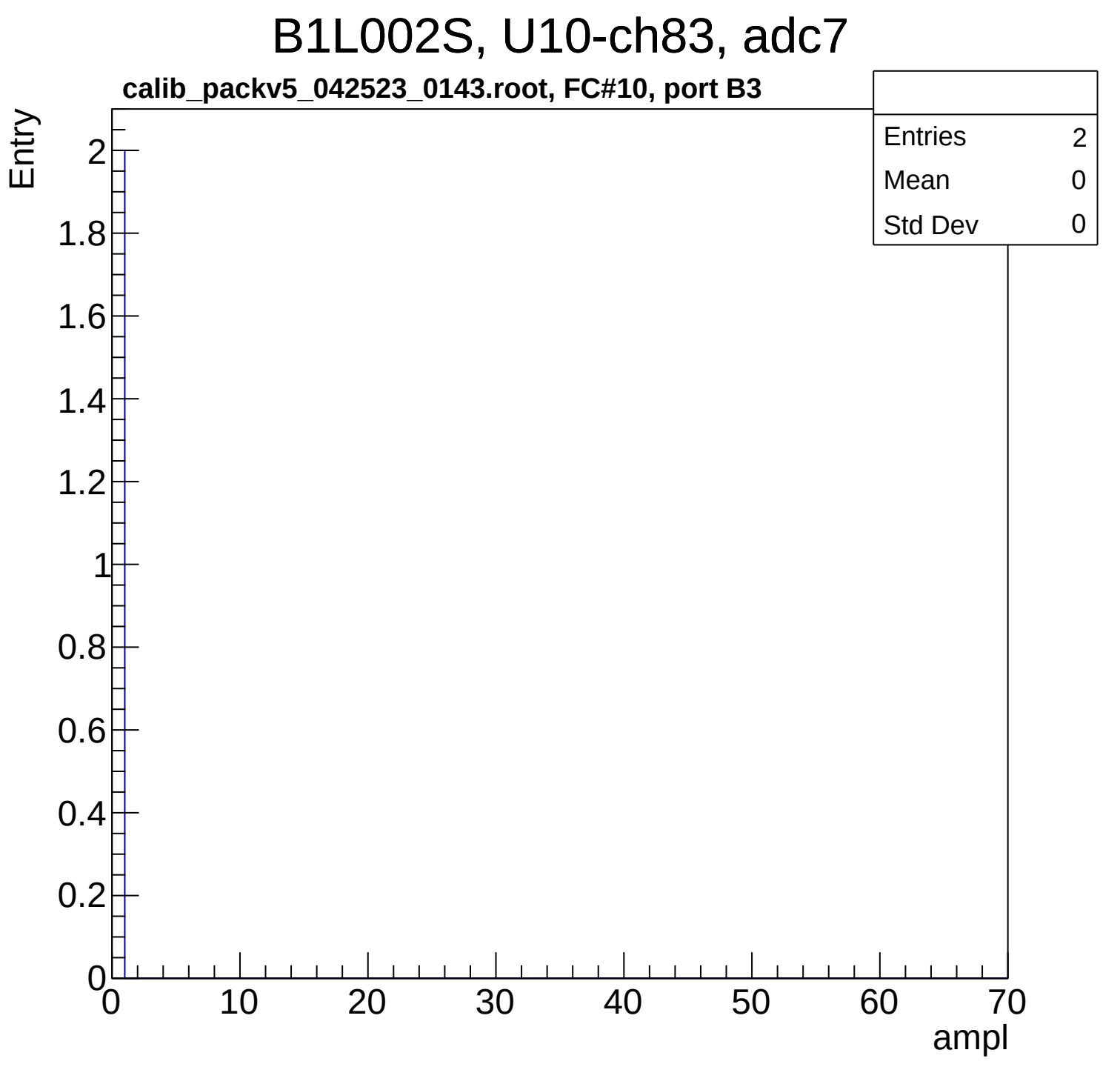
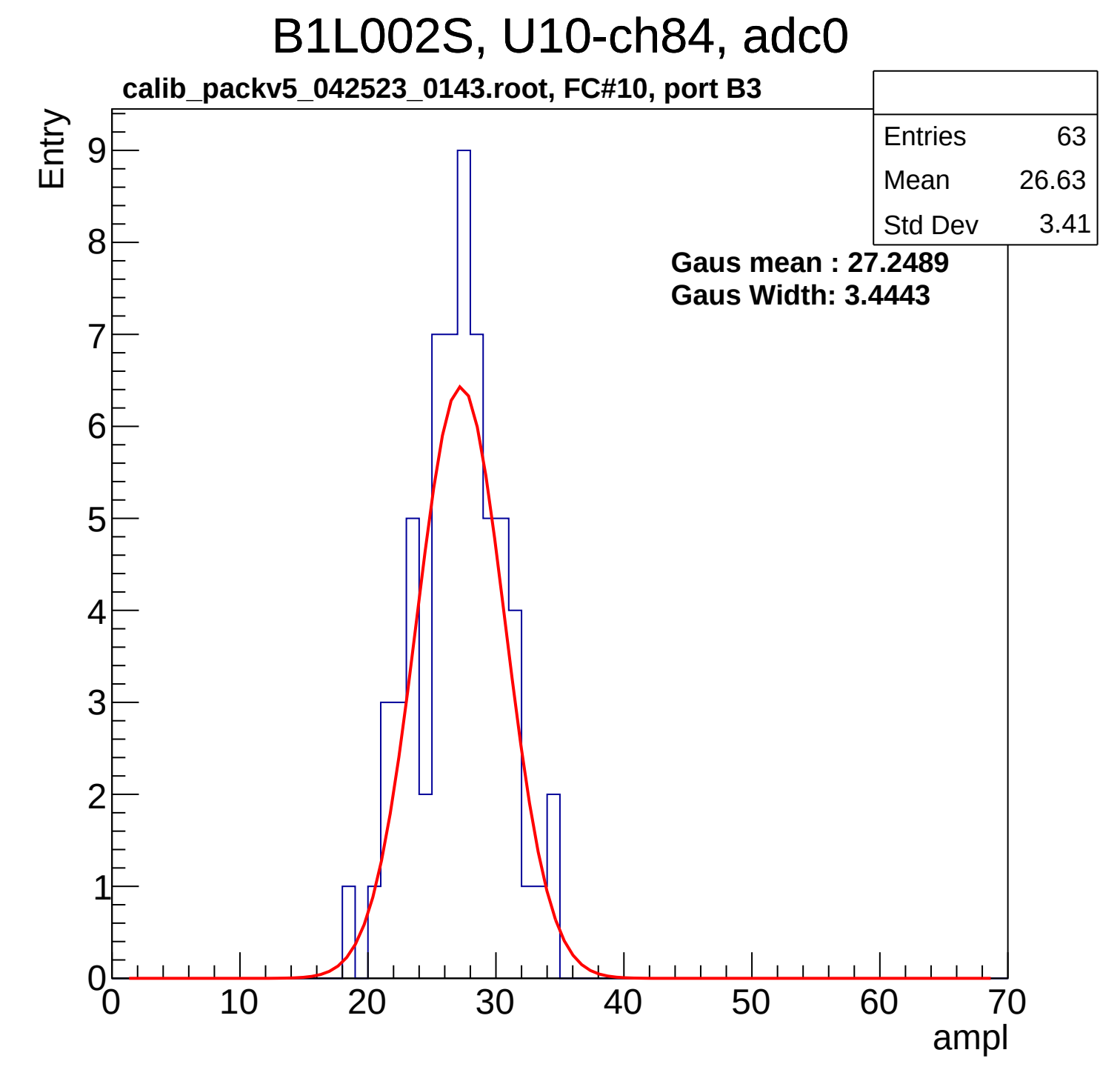
50
60
70


B1L002S, U10-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.15
Std Dev 16.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 26.63
Std Dev 3.41
Gaus mean : 27.2489
Gaus Width: 3.4443
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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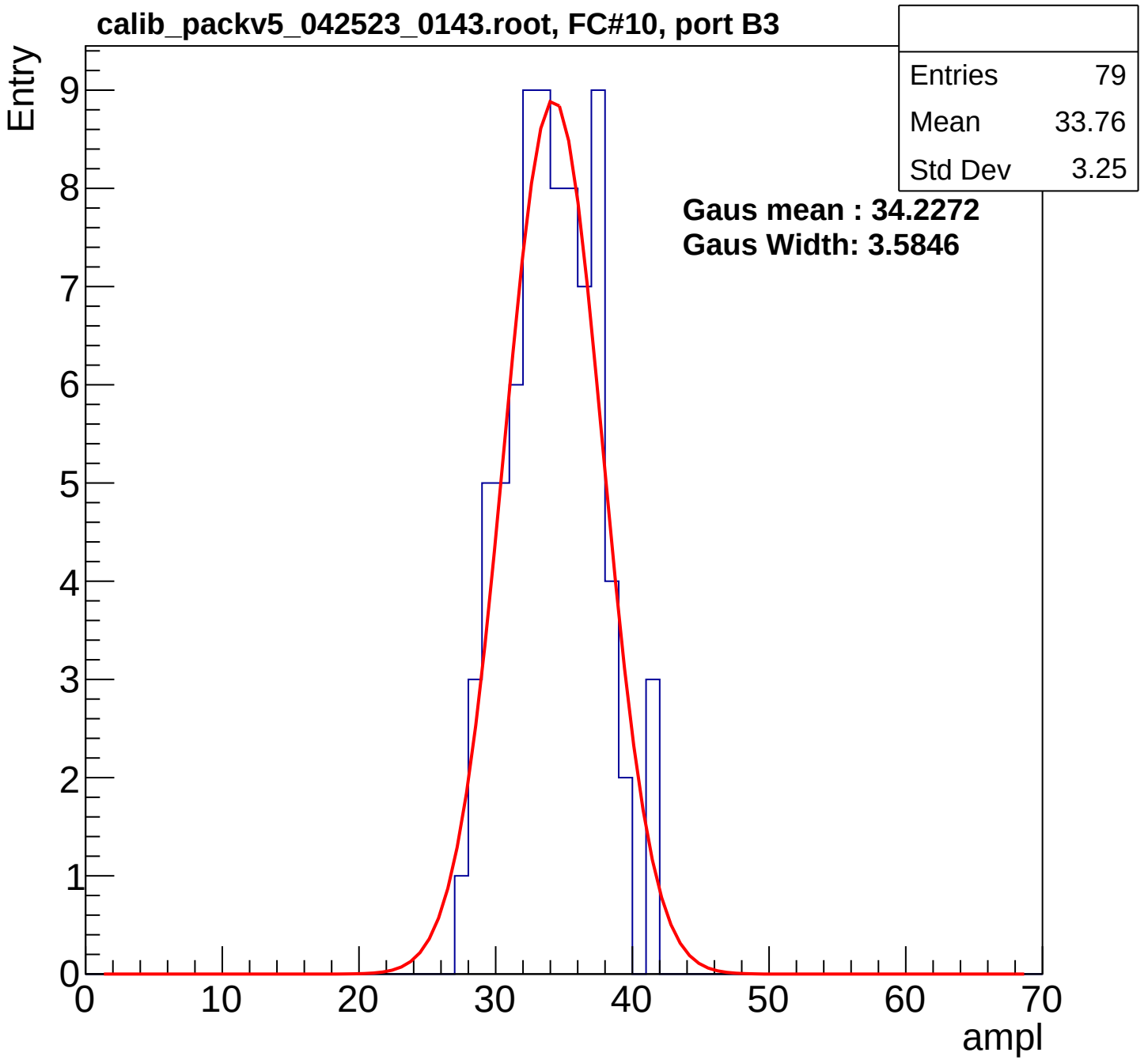
B1L002S, U10-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 33.76
Std Dev 3.25
Gaus mean : 34.2272
Gaus Width: 3.5846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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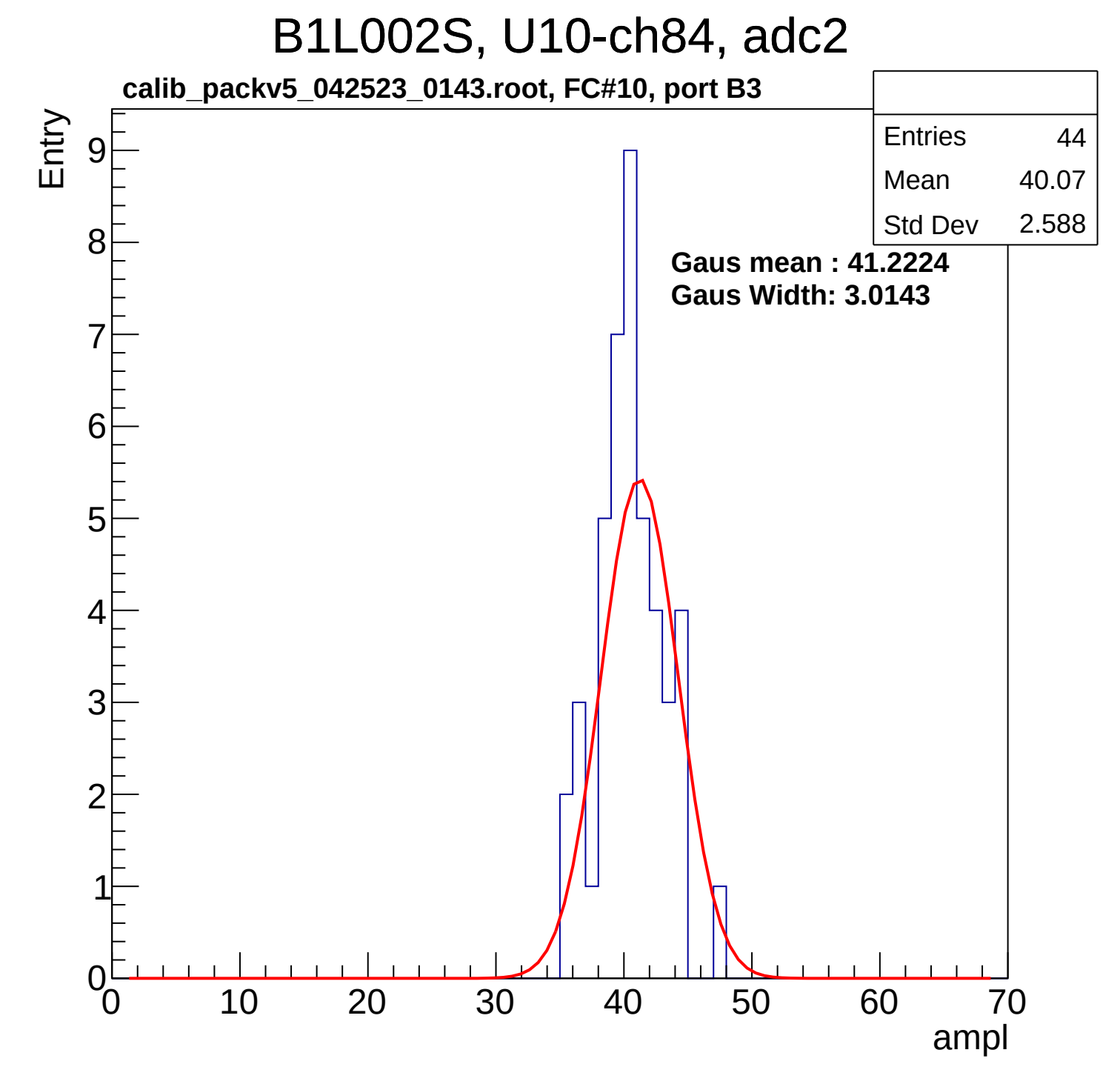
B1L002S, U10-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 40.07
Std Dev 2.588
Gaus mean : 41.2224
Gaus Width: 3.0143
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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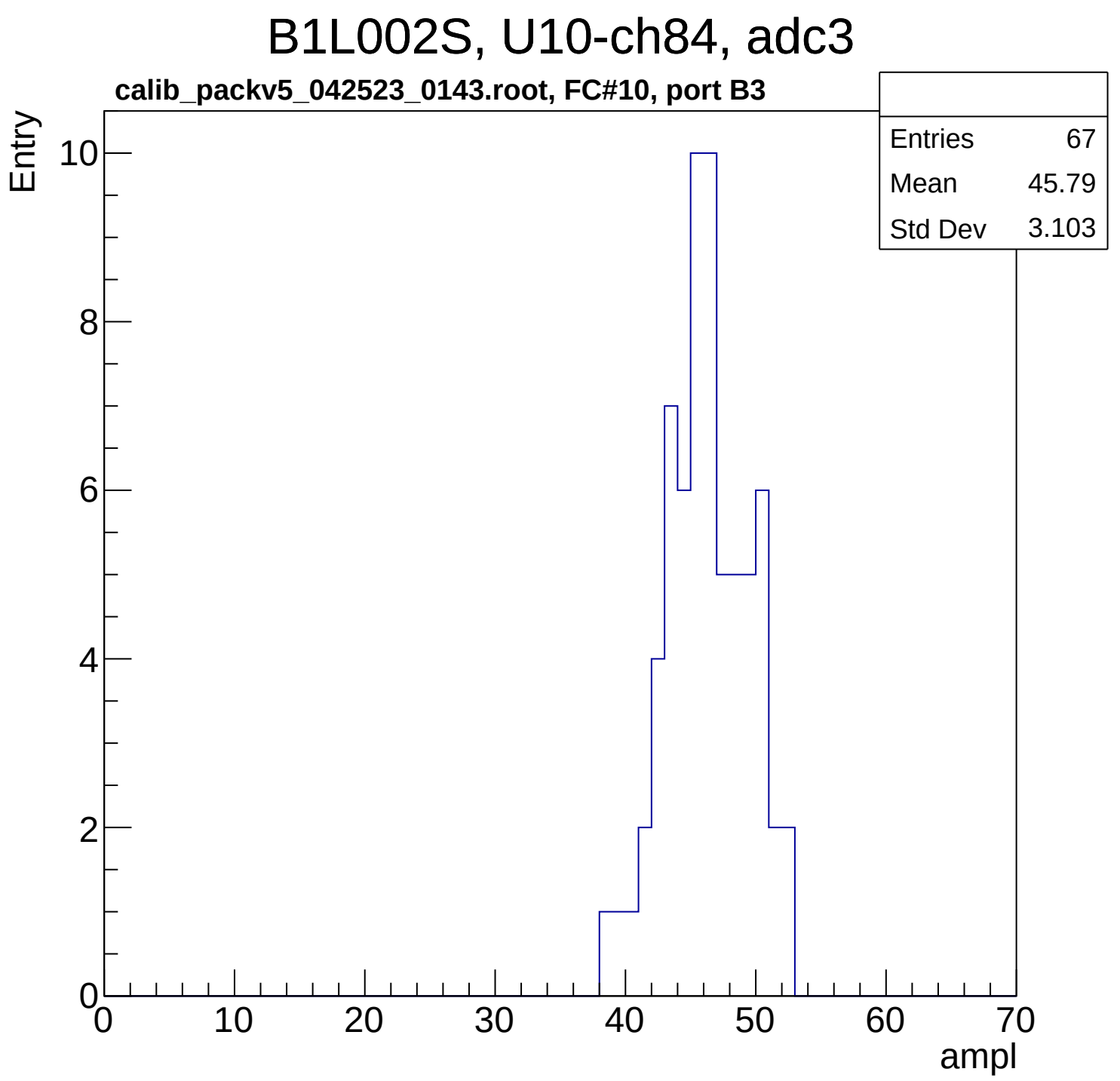
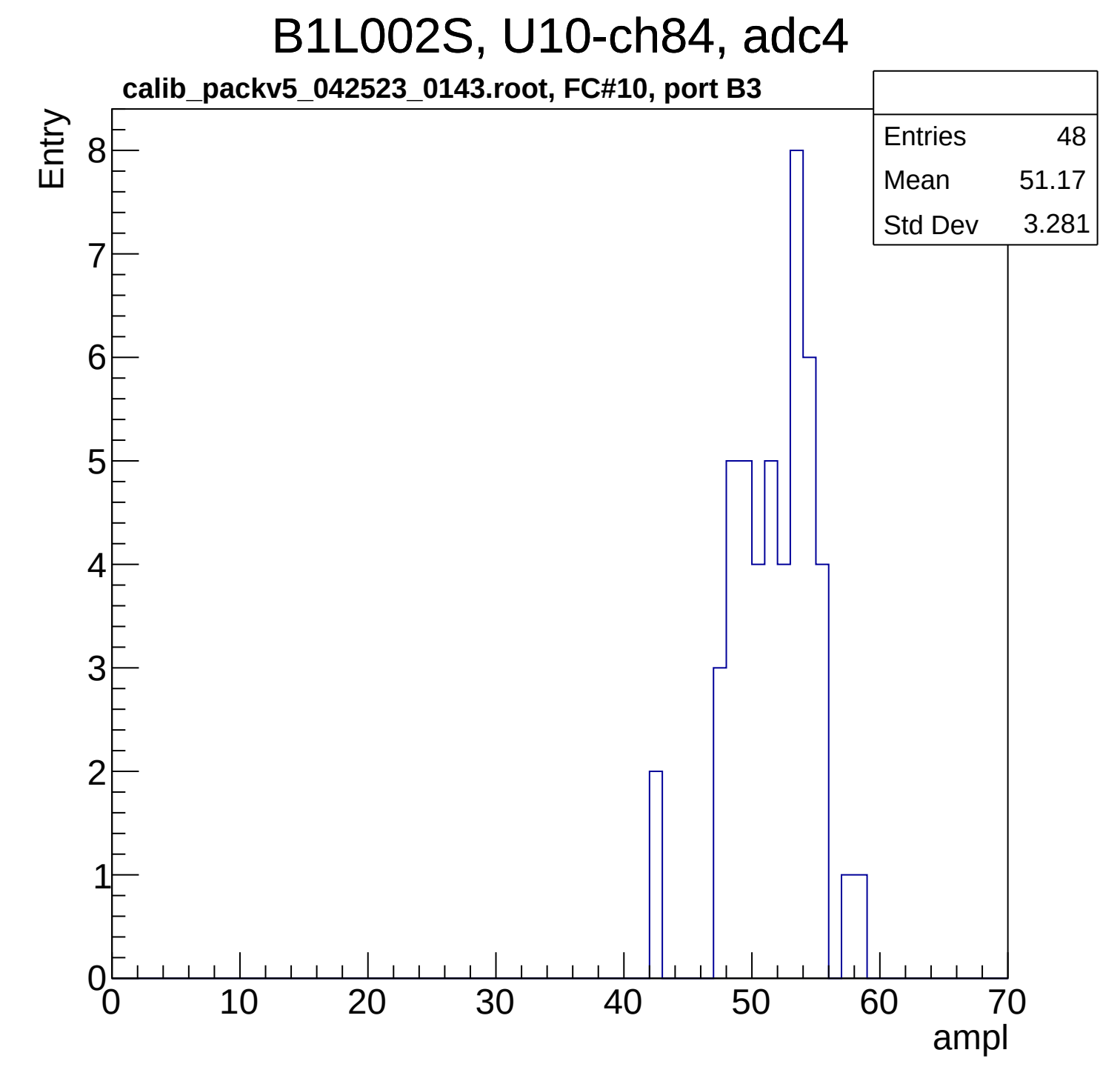
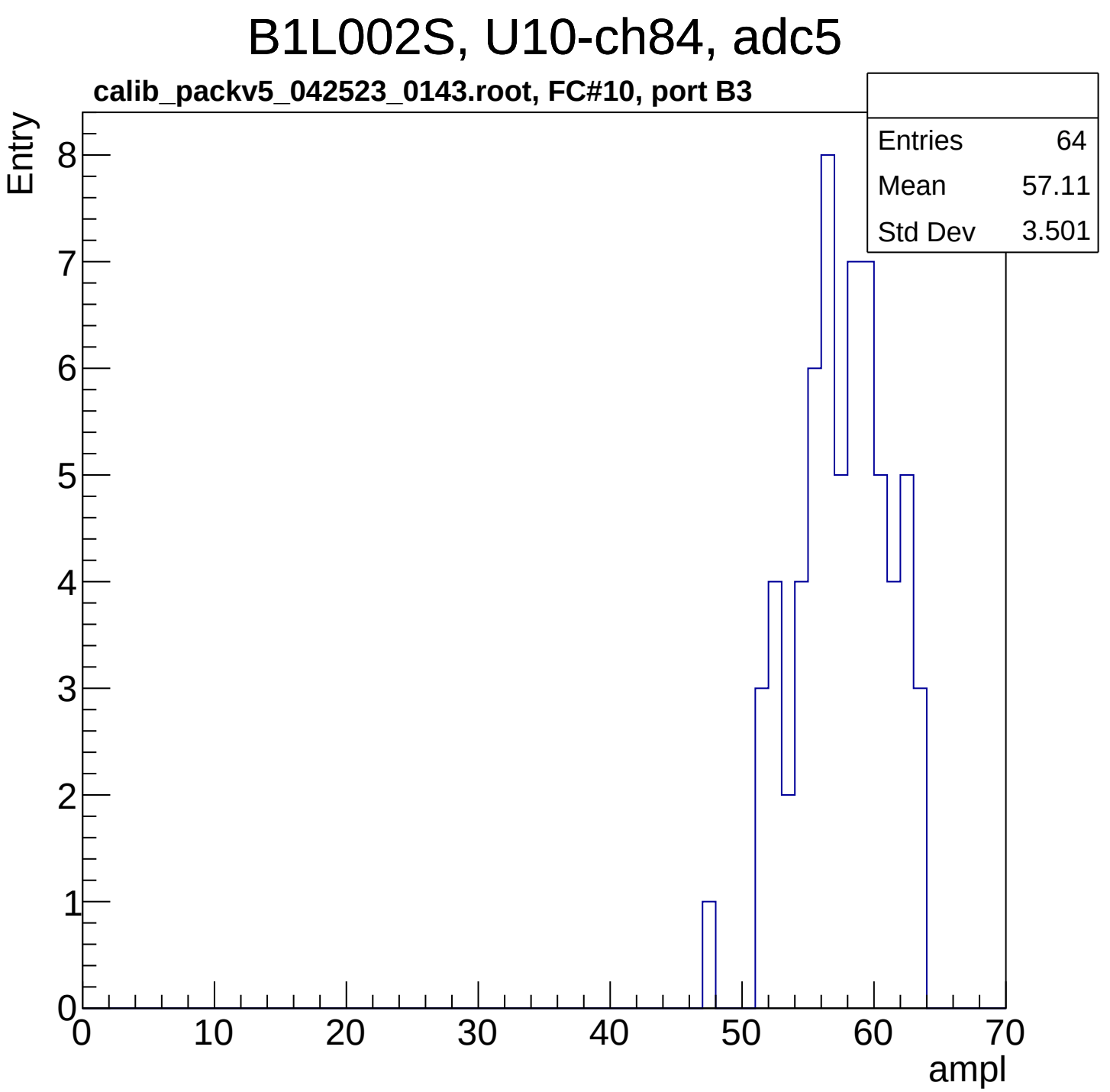

B1L002S, U10-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.79
Std Dev 3.103
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 51.17
Std Dev 3.281
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.11
Std Dev 3.501
Entry
ampl
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1
2
3
4
5
6
7
8
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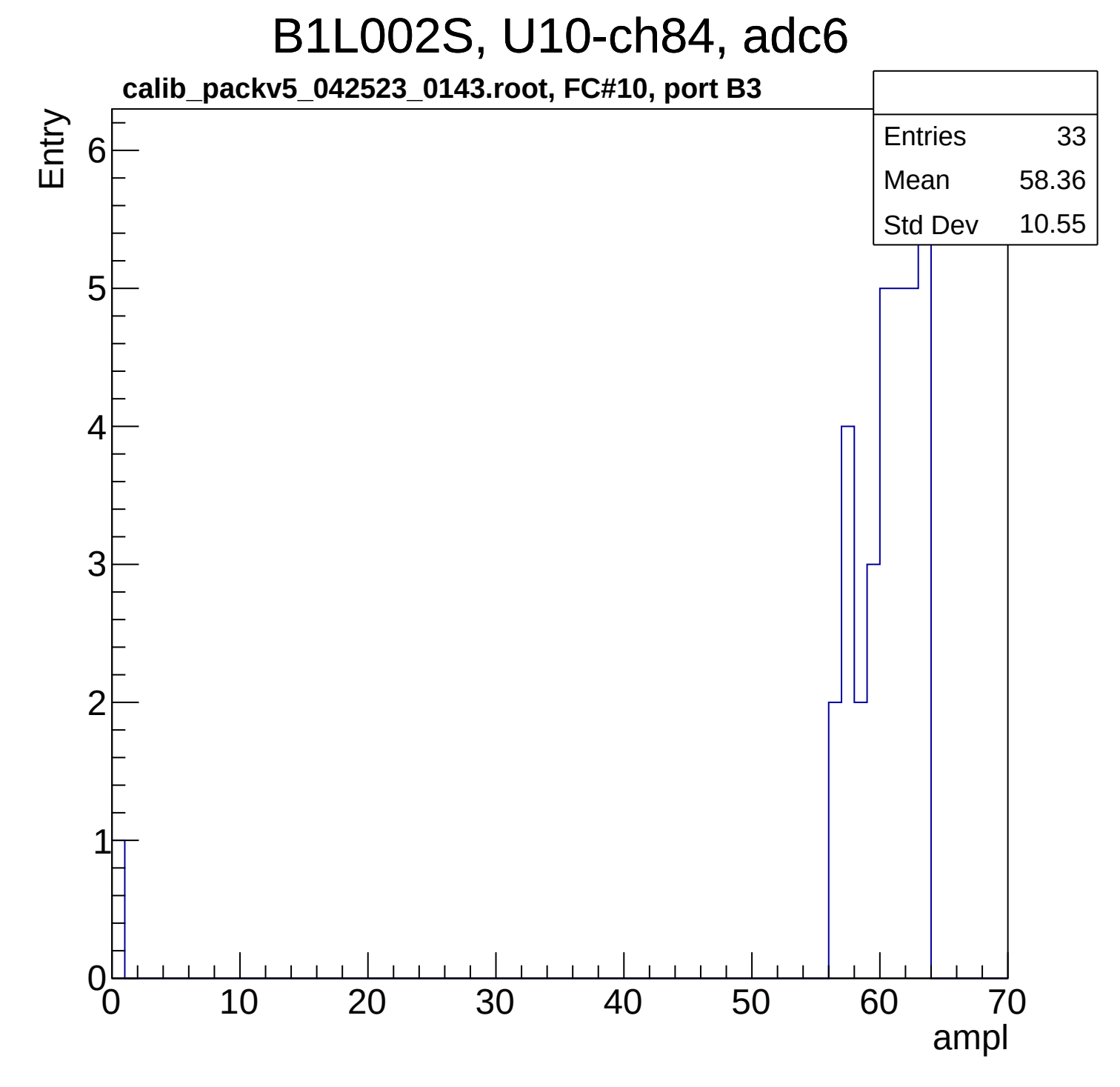
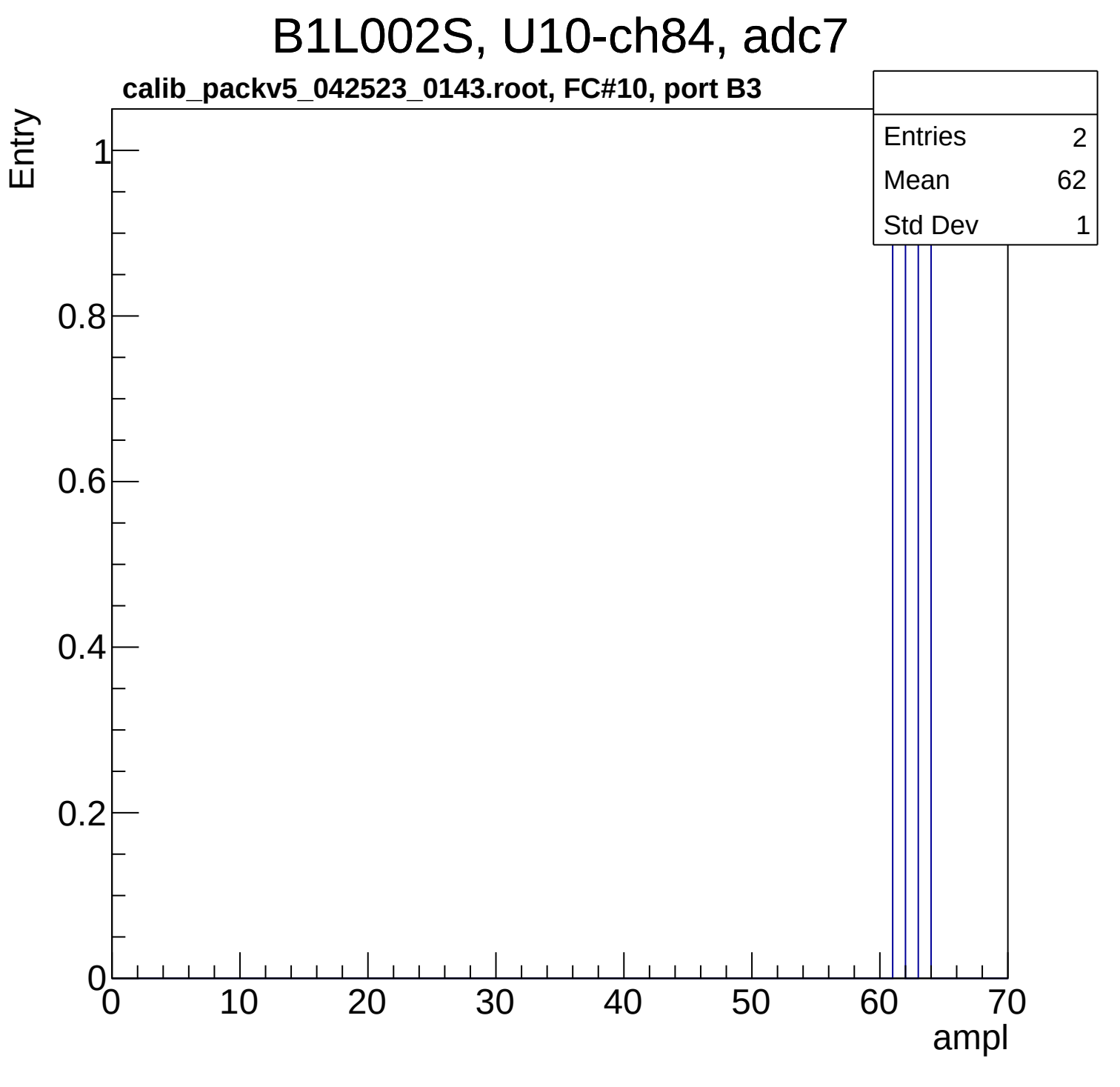
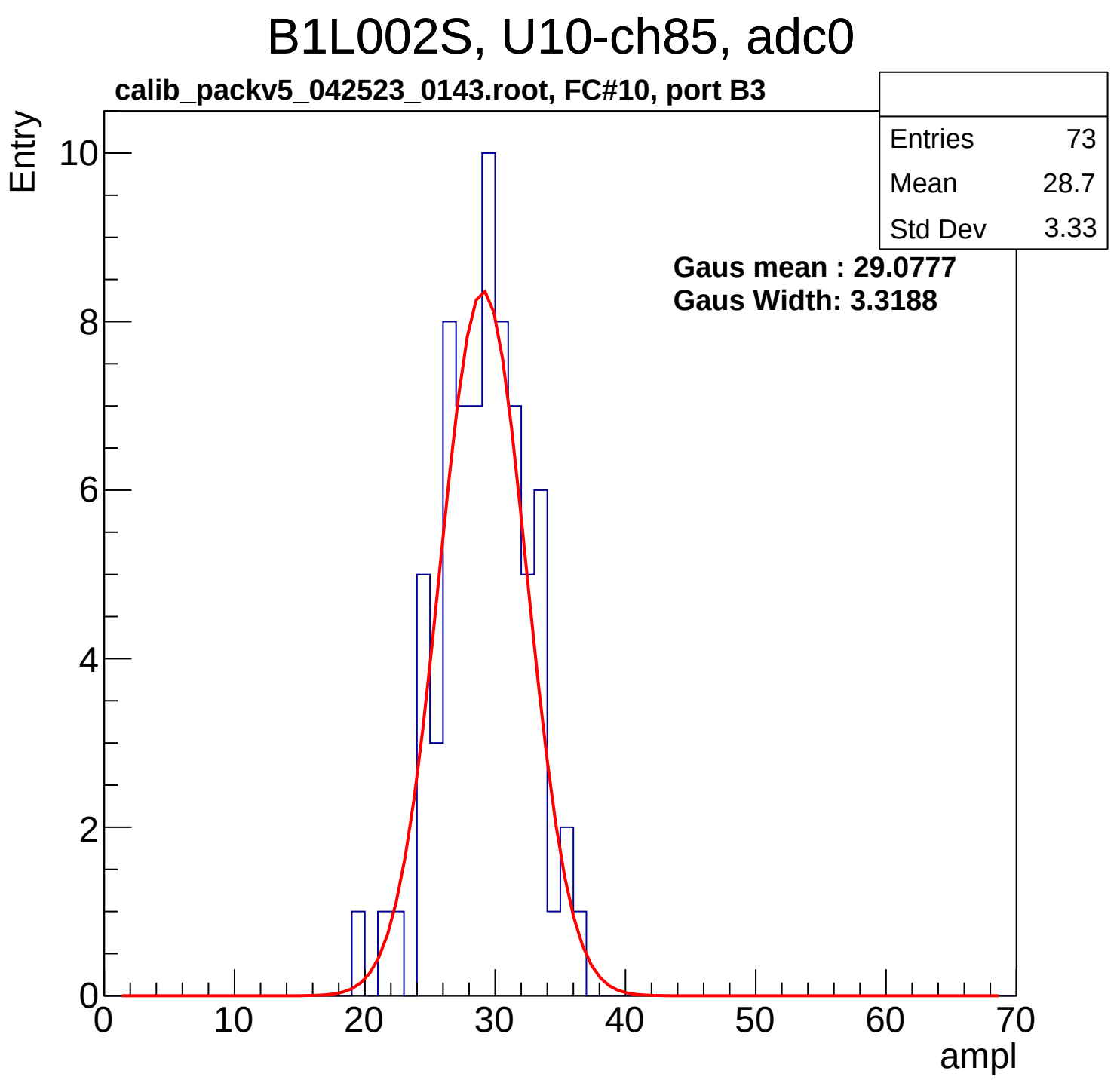
10
20
30
40
50
60
70


B1L002S, U10-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 58.36
Std Dev 10.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.7
Std Dev 3.33
Gaus mean : 29.0777
Gaus Width: 3.3188
Entry
ampl
0
2
4
6
8
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0
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20
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40
50
60
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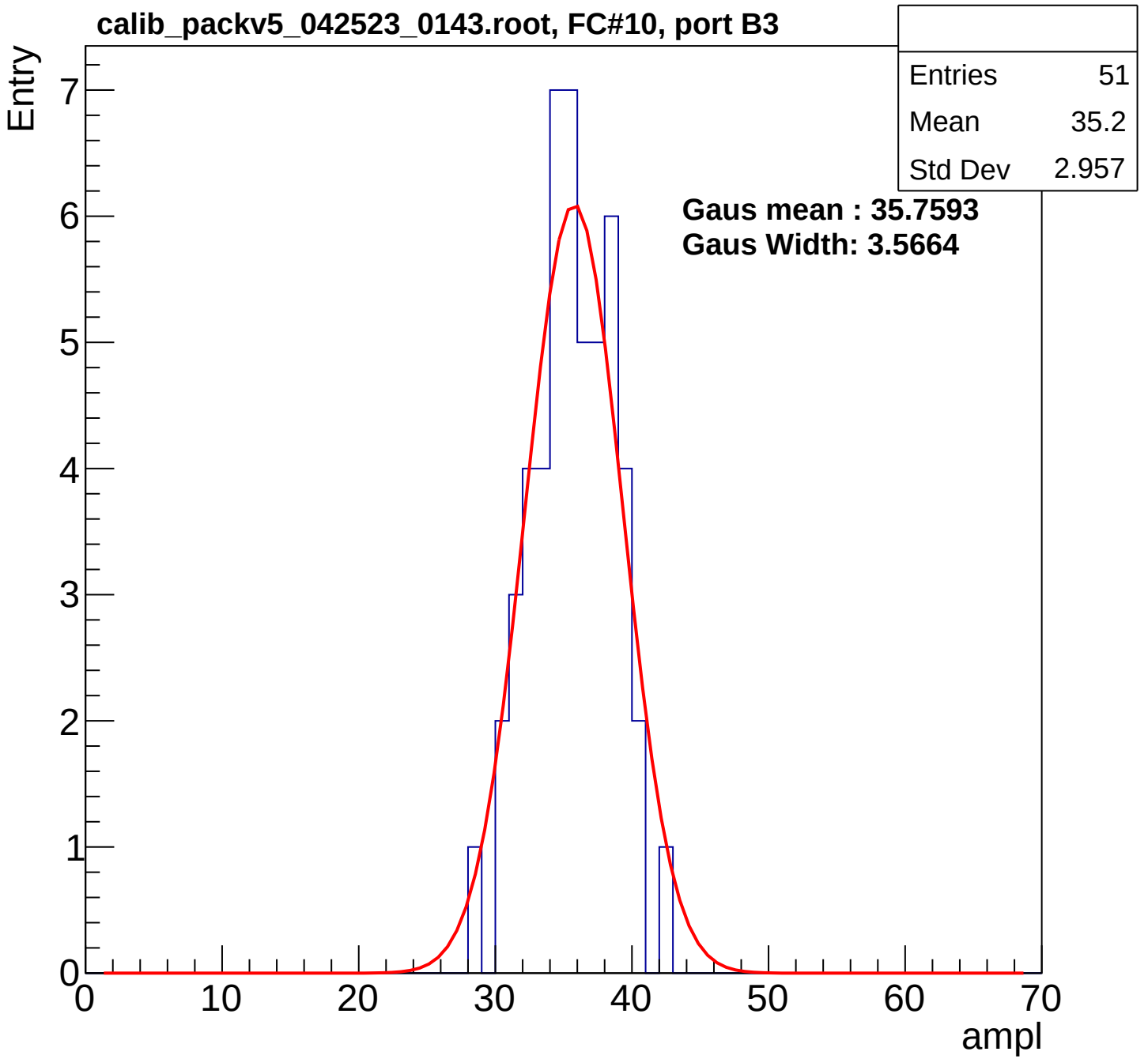
B1L002S, U10-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 35.2
Std Dev 2.957
Gaus mean : 35.7593
Gaus Width: 3.5664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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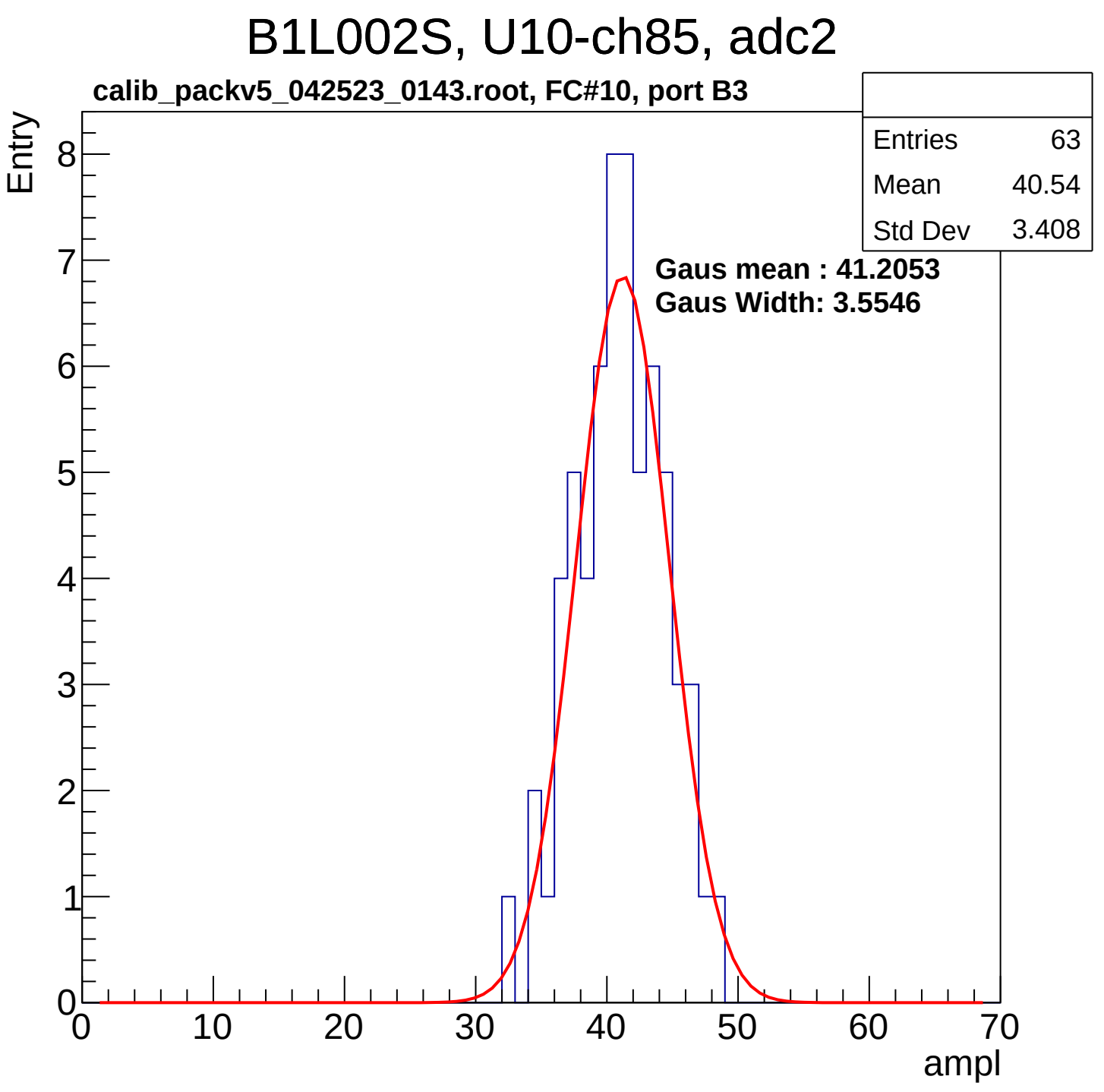
B1L002S, U10-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.54
Std Dev 3.408
Gaus mean : 41.2053
Gaus Width: 3.5546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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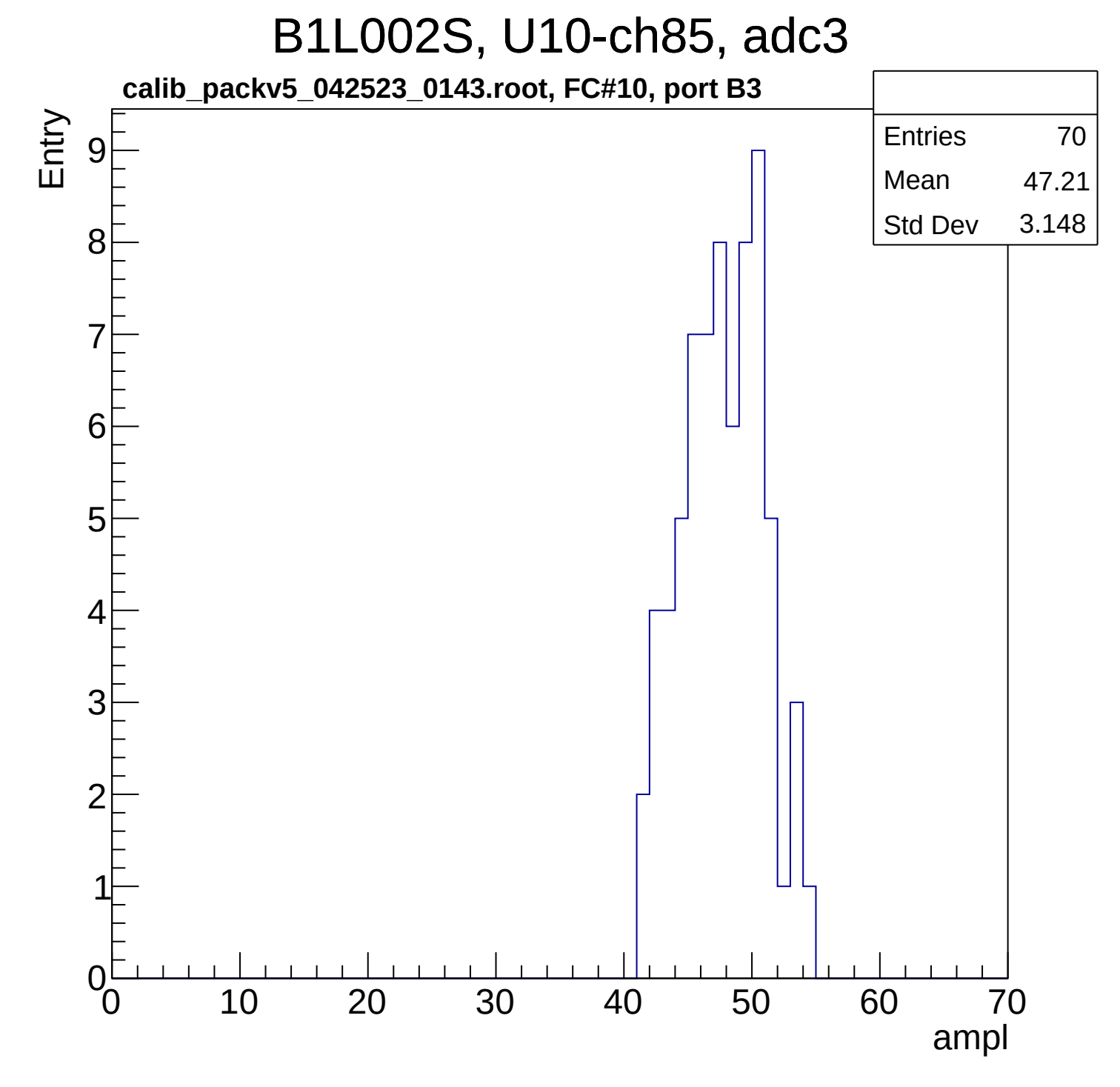
B1L002S, U10-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.21
Std Dev 3.148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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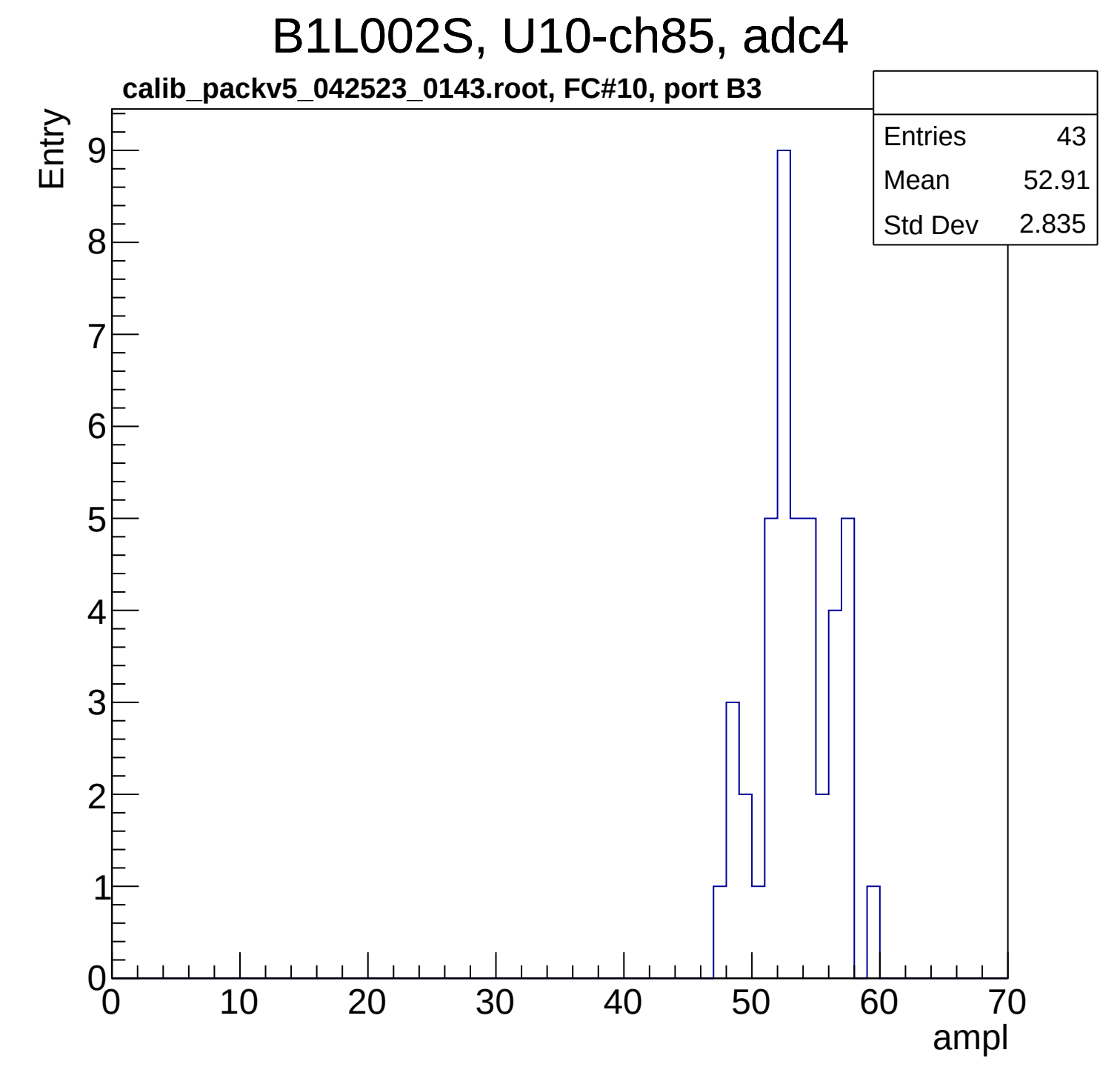
B1L002S, U10-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 52.91
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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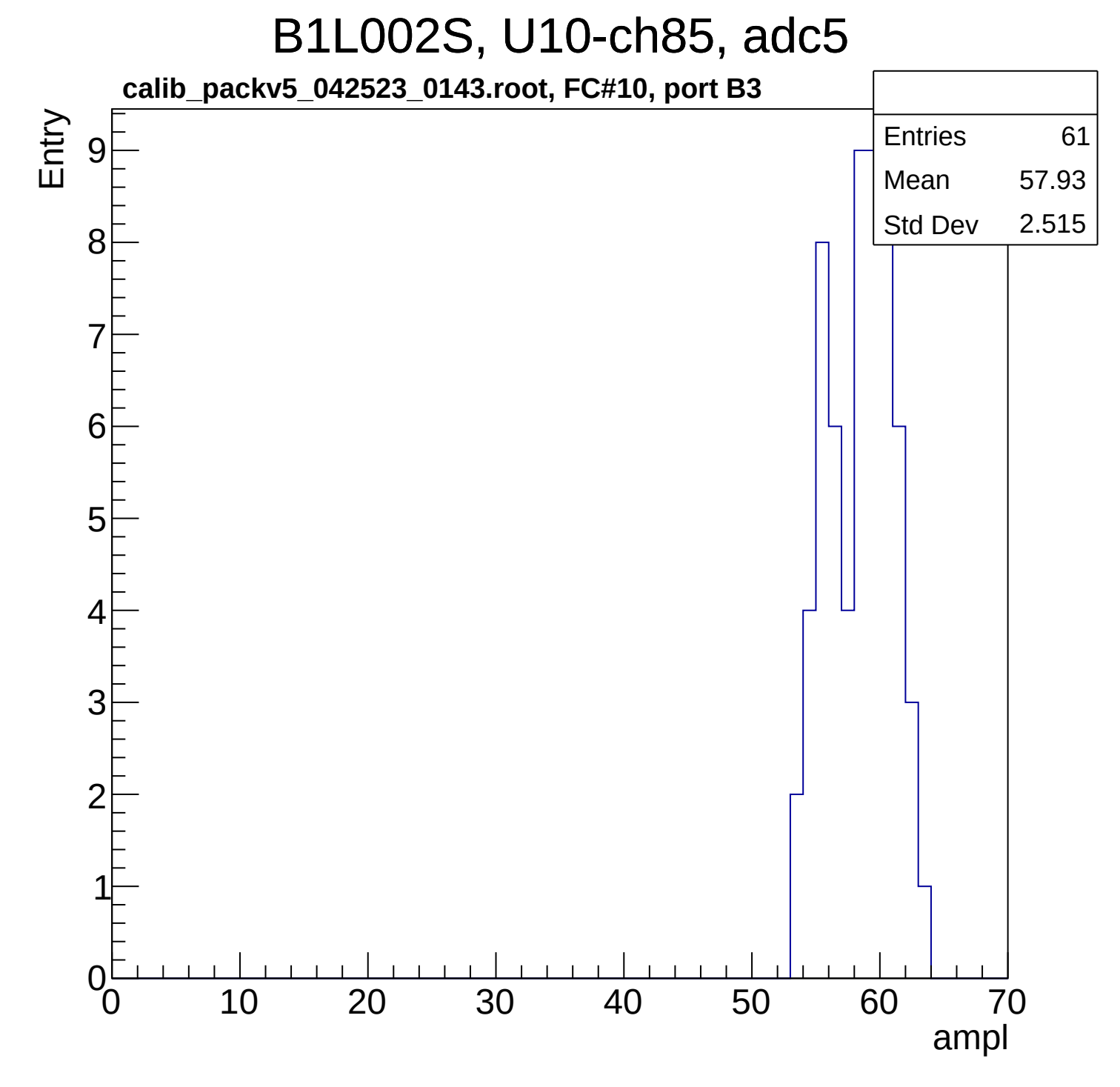
B1L002S, U10-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.93
Std Dev 2.515
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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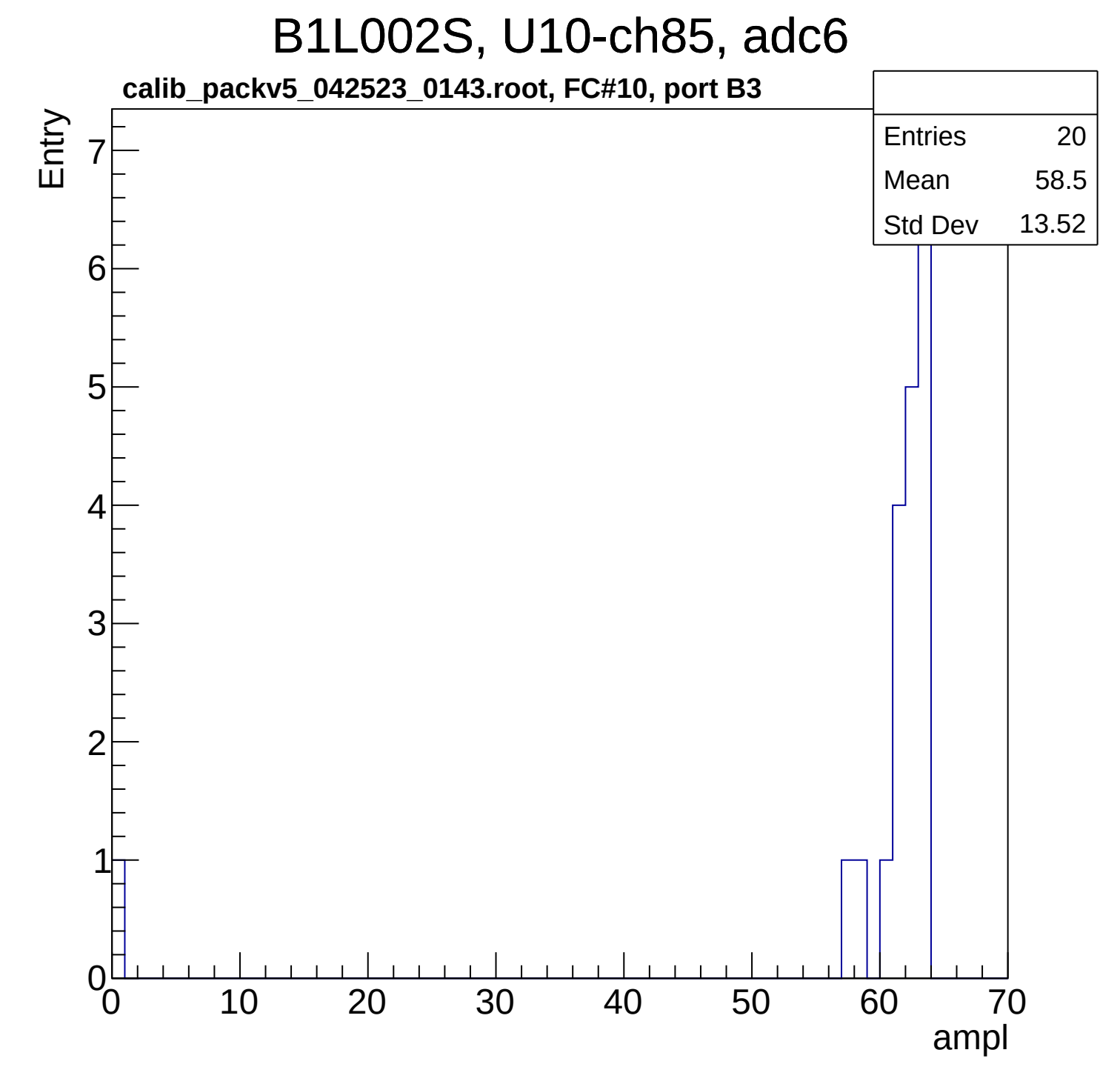
B1L002S, U10-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.5
Std Dev 13.52
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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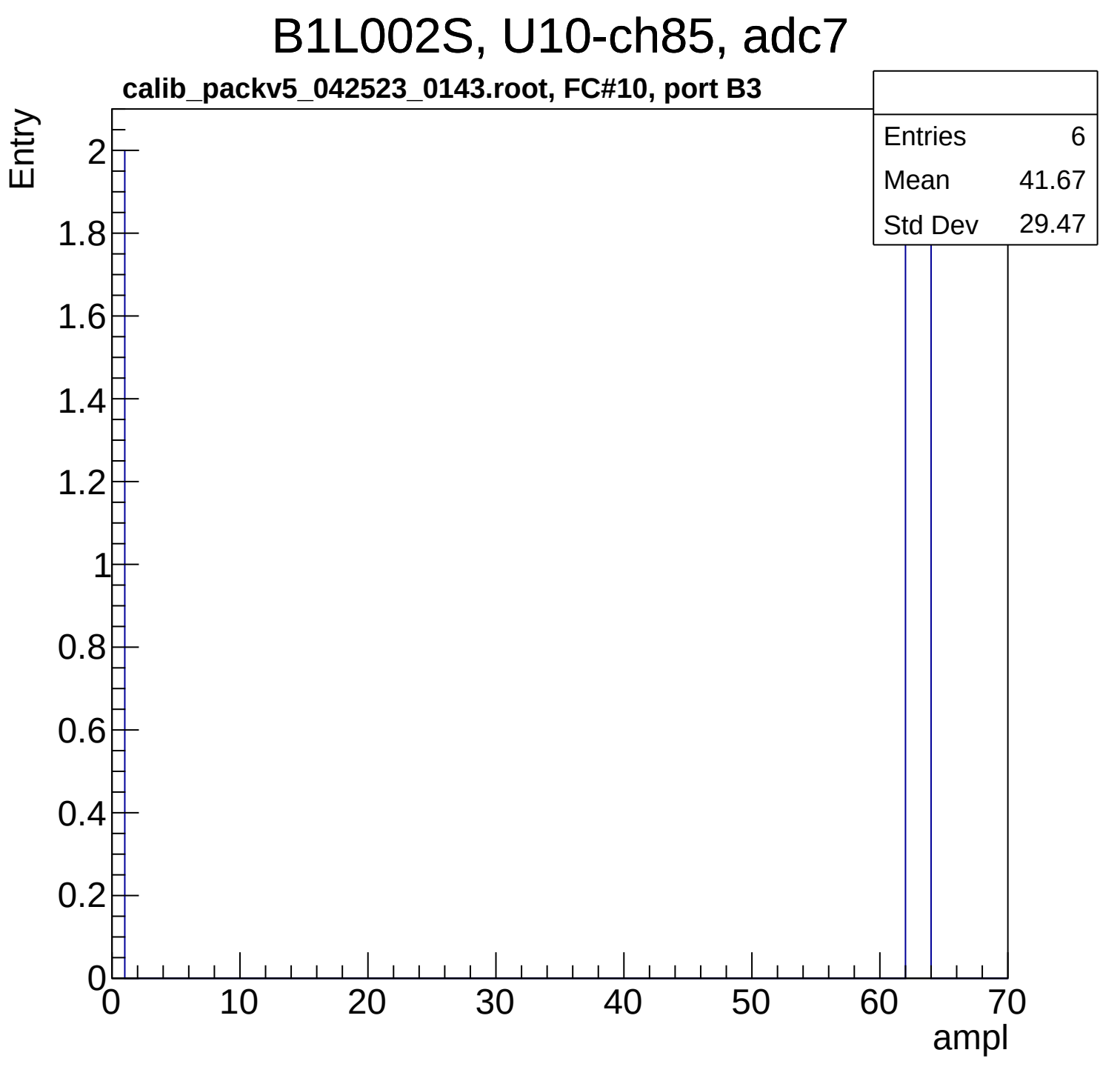
B1L002S, U10-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.67
Std Dev 29.47
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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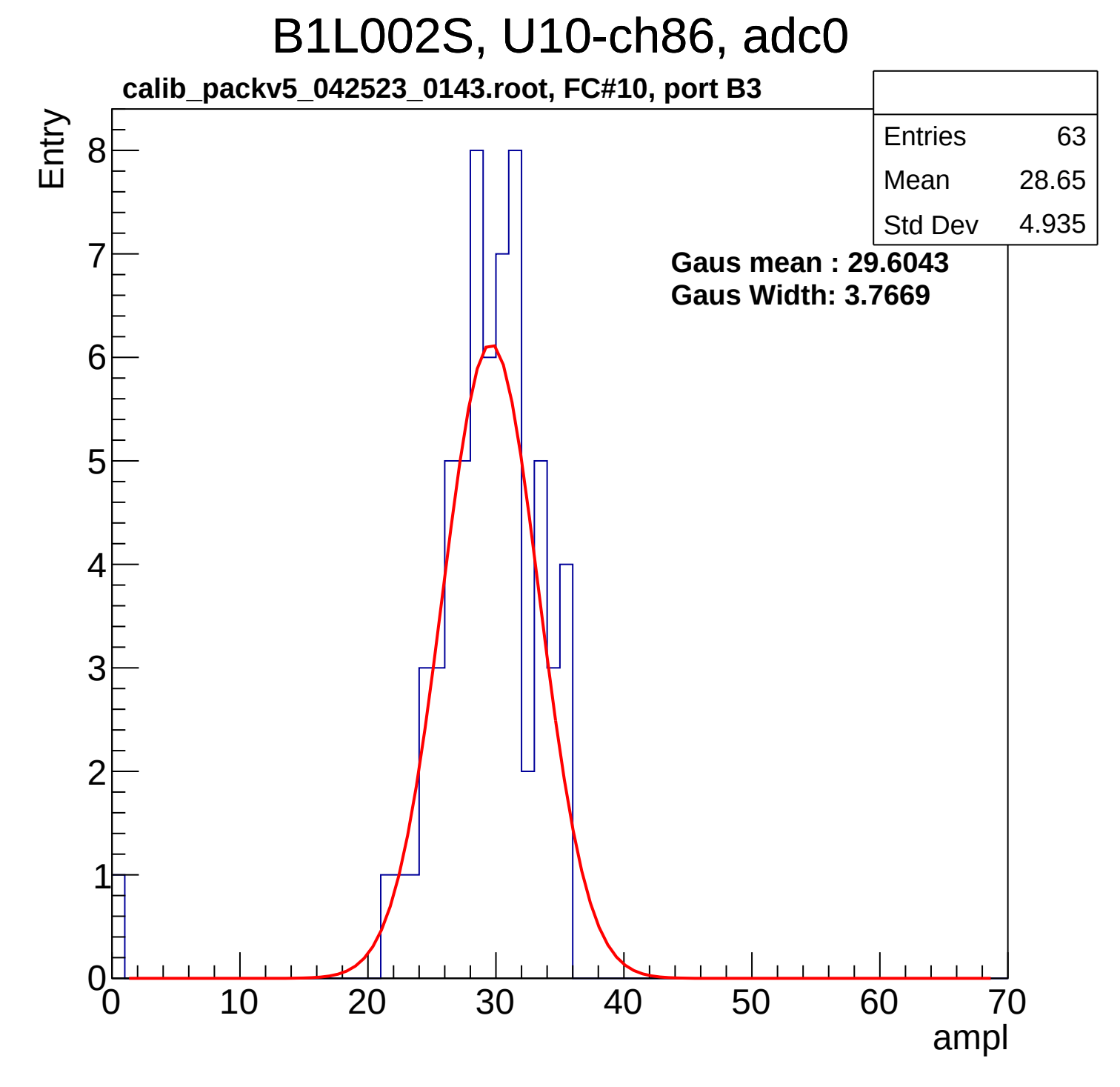
B1L002S, U10-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.65
Std Dev 4.935
Gaus mean : 29.6043
Gaus Width: 3.7669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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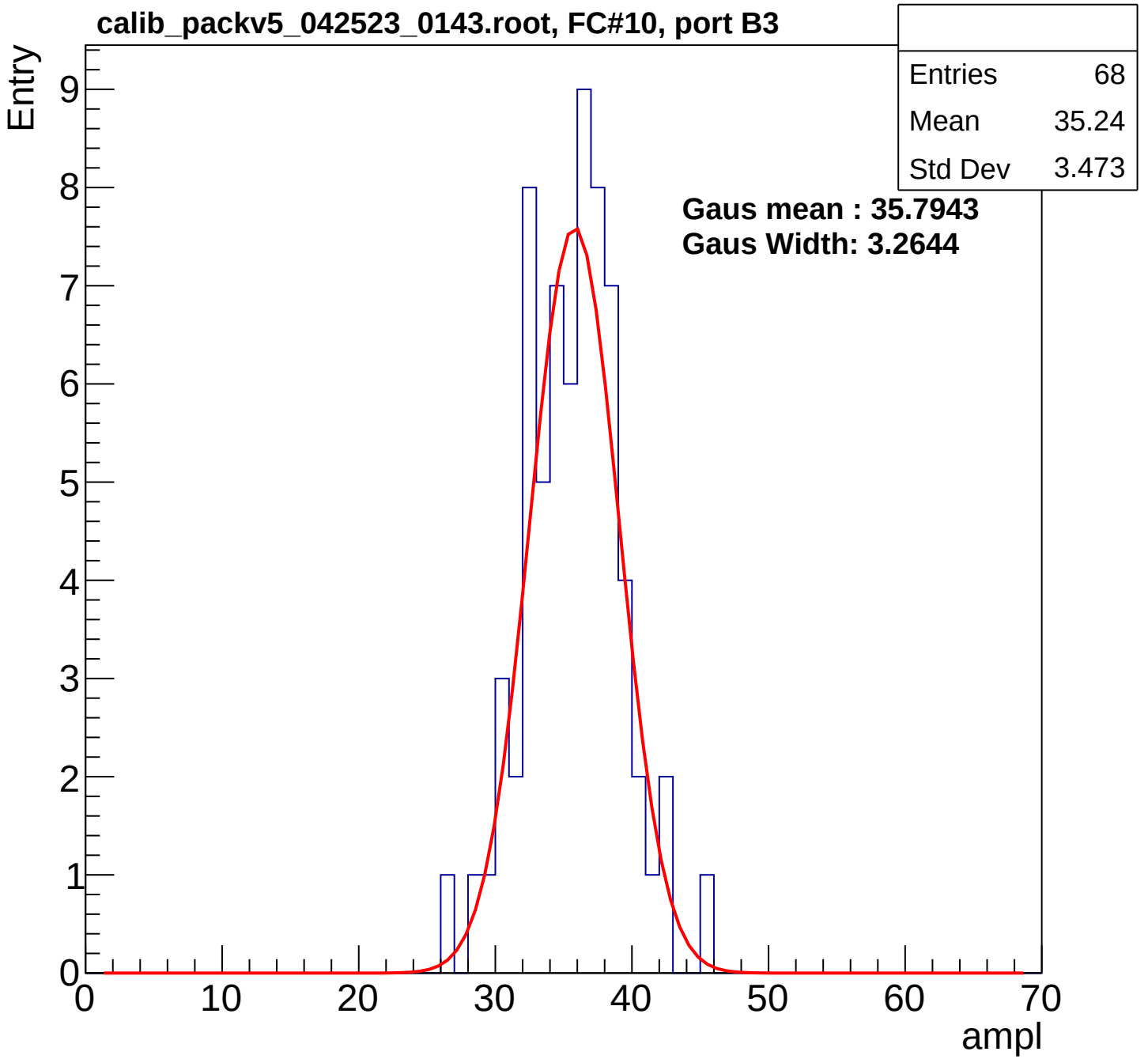
B1L002S, U10-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.24
Std Dev 3.473
Gaus mean : 35.7943
Gaus Width: 3.2644
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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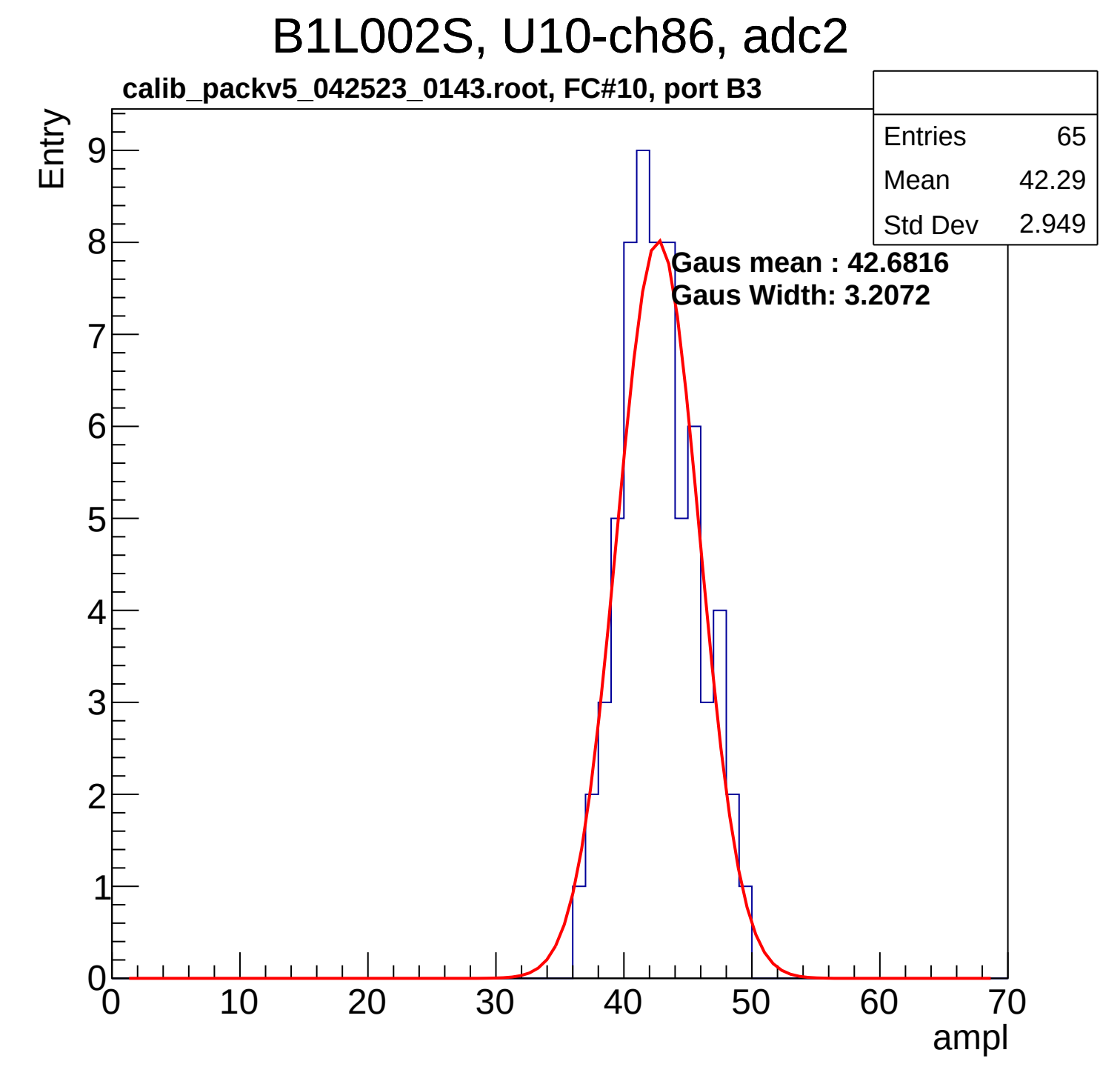
B1L002S, U10-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 42.29
Std Dev 2.949
Gaus mean : 42.6816
Gaus Width: 3.2072
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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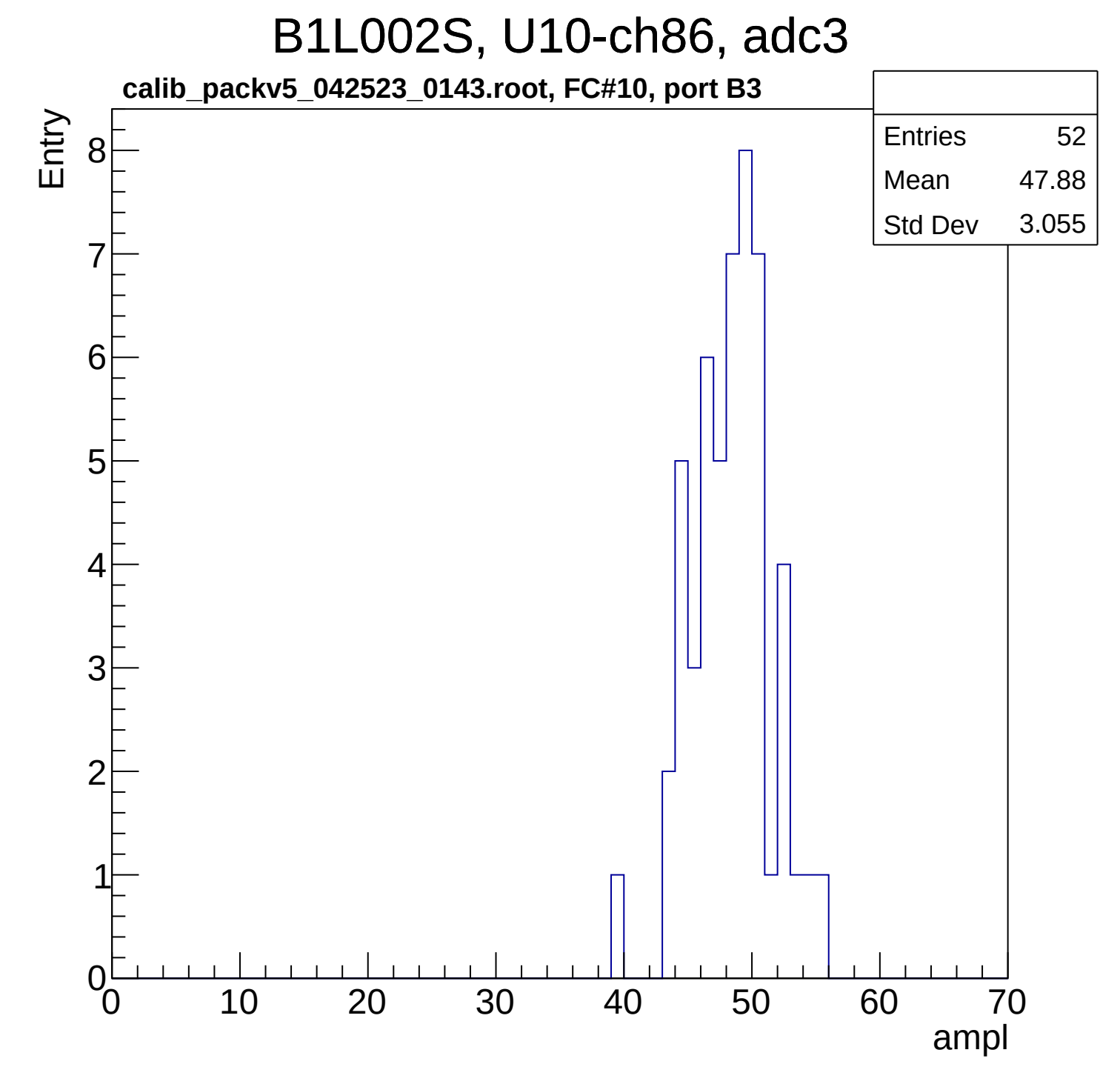
B1L002S, U10-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.88
Std Dev 3.055
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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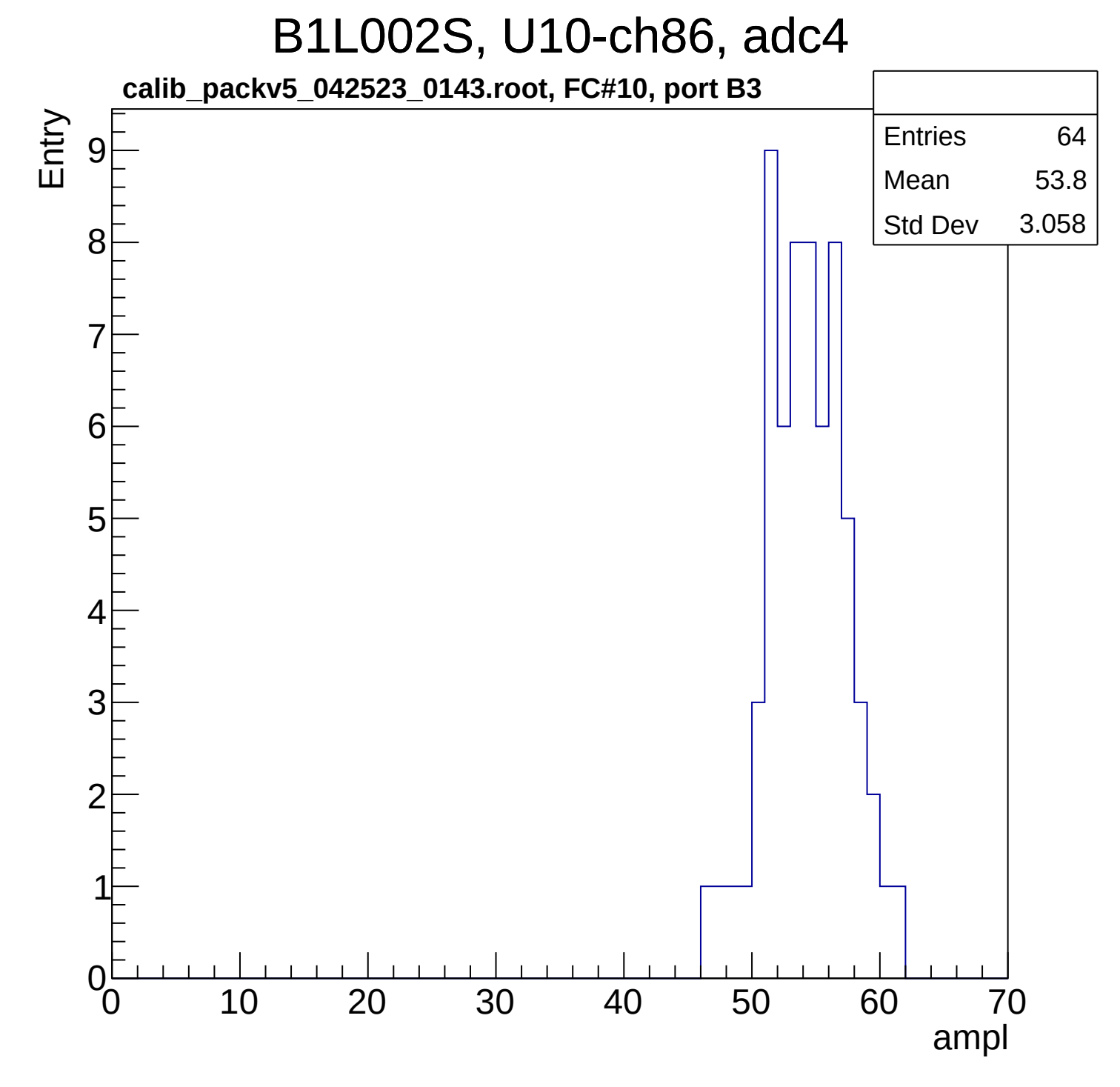
B1L002S, U10-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.8
Std Dev 3.058
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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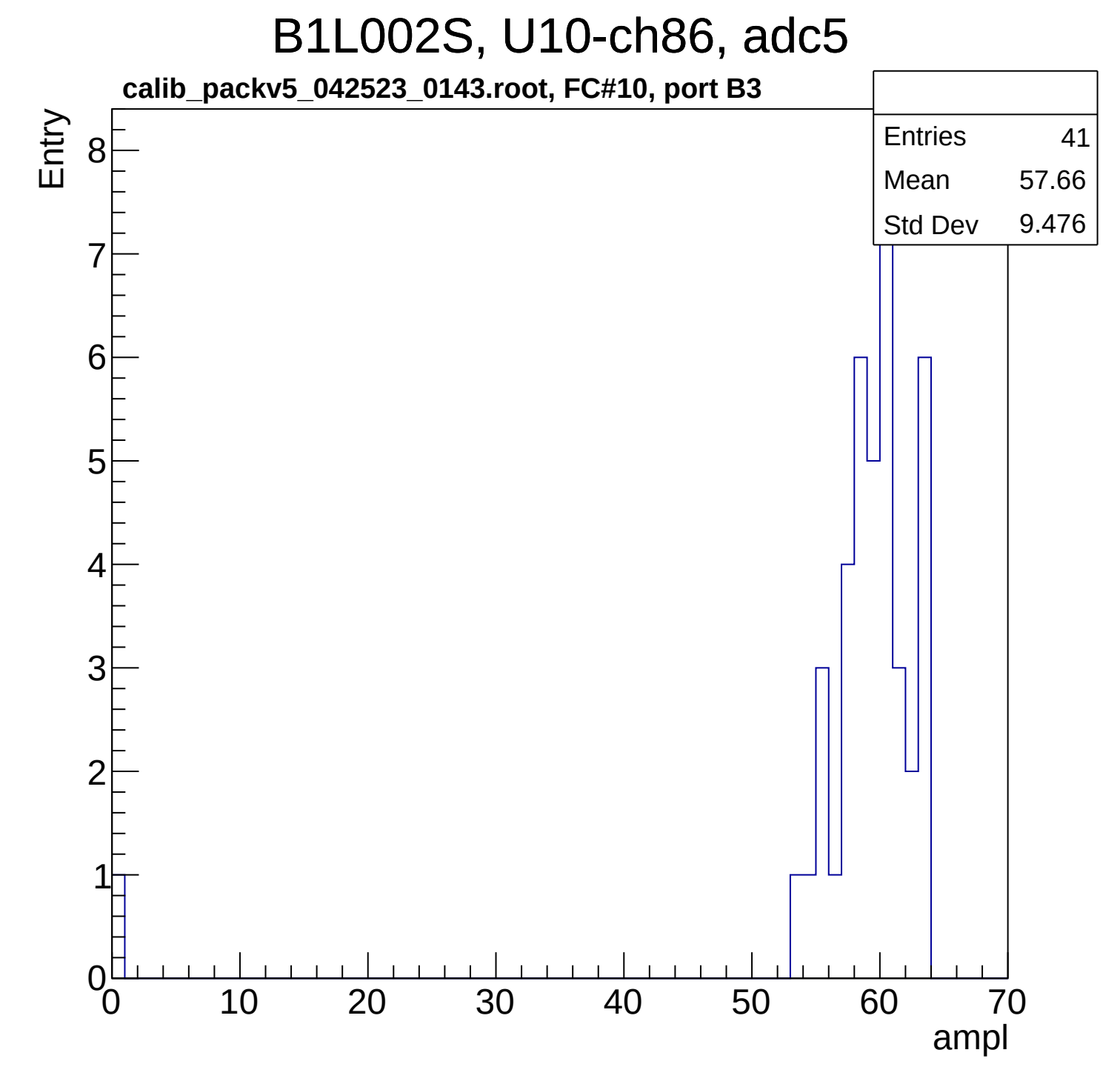
B1L002S, U10-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 57.66
Std Dev 9.476
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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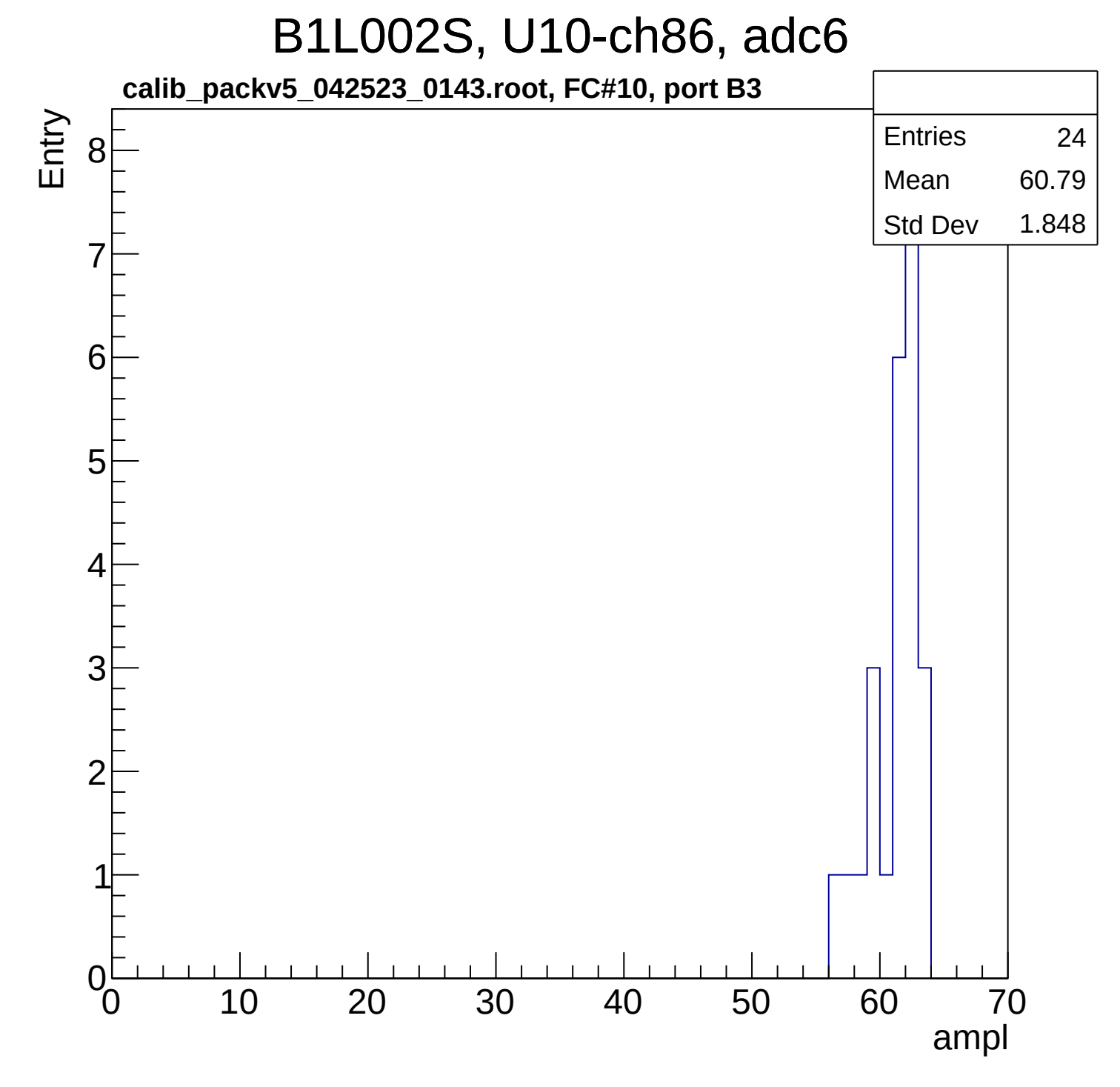
B1L002S, U10-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 60.79
Std Dev 1.848
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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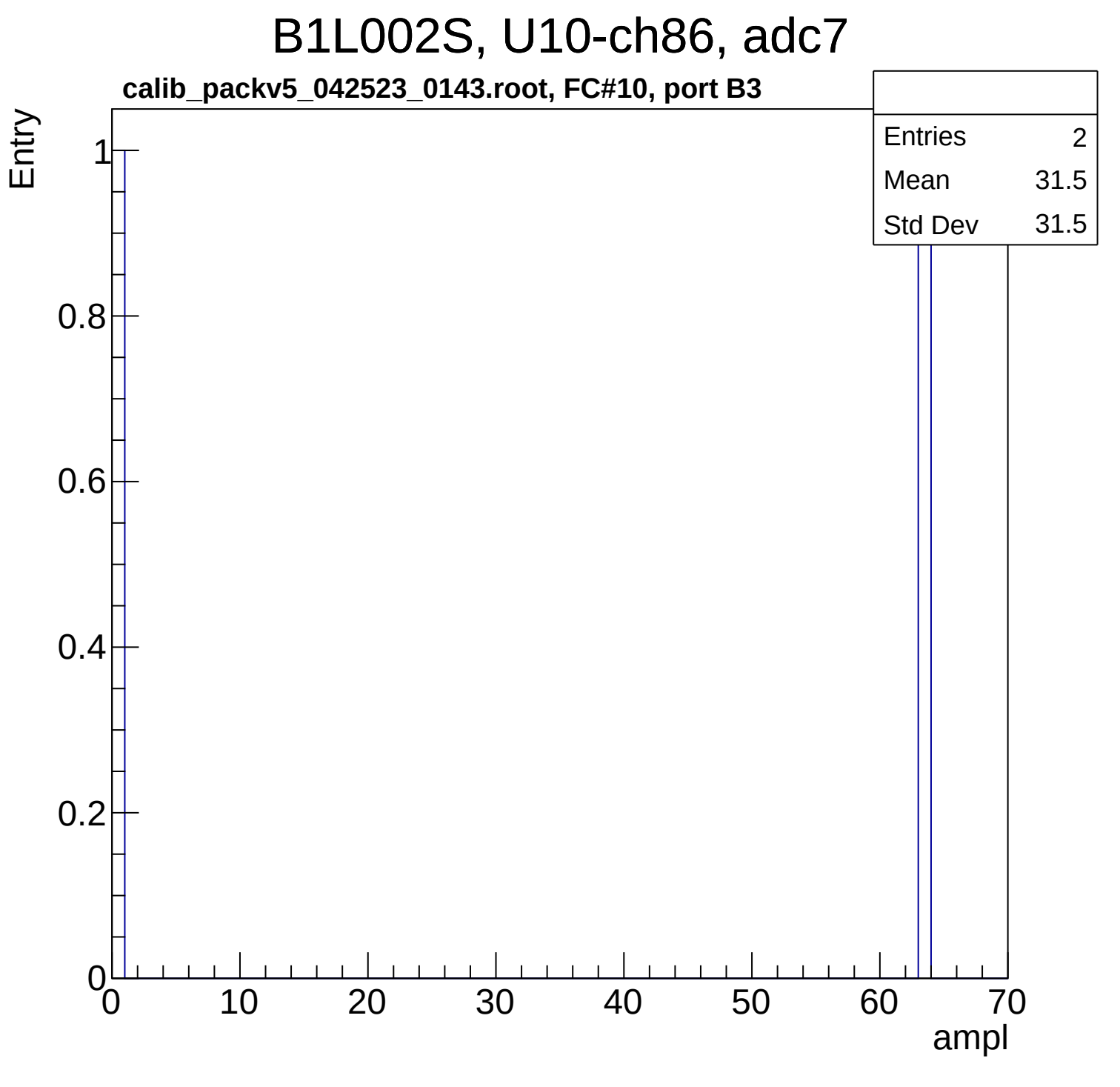
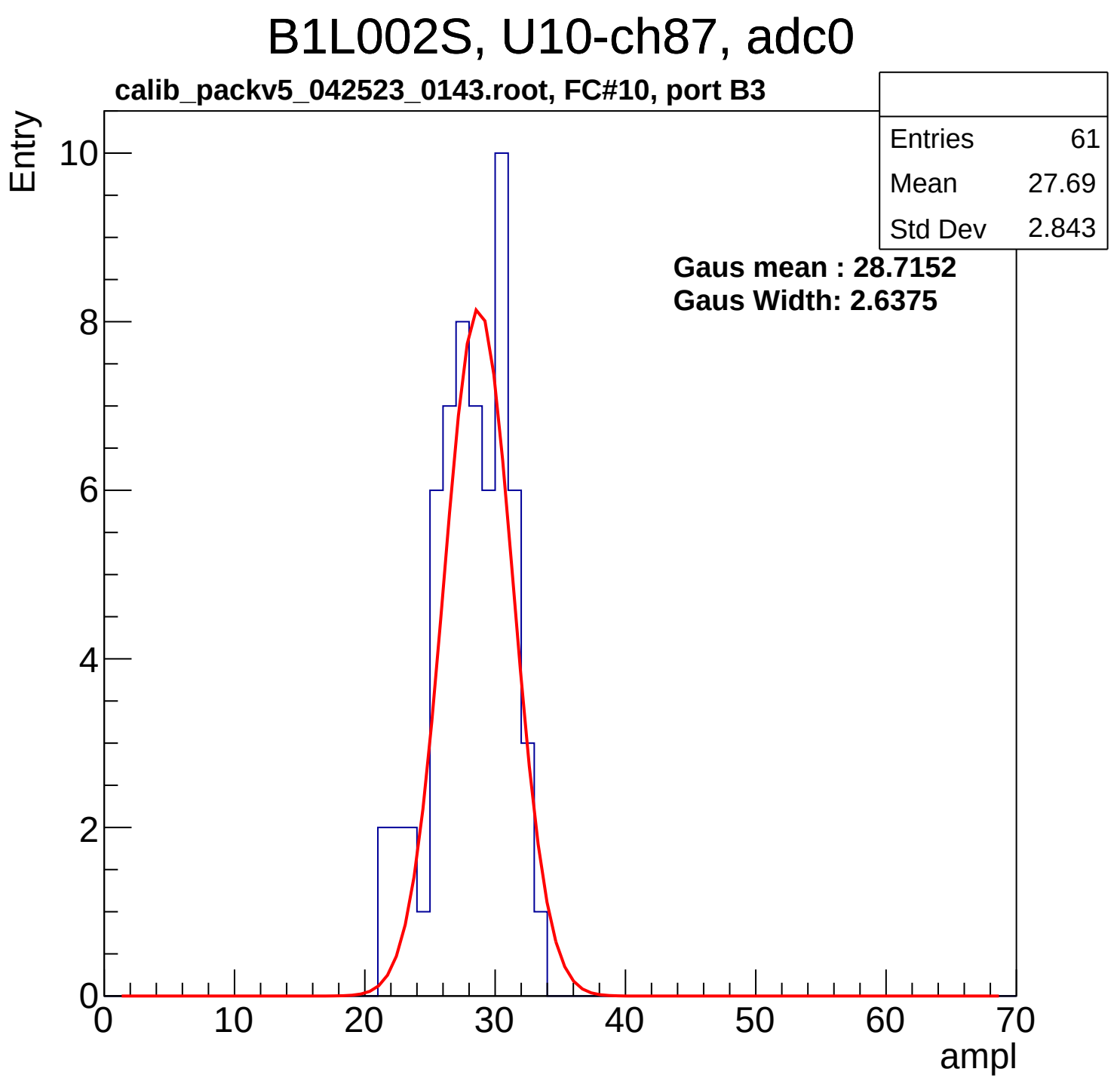

B1L002S, U10-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.69
Std Dev 2.843
Gaus mean : 28.7152
Gaus Width: 2.6375
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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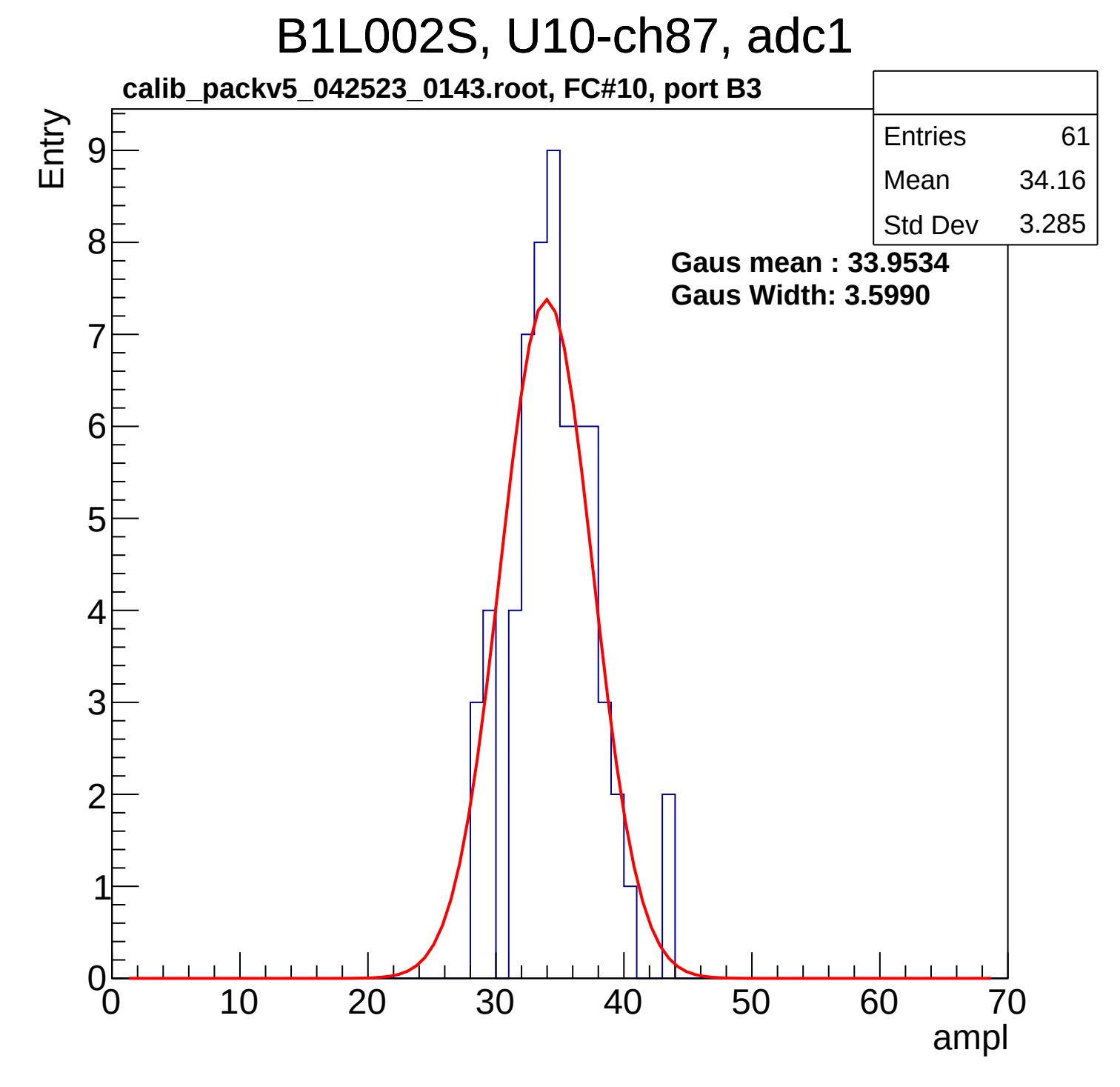
B1L002S, U10-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.16
Std Dev 3.285
Gaus mean : 33.9534
Gaus Width: 3.5990
Entry
ampl
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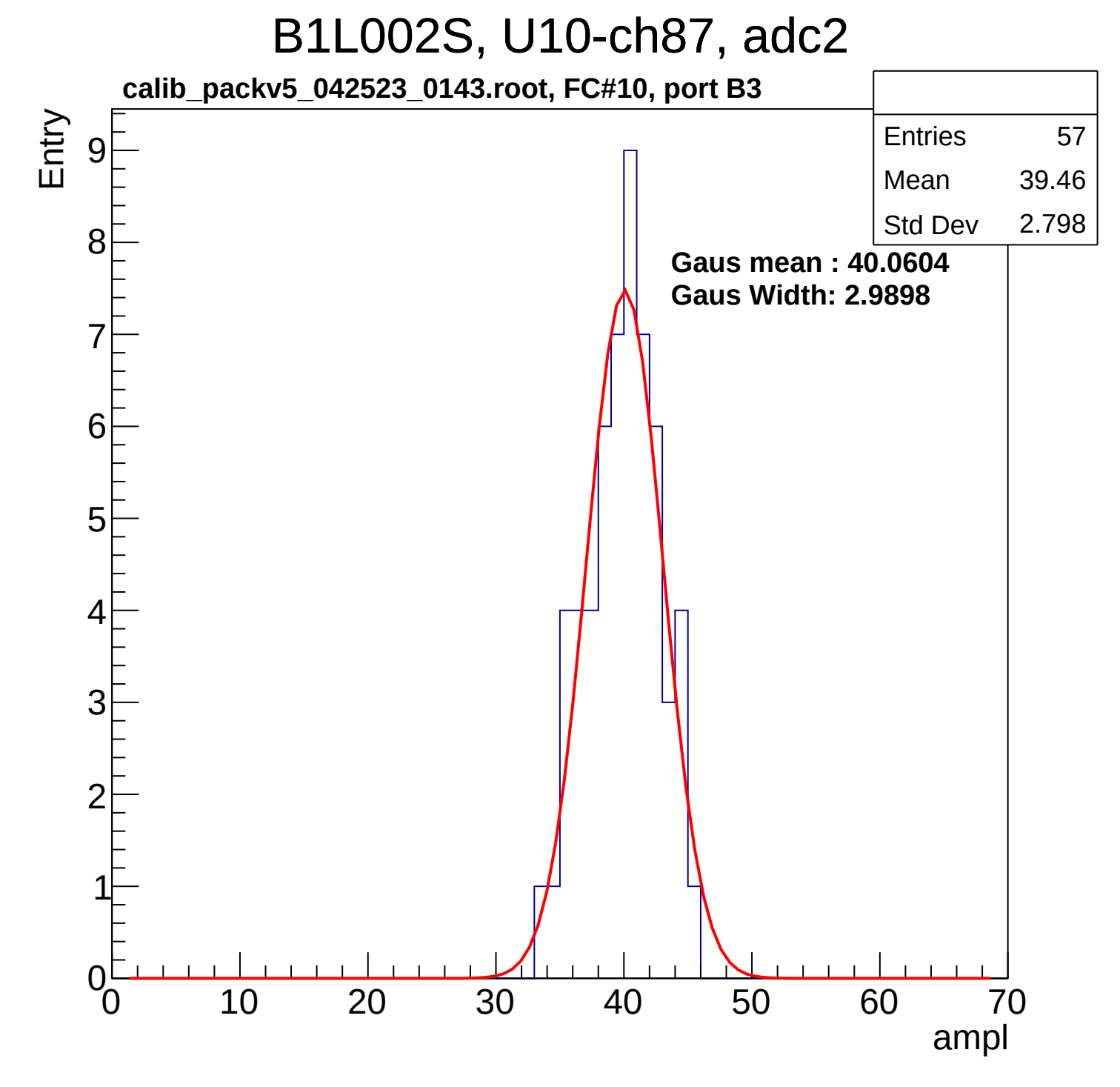
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 39.46
Std Dev 2.798
Gaus mean : 40.0604
Gaus Width: 2.9898
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
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60
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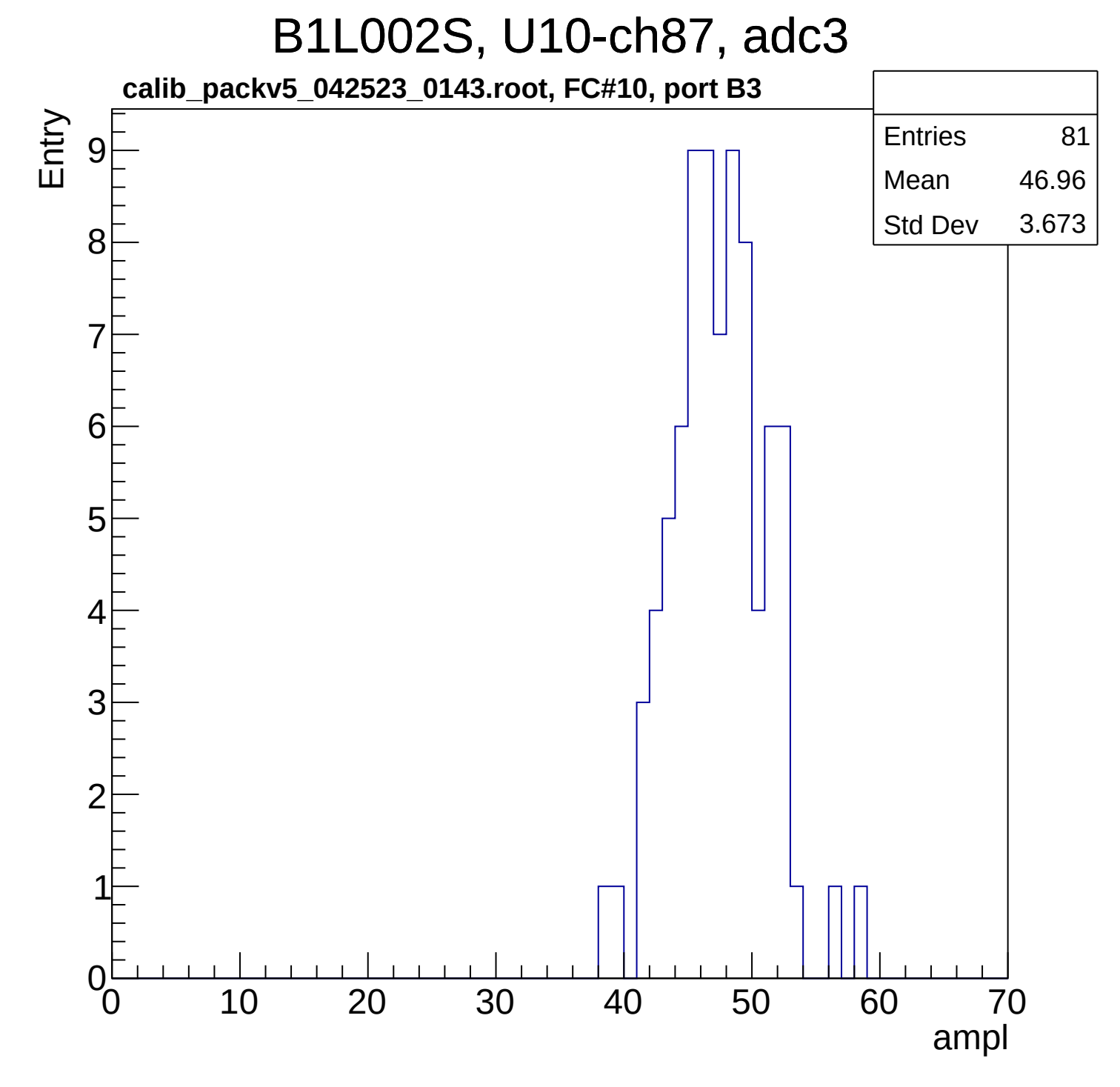
B1L002S, U10-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 46.96
Std Dev 3.673
Entry
ampl
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6
7
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0
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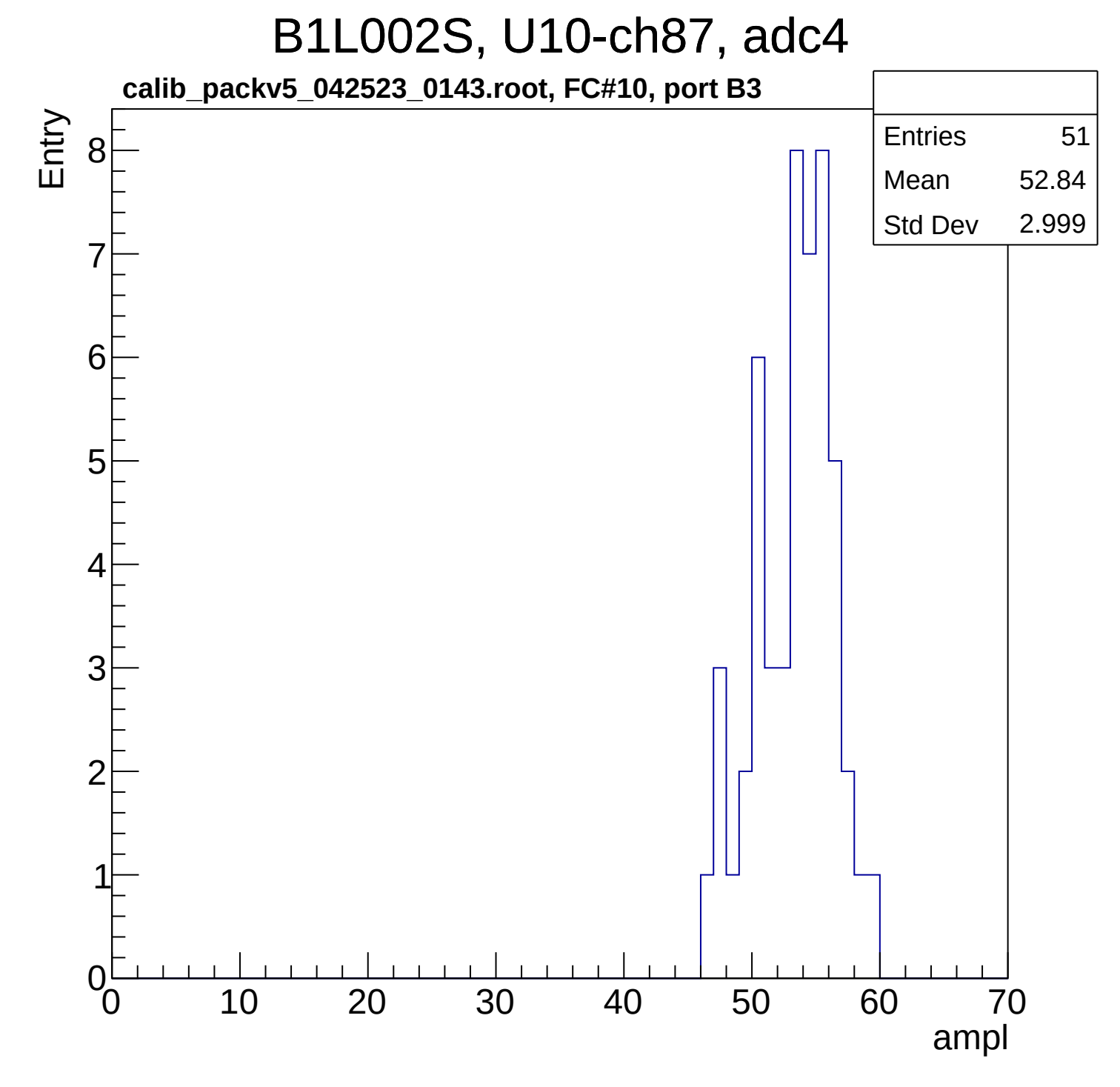
B1L002S, U10-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.84
Std Dev 2.999
Entry
ampl
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2
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0
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40
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60
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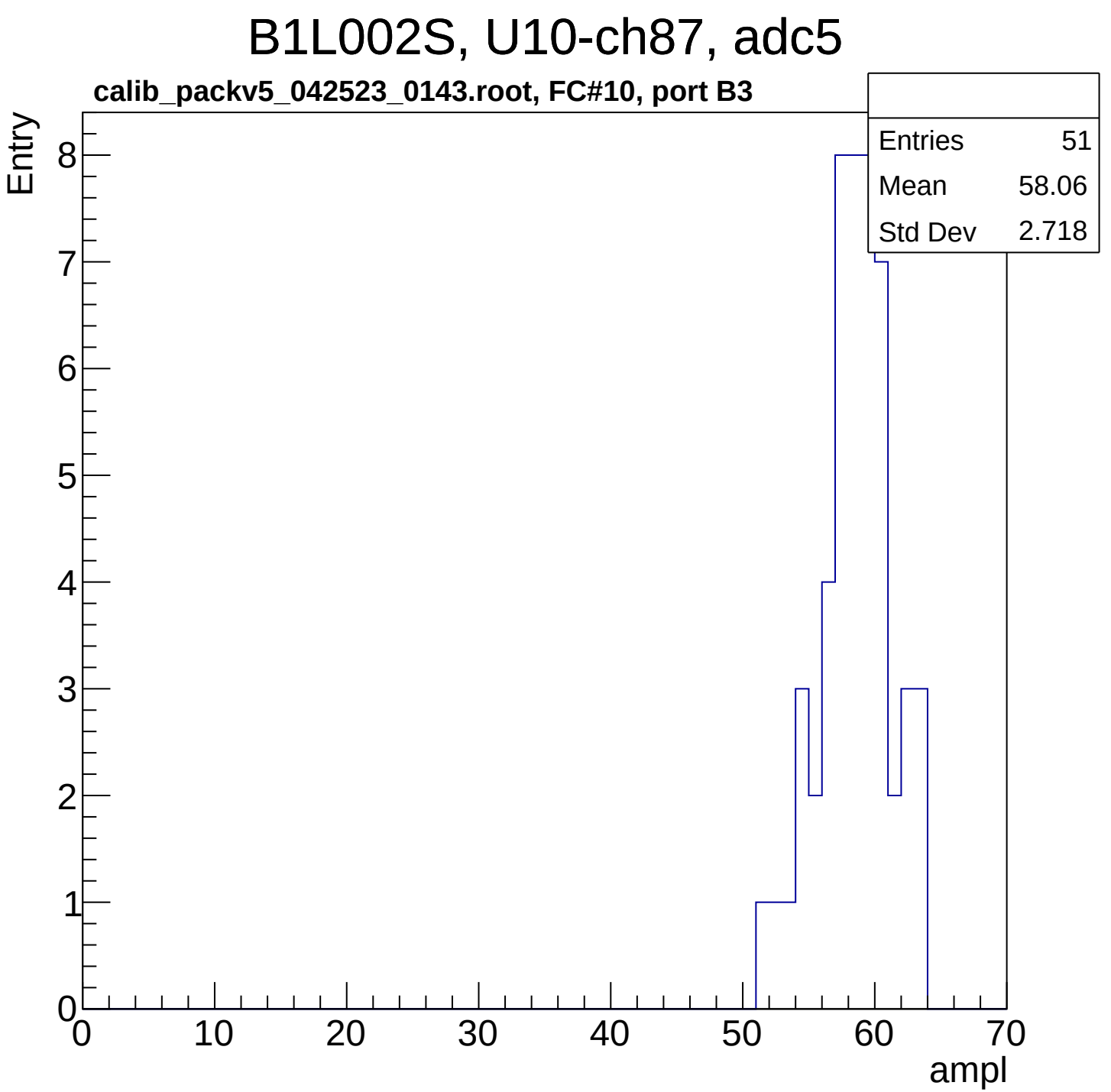
B1L002S, U10-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.06
Std Dev 2.718
Entry
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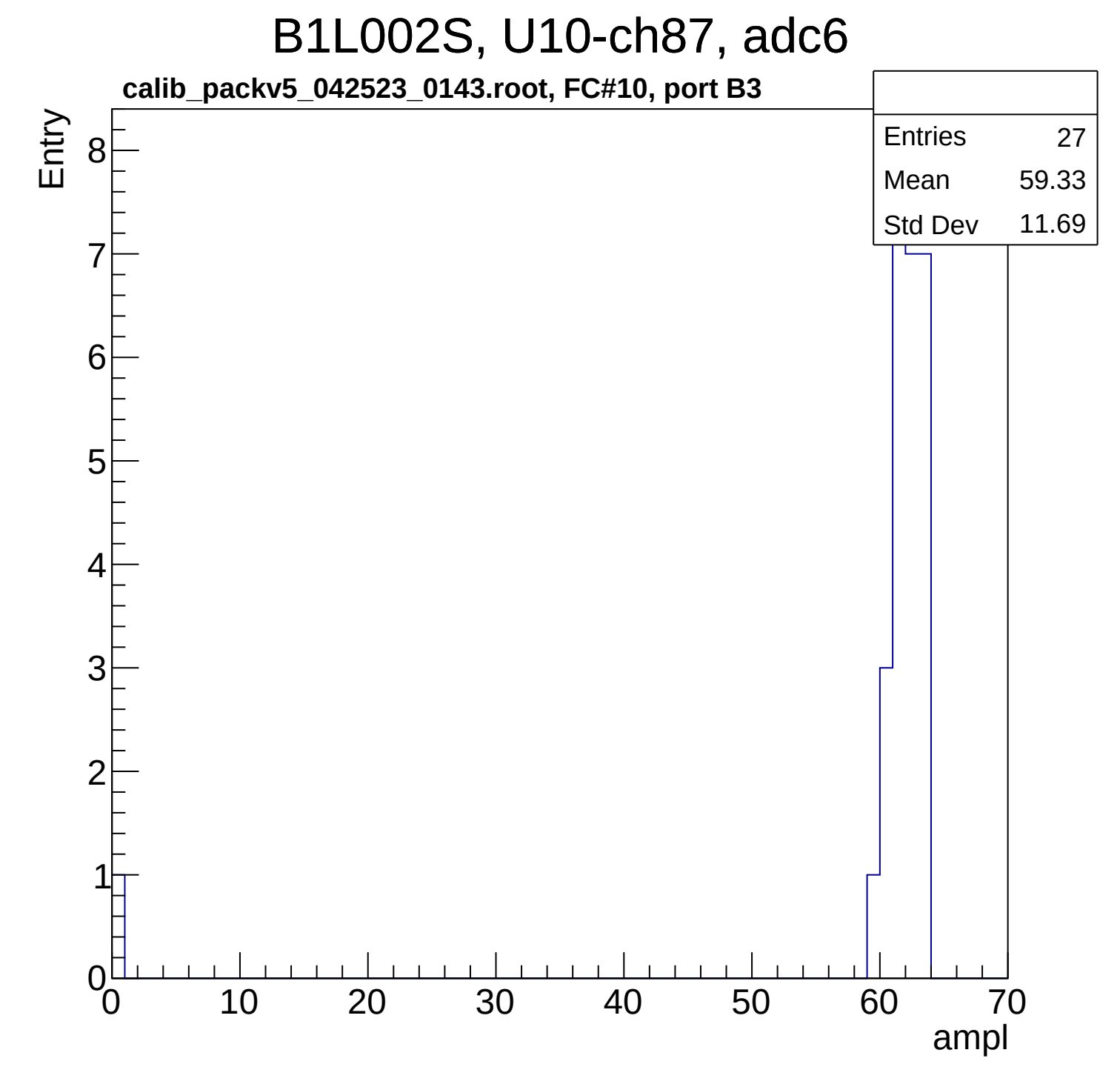
50
60
70
ampl


B1L002S, U10-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.33
Std Dev 11.69
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
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60
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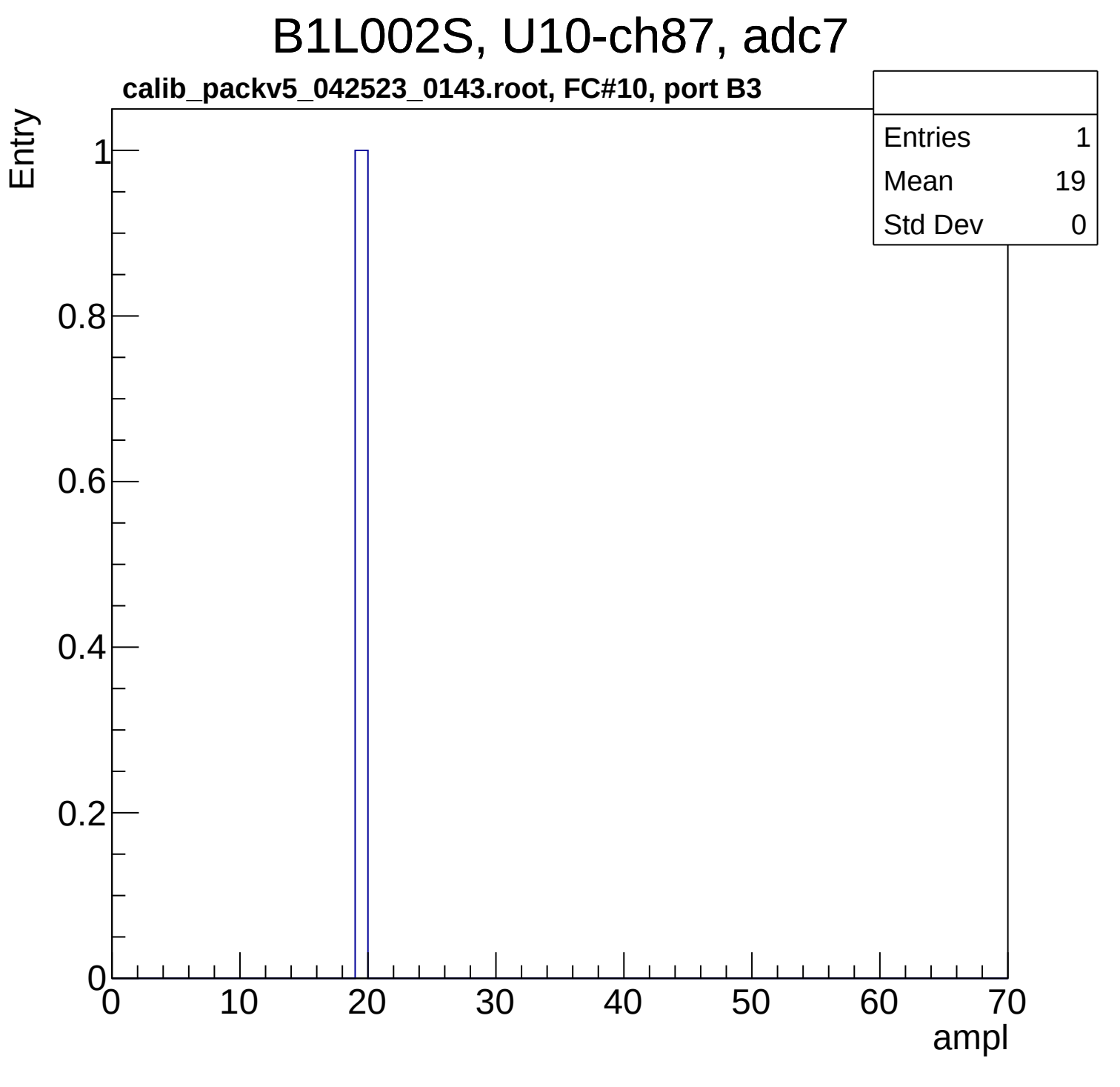
B1L002S, U10-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
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0.6
0.4
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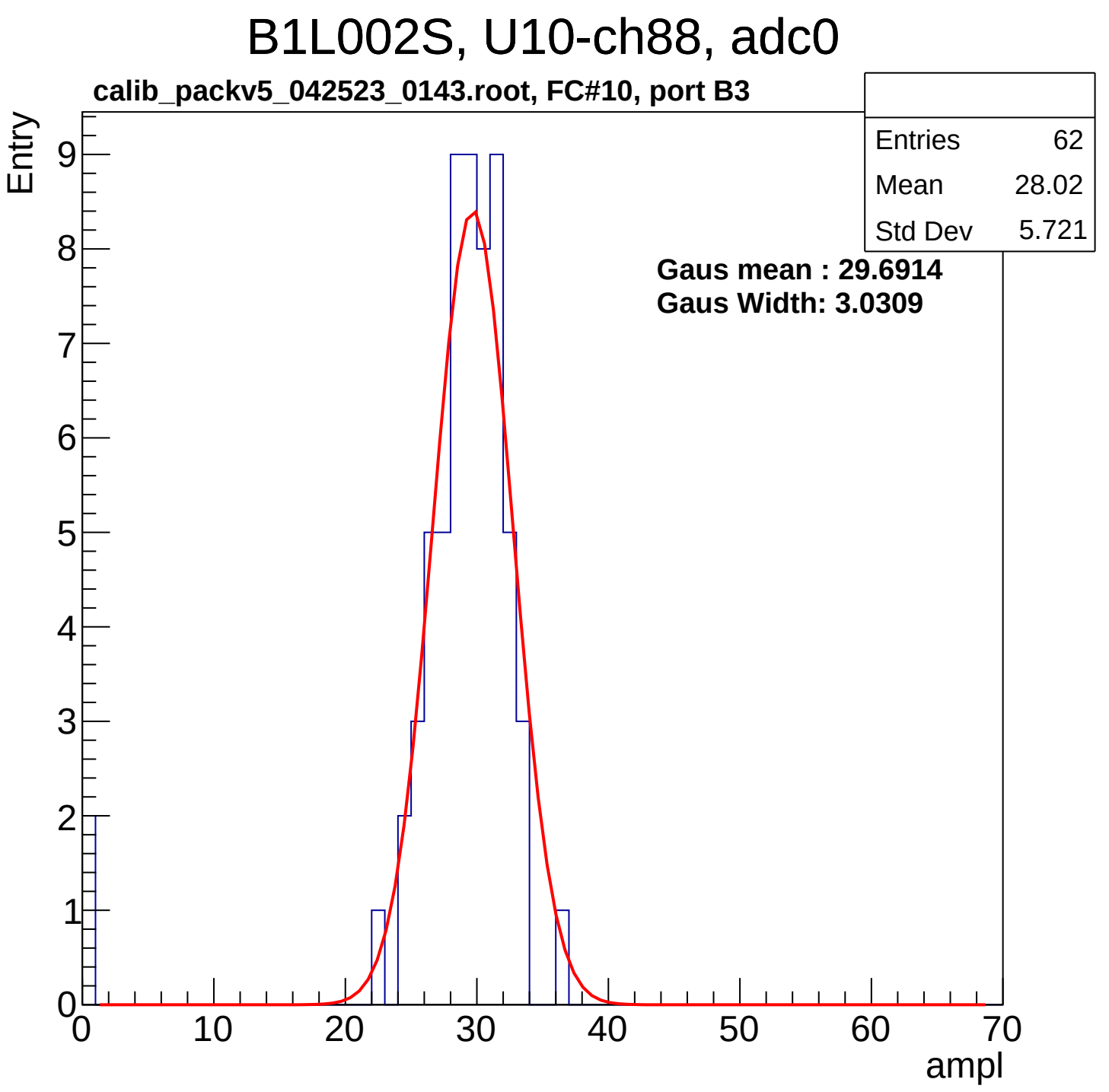
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.02
Std Dev 5.721
Gaus mean : 29.6914
Gaus Width: 3.0309
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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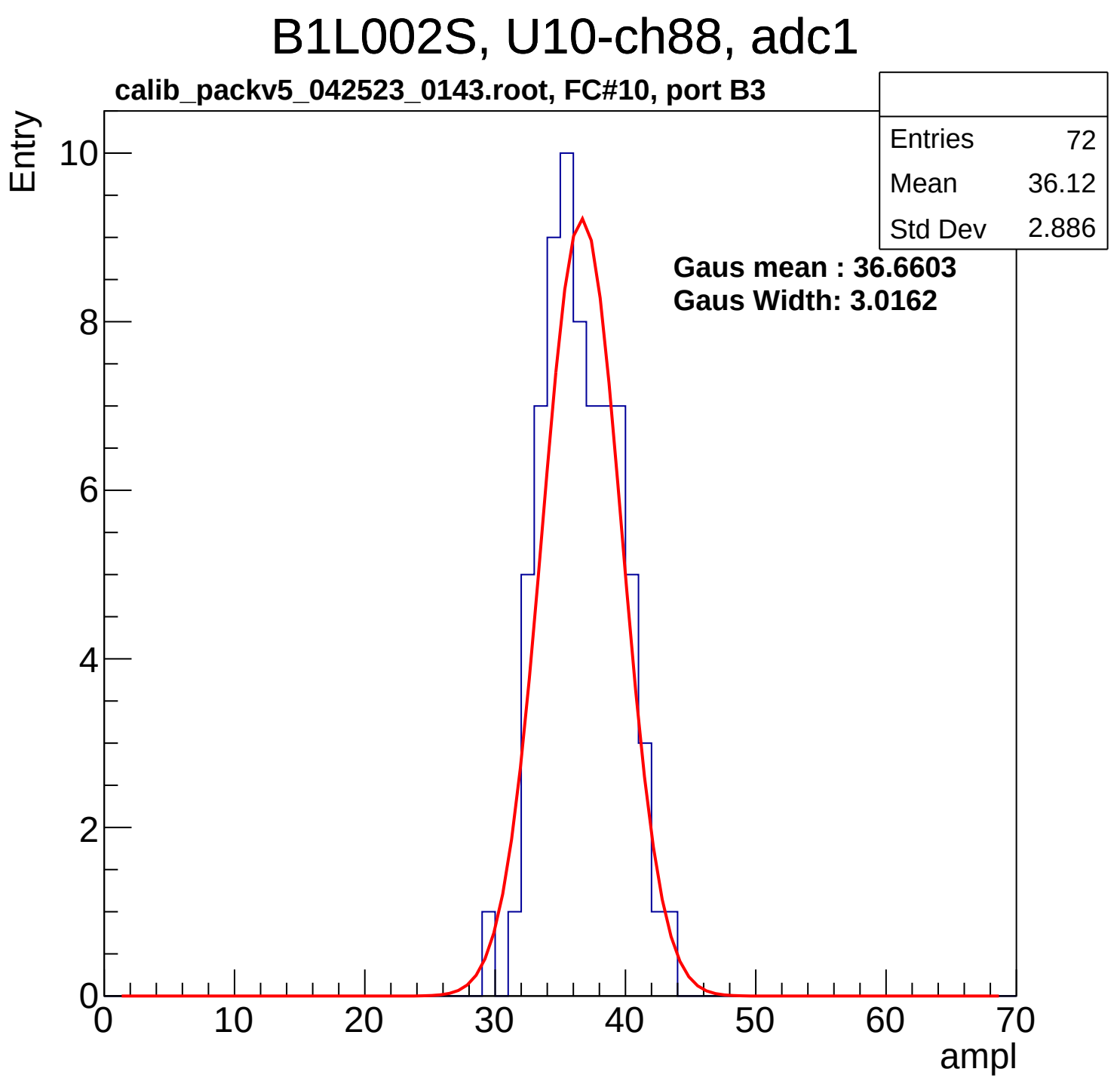
B1L002S, U10-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 36.12
Std Dev 2.886
Gaus mean : 36.6603
Gaus Width: 3.0162
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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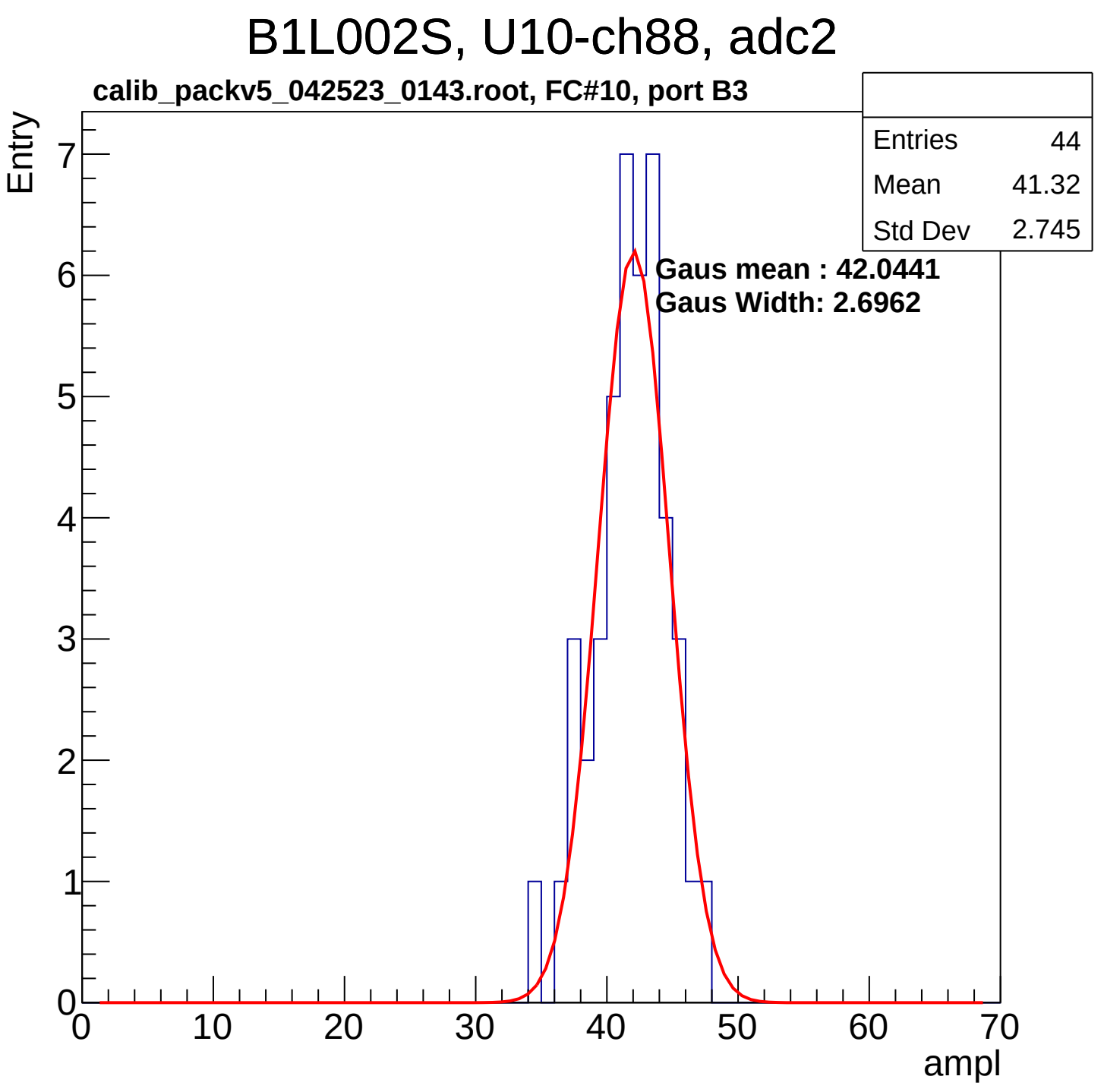
B1L002S, U10-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 41.32
Std Dev 2.745
Gaus mean : 42.0441
Gaus Width: 2.6962
Entry
ampl
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2
3
4
5
6
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0
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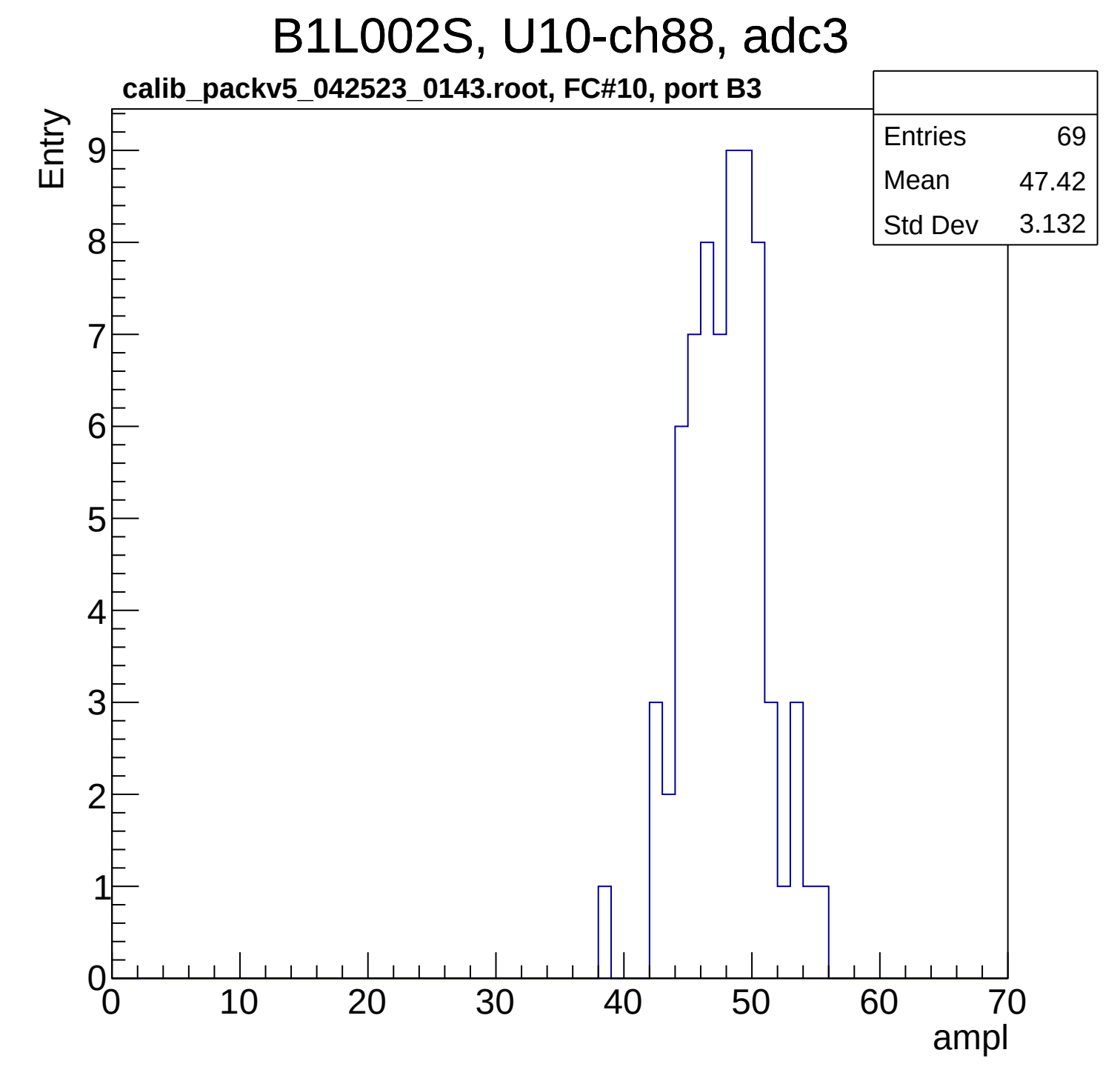
B1L002S, U10-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.42
Std Dev 3.132
Entry
ampl
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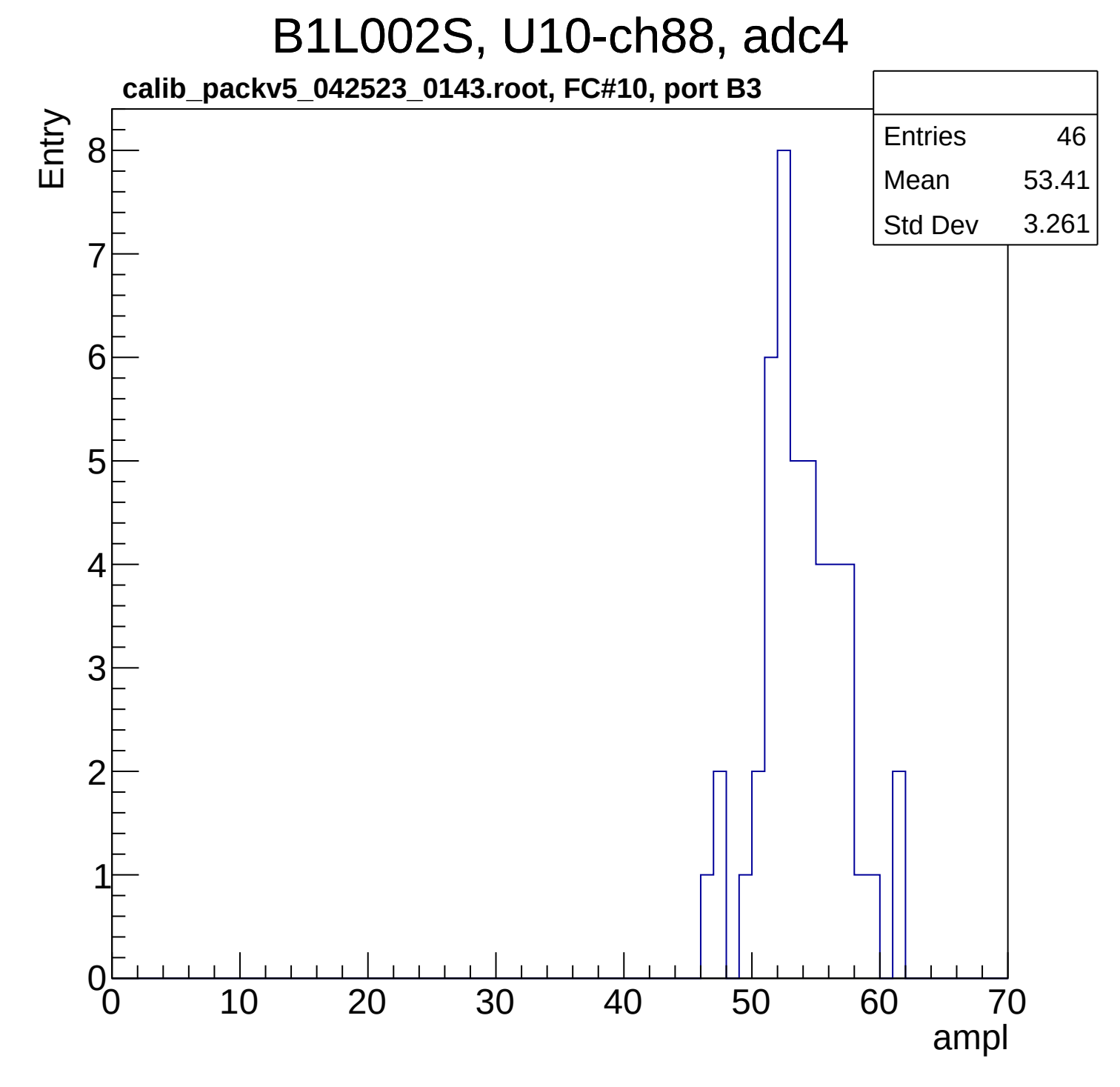
B1L002S, U10-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 53.41
Std Dev 3.261
Entry
ampl
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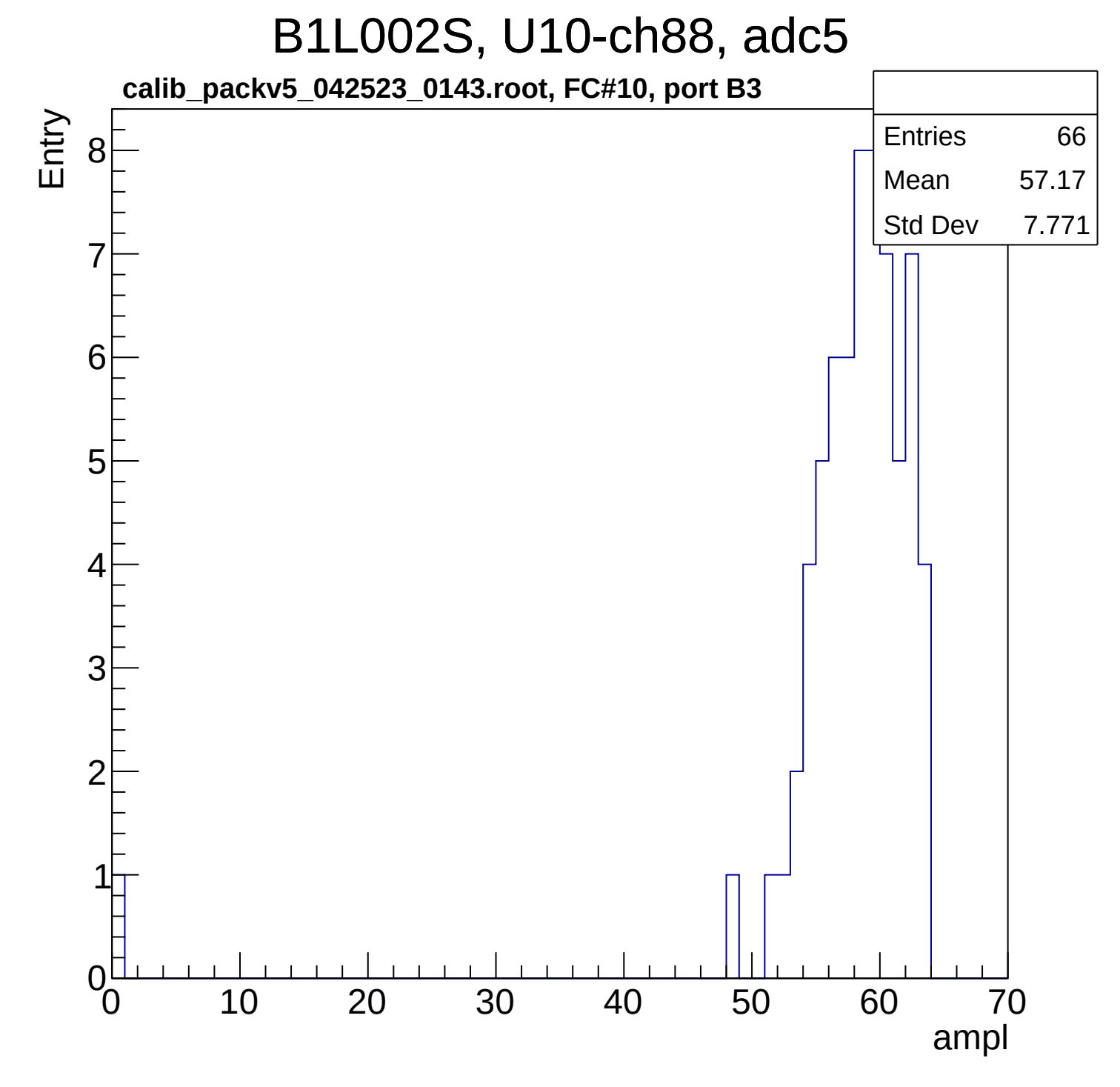
B1L002S, U10-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.17
Std Dev 7.771
Entry
ampl
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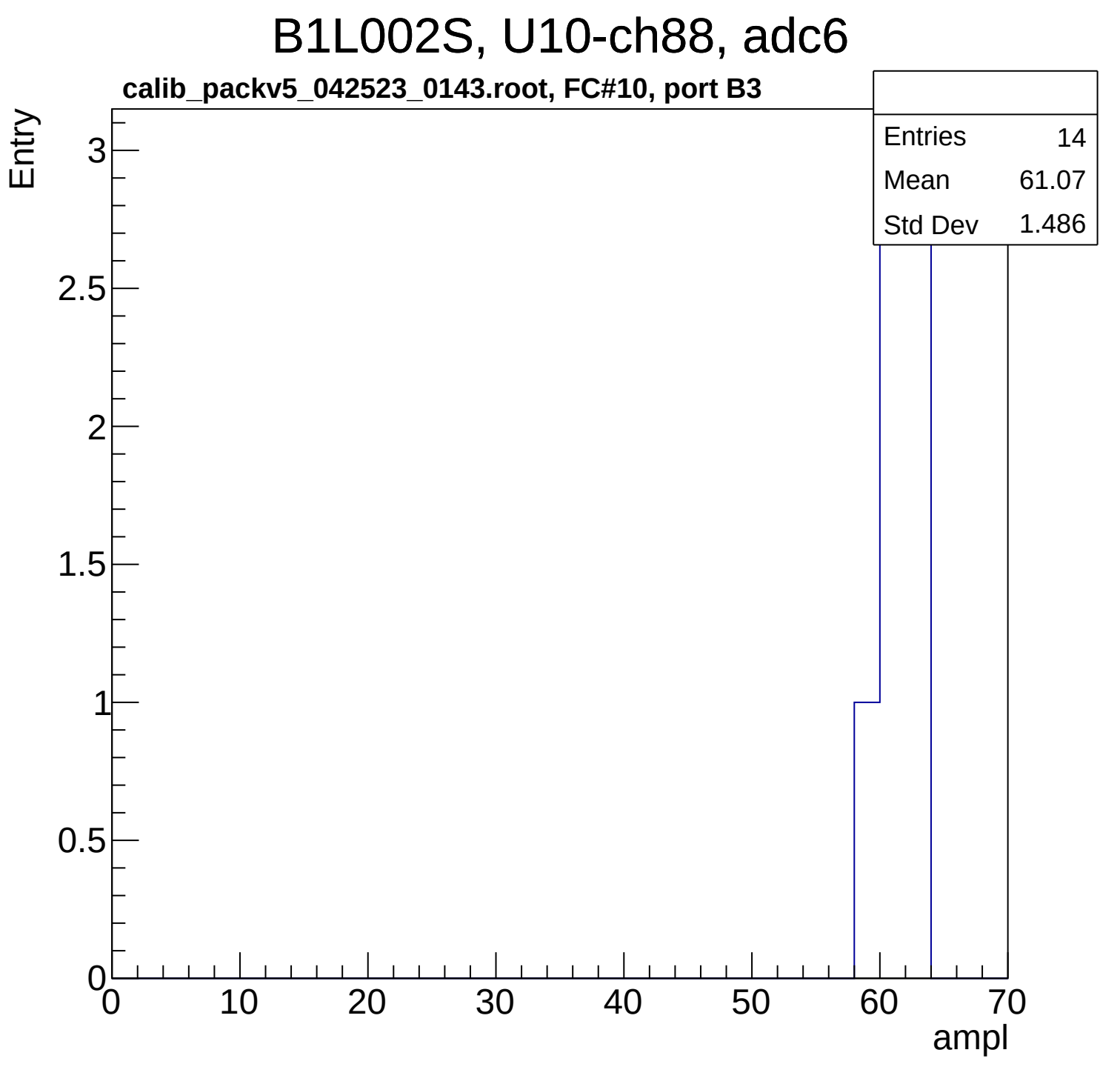
B1L002S, U10-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.07
Std Dev 1.486
Entry
ampl
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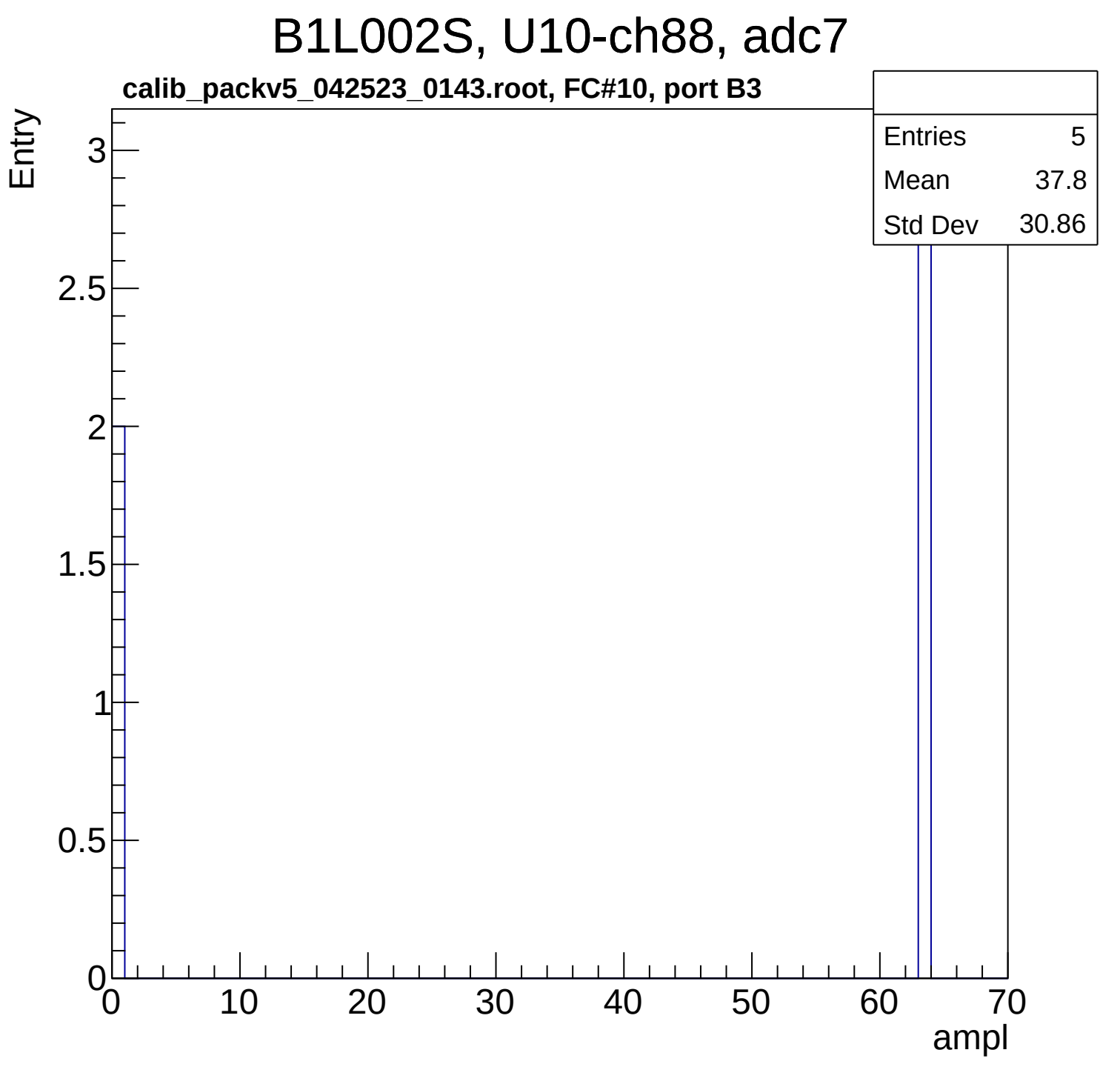
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.8
Std Dev 30.86
Entry
0
0.5
1
1.5
2
2.5
3
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10
20
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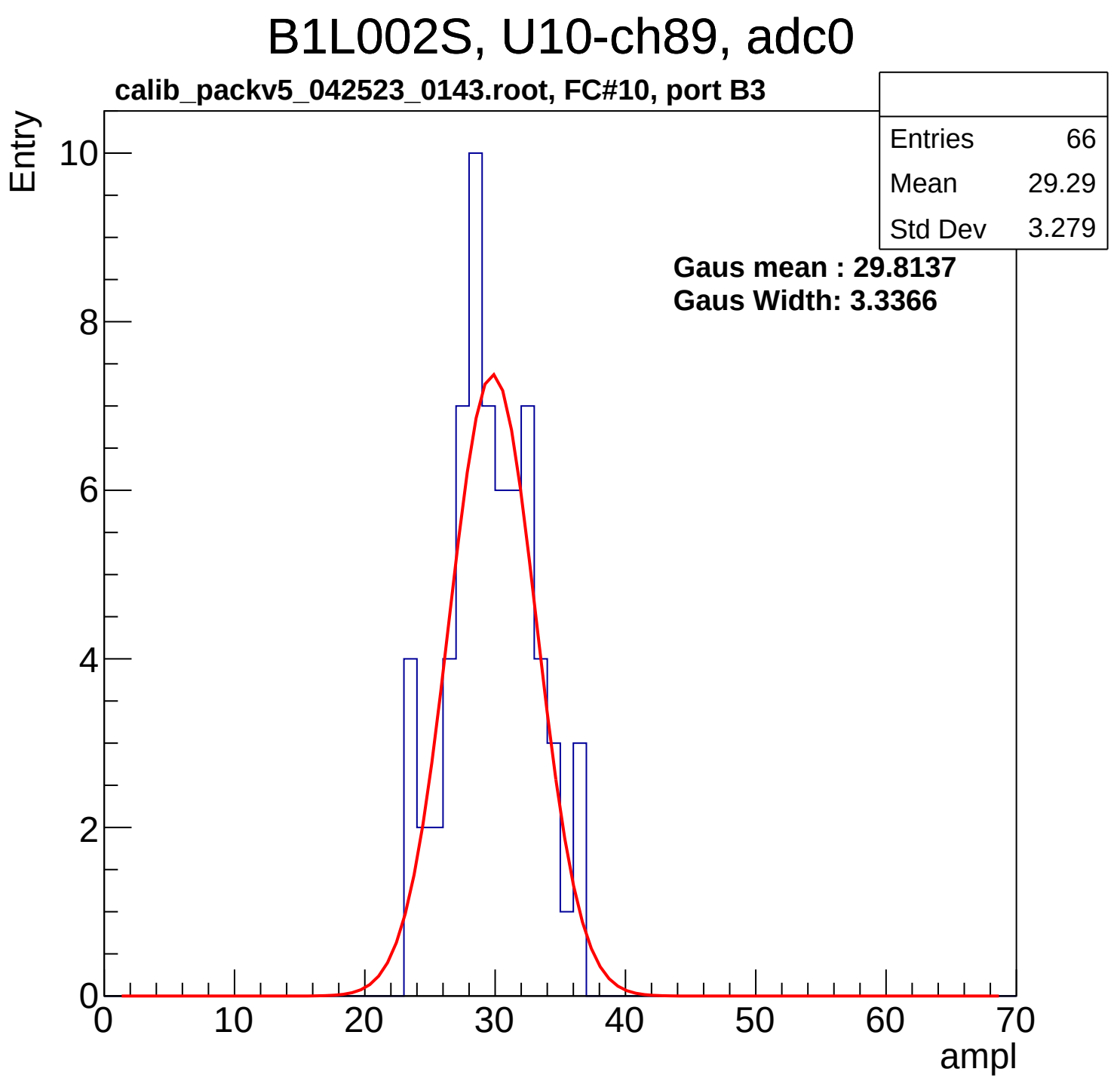
50
60
70
ampl


B1L002S, U10-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.29
Std Dev 3.279
Gaus mean : 29.8137
Gaus Width: 3.3366
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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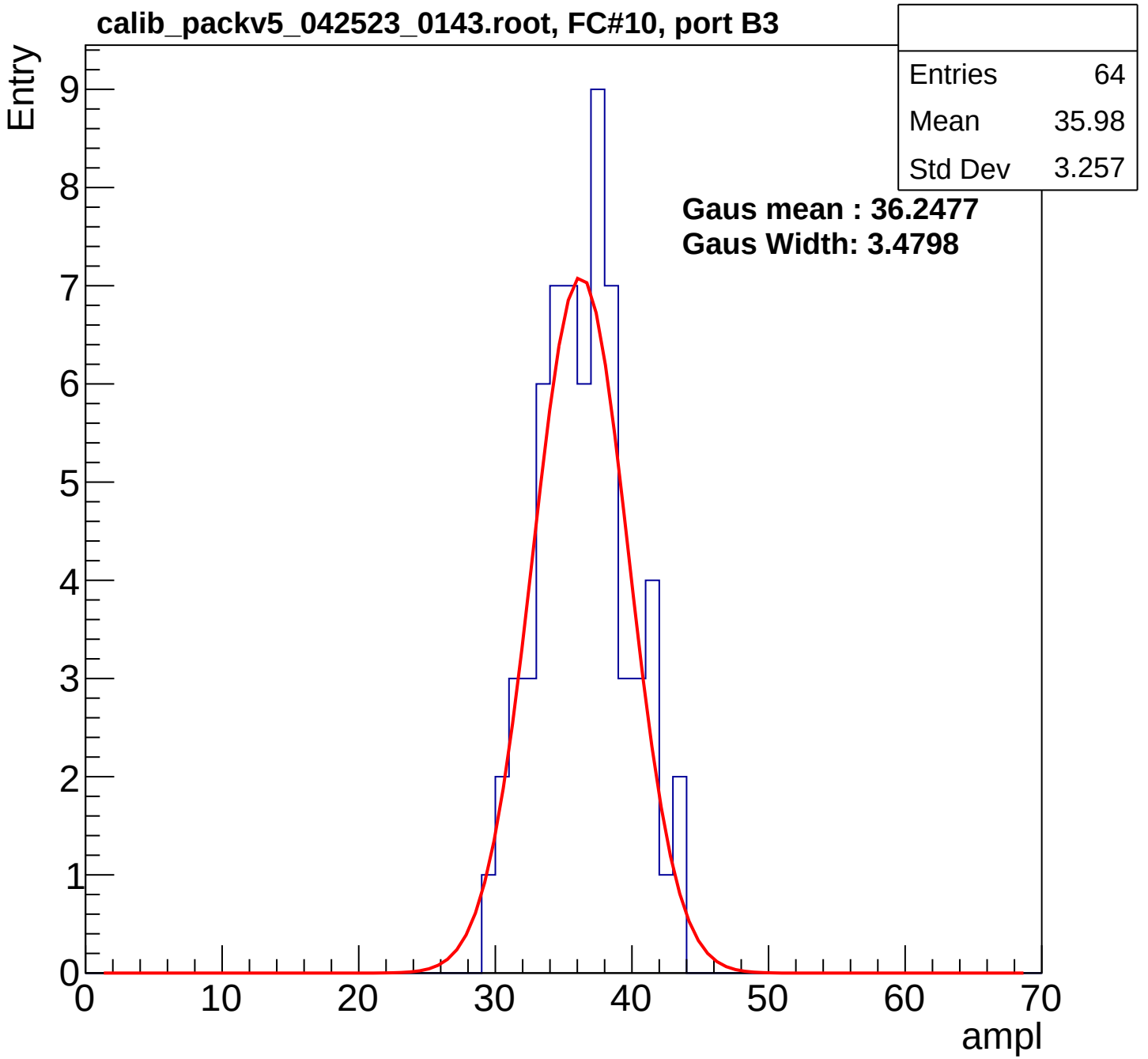
B1L002S, U10-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.98
Std Dev 3.257
Gaus mean : 36.2477
Gaus Width: 3.4798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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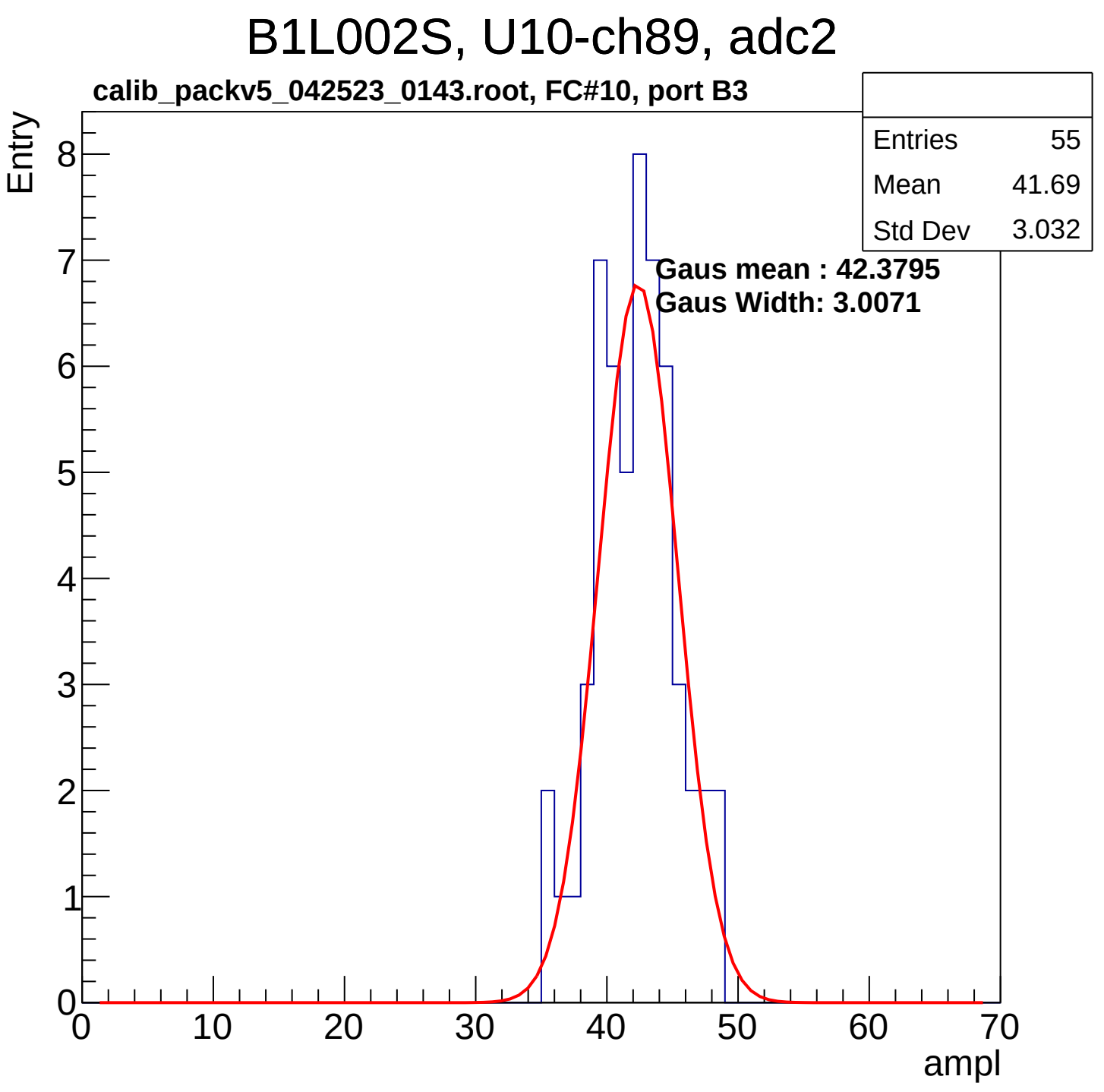
B1L002S, U10-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.69
Std Dev 3.032
Gaus mean : 42.3795
Gaus Width: 3.0071
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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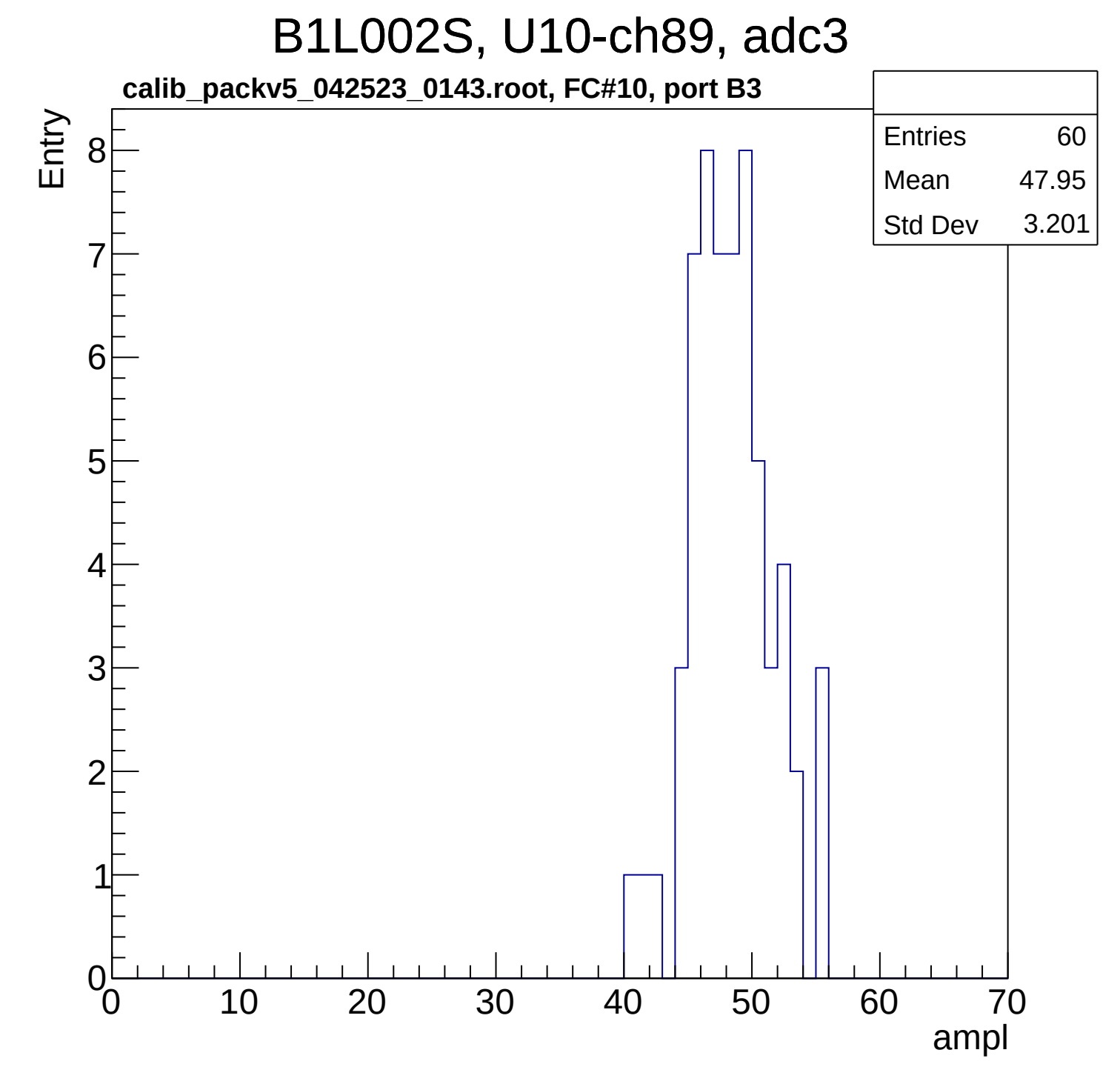
B1L002S, U10-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.95
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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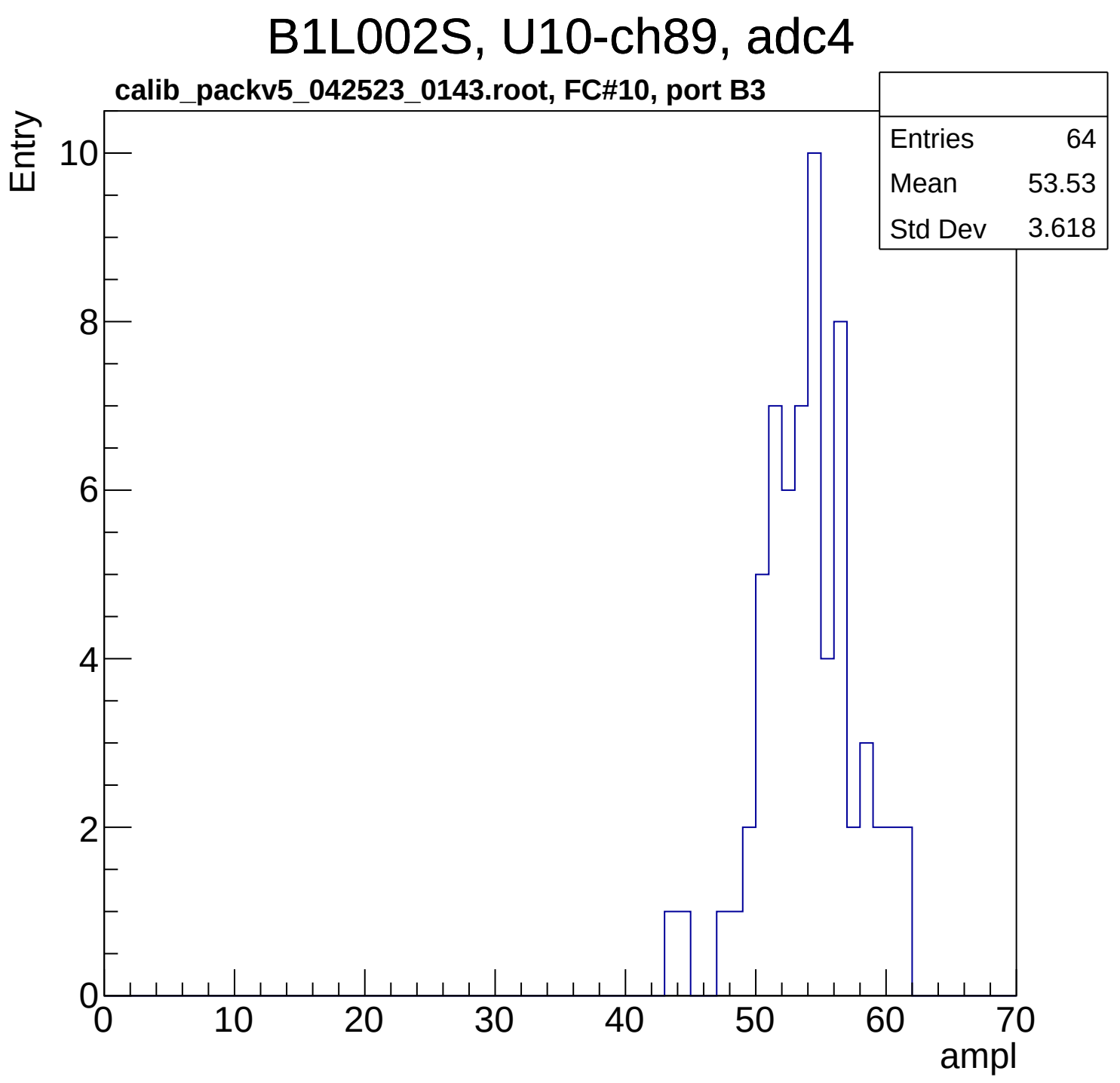
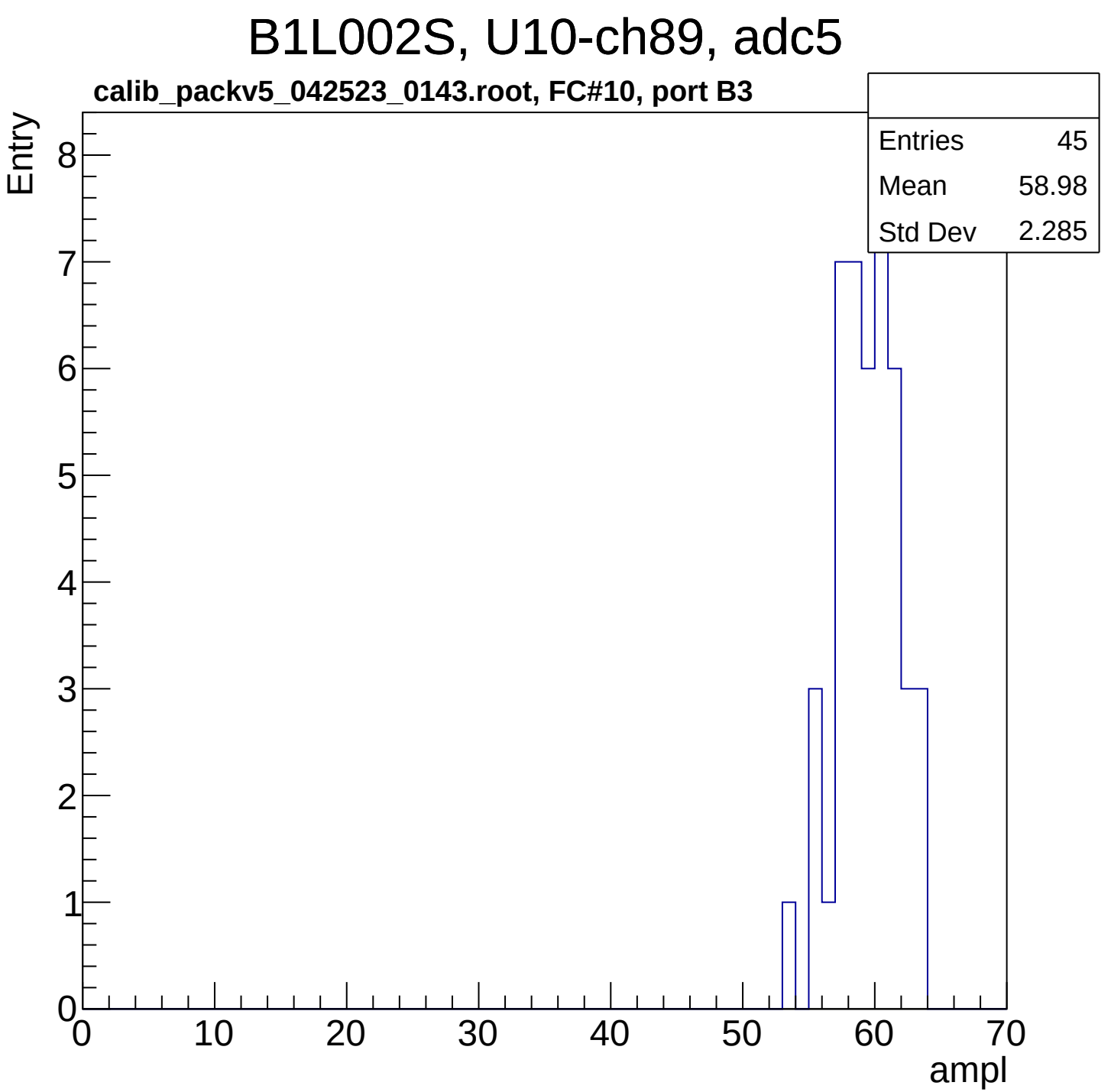

B1L002S, U10-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.53
Std Dev 3.618
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.98
Std Dev 2.285
Entry
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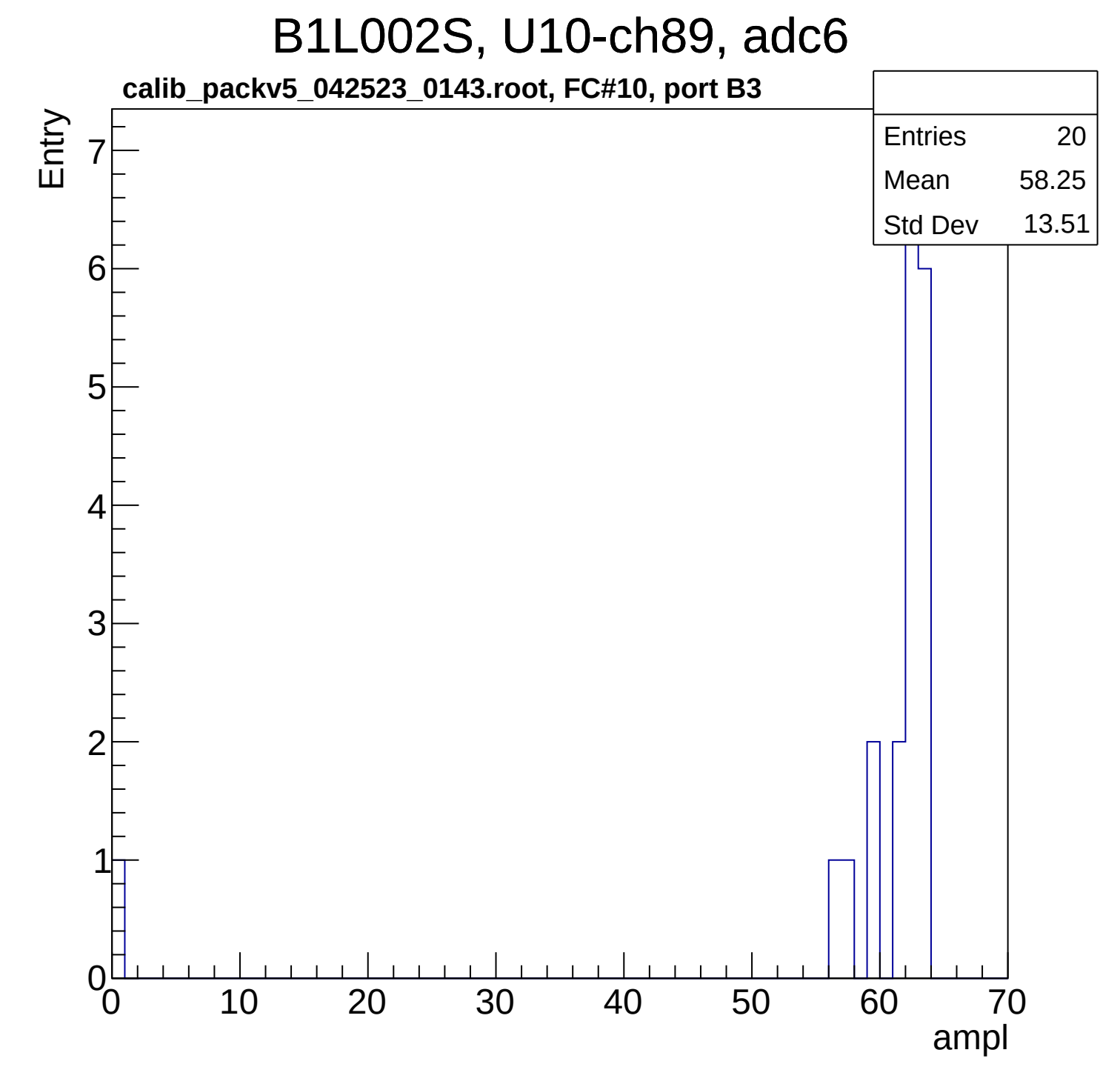
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.25
Std Dev 13.51
Entry
ampl
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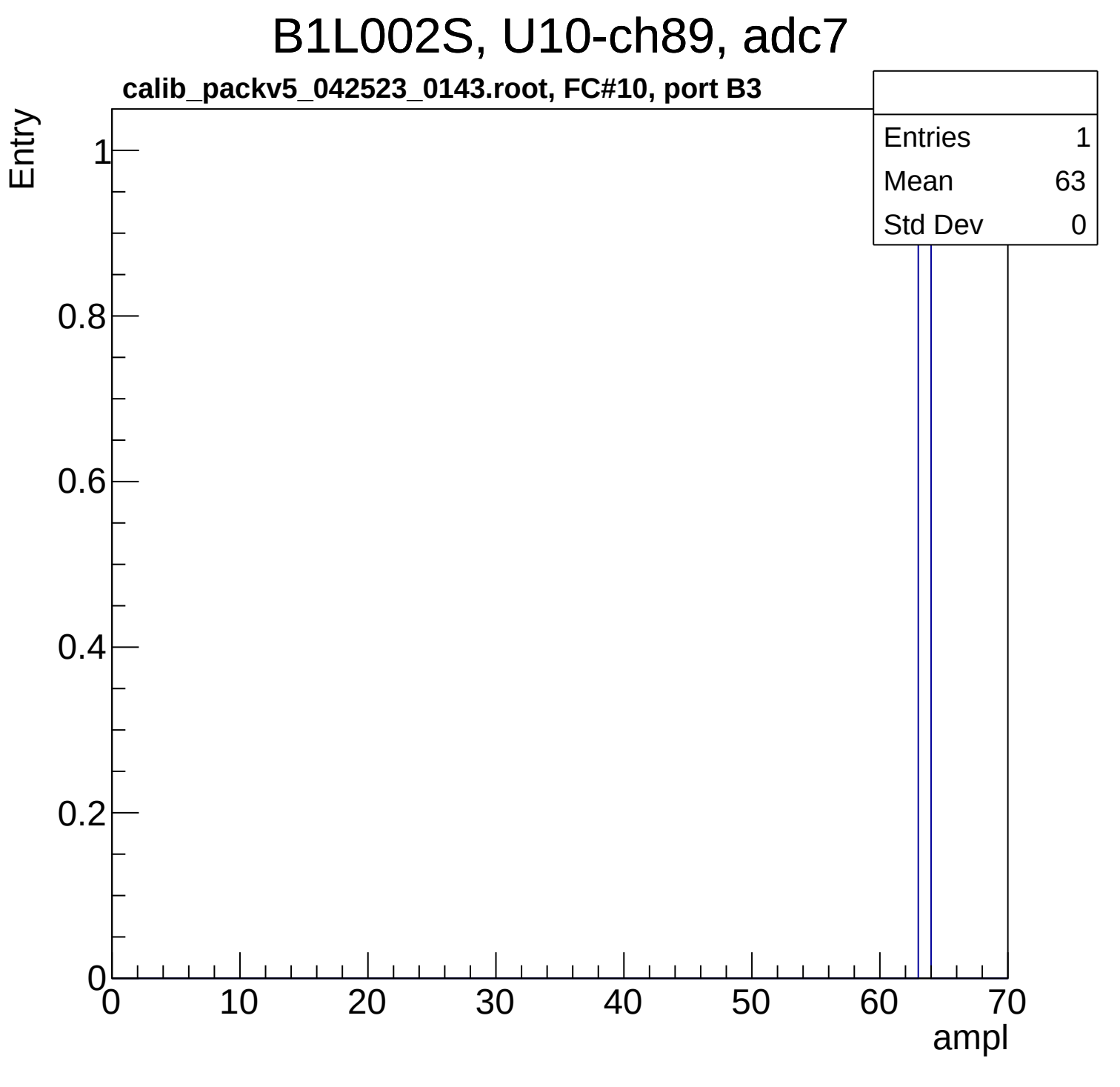
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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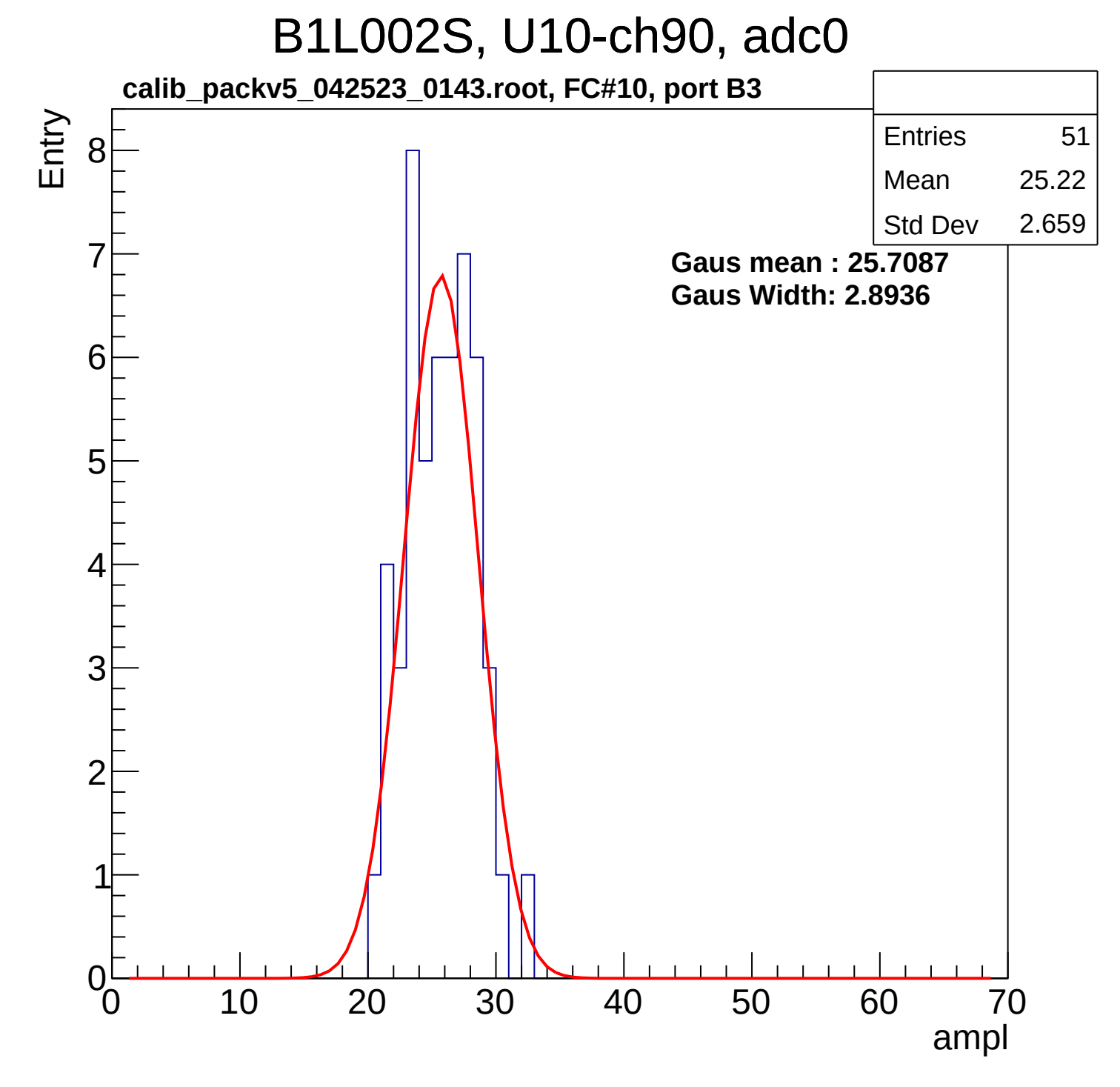
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 25.22
Std Dev 2.659
Gaus mean : 25.7087
Gaus Width: 2.8936
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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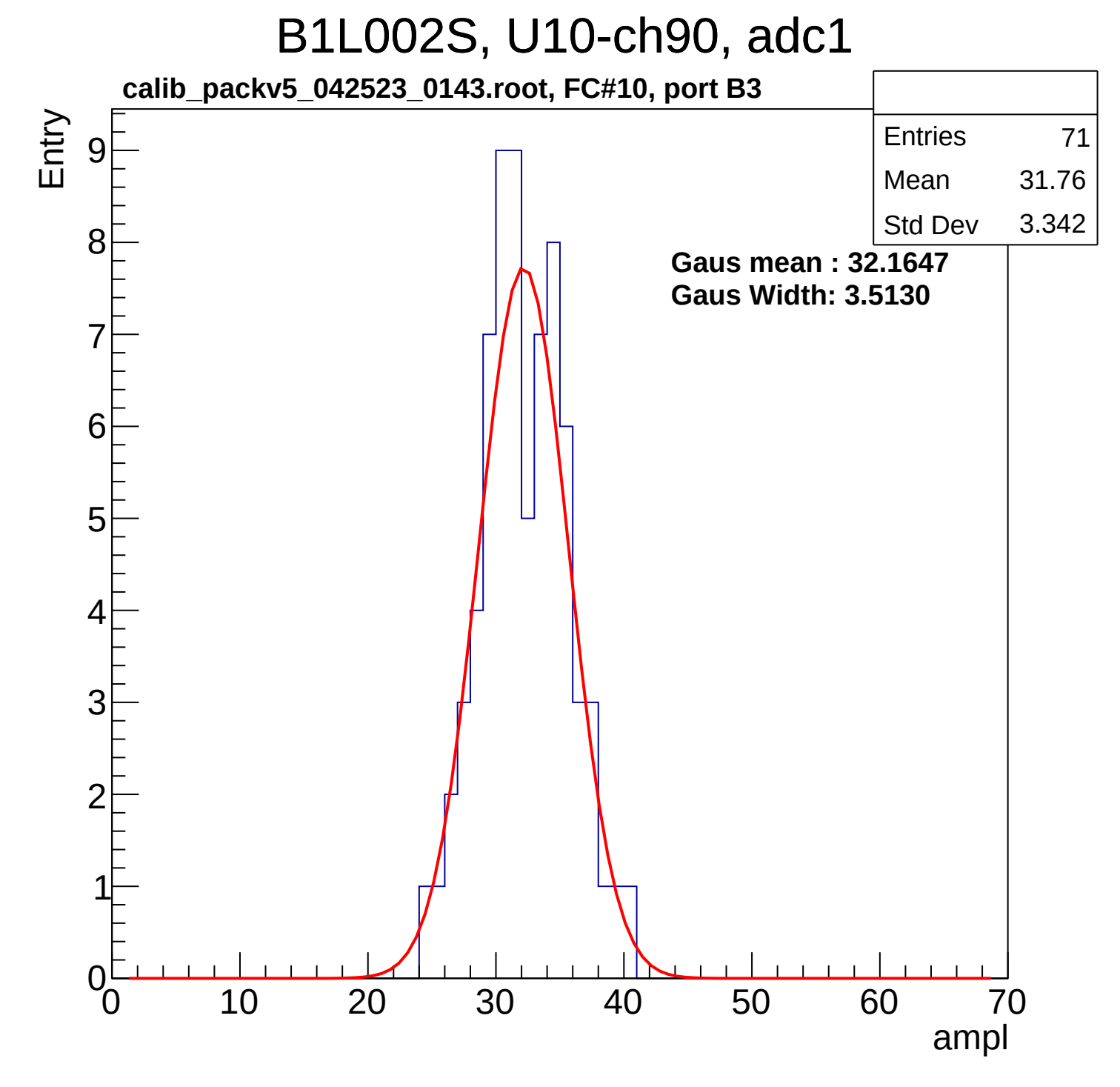
B1L002S, U10-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 31.76
Std Dev 3.342
Gaus mean : 32.1647
Gaus Width: 3.5130
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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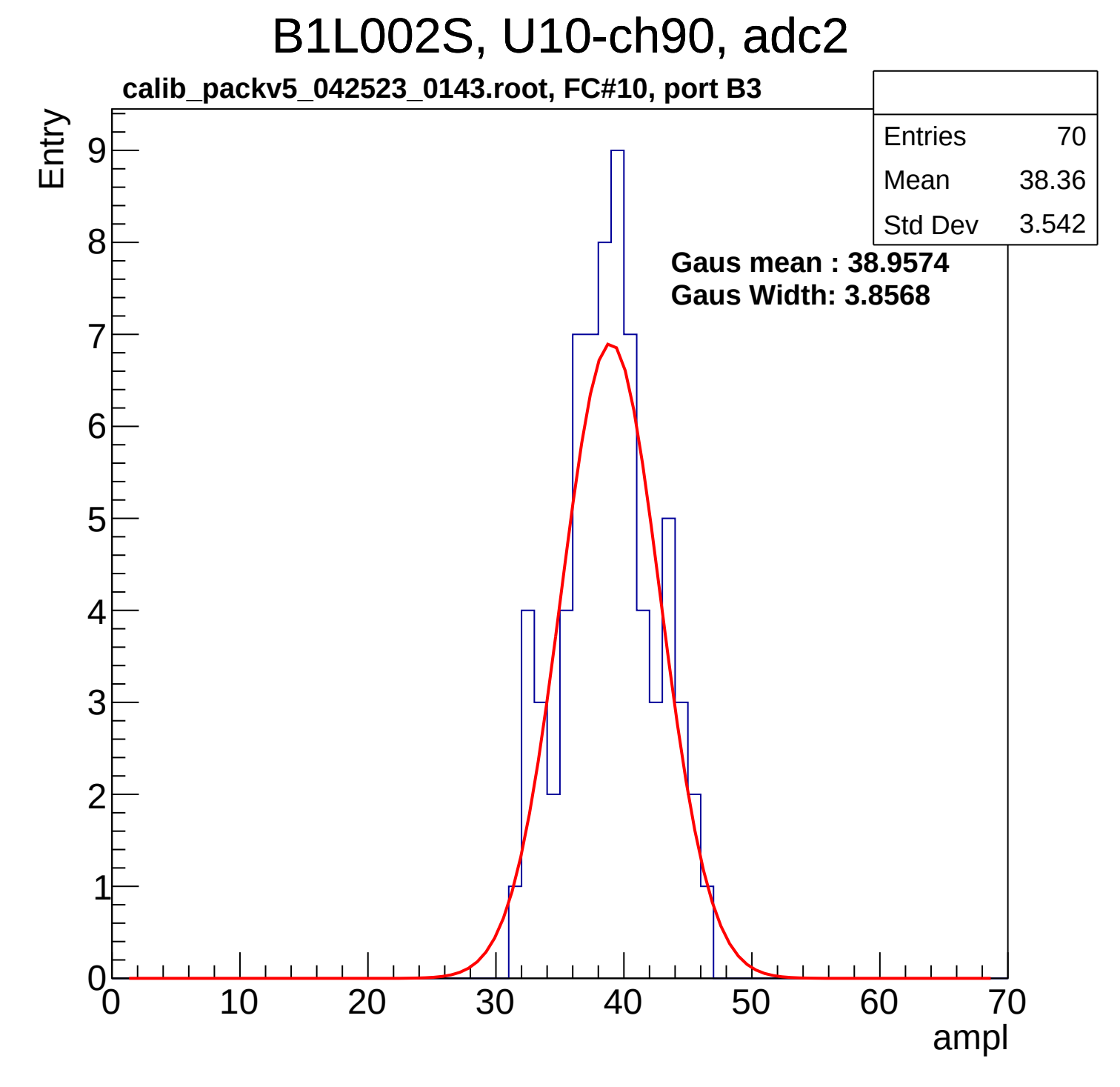
B1L002S, U10-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 38.36
Std Dev 3.542
Gaus mean : 38.9574
Gaus Width: 3.8568
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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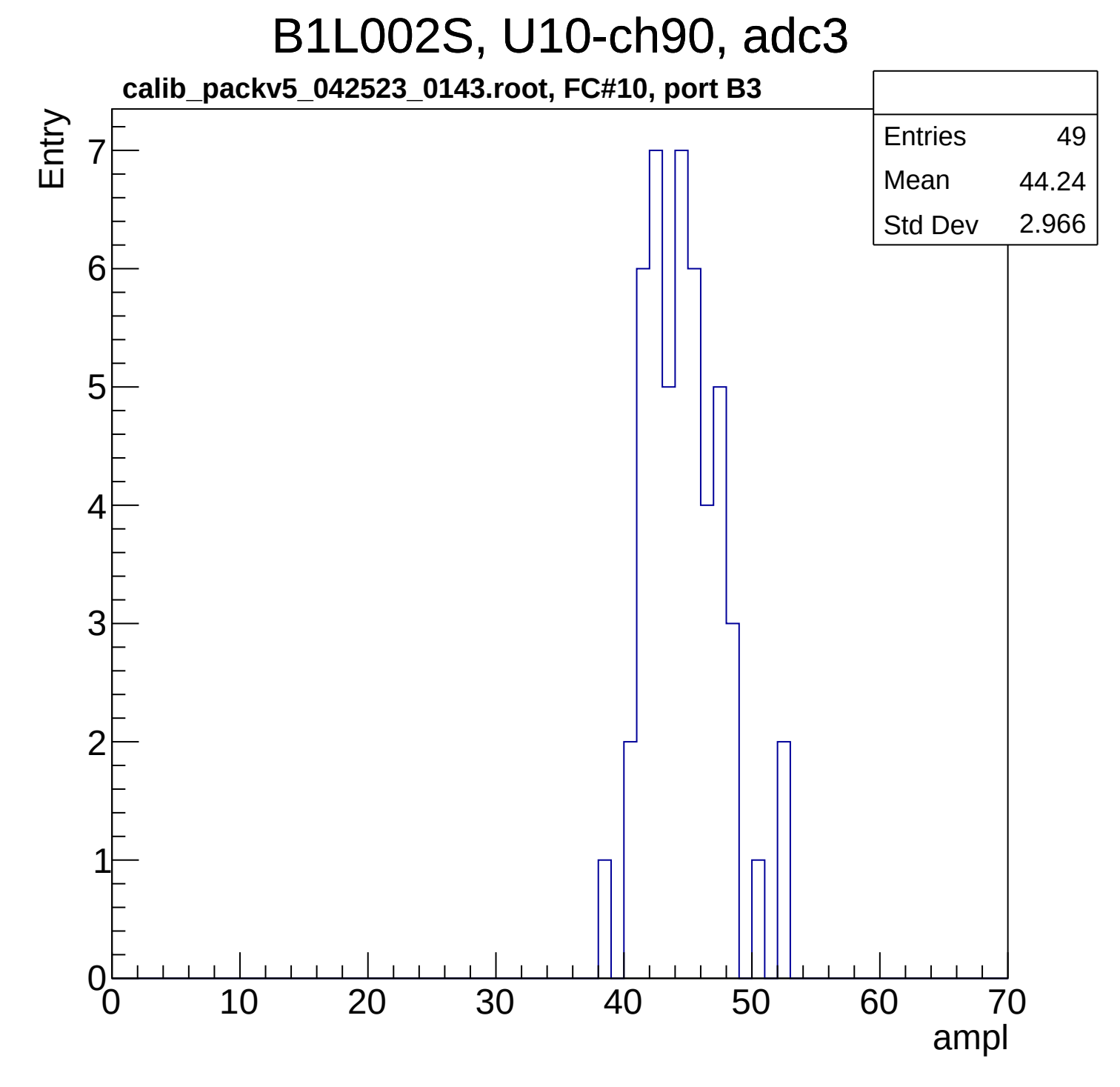
B1L002S, U10-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 44.24
Std Dev 2.966
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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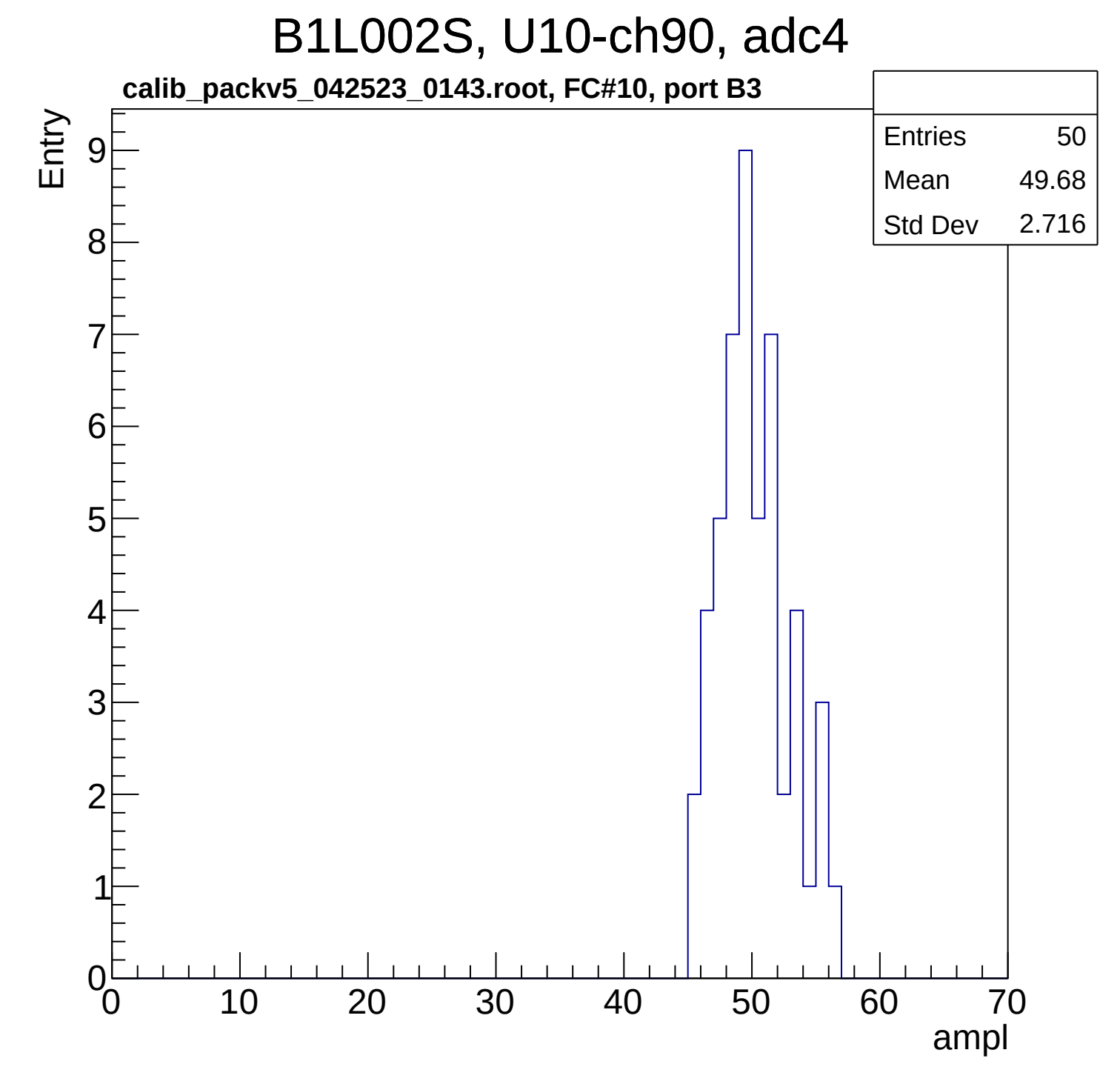
B1L002S, U10-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 49.68
Std Dev 2.716
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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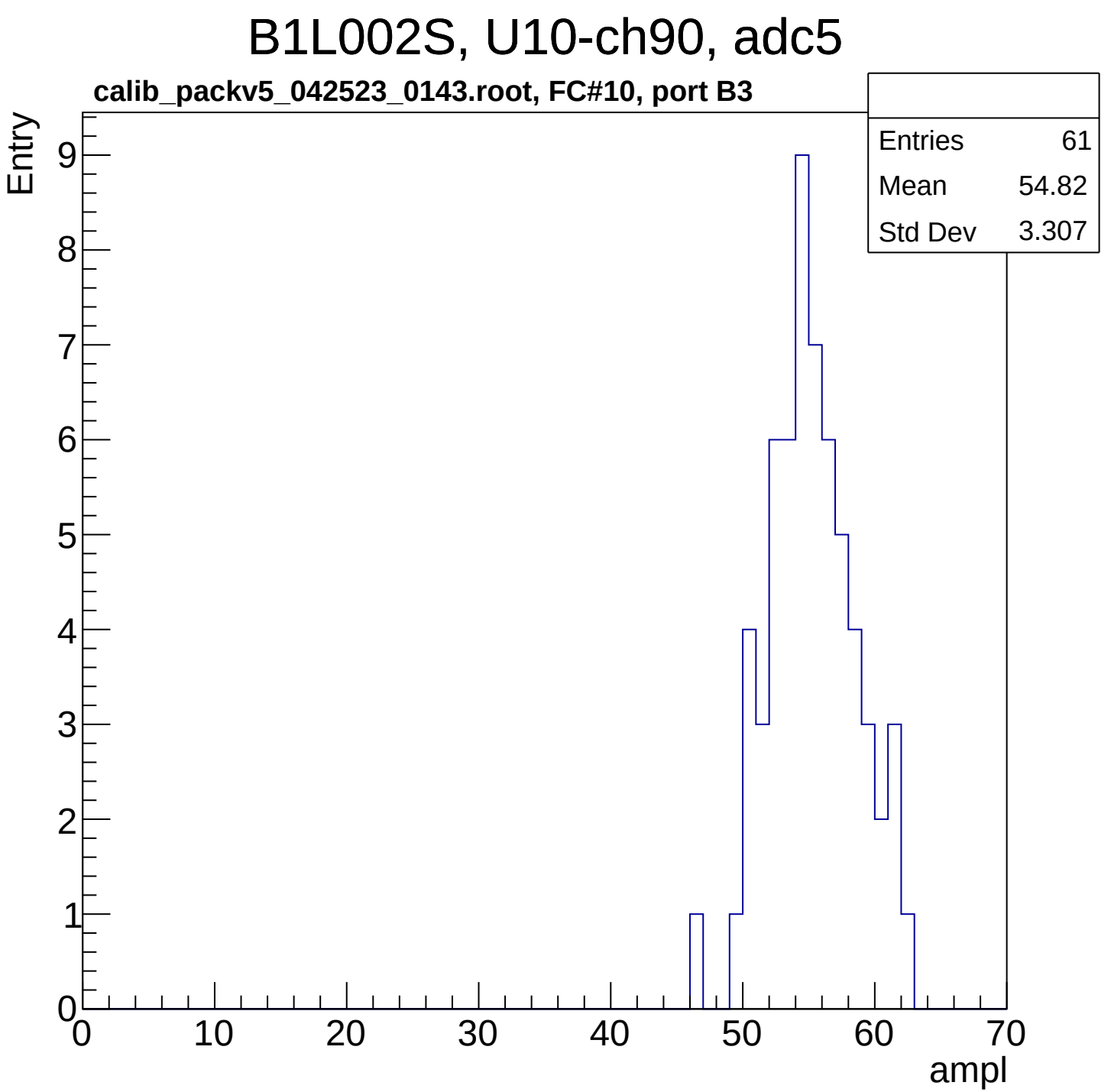
B1L002S, U10-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.82
Std Dev 3.307
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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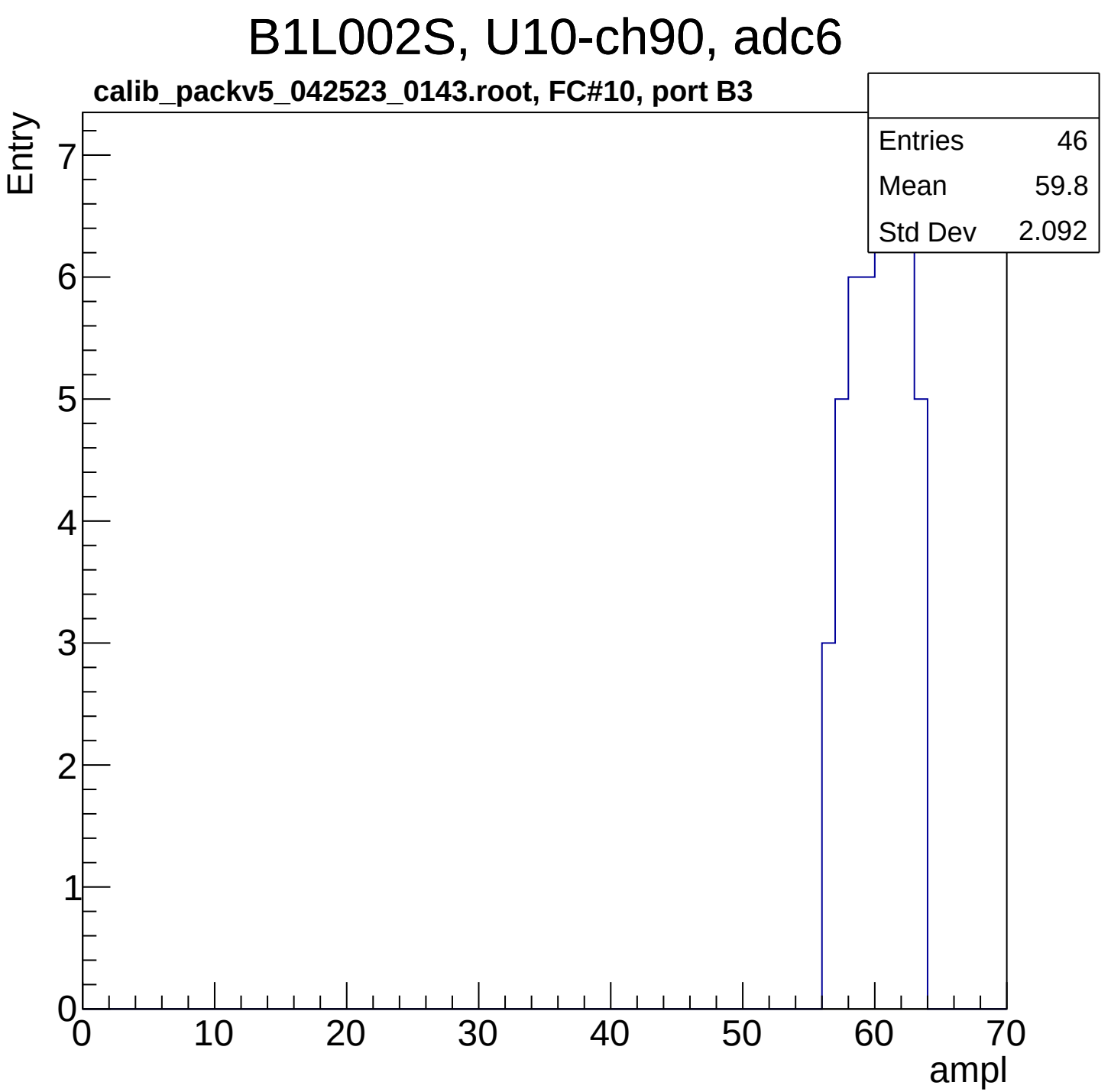
B1L002S, U10-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 59.8
Std Dev 2.092
Entry
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1
2
3
4
5
6
7
0
10
20
30
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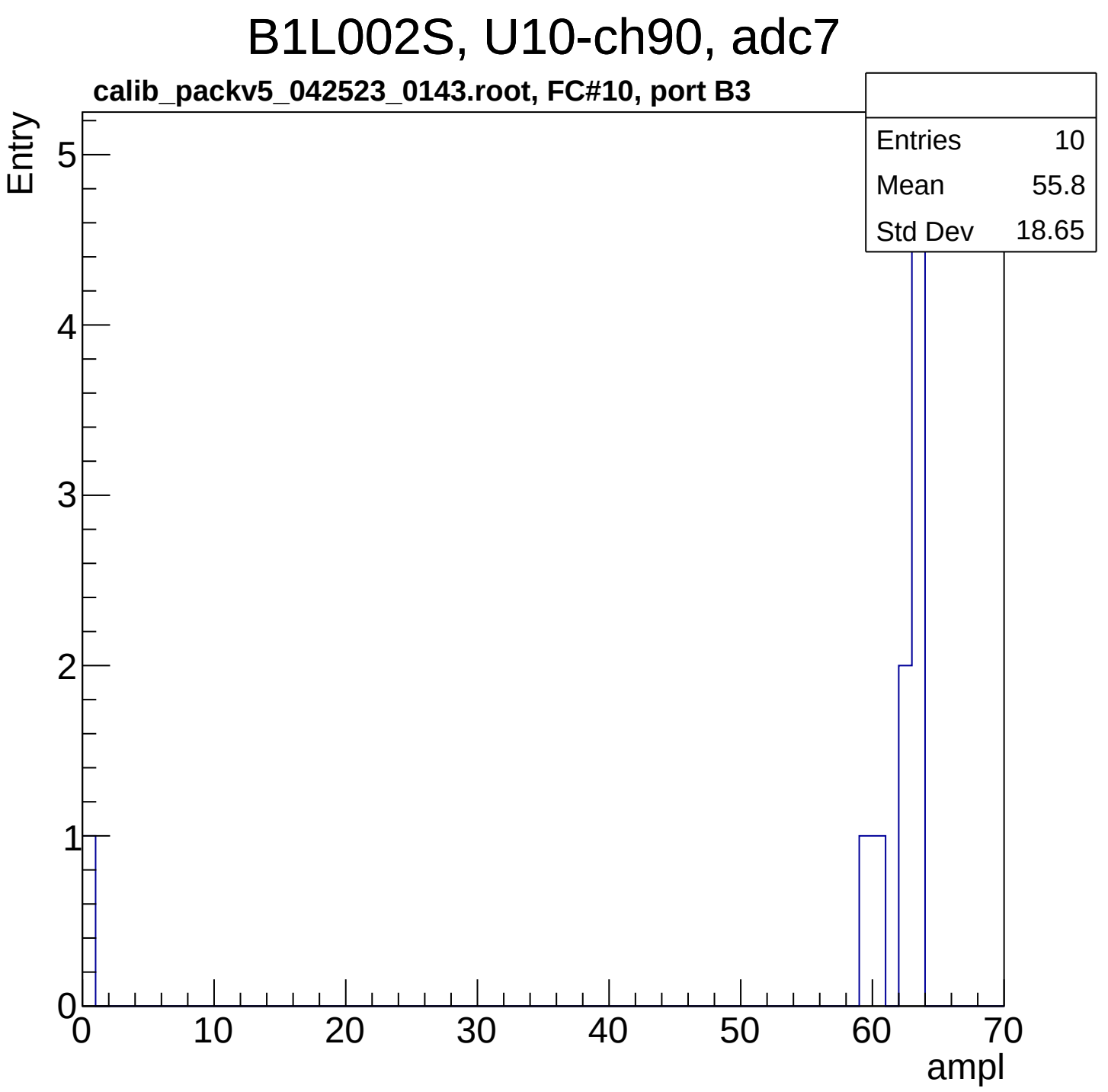
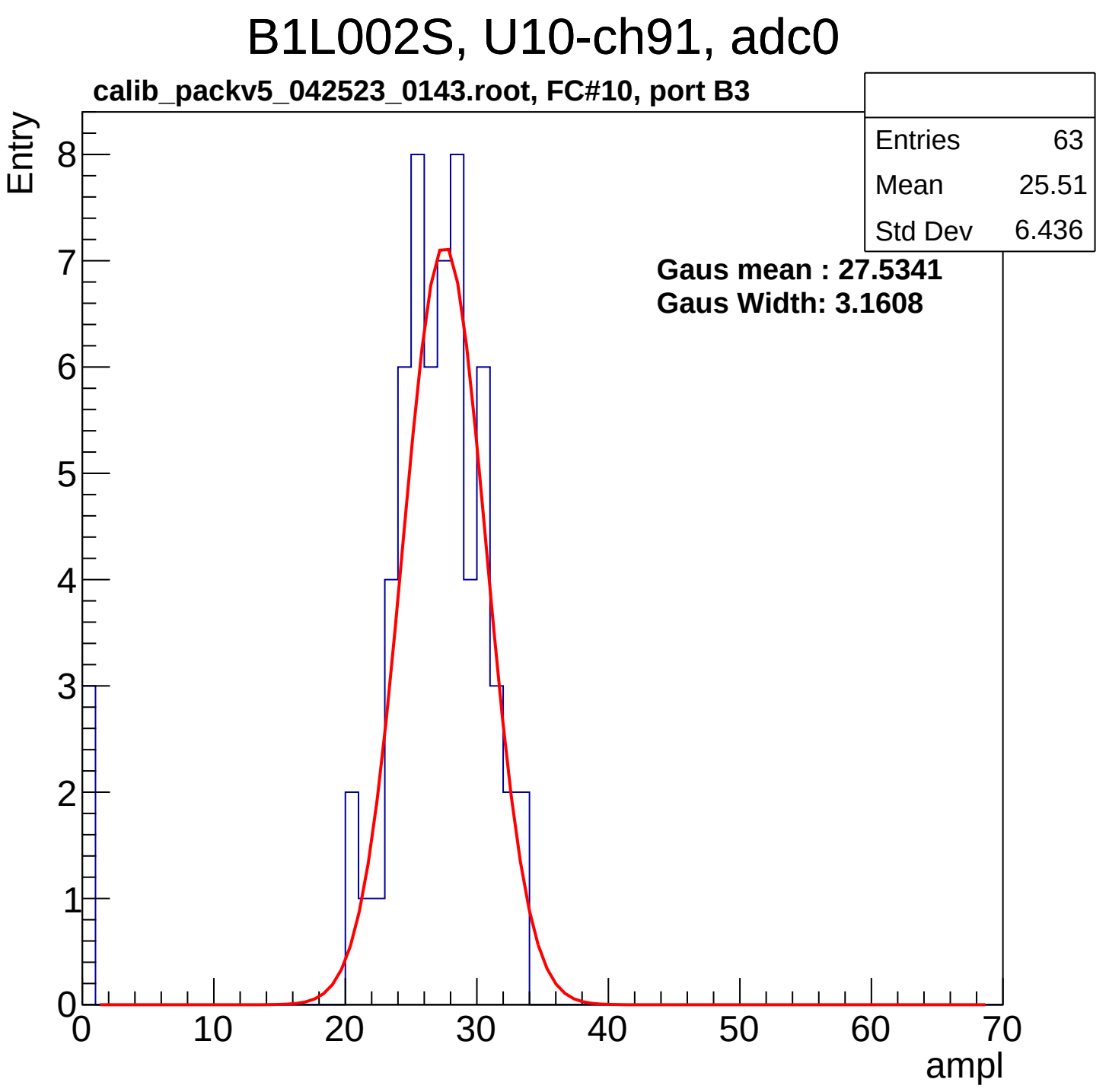
50
60
70
ampl


B1L002S, U10-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.65
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 25.51
Std Dev 6.436
Gaus mean : 27.5341
Gaus Width: 3.1608
Entry
ampl
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3
4
5
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7
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0
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20
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40
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60
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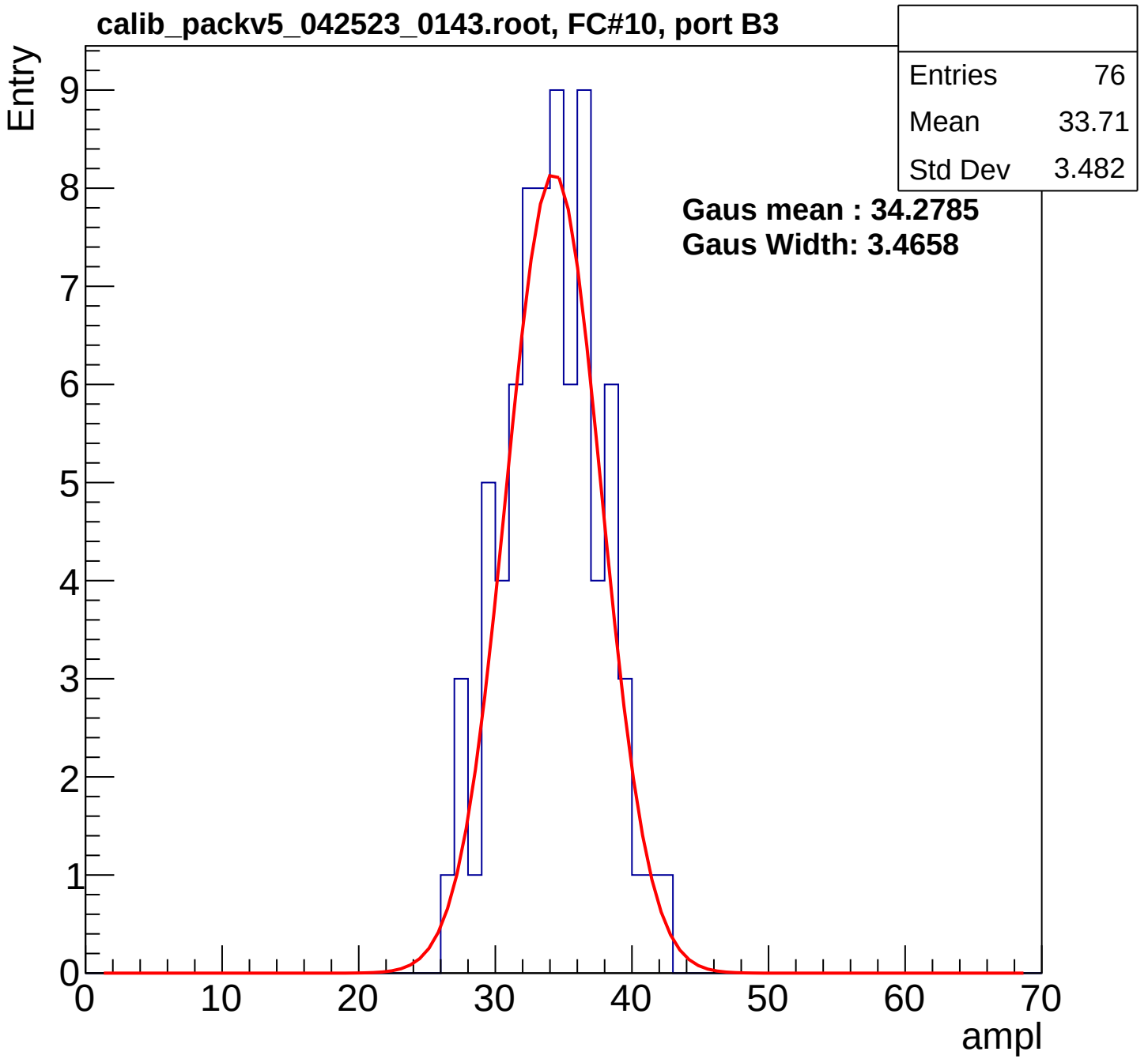
B1L002S, U10-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.71
Std Dev 3.482
Gaus mean : 34.2785
Gaus Width: 3.4658
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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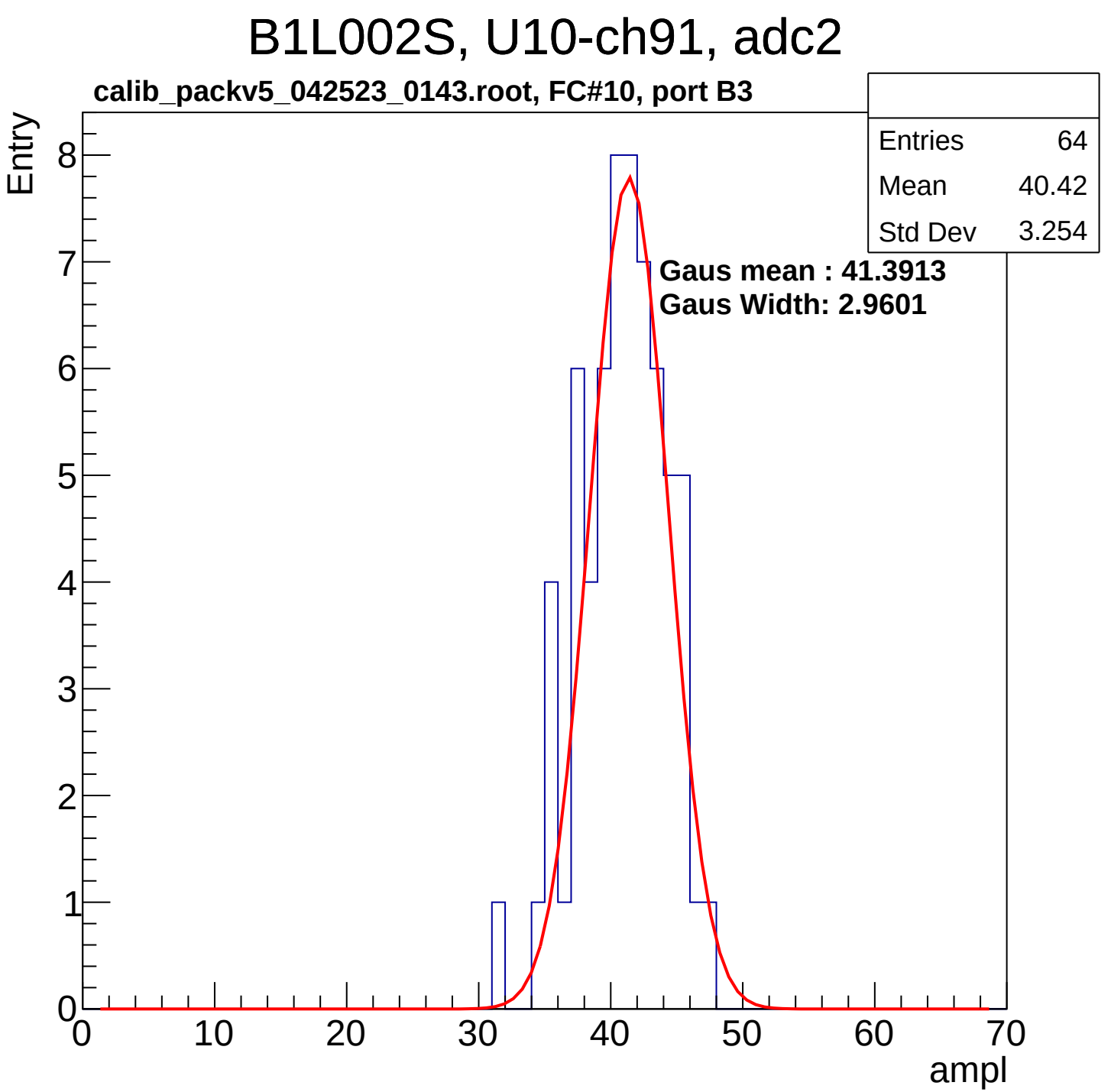
B1L002S, U10-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.42
Std Dev 3.254
Gaus mean : 41.3913
Gaus Width: 2.9601
Entry
ampl
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2
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4
5
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0
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40
50
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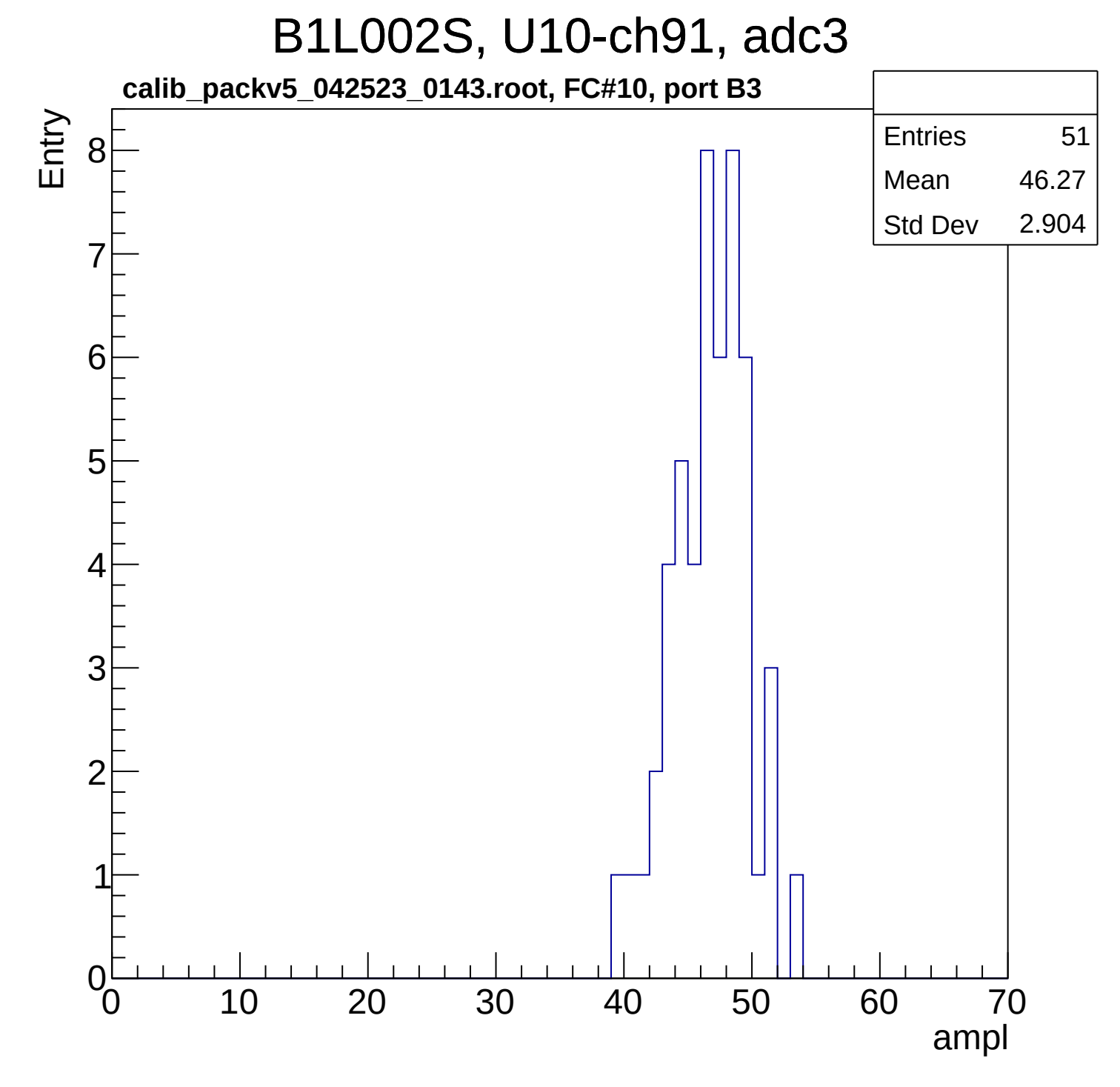
B1L002S, U10-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.27
Std Dev 2.904
Entry
ampl
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2
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4
5
6
7
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0
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40
50
60
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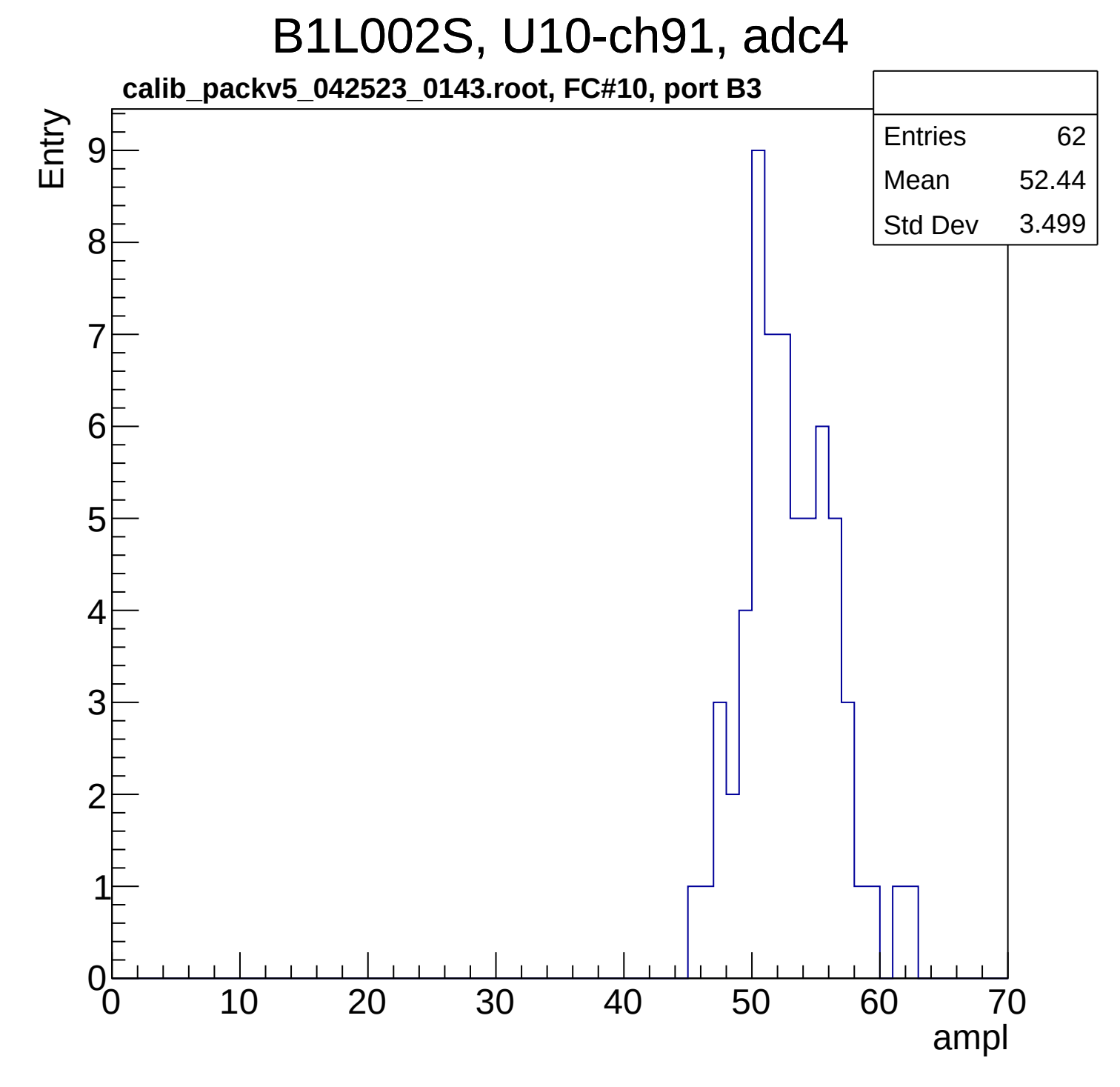
B1L002S, U10-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.44
Std Dev 3.499
Entry
ampl
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4
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6
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0
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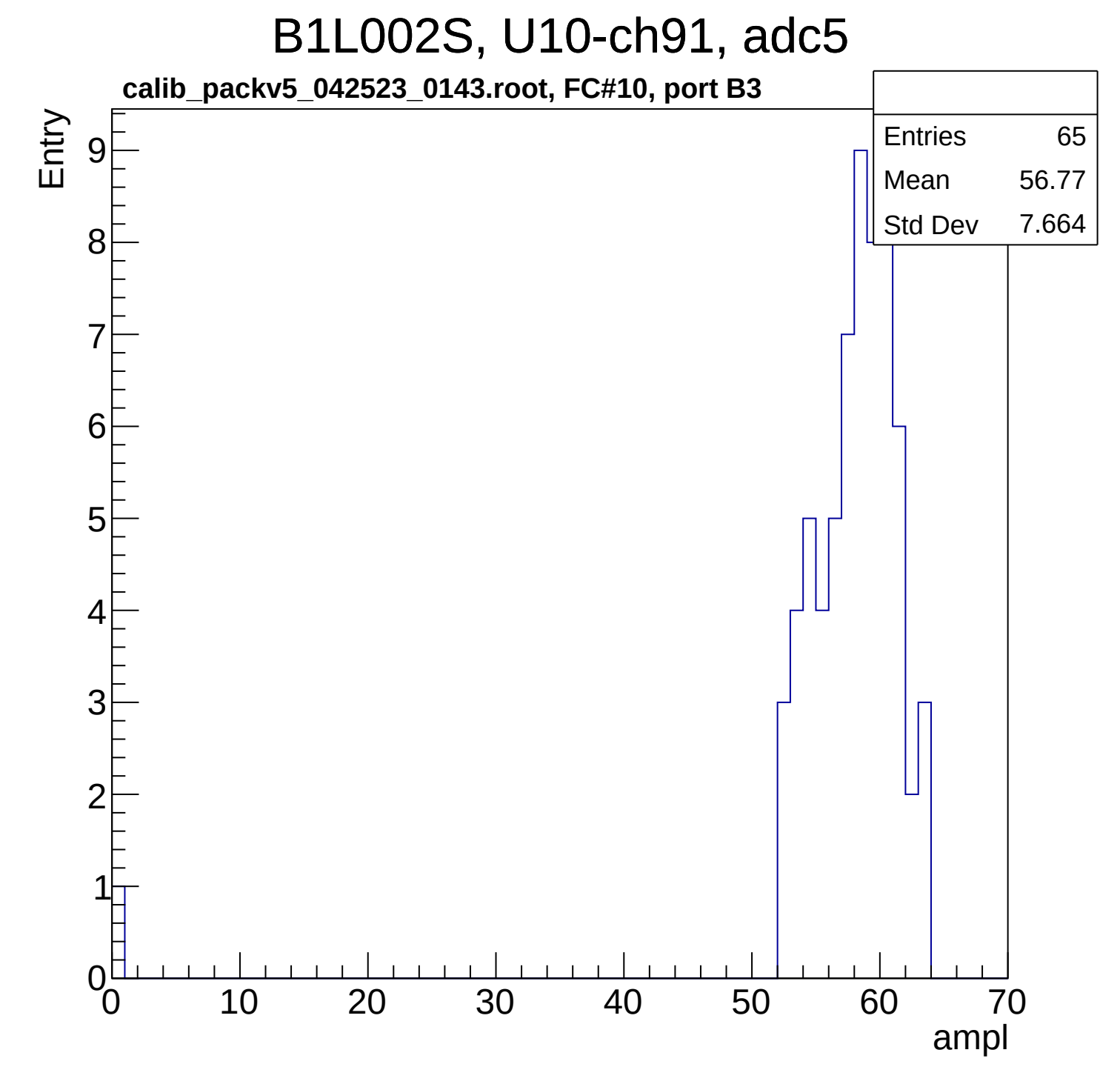
B1L002S, U10-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 56.77
Std Dev 7.664
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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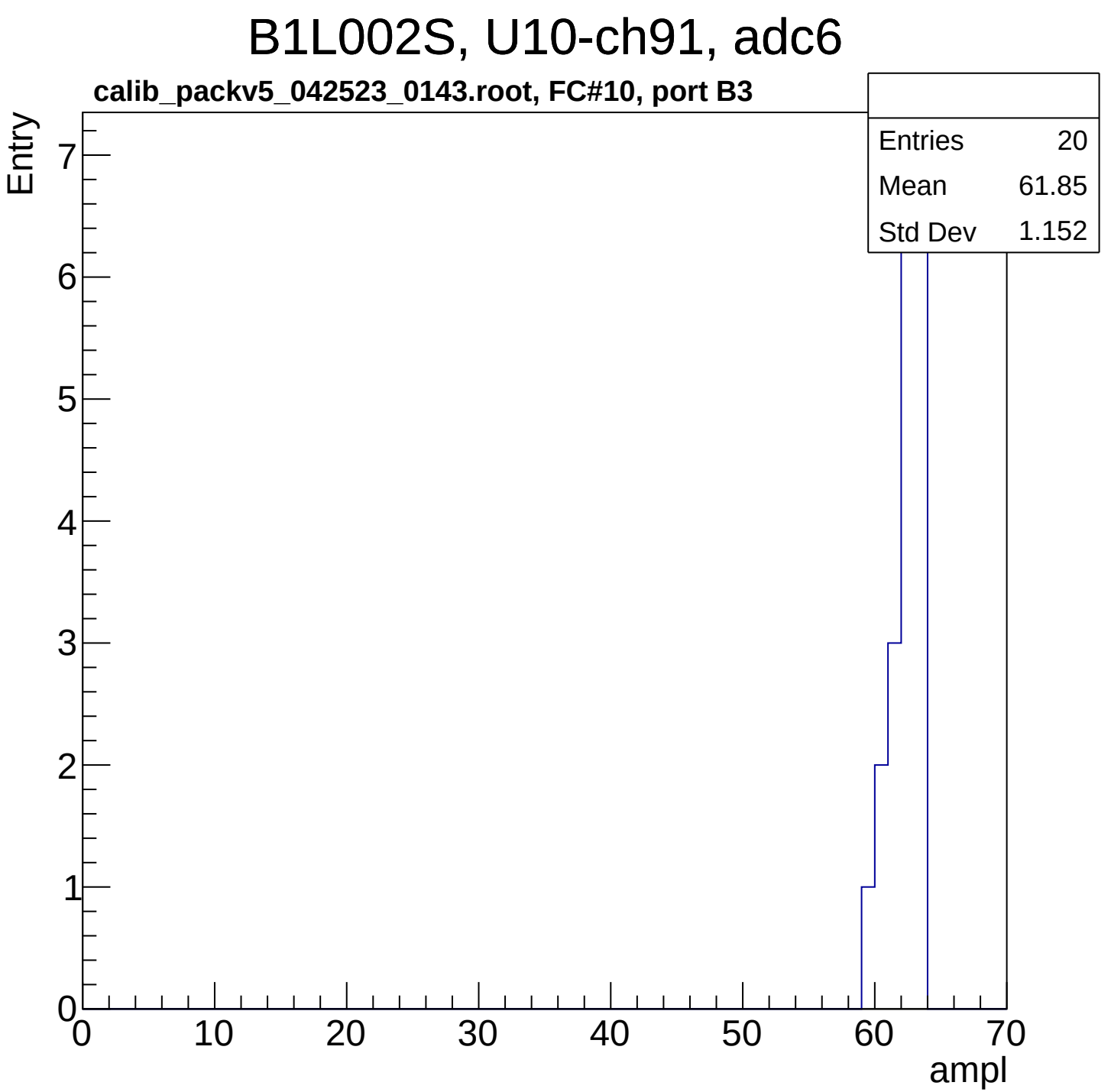
B1L002S, U10-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.85
Std Dev 1.152
Entry
ampl
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3
4
5
6
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0
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60
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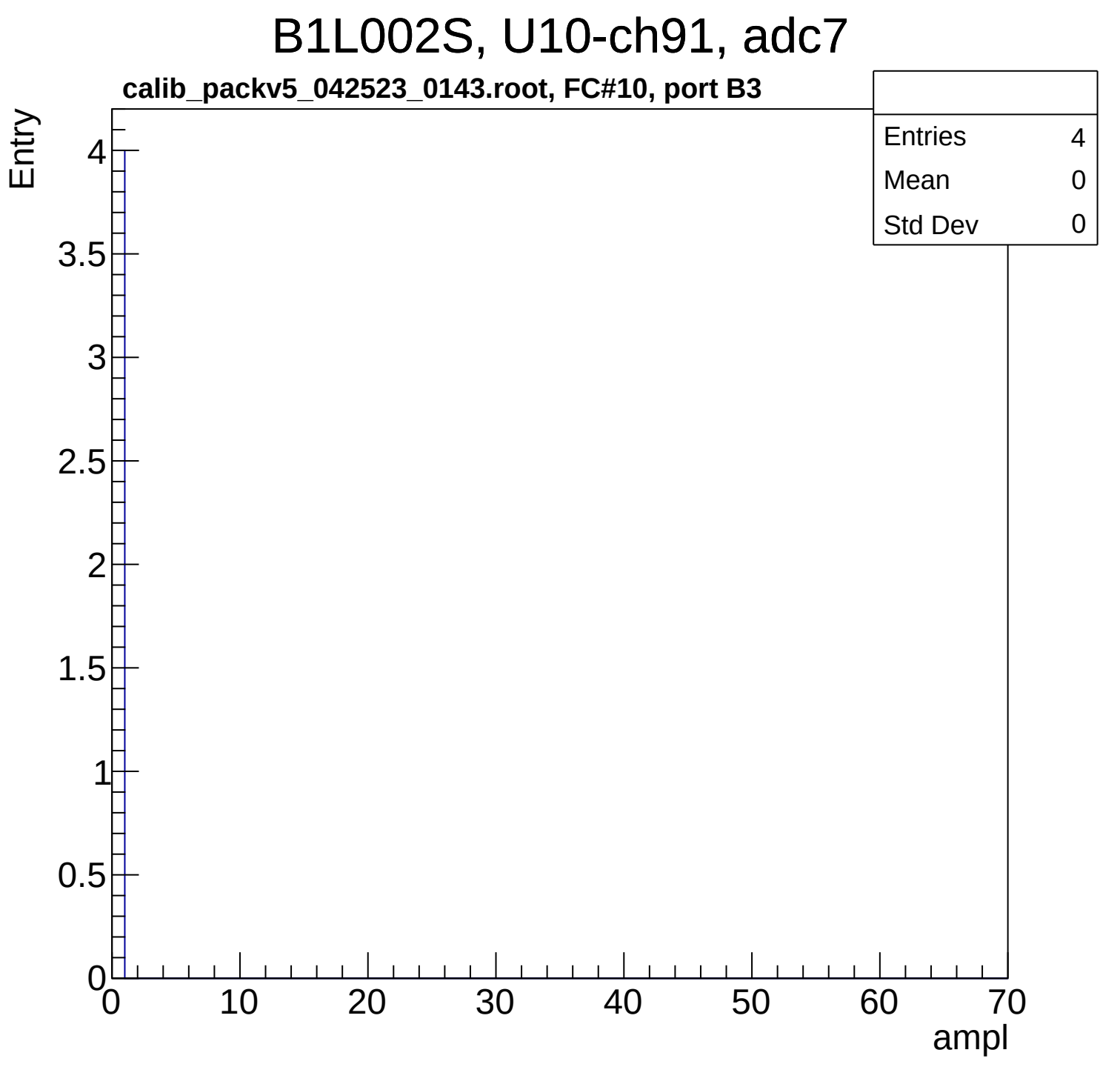
B1L002S, U10-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 0
Std Dev 0
Entry
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0.5
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2
2.5
3
3.5
4
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10
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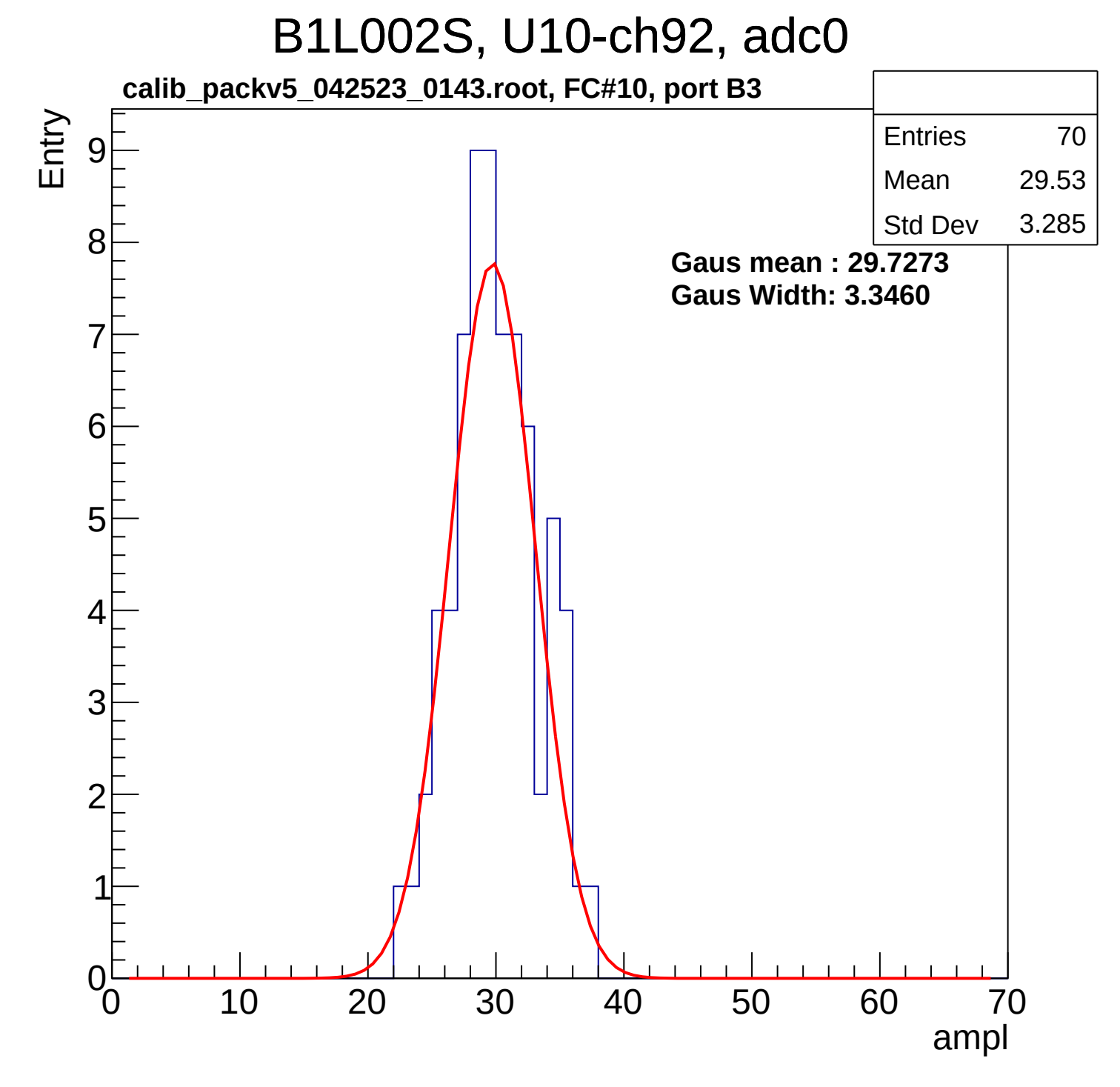
50
60
70
ampl


B1L002S, U10-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.53
Std Dev 3.285
Gaus mean : 29.7273
Gaus Width: 3.3460
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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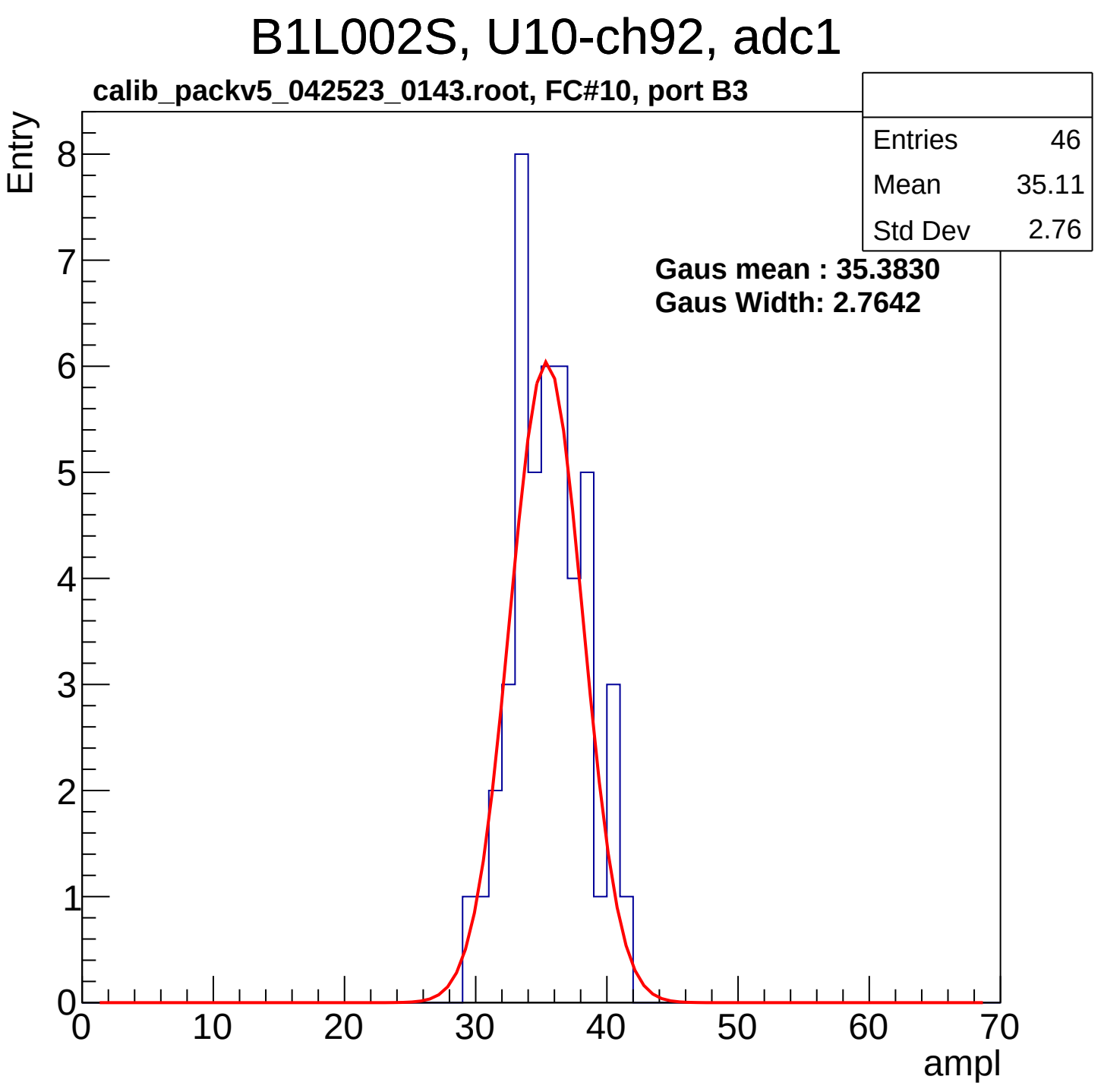
B1L002S, U10-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 35.11
Std Dev 2.76
Gaus mean : 35.3830
Gaus Width: 2.7642
Entry
ampl
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2
3
4
5
6
7
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0
10
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60
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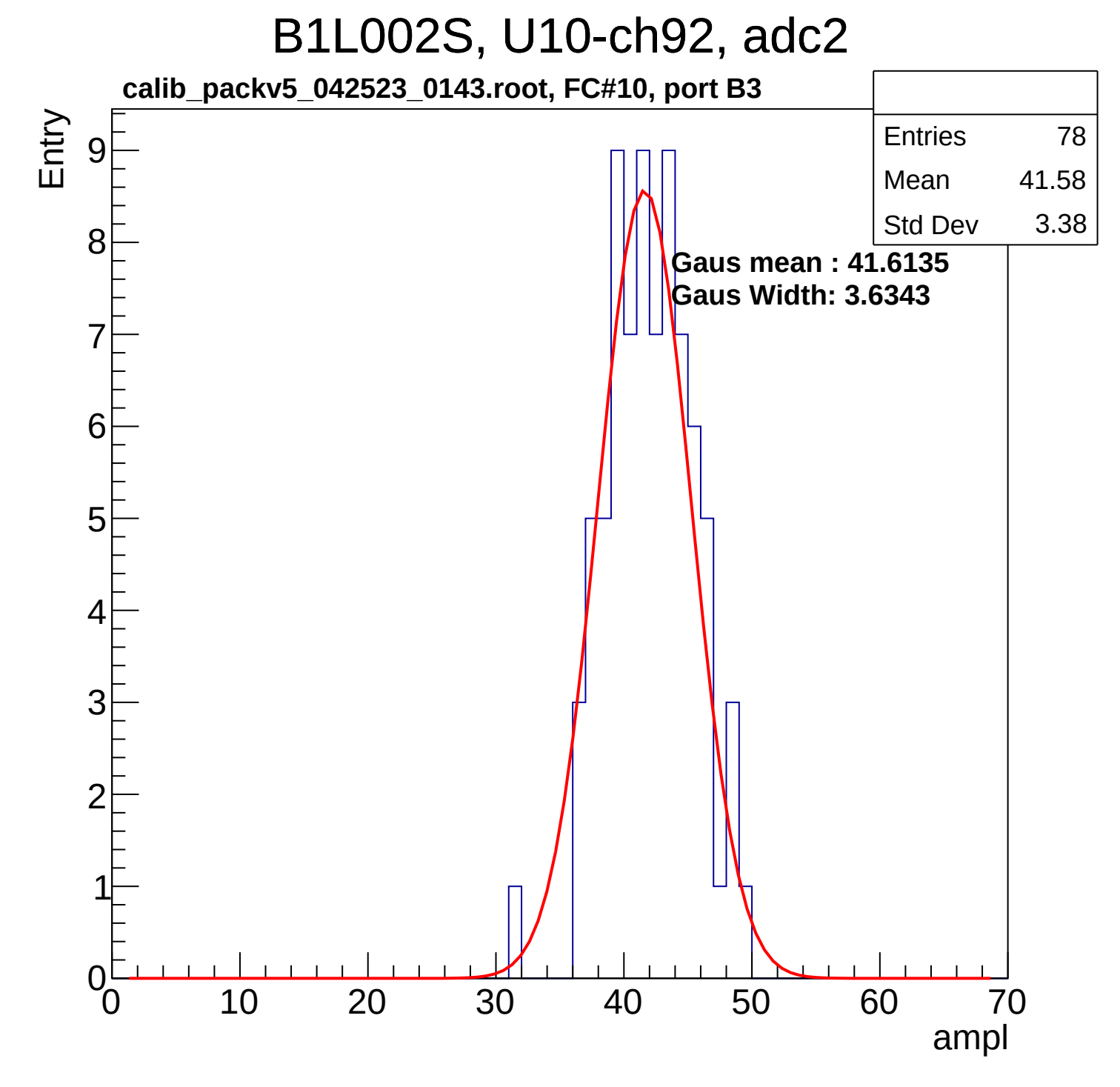
B1L002S, U10-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 41.58
Std Dev 3.38
Gaus mean : 41.6135
Gaus Width: 3.6343
Entry
ampl
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5
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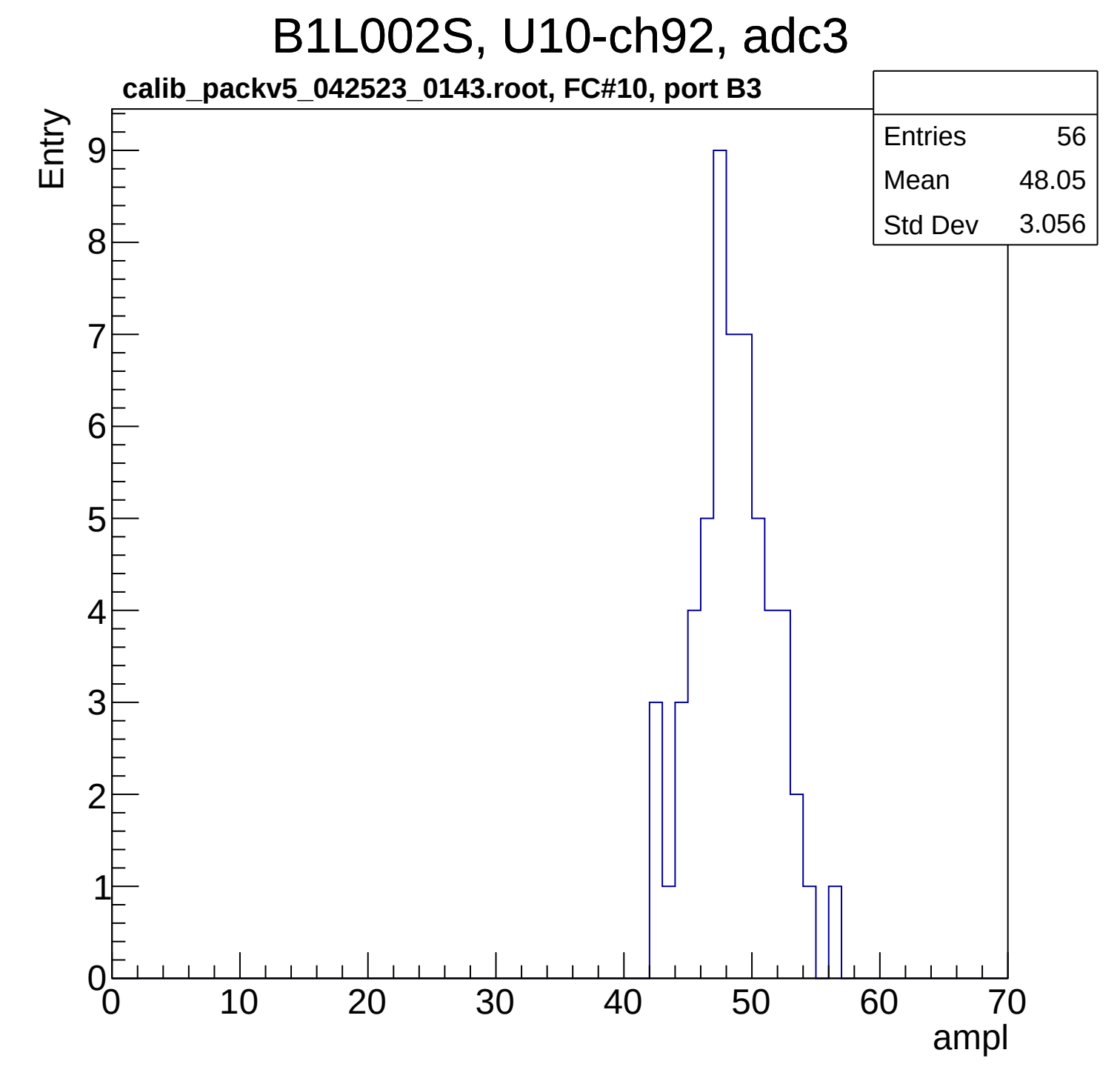
B1L002S, U10-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 48.05
Std Dev 3.056
Entry
ampl
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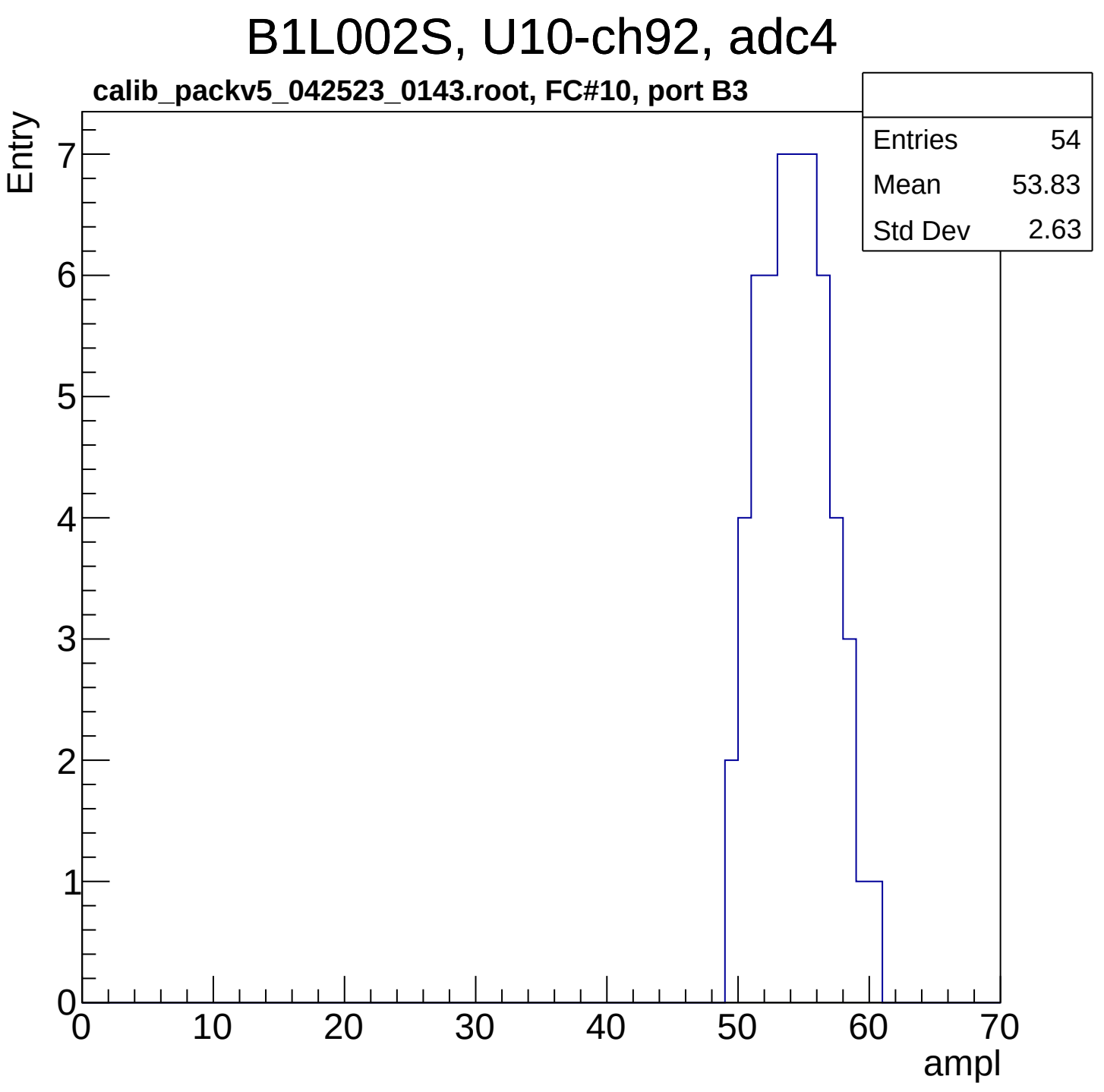
B1L002S, U10-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.83
Std Dev 2.63
Entry
ampl
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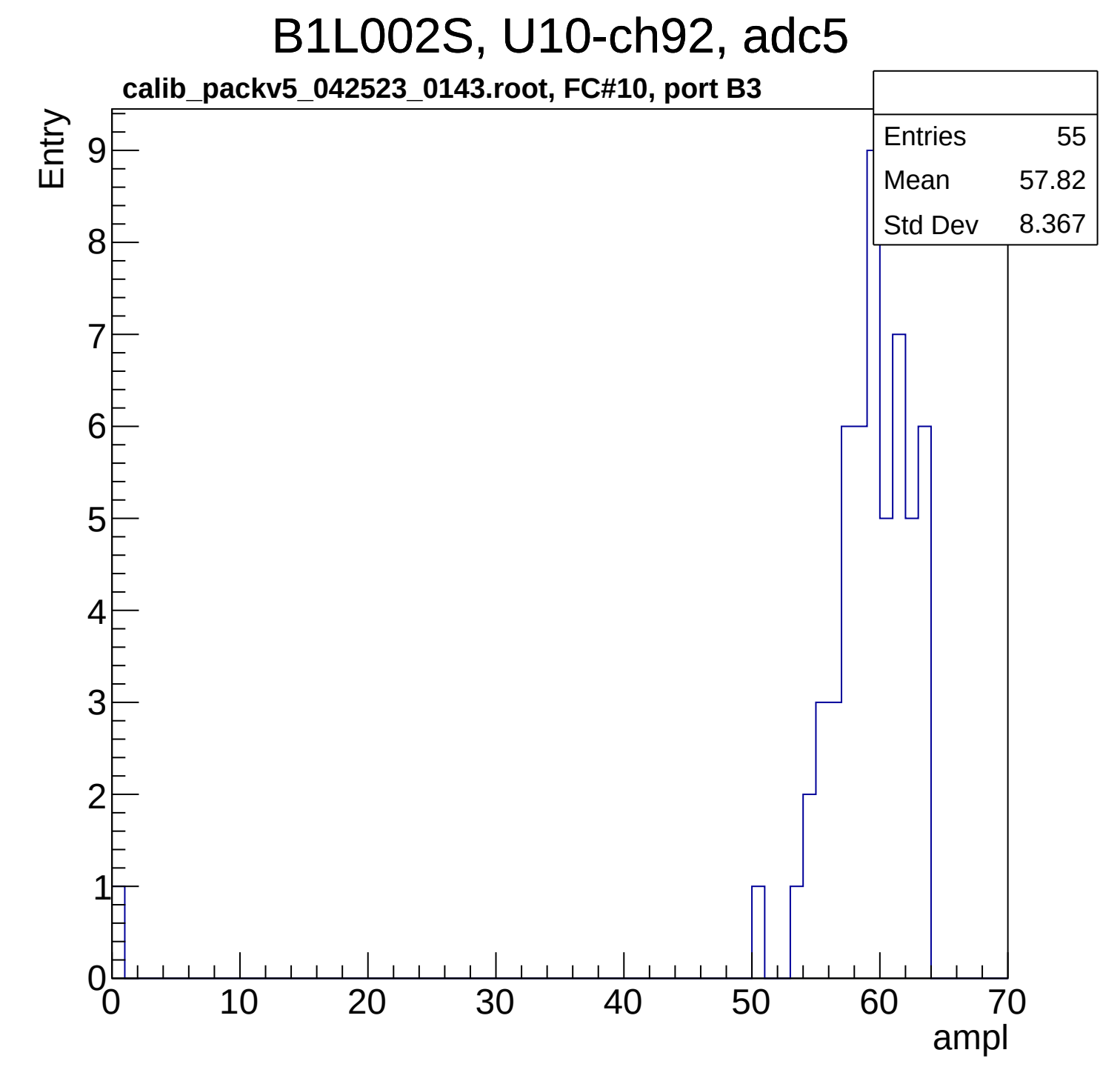
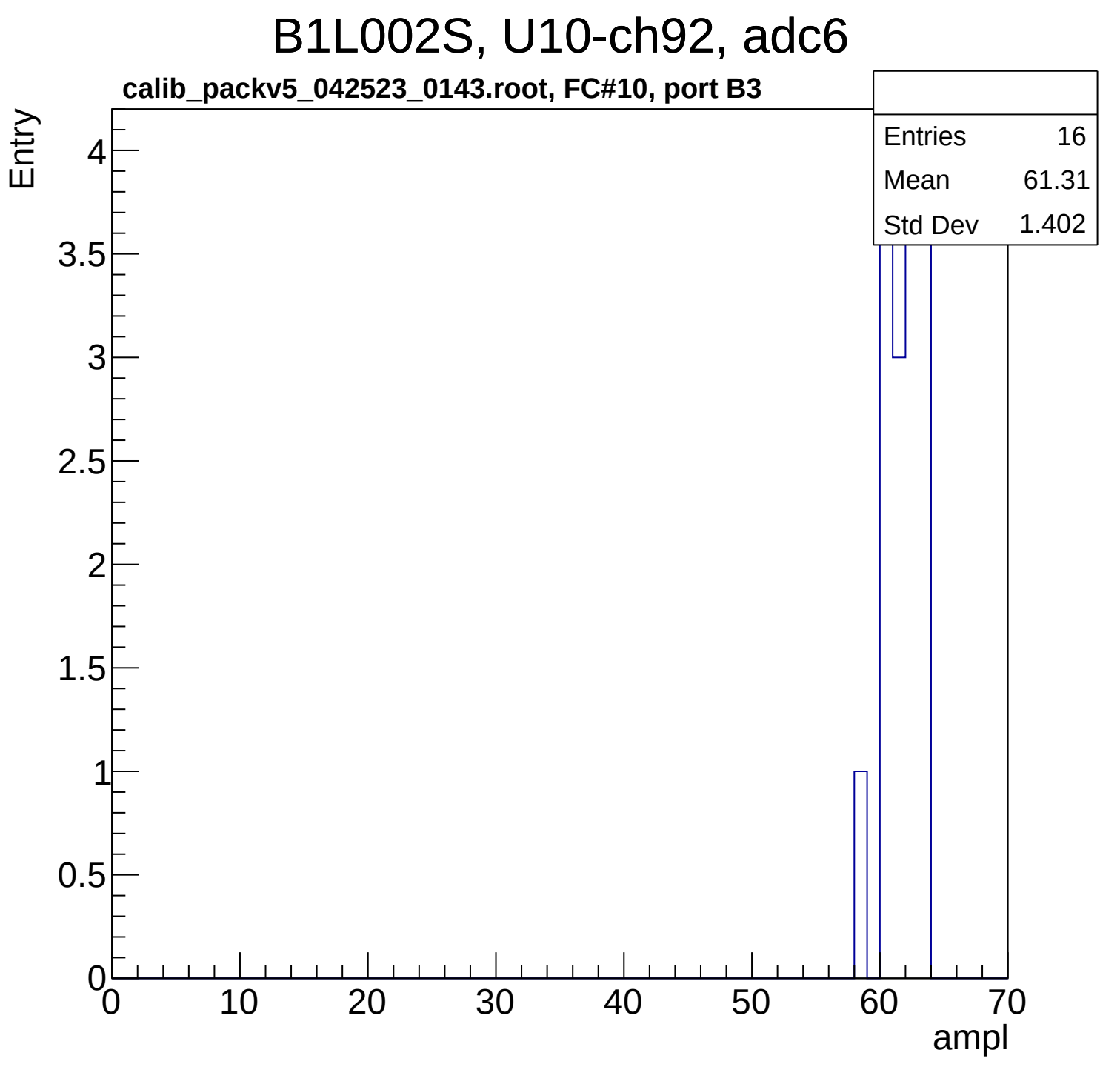

B1L002S, U10-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.82
Std Dev 8.367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.31
Std Dev 1.402
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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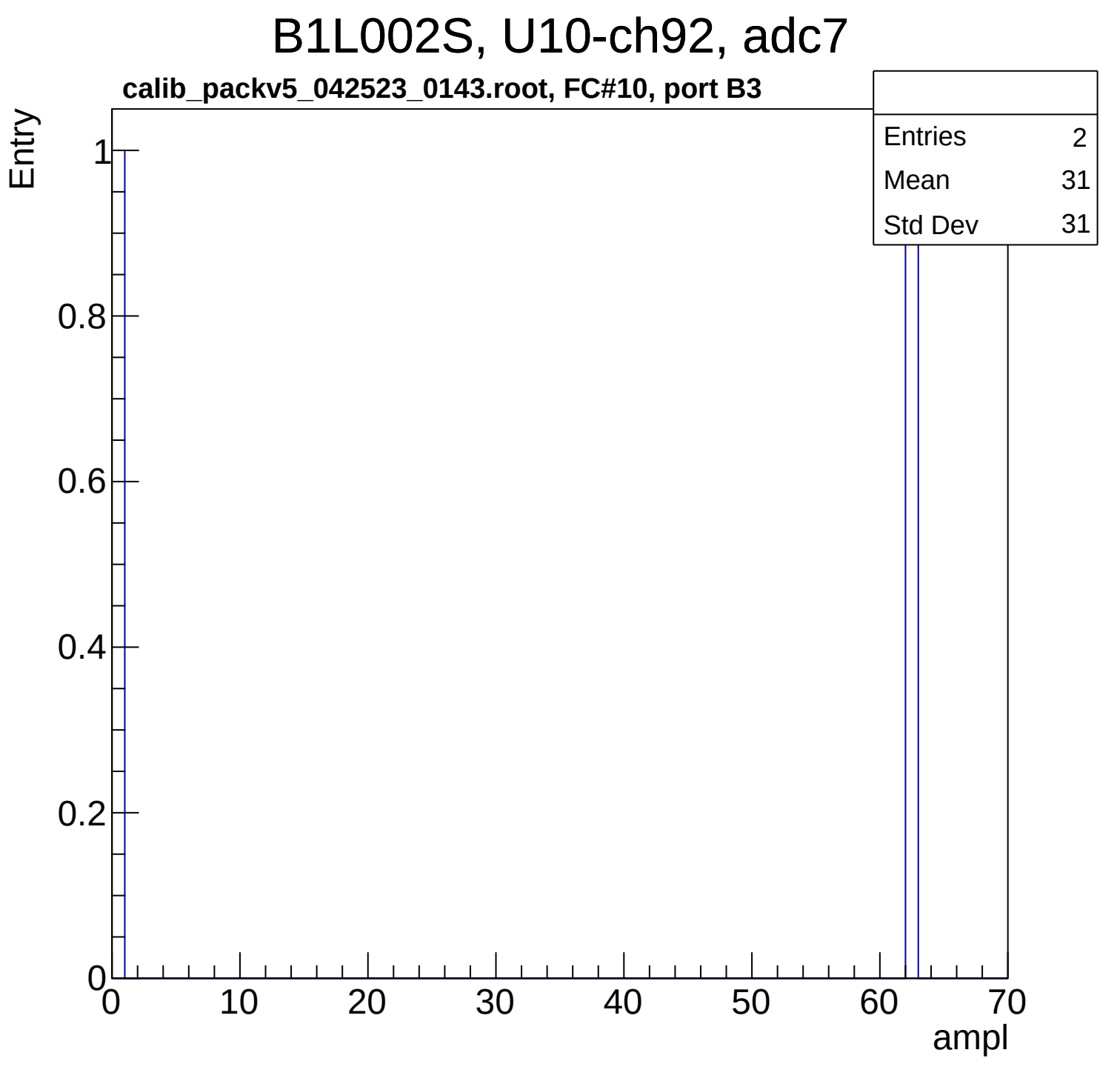
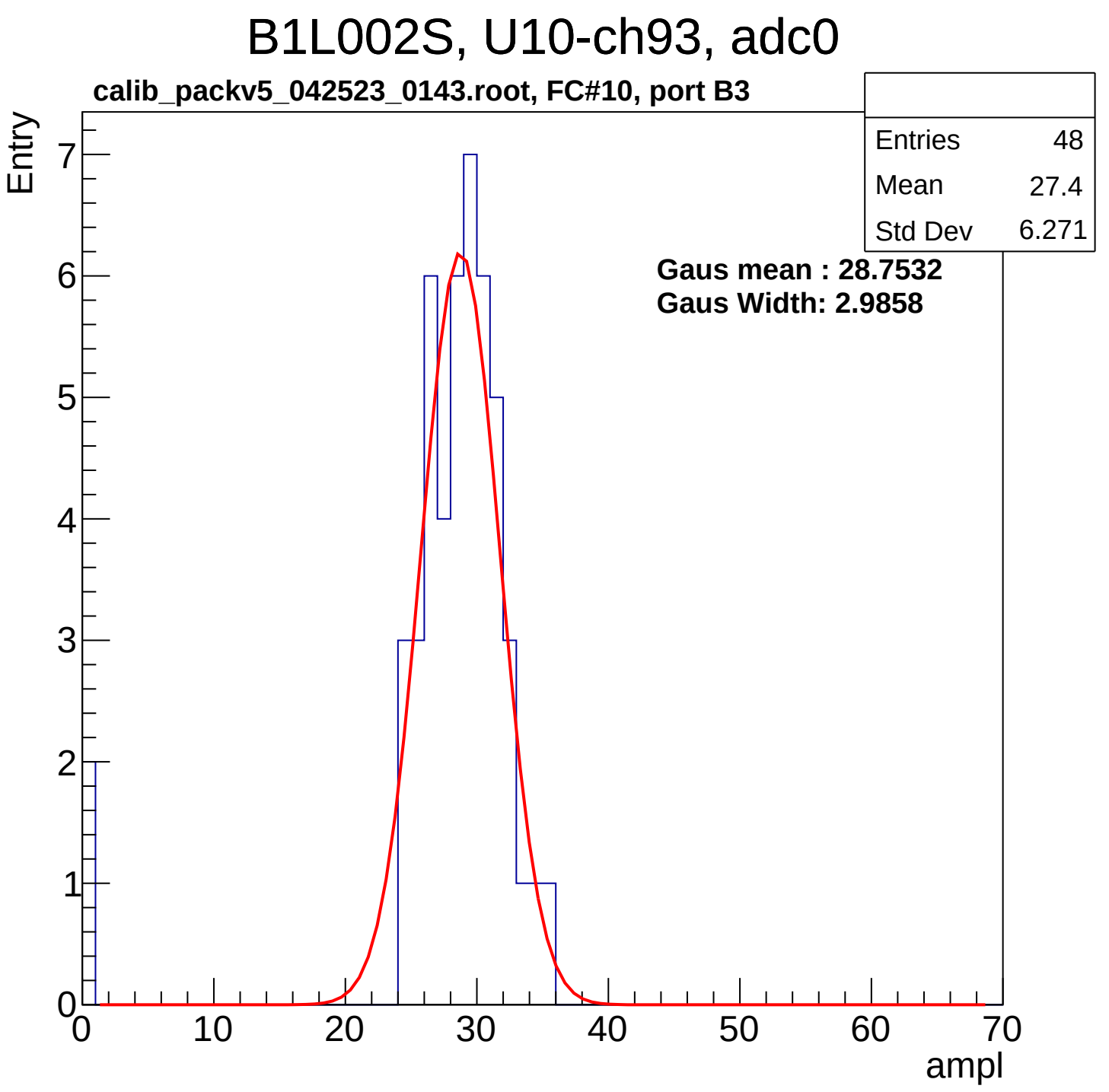
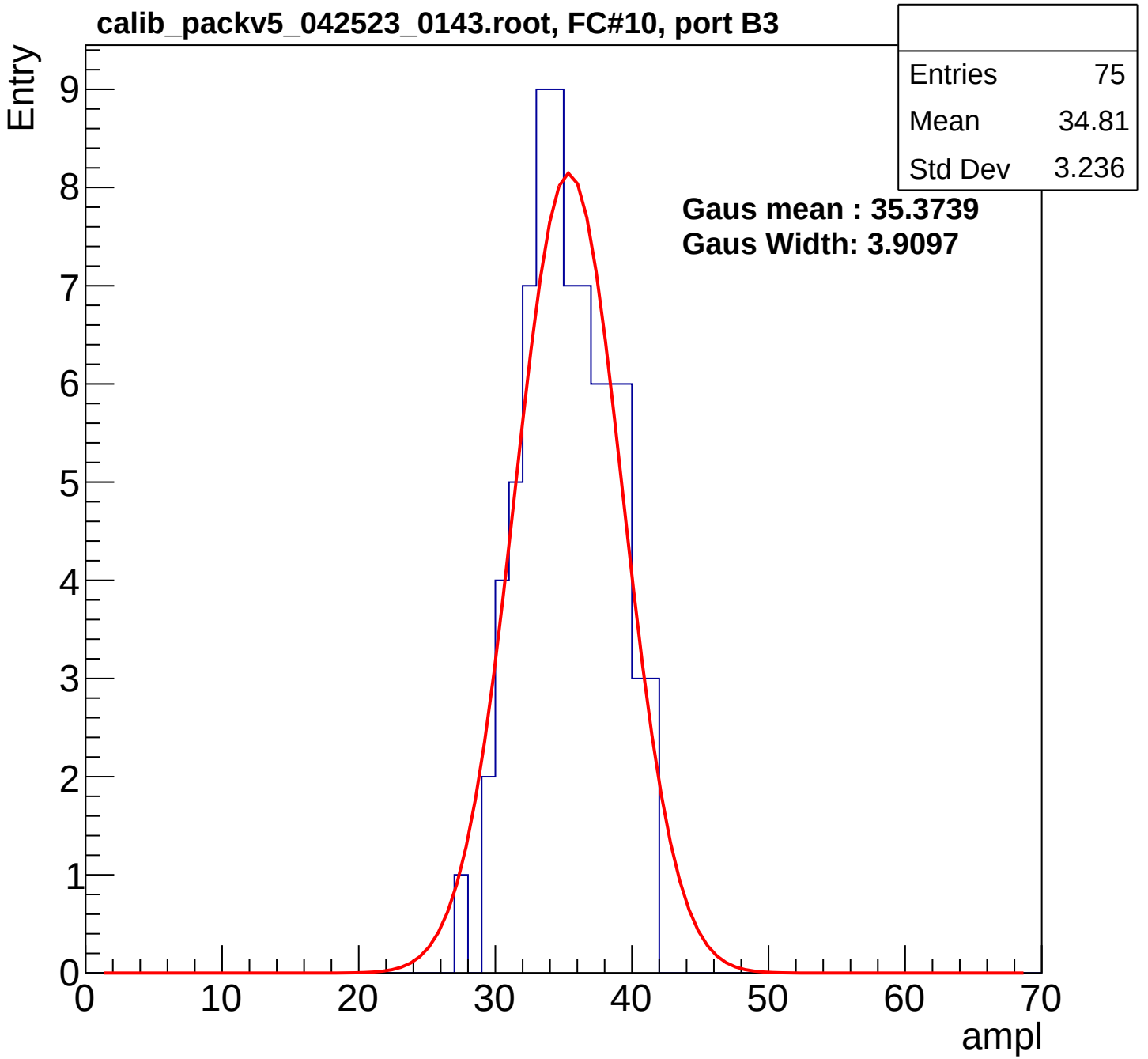
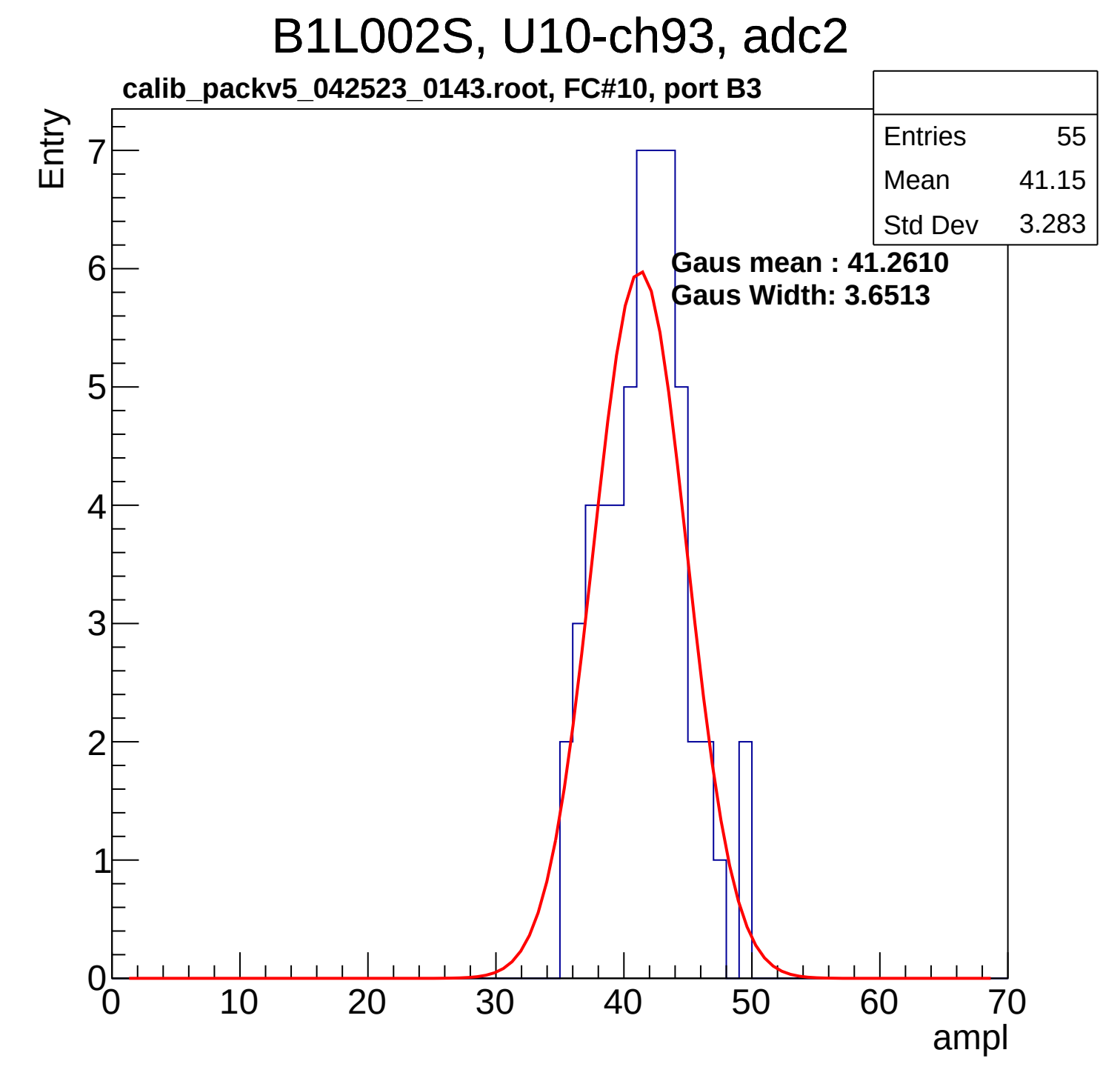
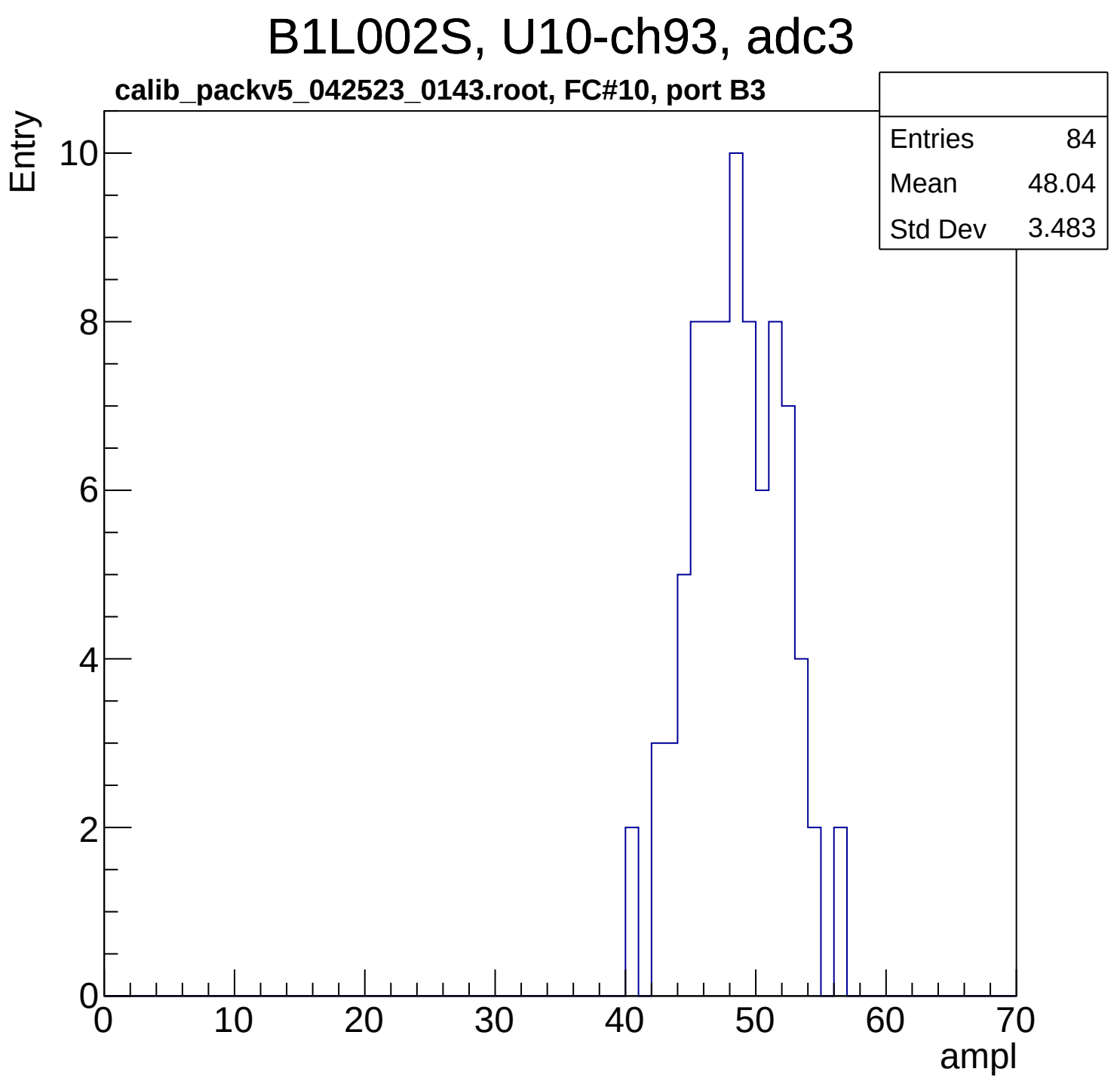
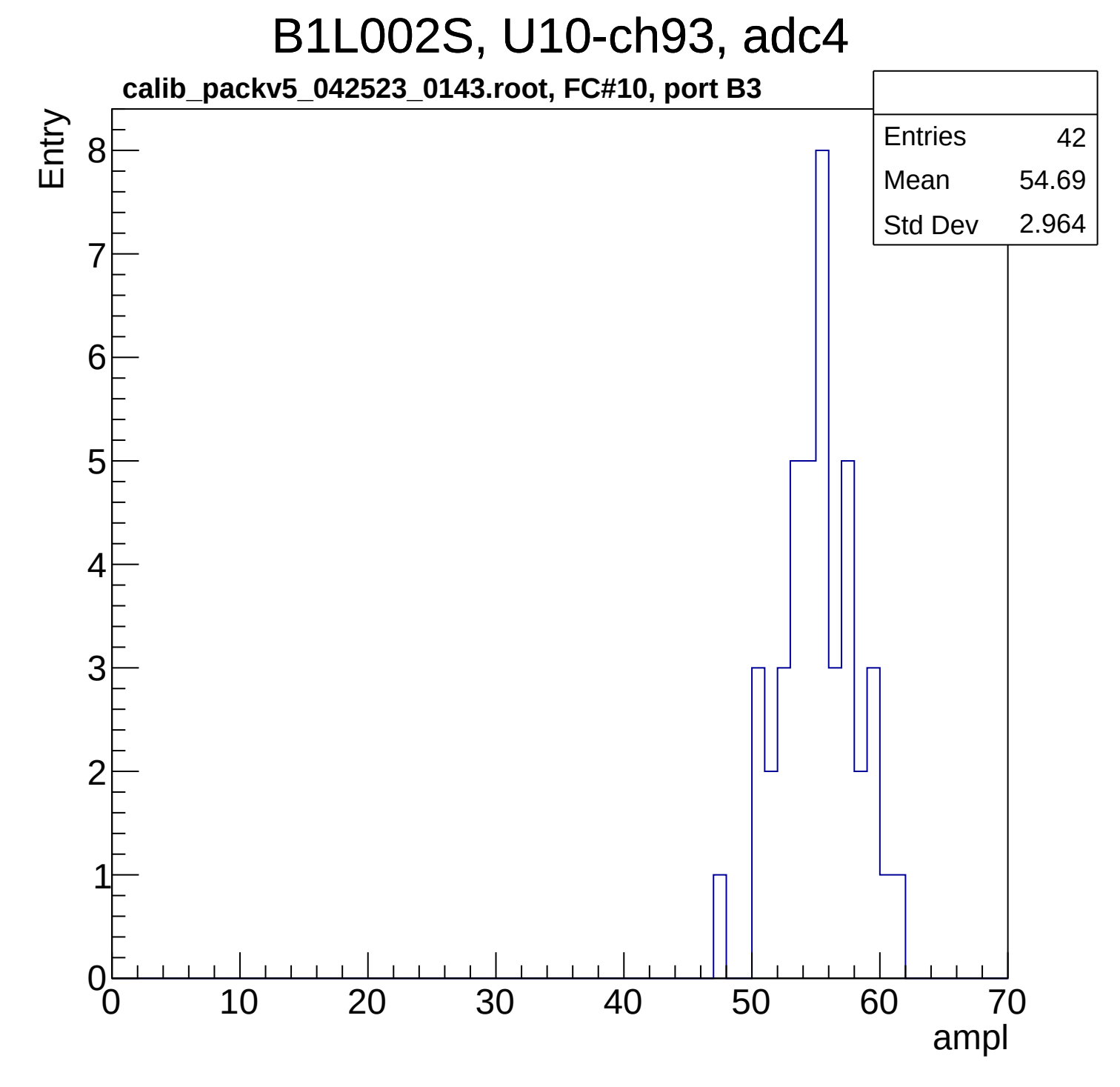

B1L002S, U10-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

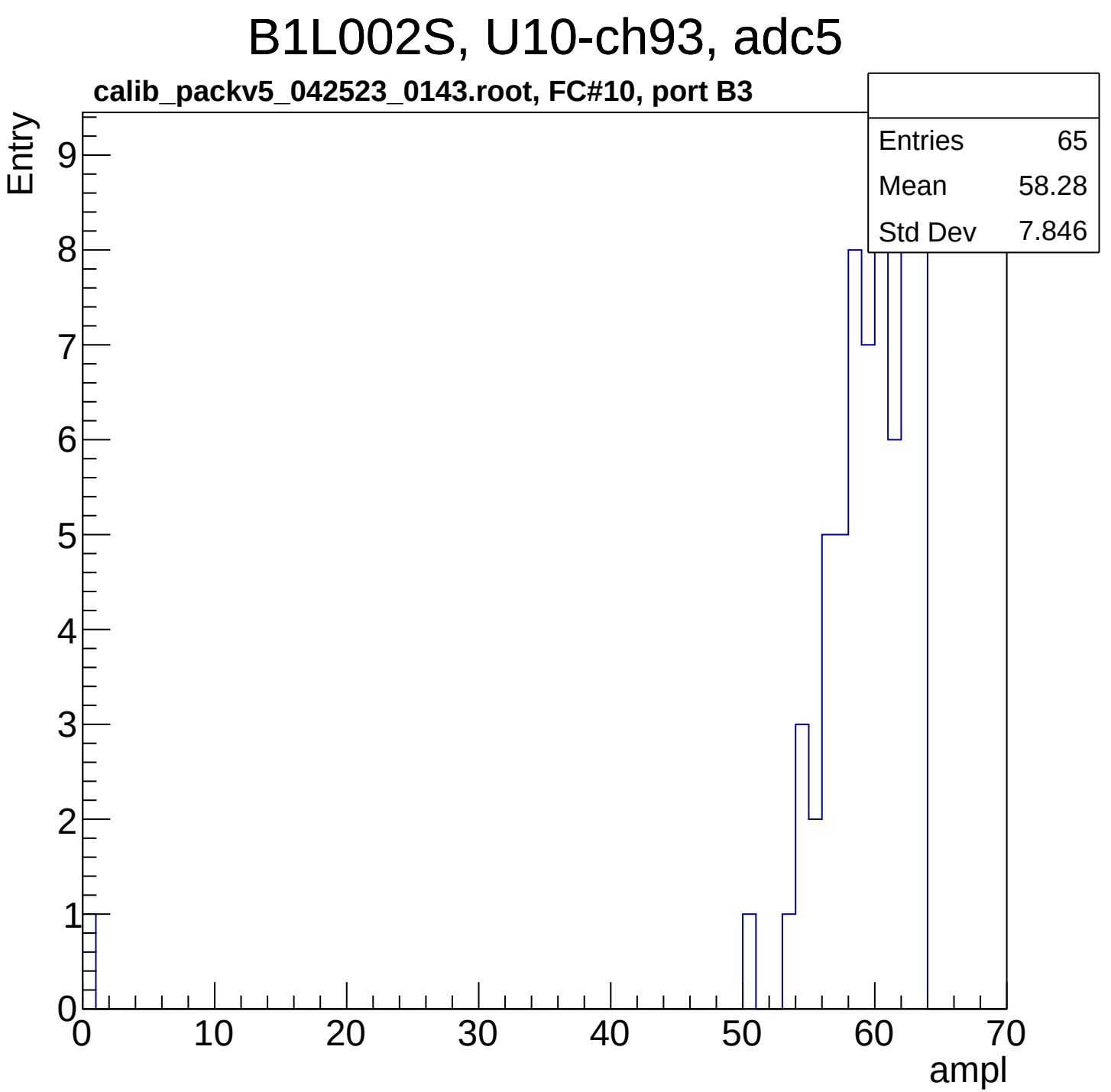

B1L002S, U10-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 27.4
Std Dev 6.271
Gaus mean : 28.7532
Gaus Width: 2.9858
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

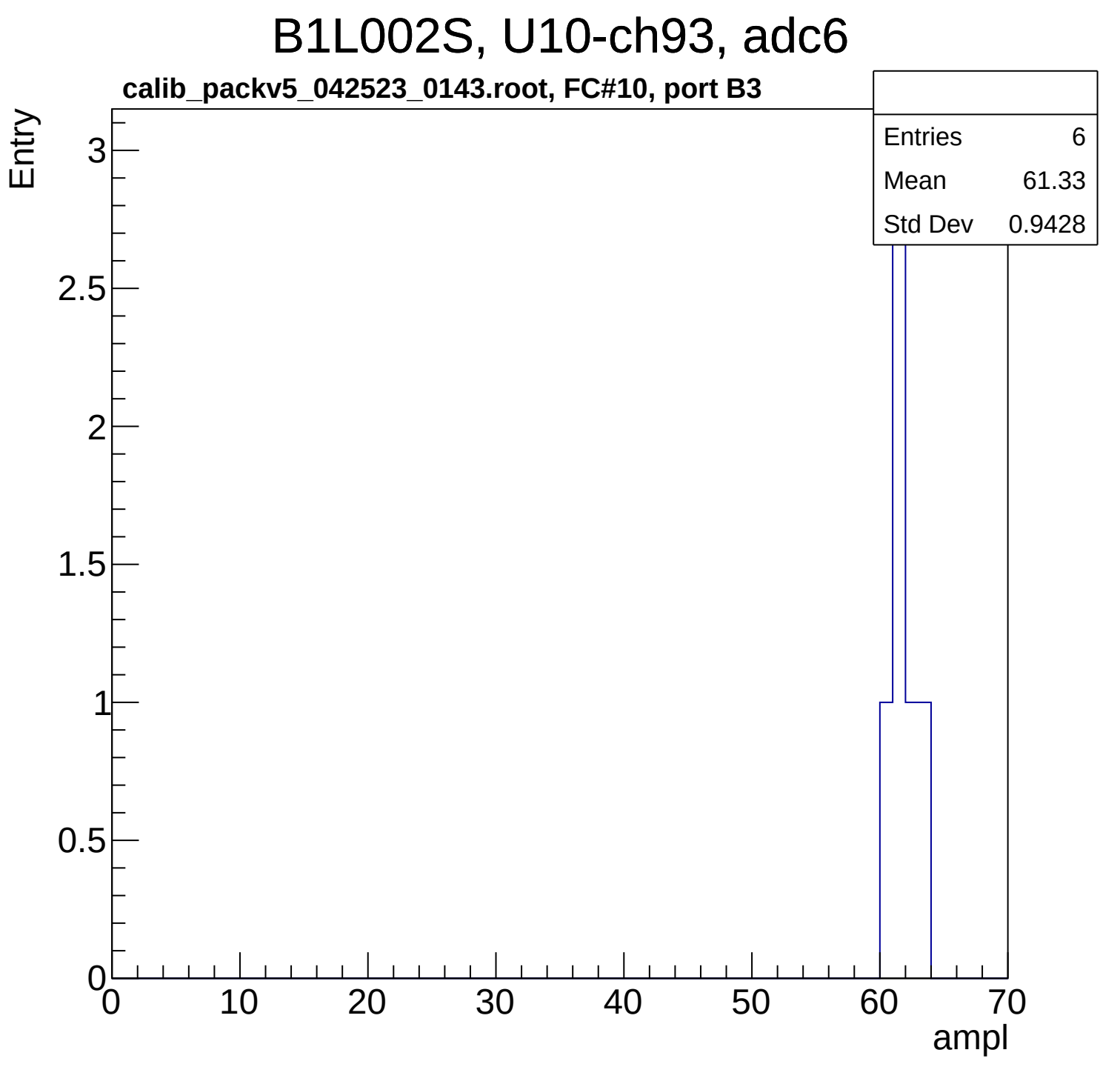

B1L002S, U10-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.81
Std Dev 3.236
Gaus mean : 35.3739
Gaus Width: 3.9097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

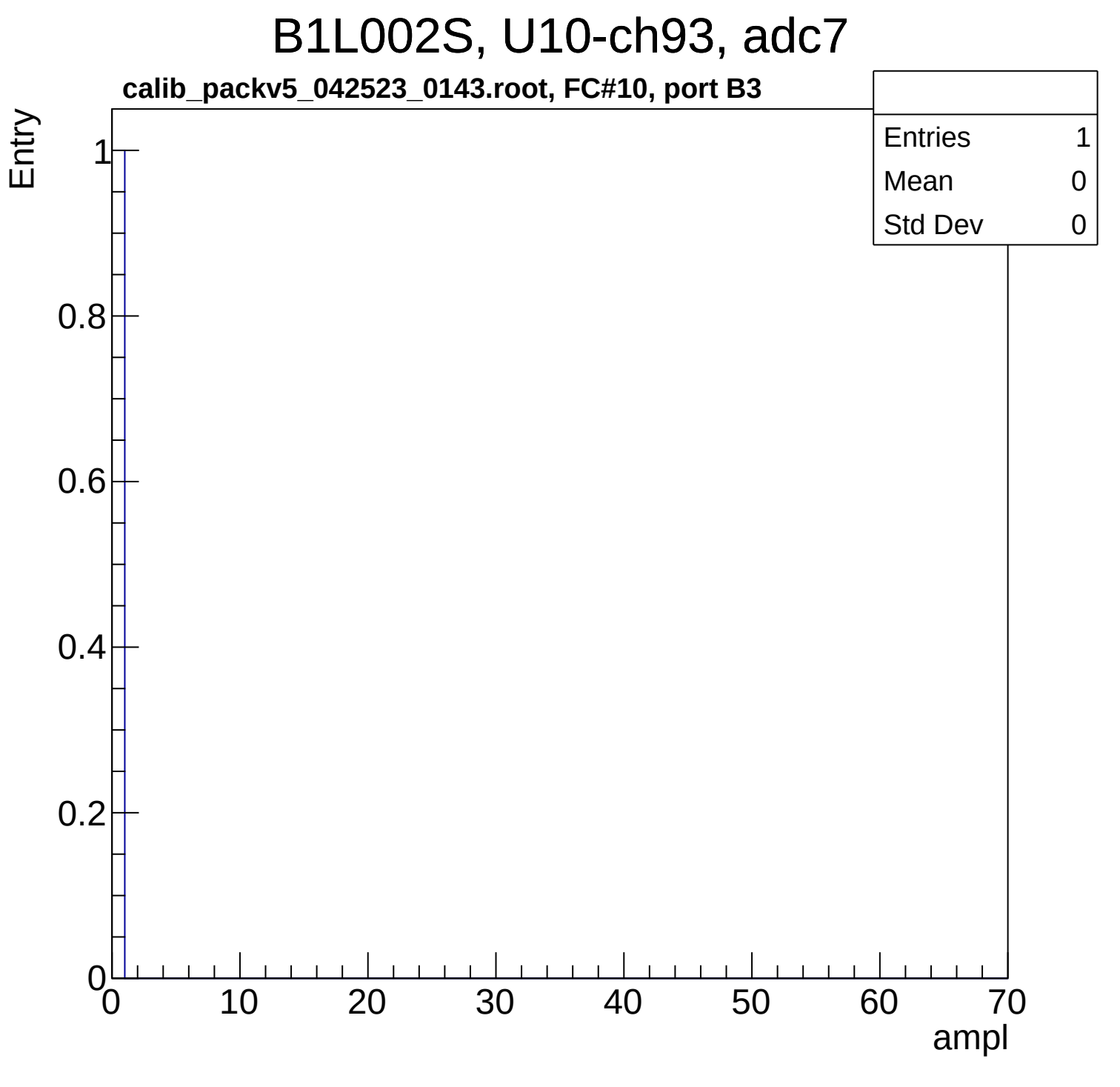

B1L002S, U10-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.15
Std Dev 3.283
Gaus mean : 41.2610
Gaus Width: 3.6513
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

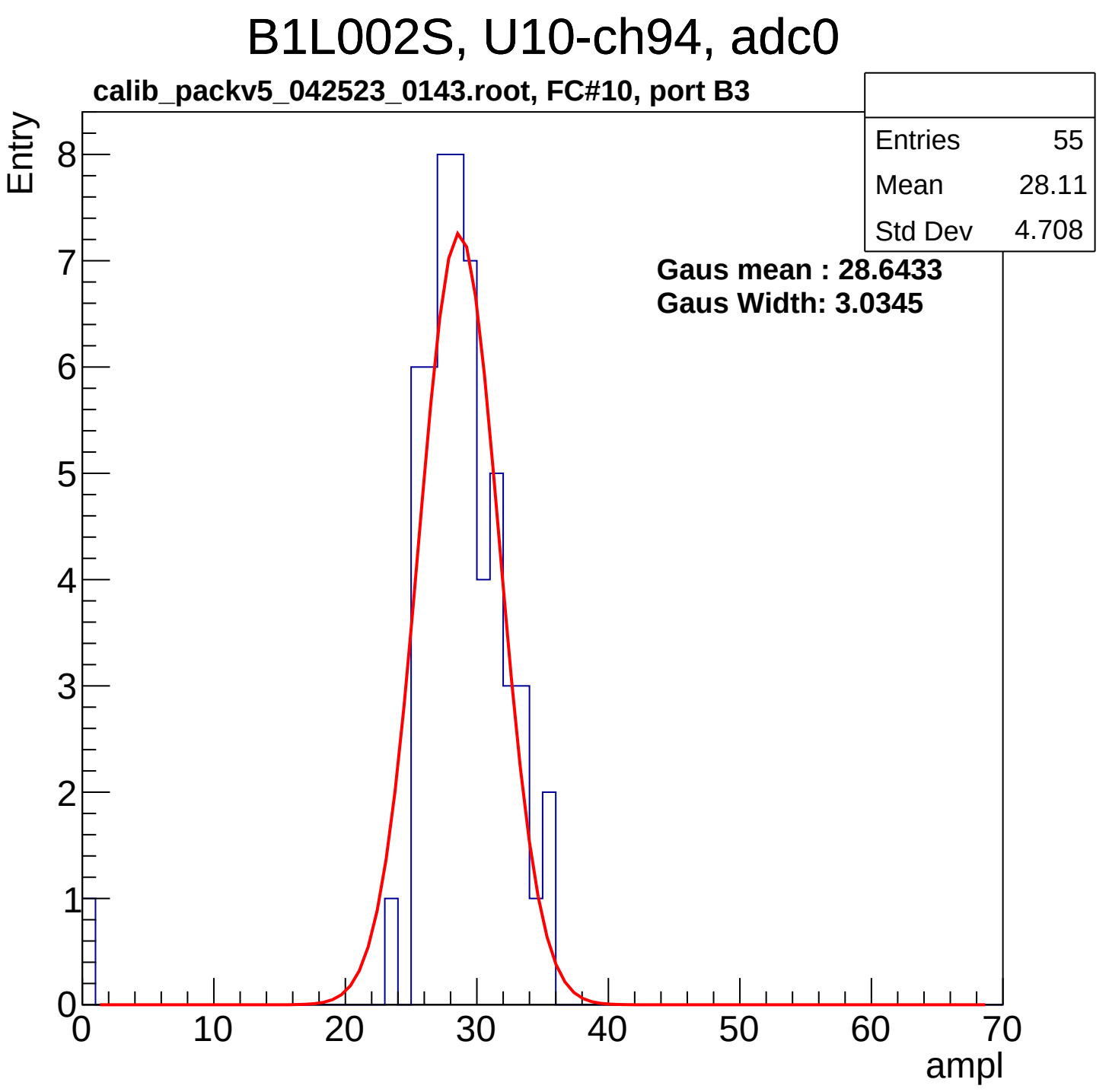

B1L002S, U10-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 48.04
Std Dev 3.483
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

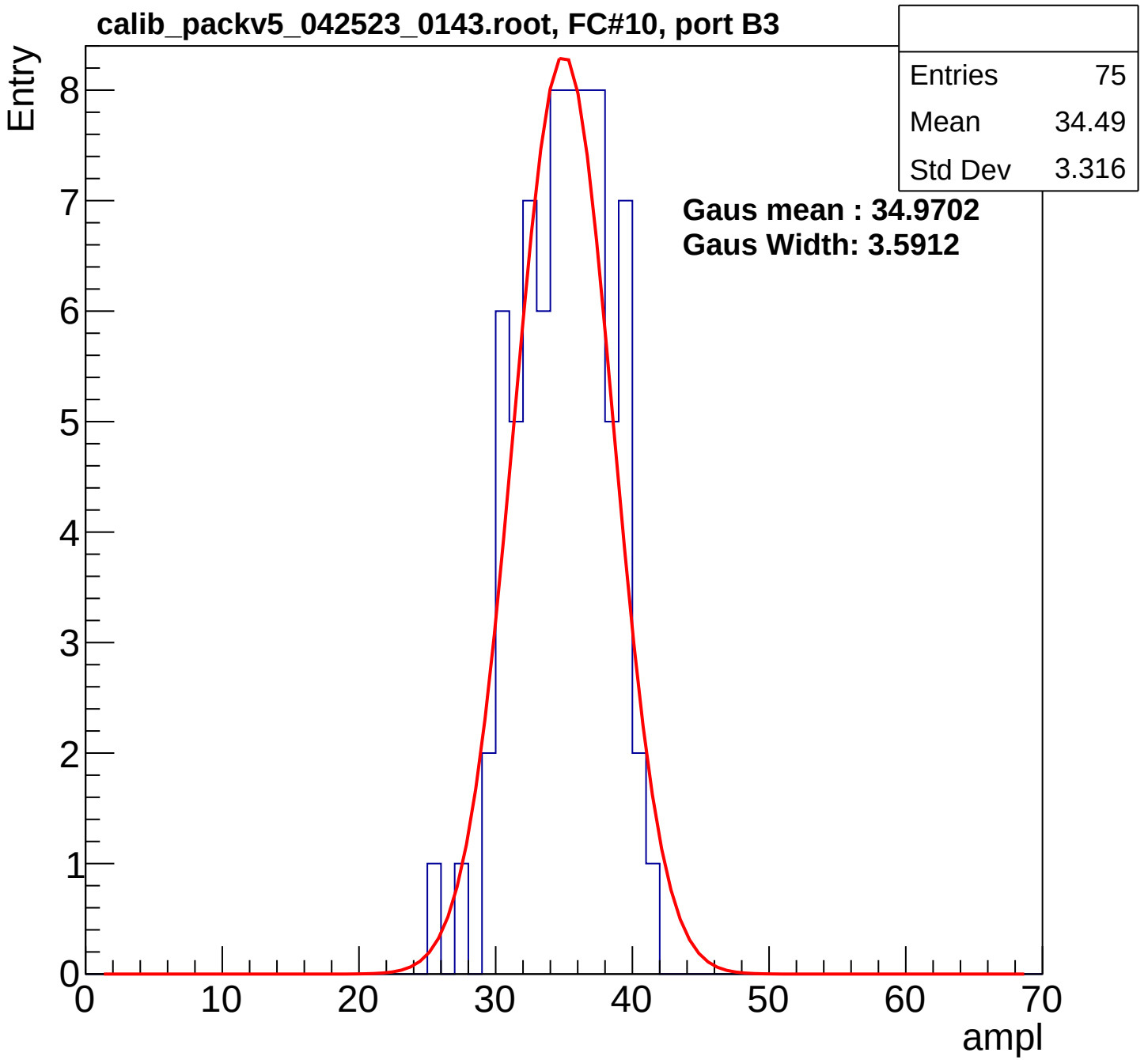

B1L002S, U10-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 54.69
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

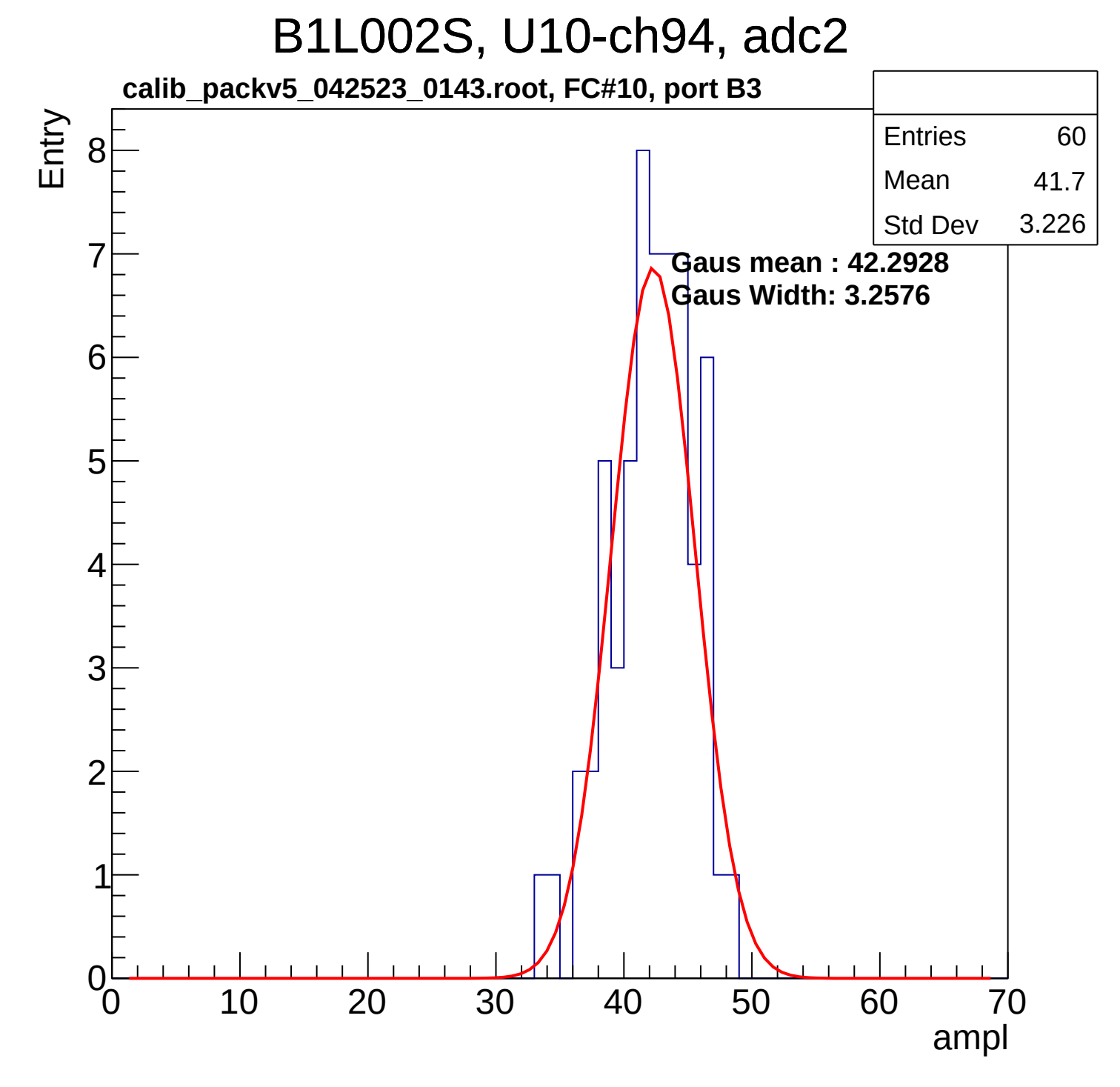

B1L002S, U10-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.28
Std Dev 7.846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

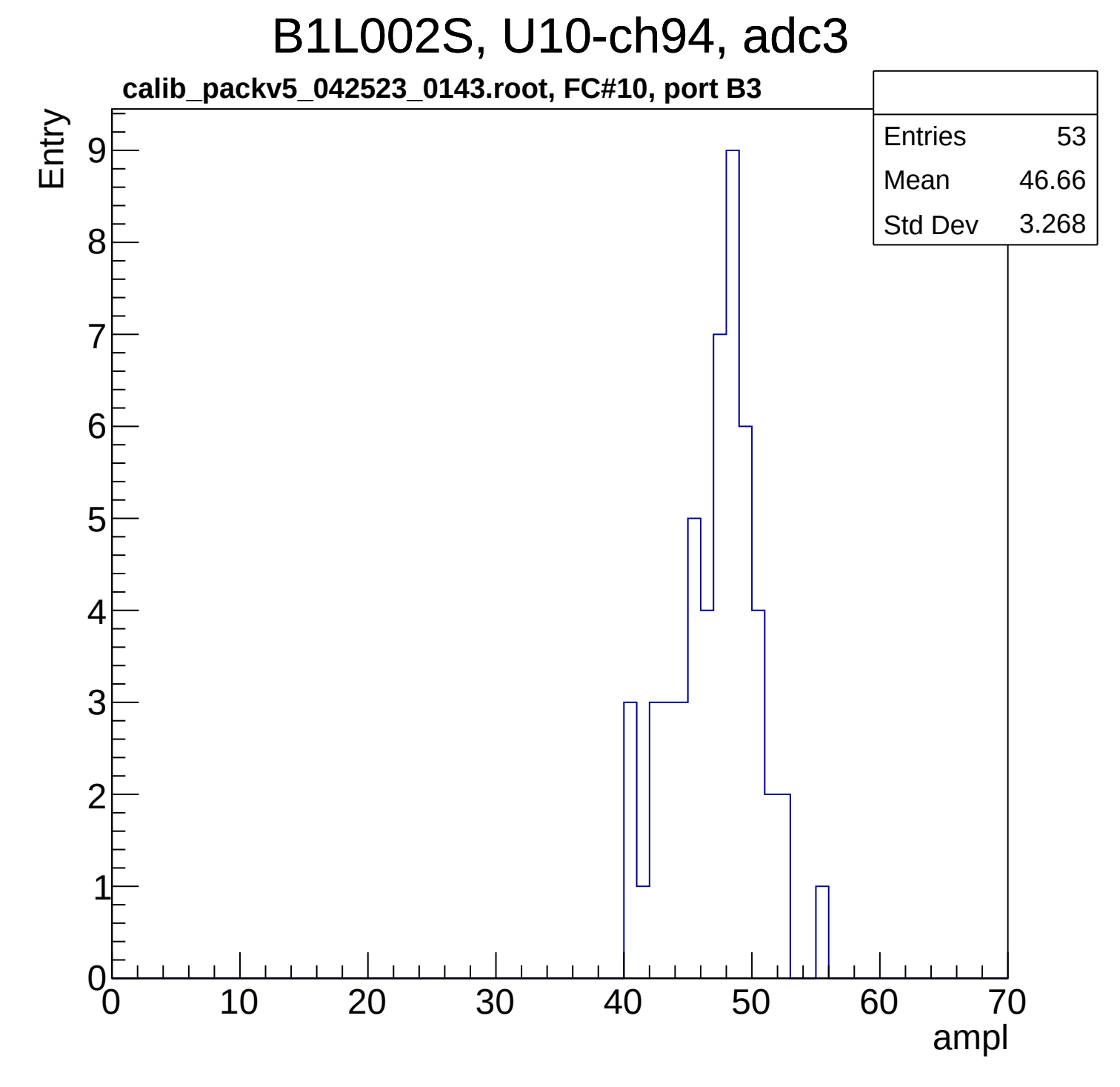

B1L002S, U10-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.11
Std Dev 4.708
Gaus mean : 28.6433
Gaus Width: 3.0345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.49
Std Dev 3.316
Gaus mean : 34.9702
Gaus Width: 3.5912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.7
Std Dev 3.226
Gaus mean : 42.2928
Gaus Width: 3.2576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 46.66
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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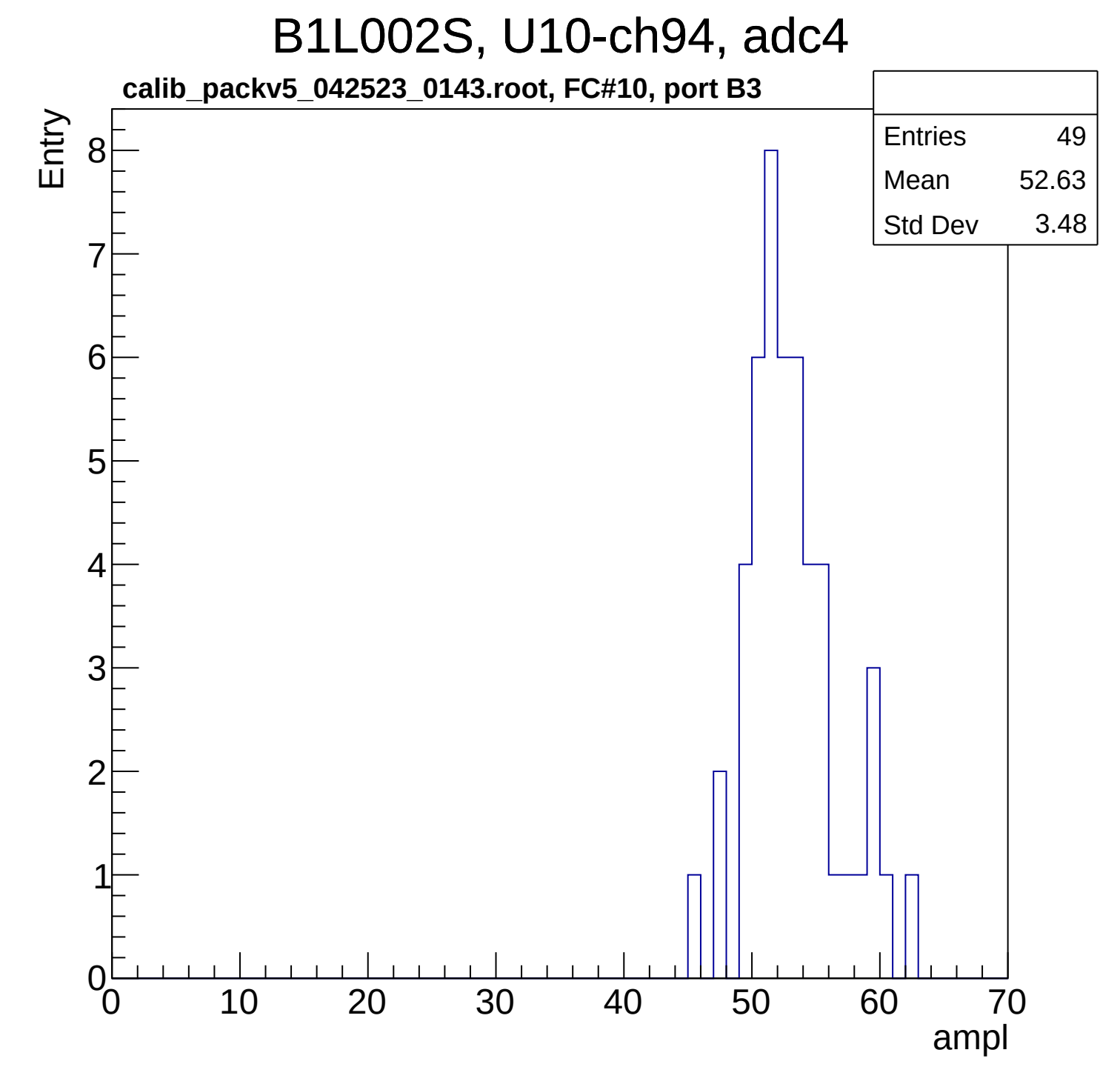
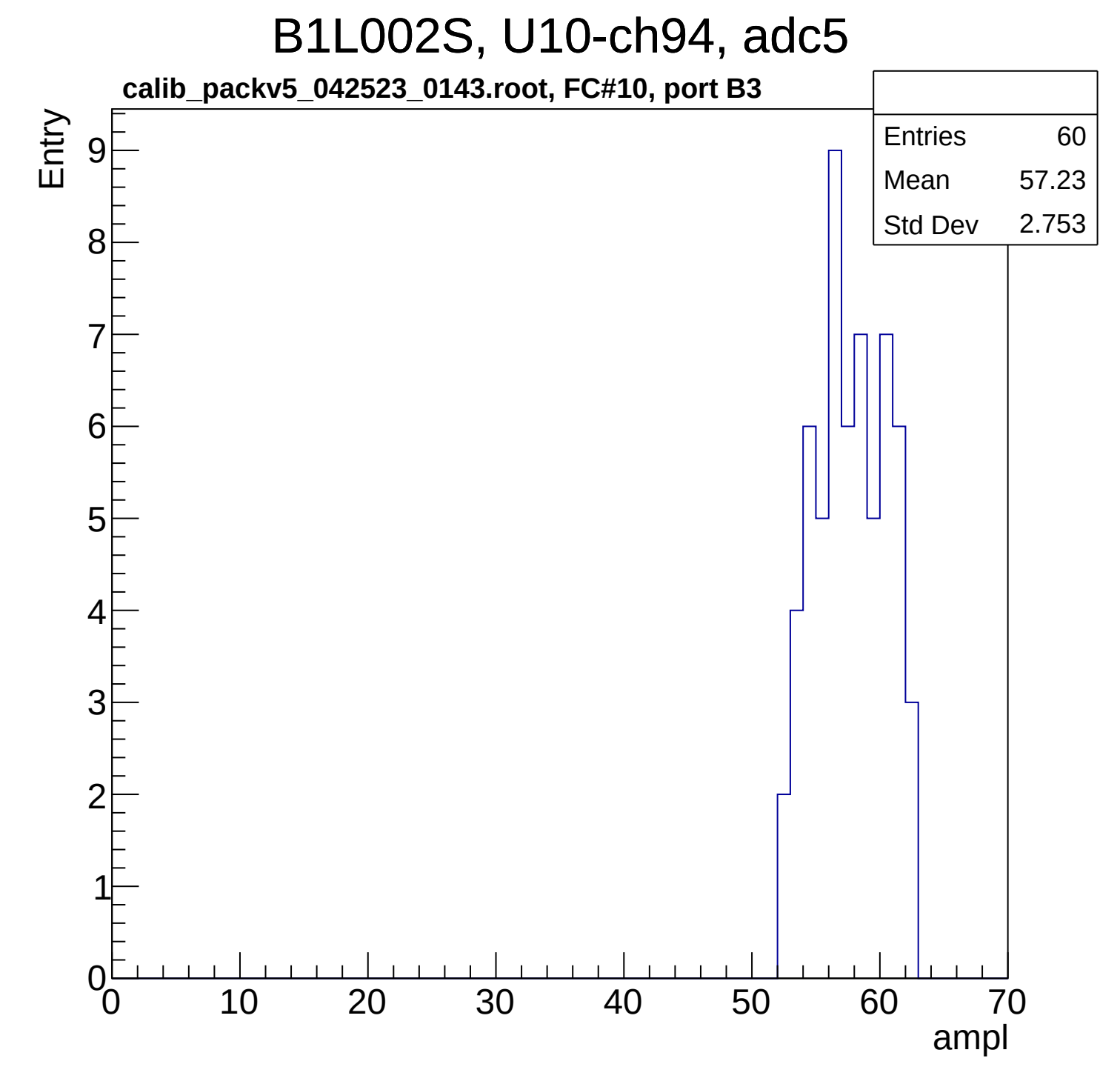
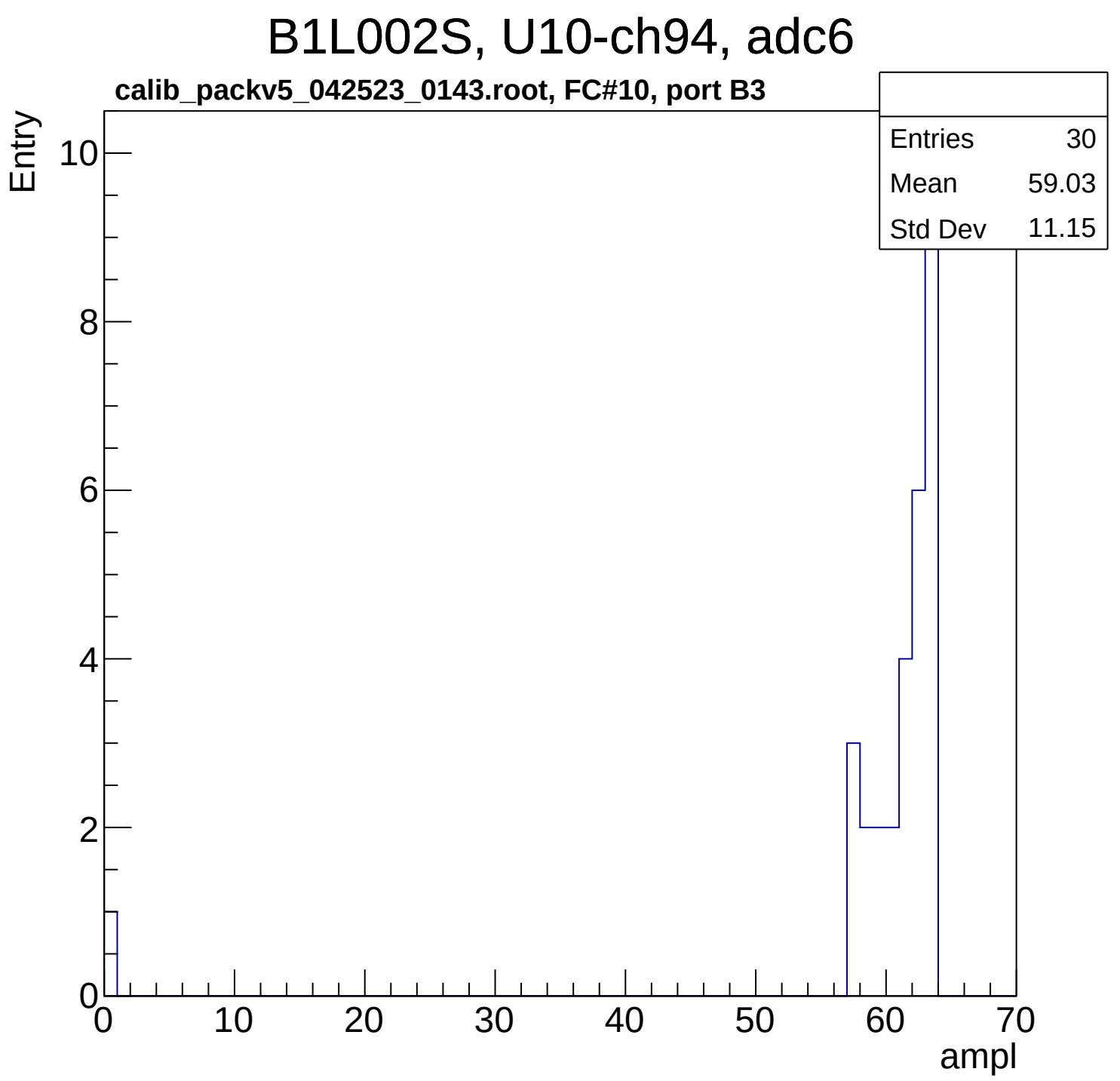
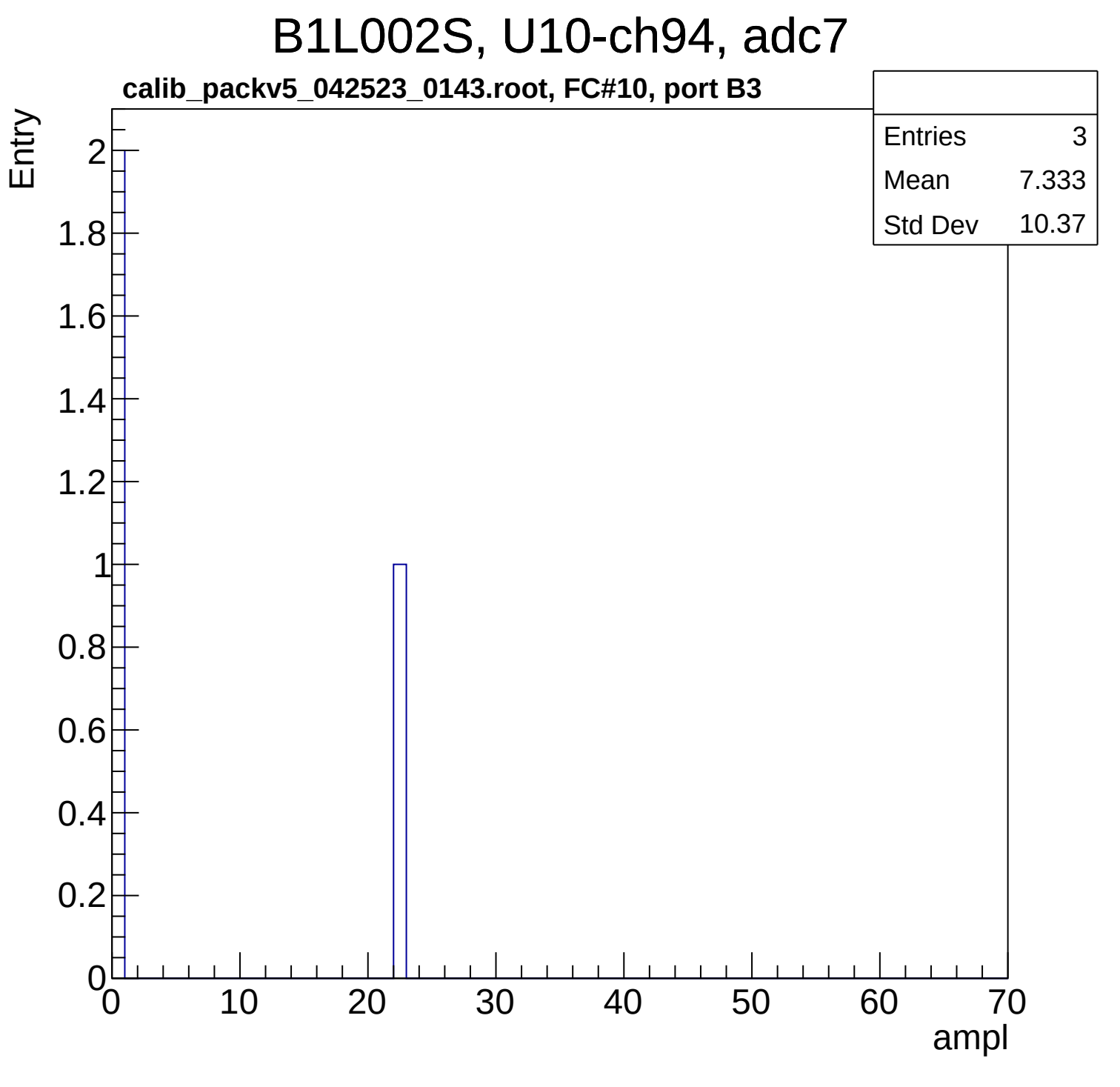
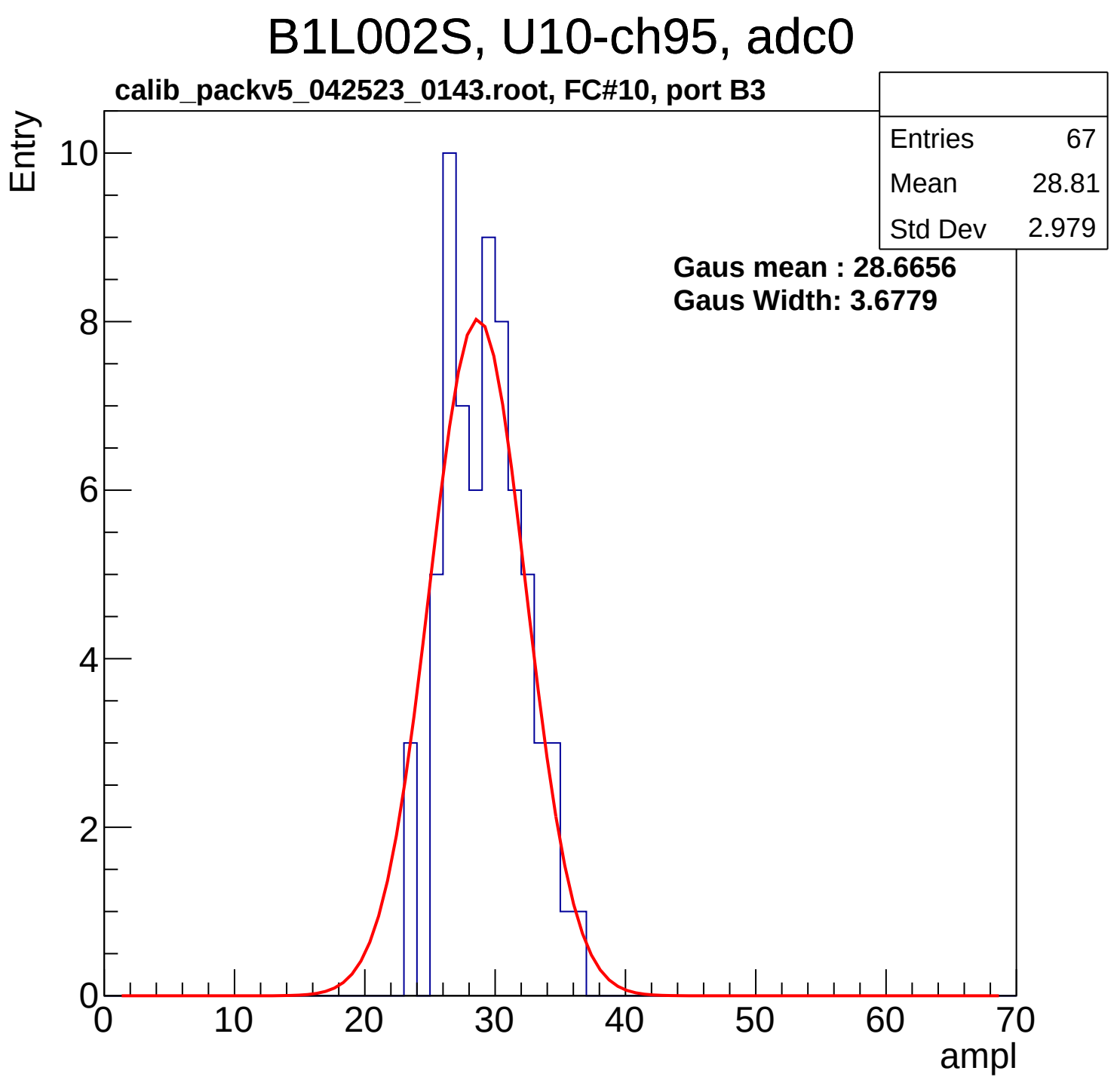
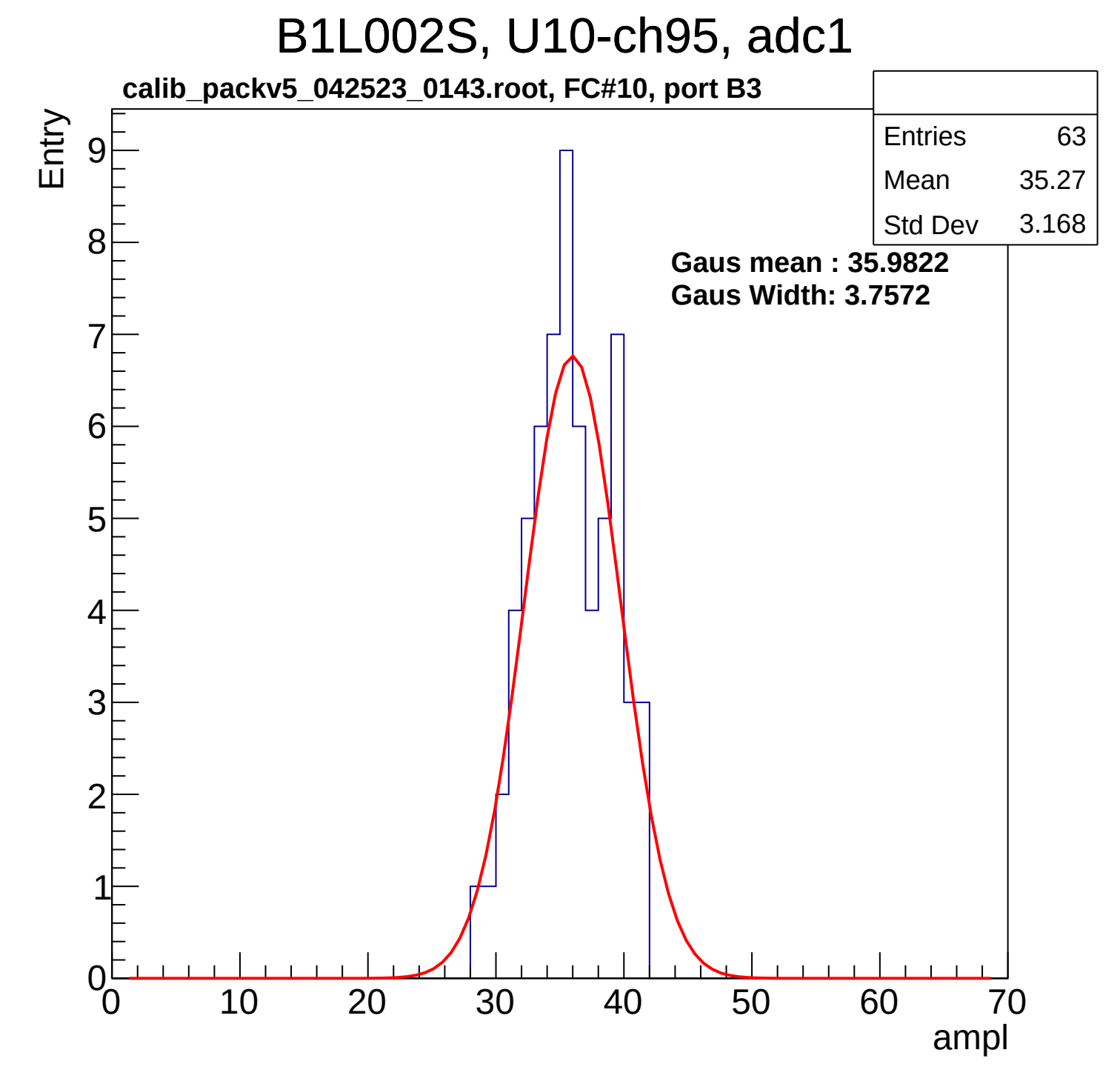

B1L002S, U10-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.63
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

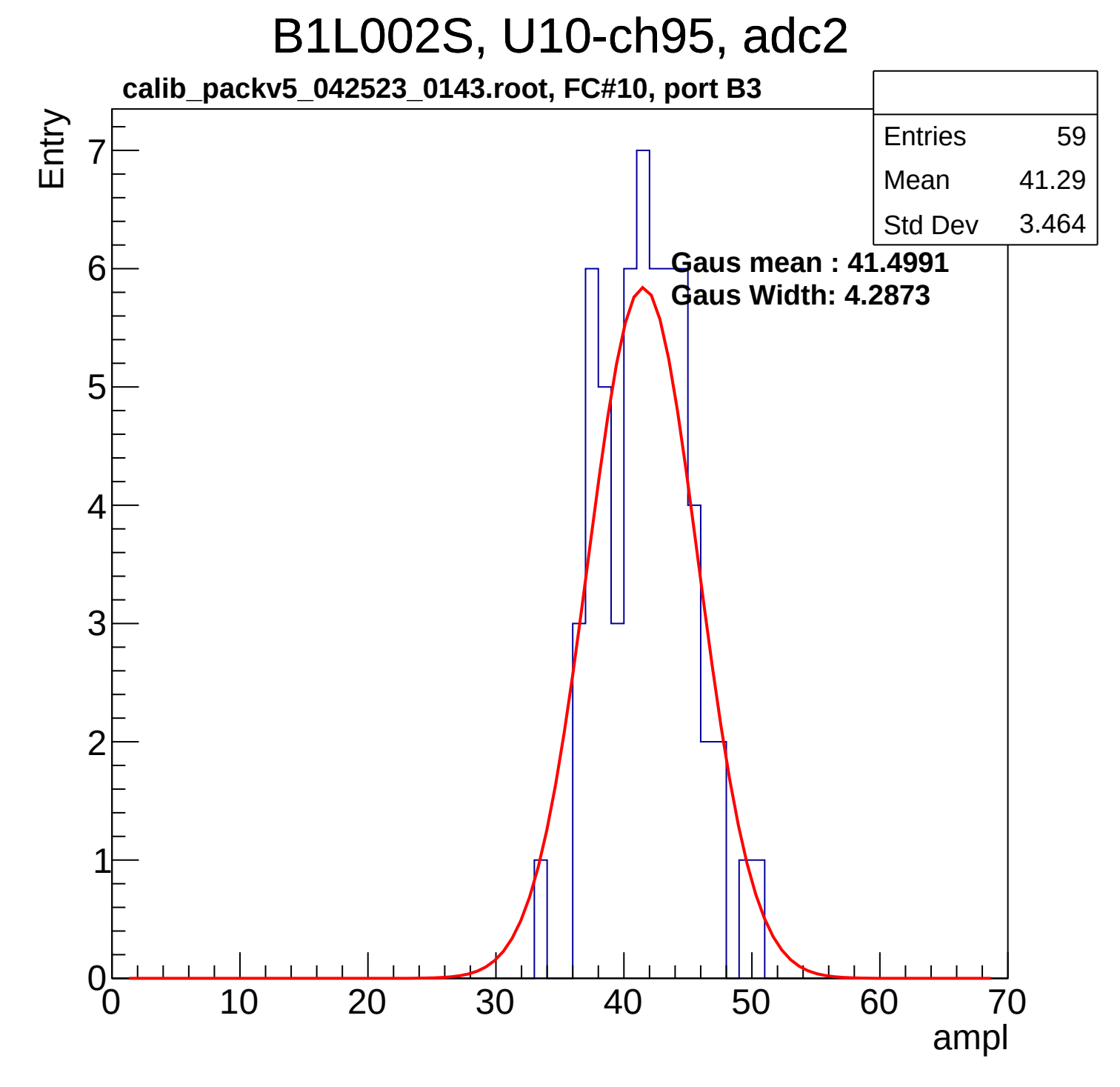

B1L002S, U10-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.23
Std Dev 2.753
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 59.03
Std Dev 11.15
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.81
Std Dev 2.979
Gaus mean : 28.6656
Gaus Width: 3.6779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.27
Std Dev 3.168
Gaus mean : 35.9822
Gaus Width: 3.7572
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.29
Std Dev 3.464
Gaus mean : 41.4991
Gaus Width: 4.2873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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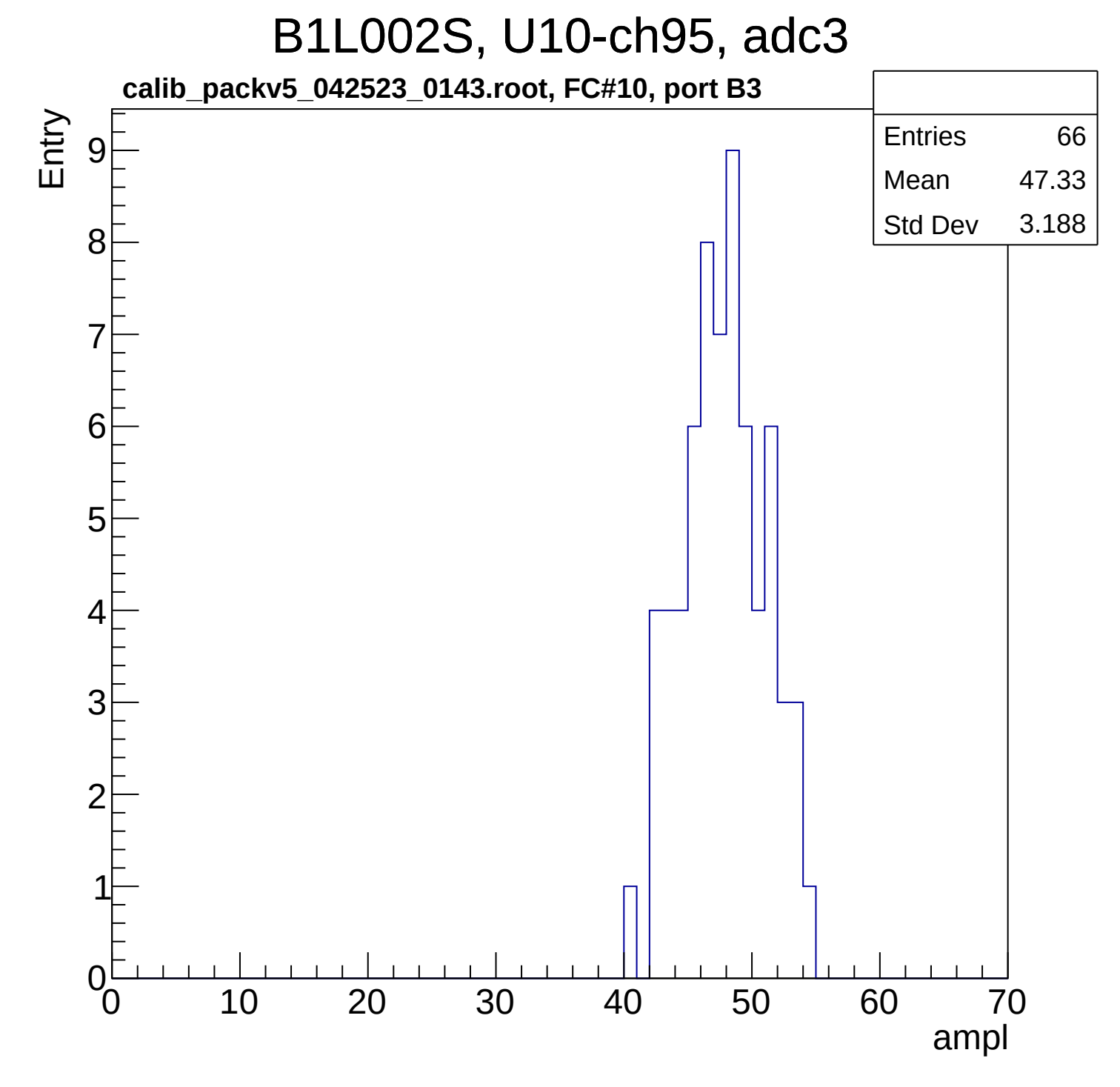
60
70


B1L002S, U10-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.33
Std Dev 3.188
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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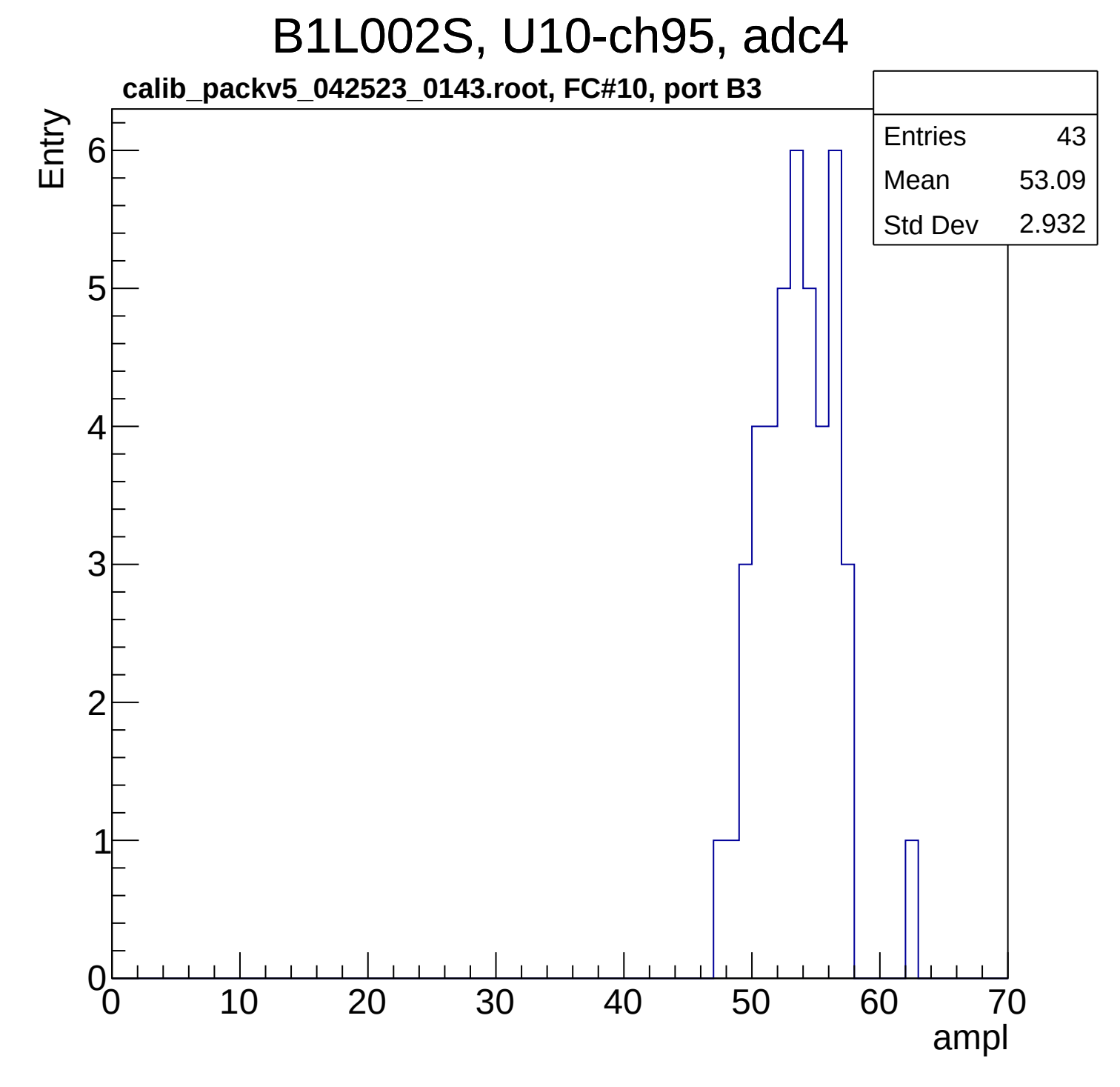
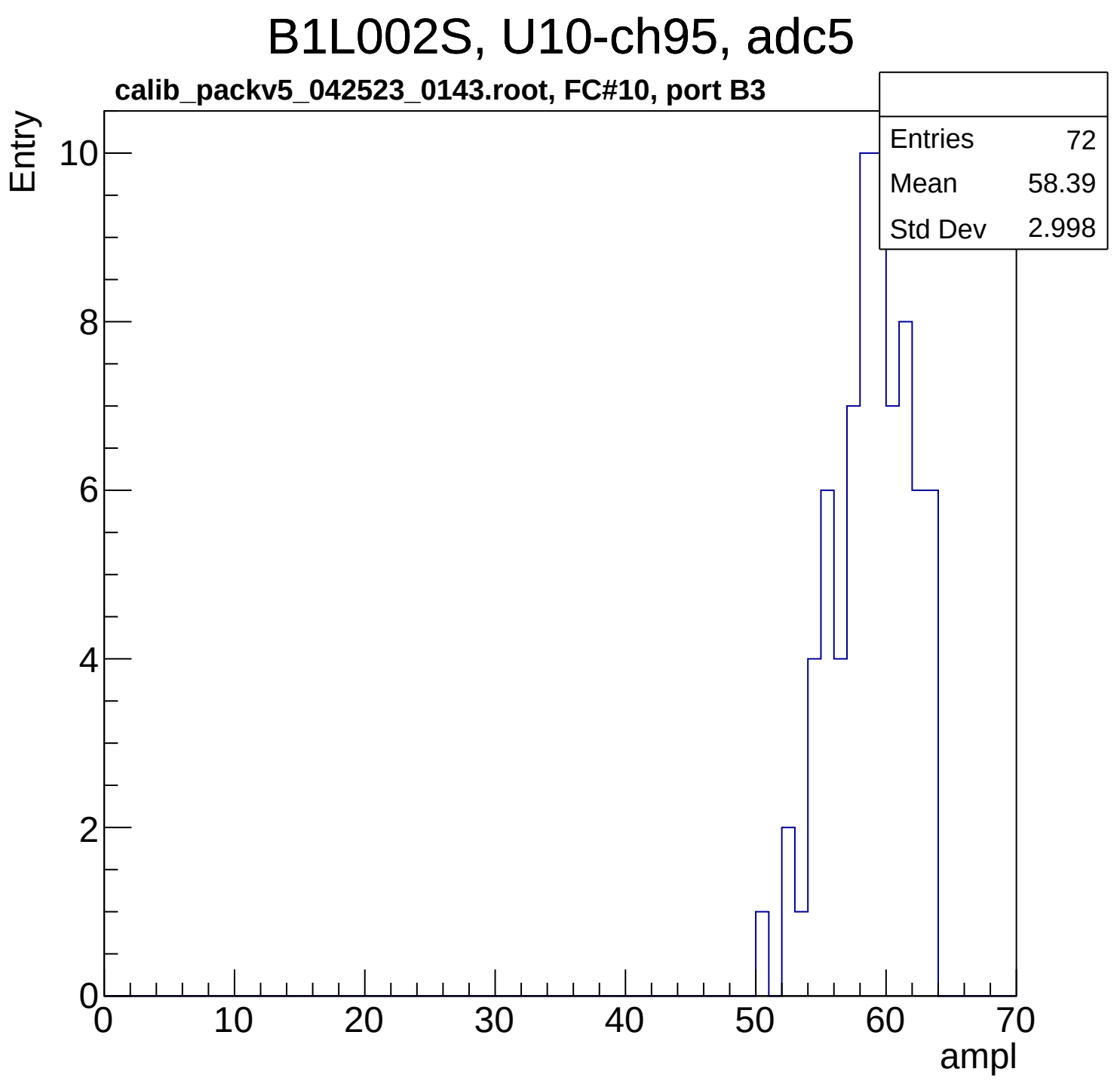
40
50
60
70


B1L002S, U10-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.09
Std Dev 2.932
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 58.39
Std Dev 2.998
Entry
ampl
0
2
4
6
8
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0
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30
40
50
60
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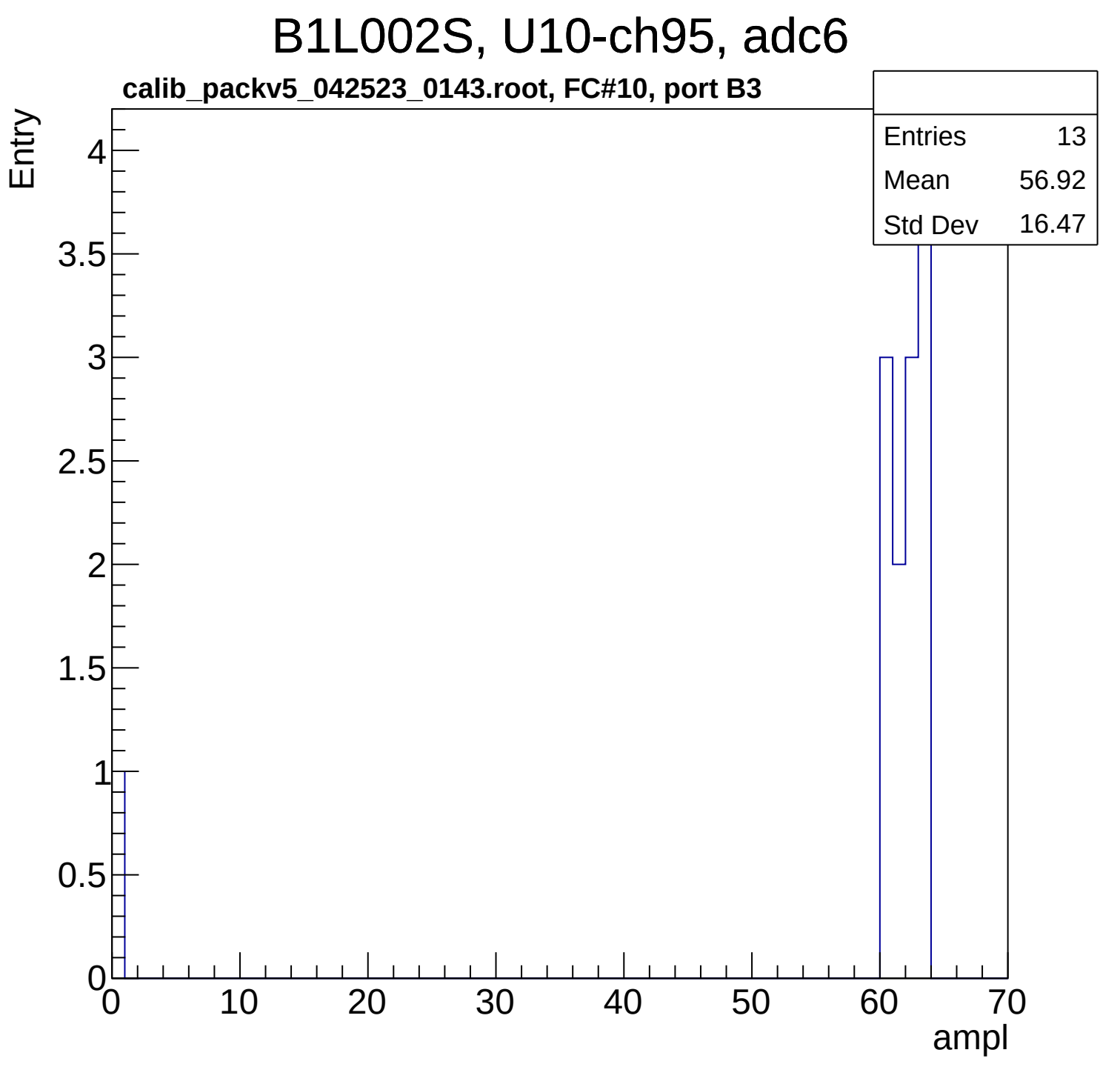
B1L002S, U10-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 56.92
Std Dev 16.47
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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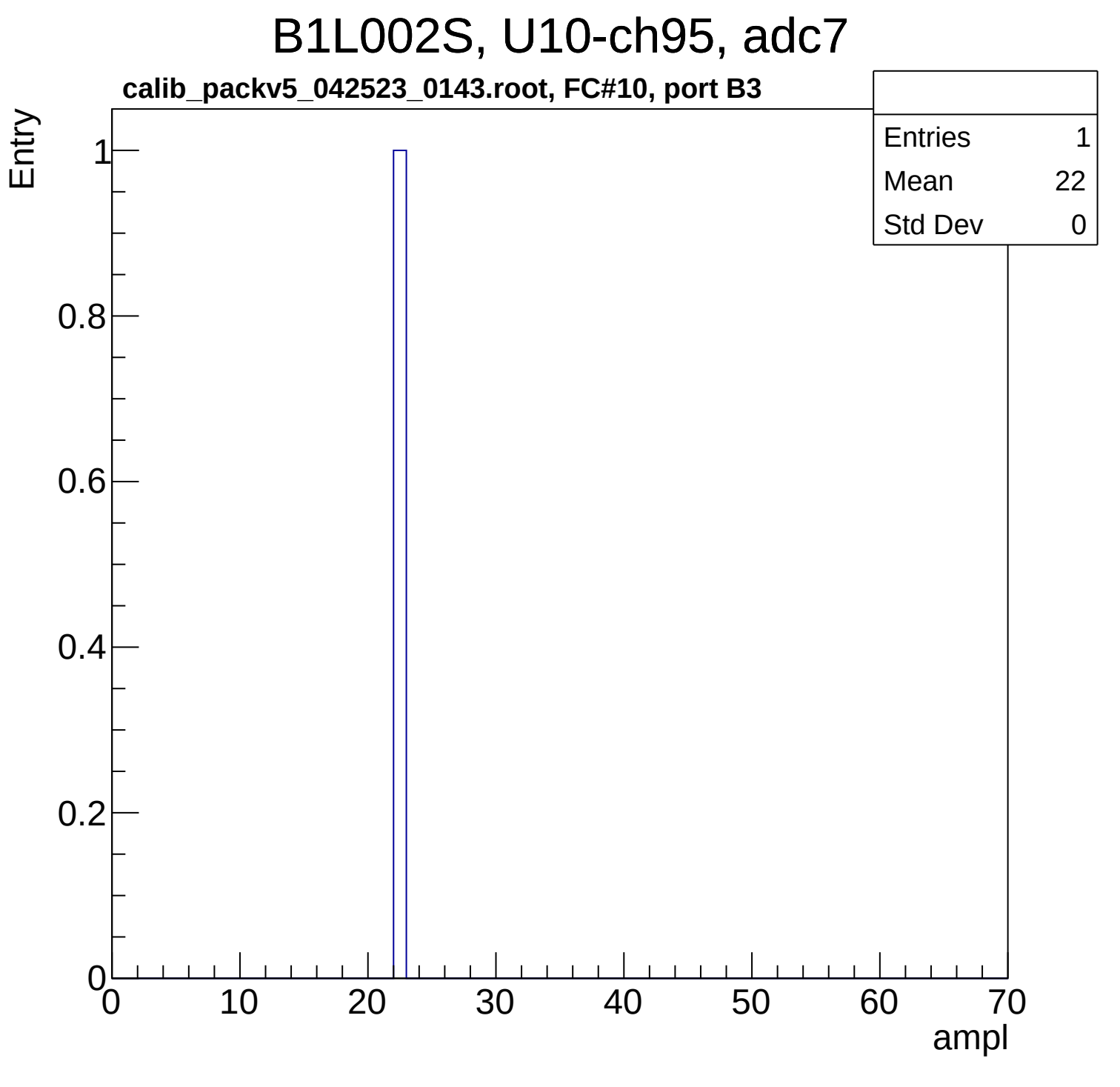
40
50
60
70


B1L002S, U10-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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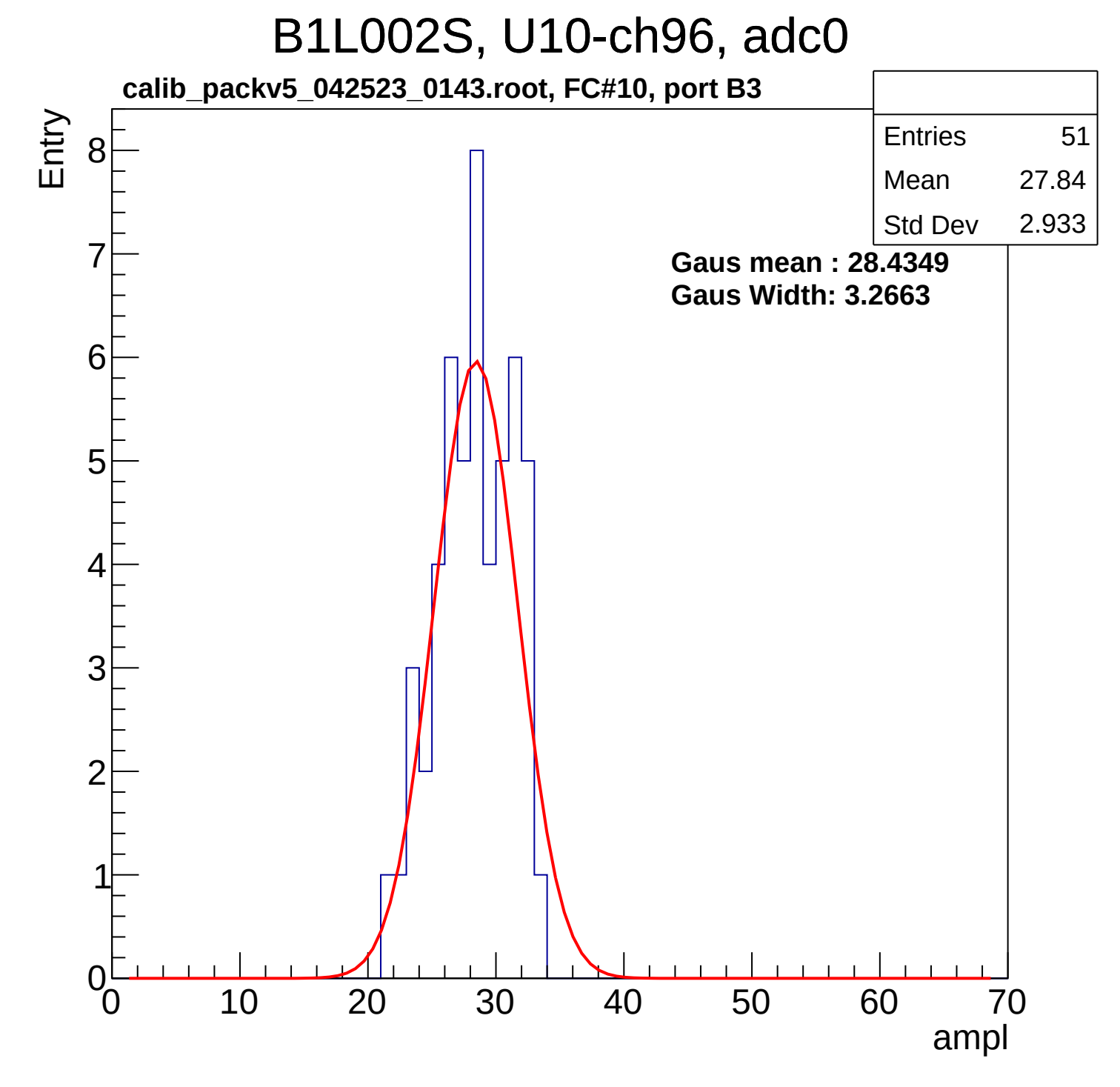
50
60
70
ampl


B1L002S, U10-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27.84
Std Dev 2.933
Gaus mean : 28.4349
Gaus Width: 3.2663
Entry
ampl
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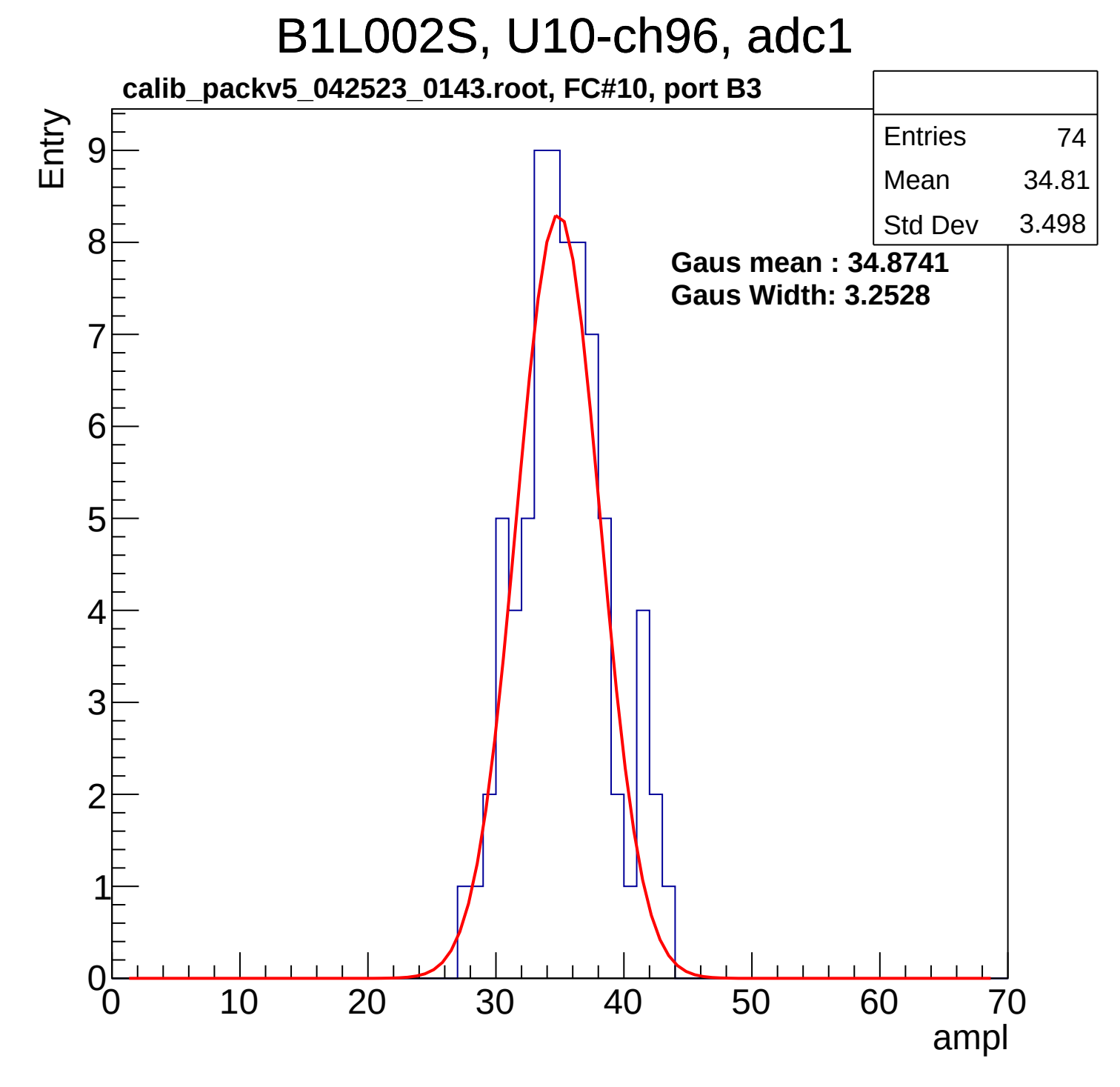
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.81
Std Dev 3.498
Gaus mean : 34.8741
Gaus Width: 3.2528
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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40
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60
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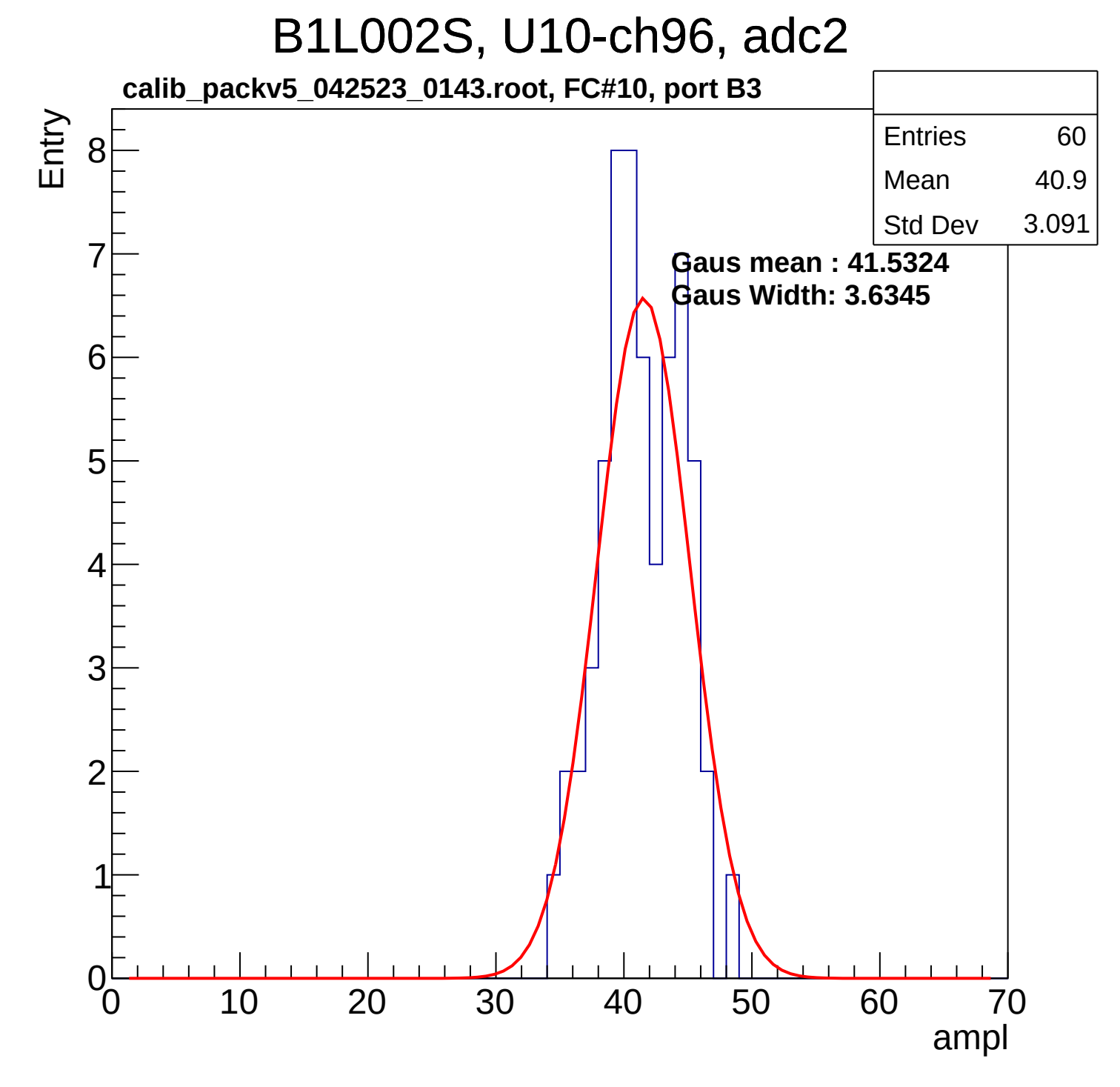
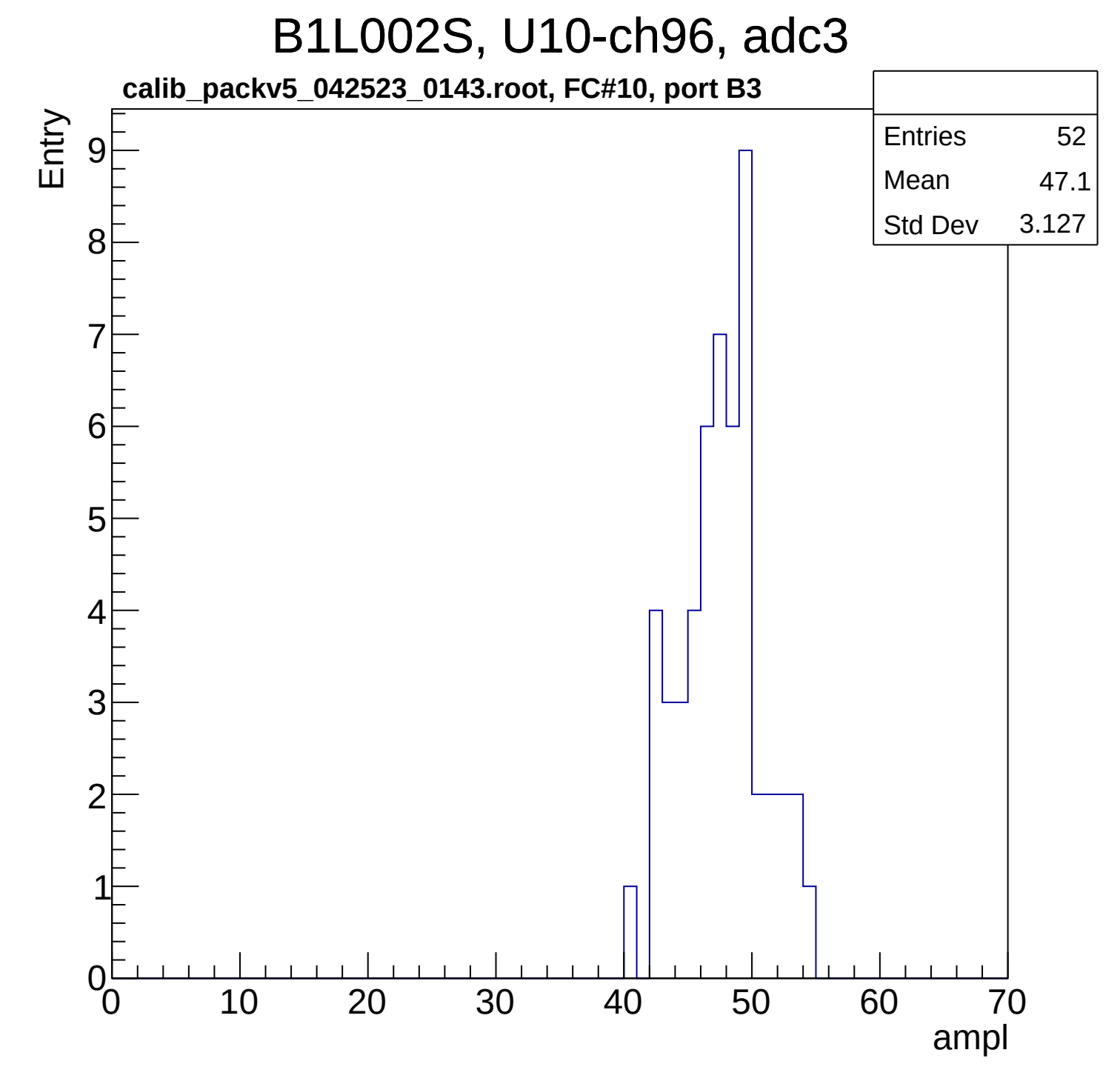
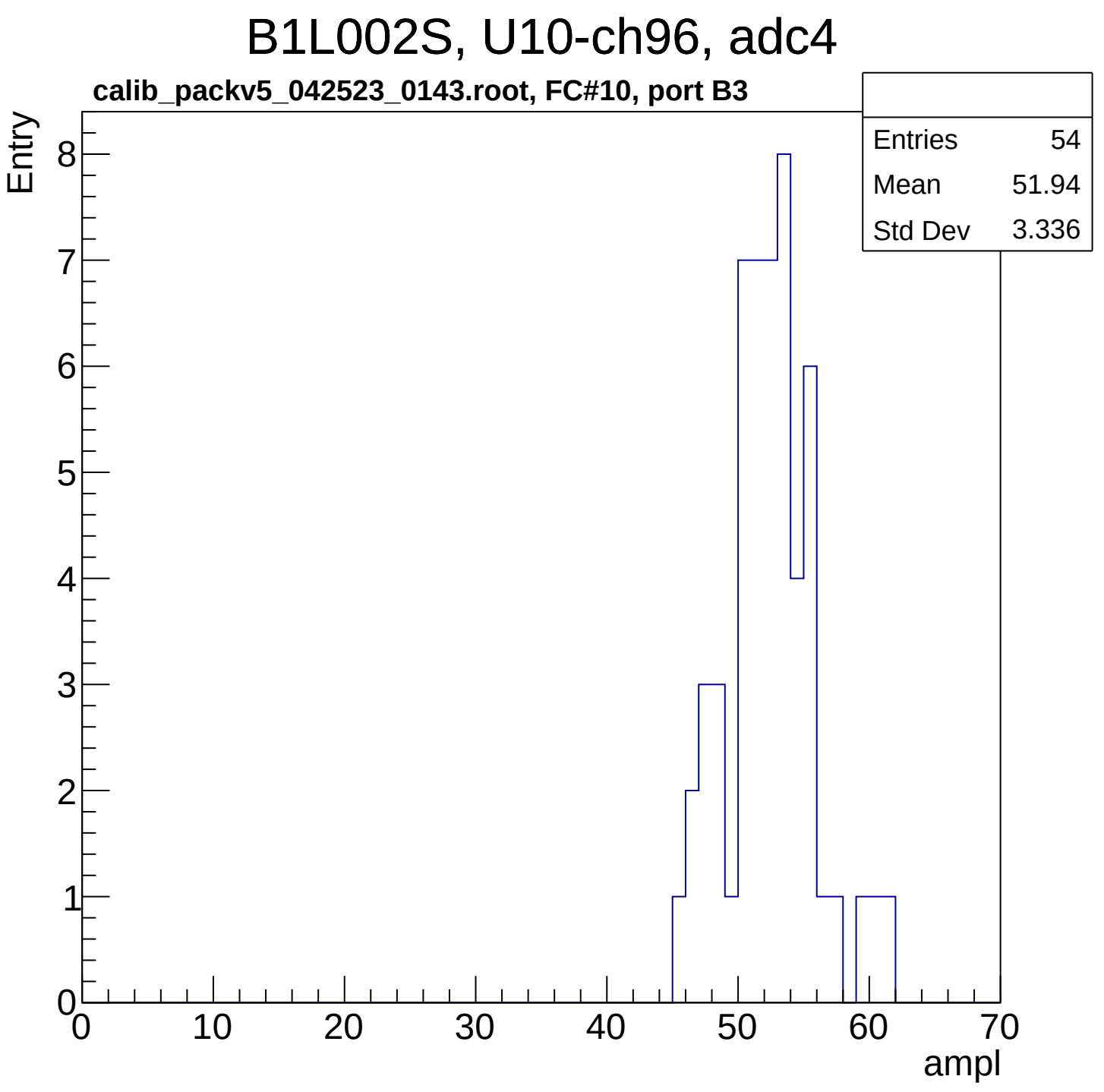

B1L002S, U10-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.9
Std Dev 3.091
Gaus mean : 41.5324
Gaus Width: 3.6345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.1
Std Dev 3.127
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.94
Std Dev 3.336
Entry
ampl
0
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2
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5
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7
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0
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60
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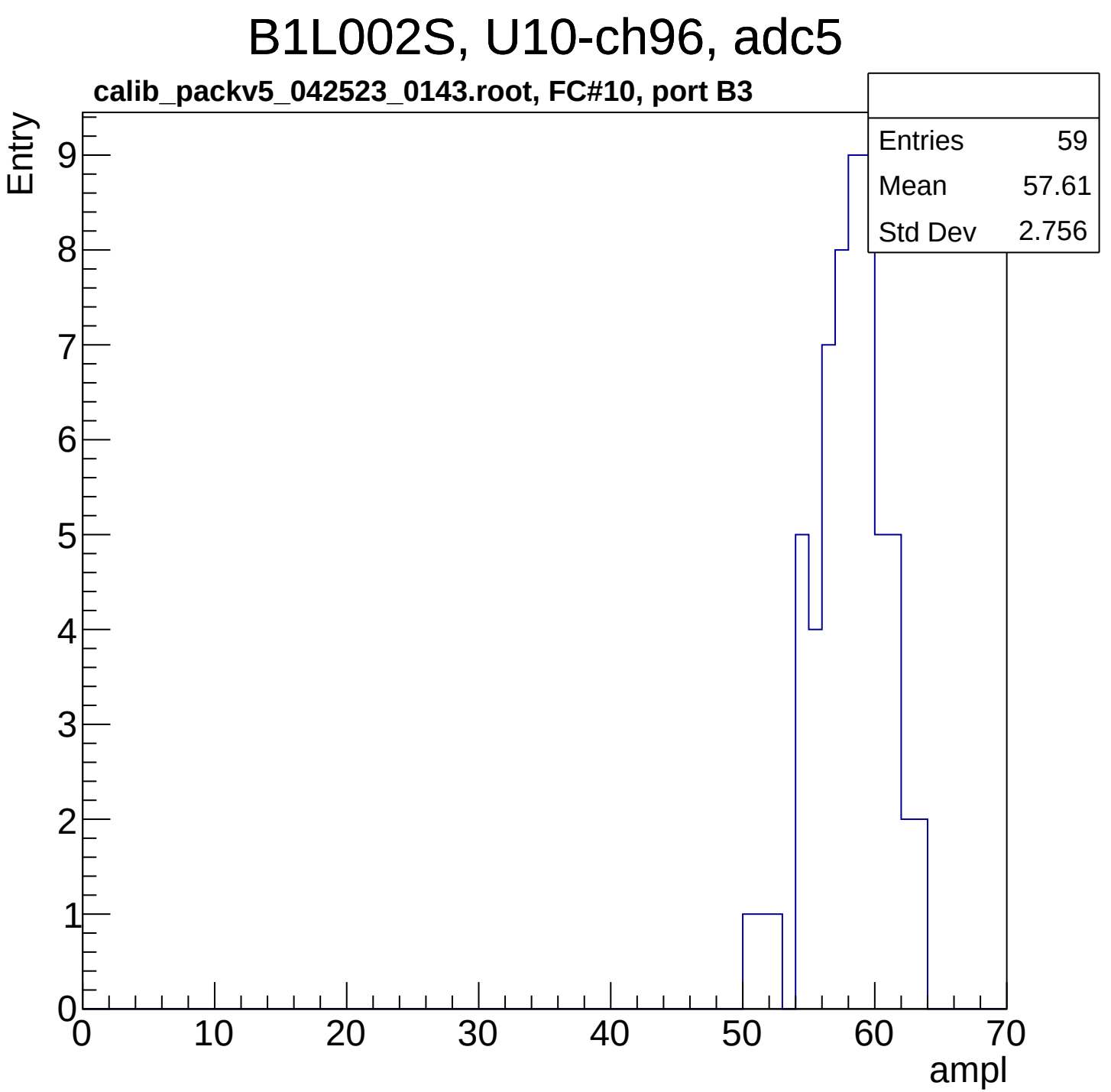
B1L002S, U10-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.61
Std Dev 2.756
Entry
ampl
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2
3
4
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6
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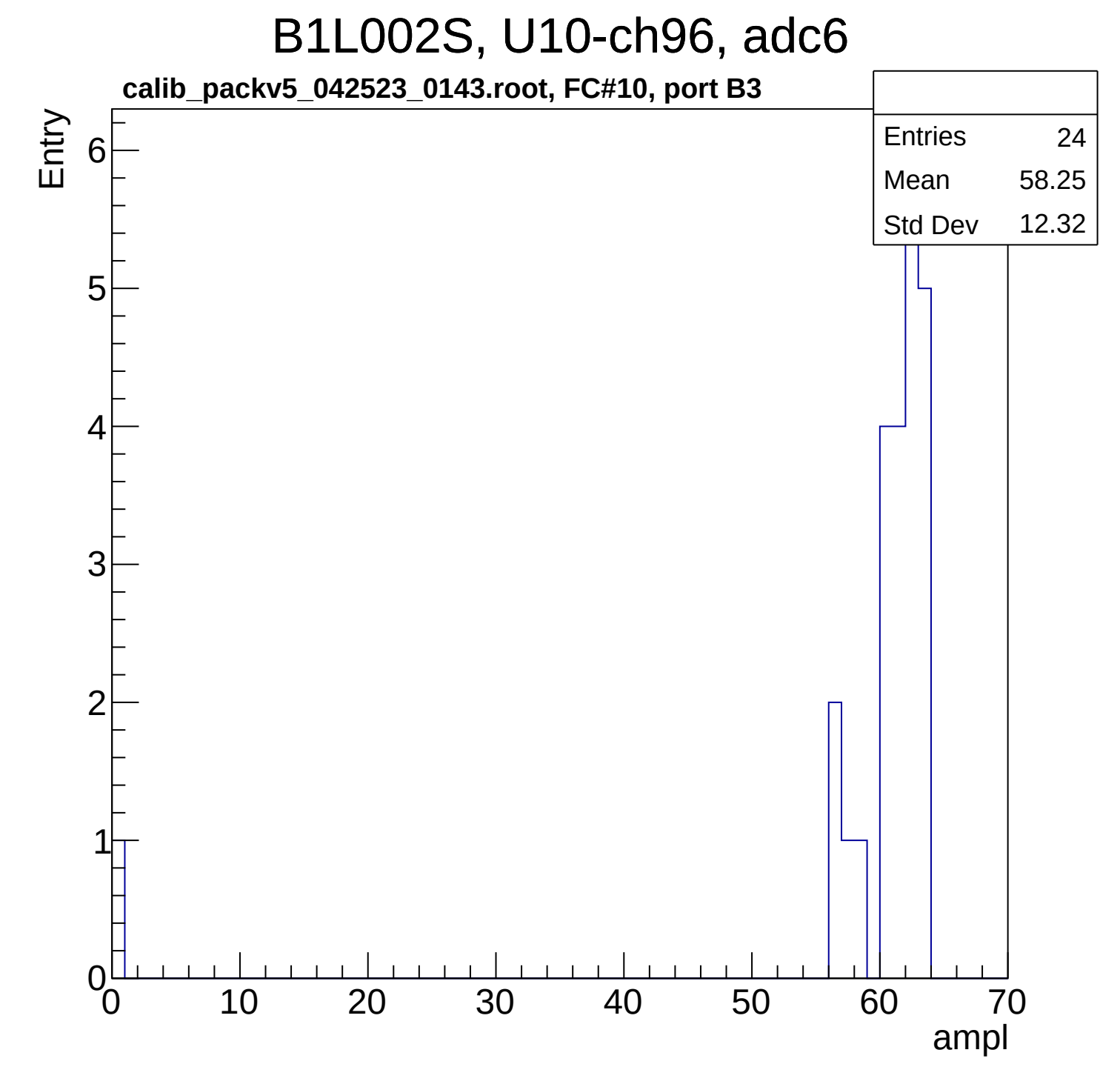
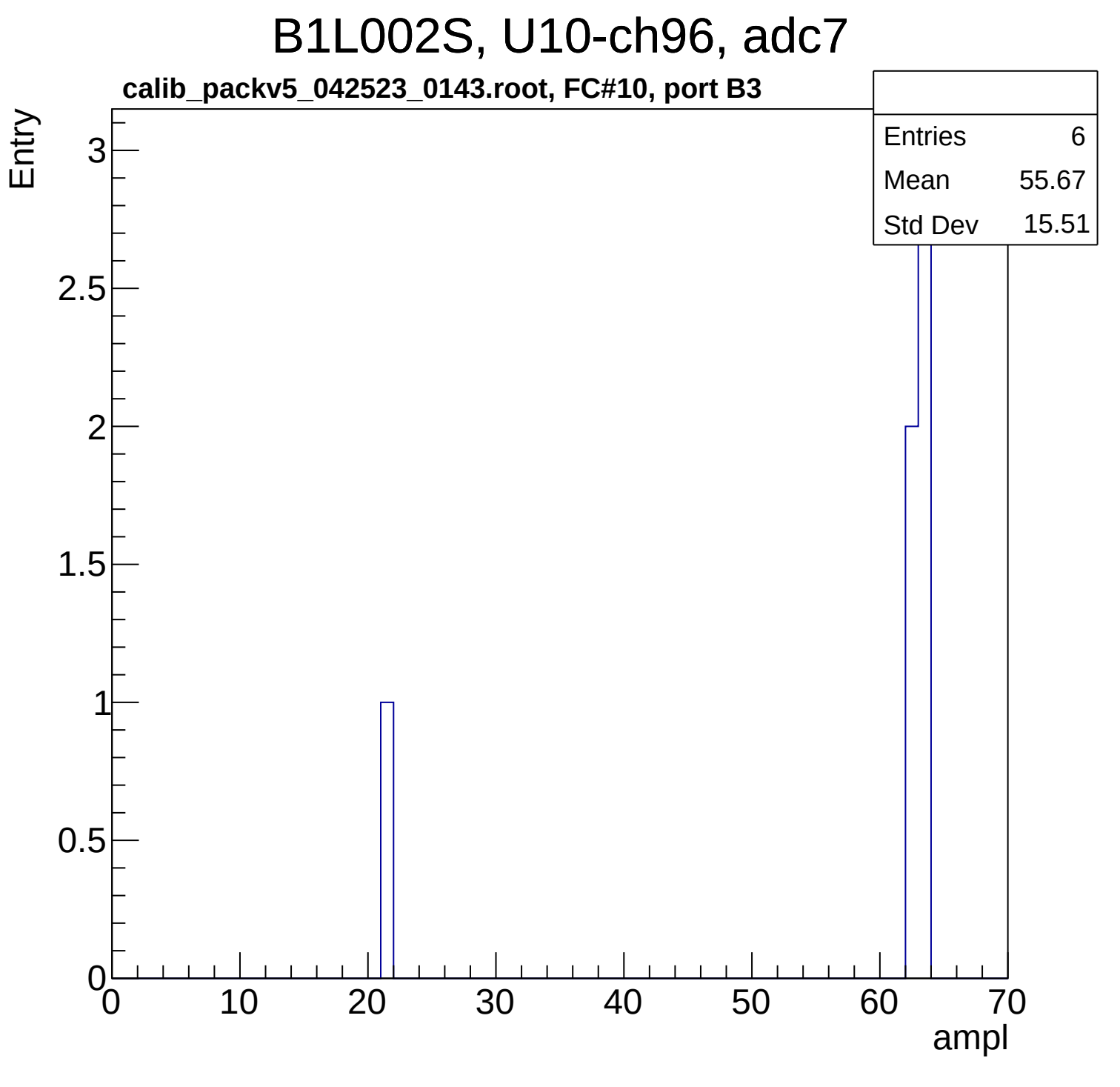

B1L002S, U10-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.25
Std Dev 12.32
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 55.67
Std Dev 15.51
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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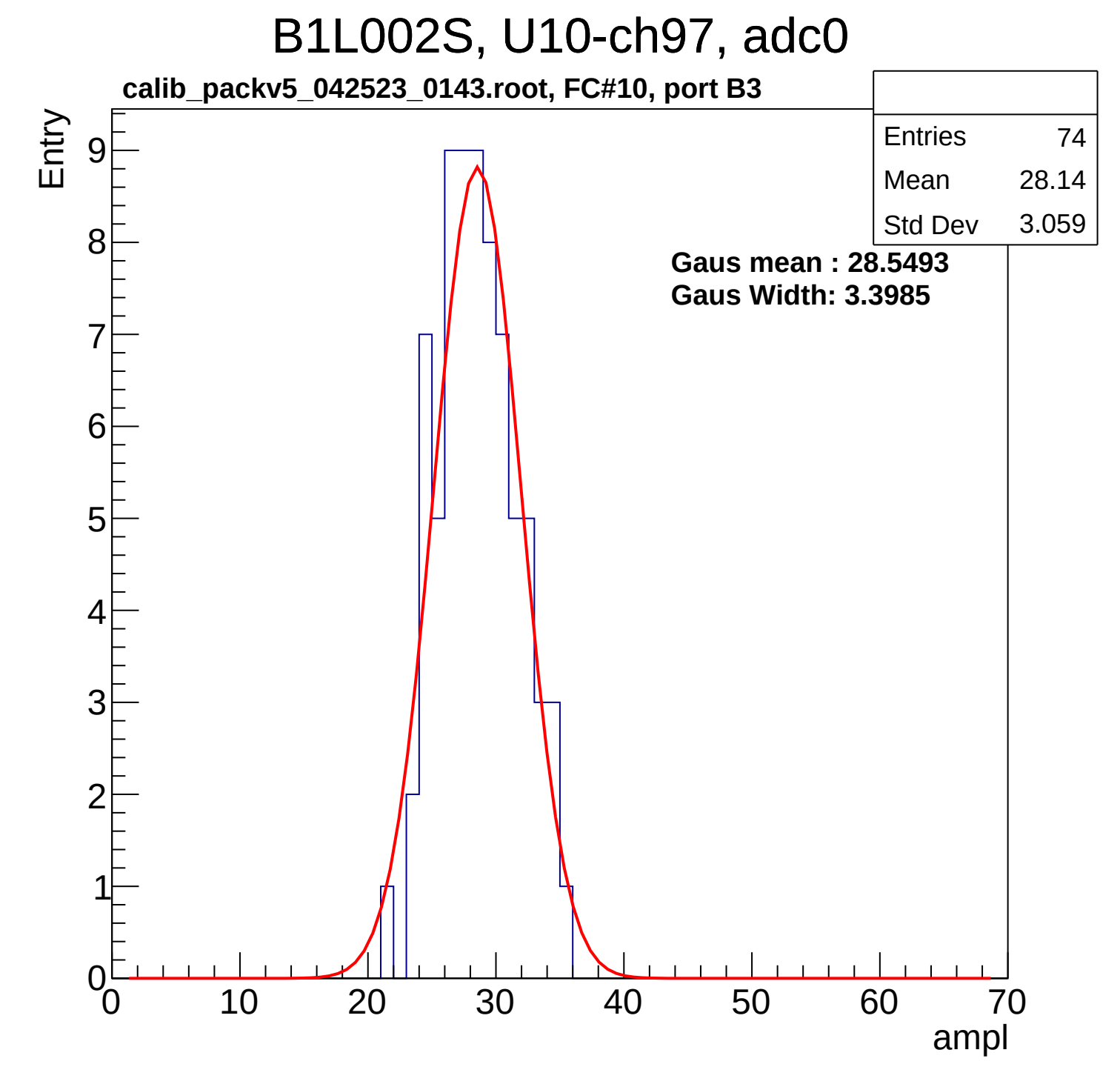
B1L002S, U10-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.14
Std Dev 3.059
Gaus mean : 28.5493
Gaus Width: 3.3985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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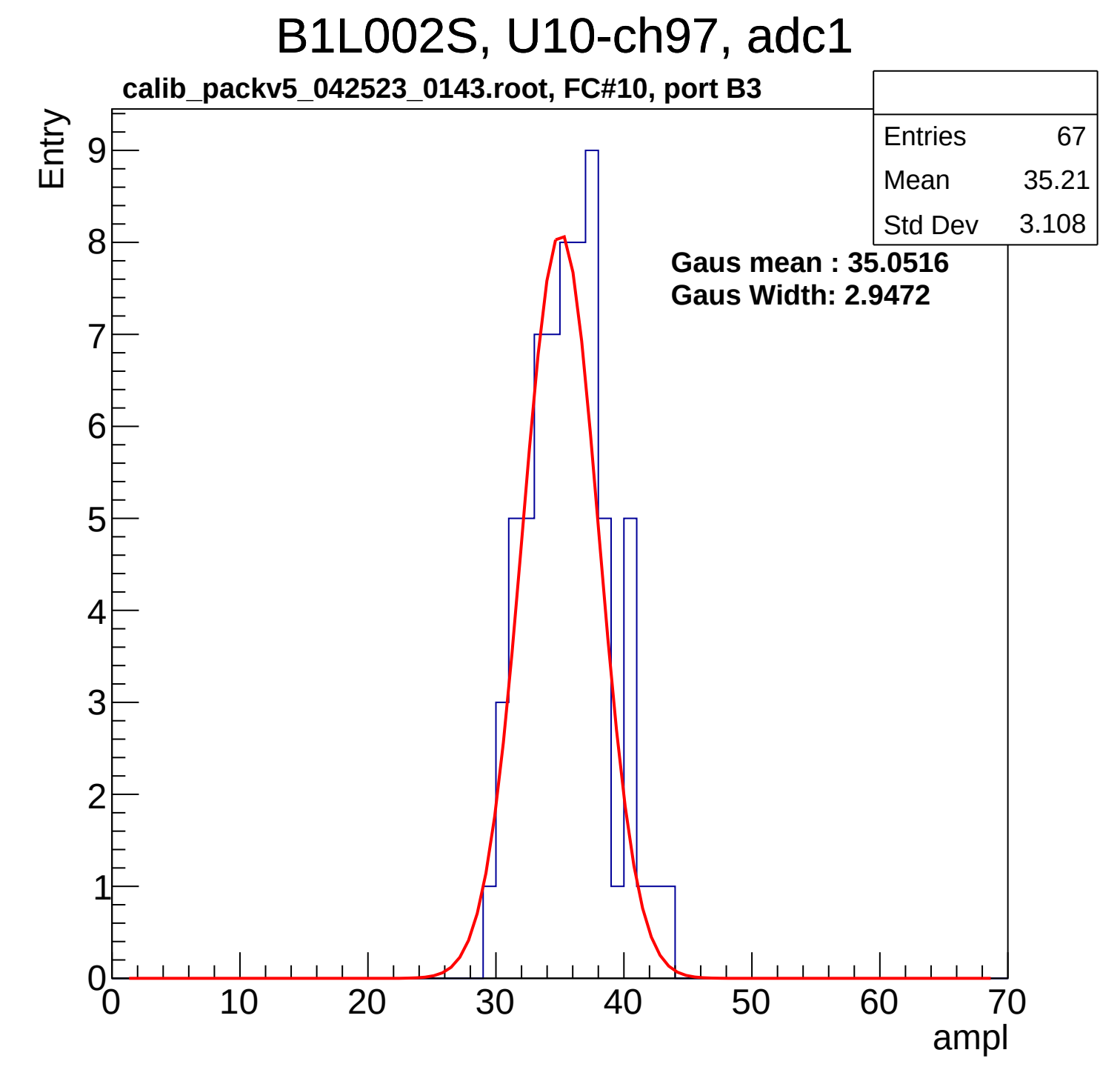
B1L002S, U10-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.21
Std Dev 3.108
Gaus mean : 35.0516
Gaus Width: 2.9472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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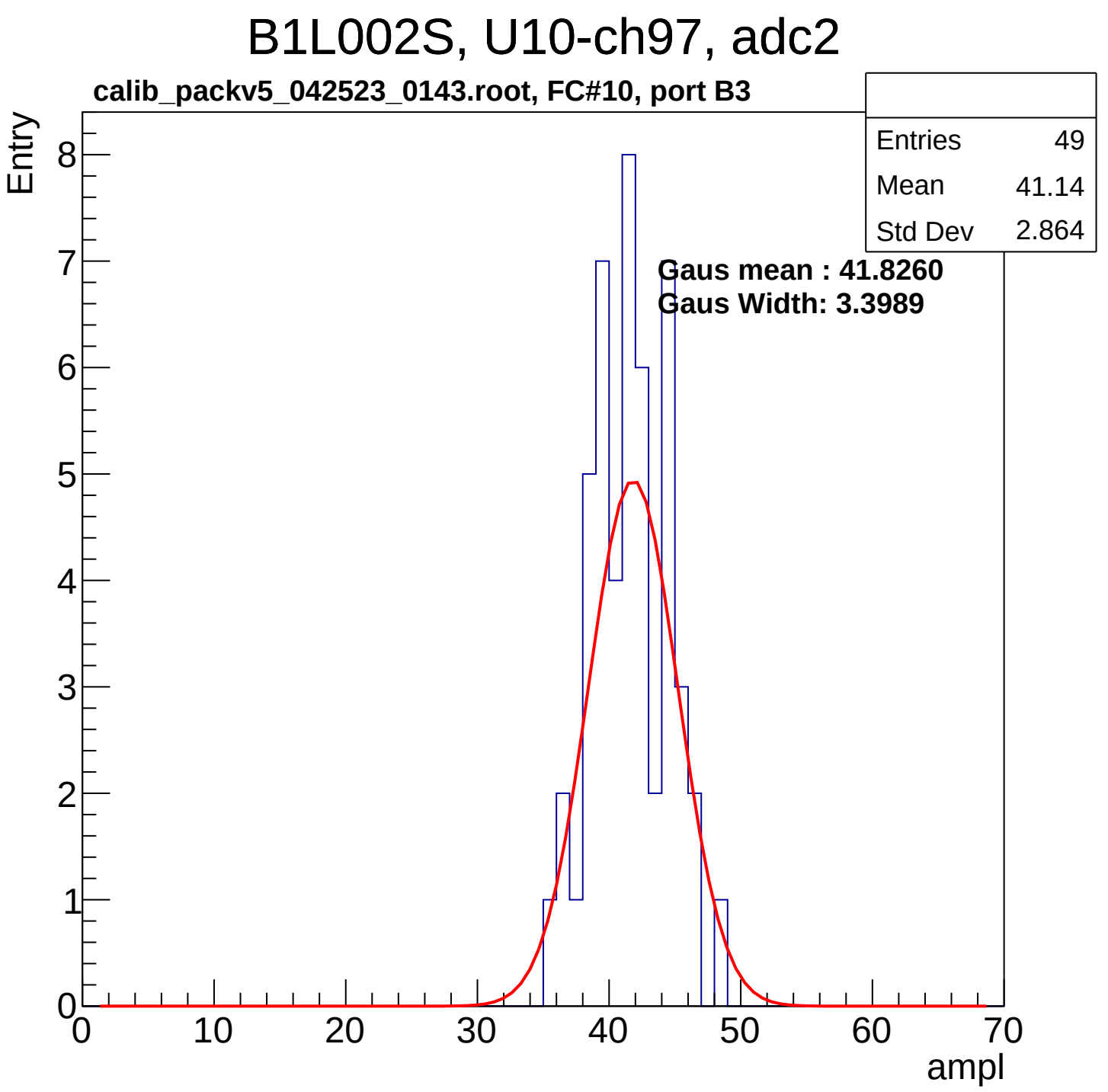
B1L002S, U10-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.14
Std Dev 2.864
Gaus mean : 41.8260
Gaus Width: 3.3989
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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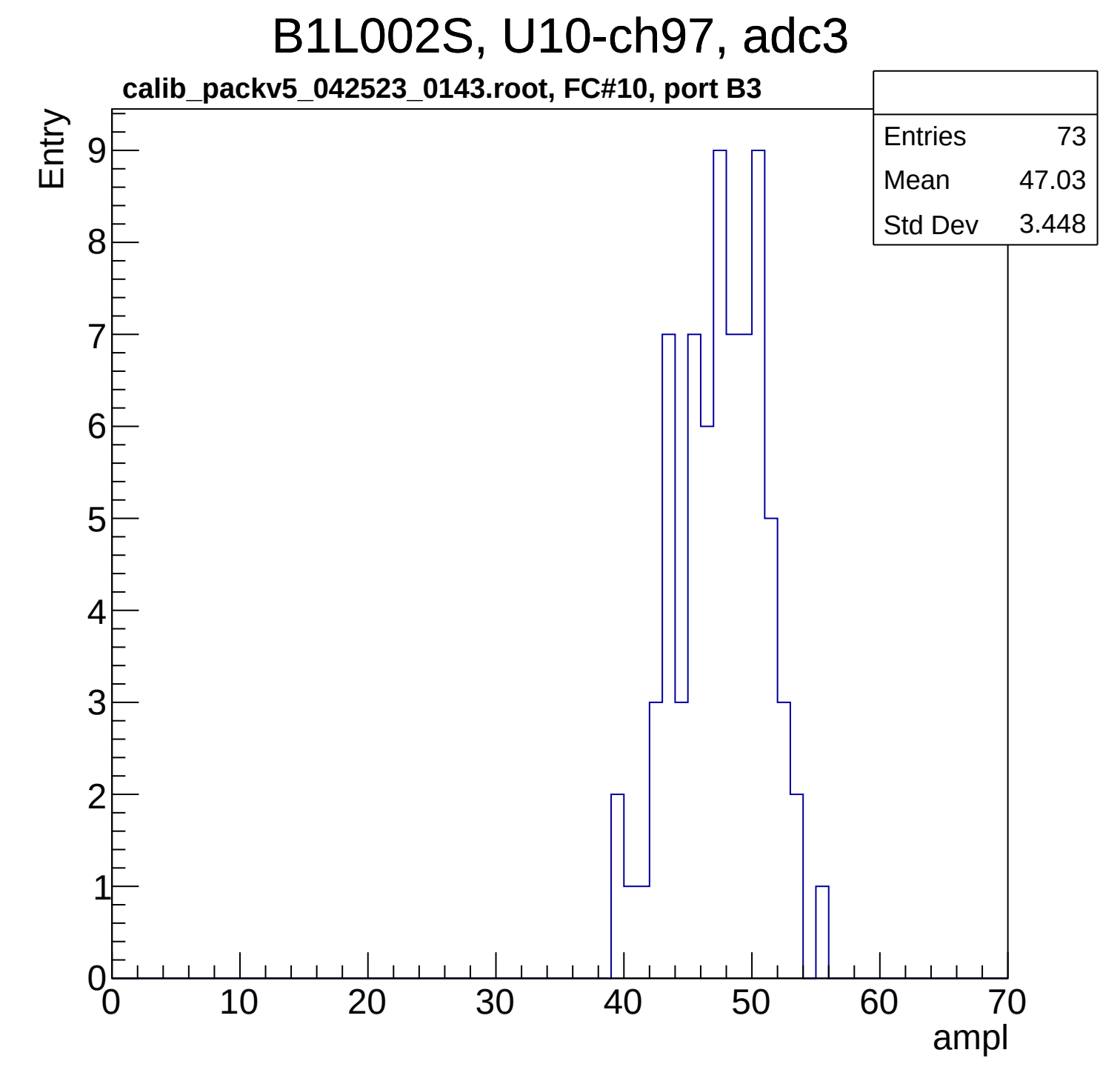
B1L002S, U10-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.03
Std Dev 3.448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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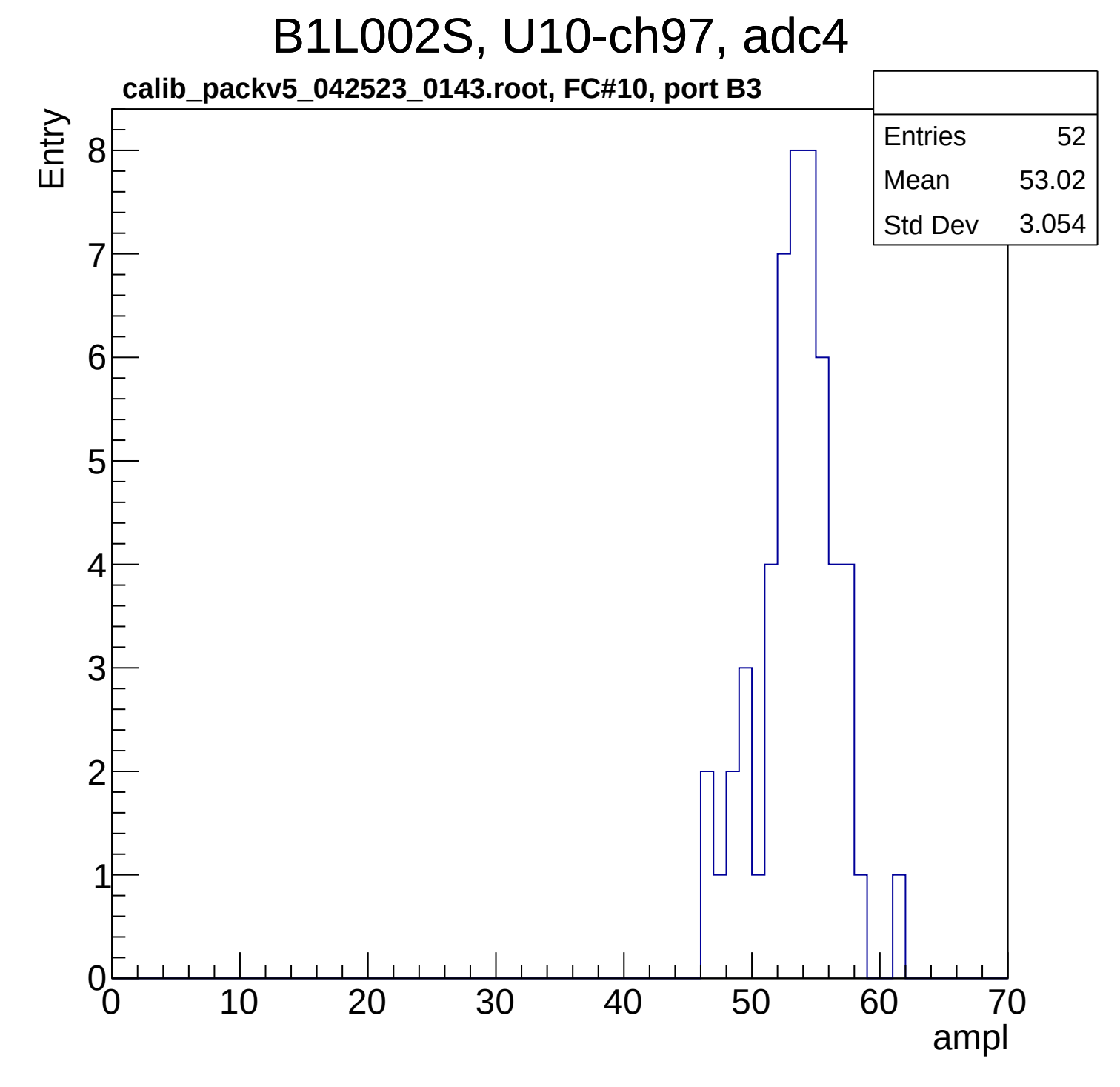
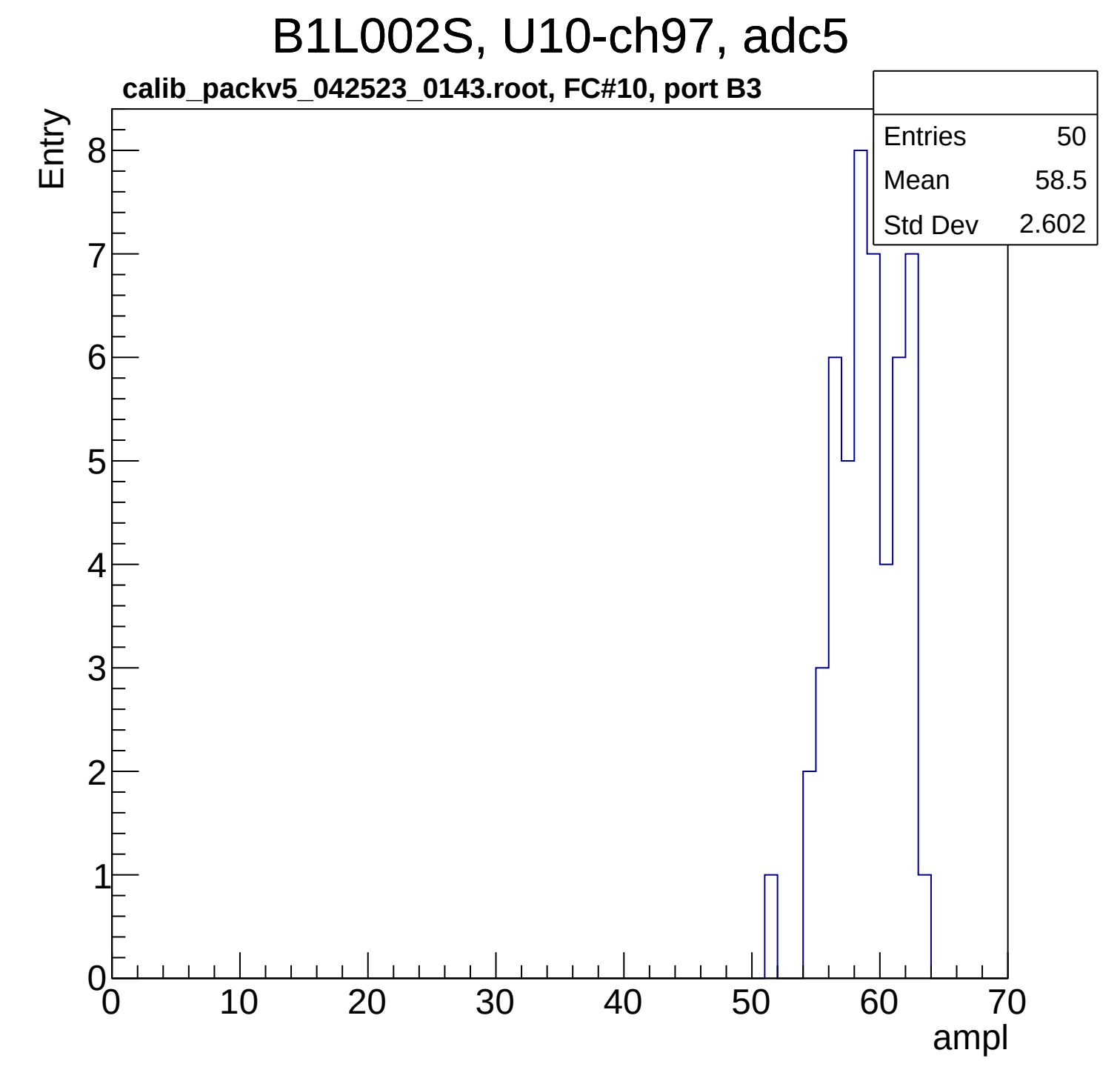

B1L002S, U10-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.02
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.5
Std Dev 2.602
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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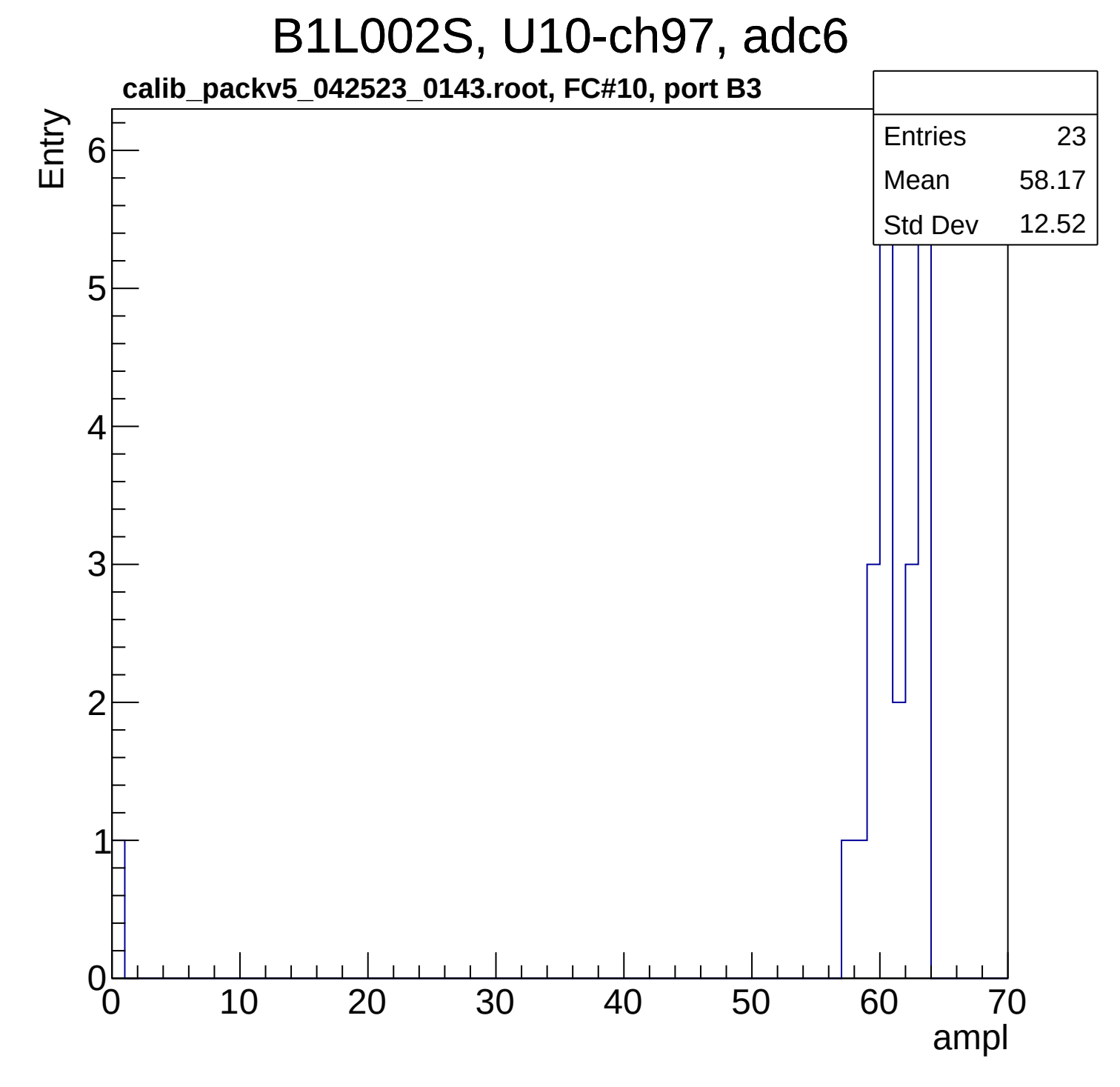
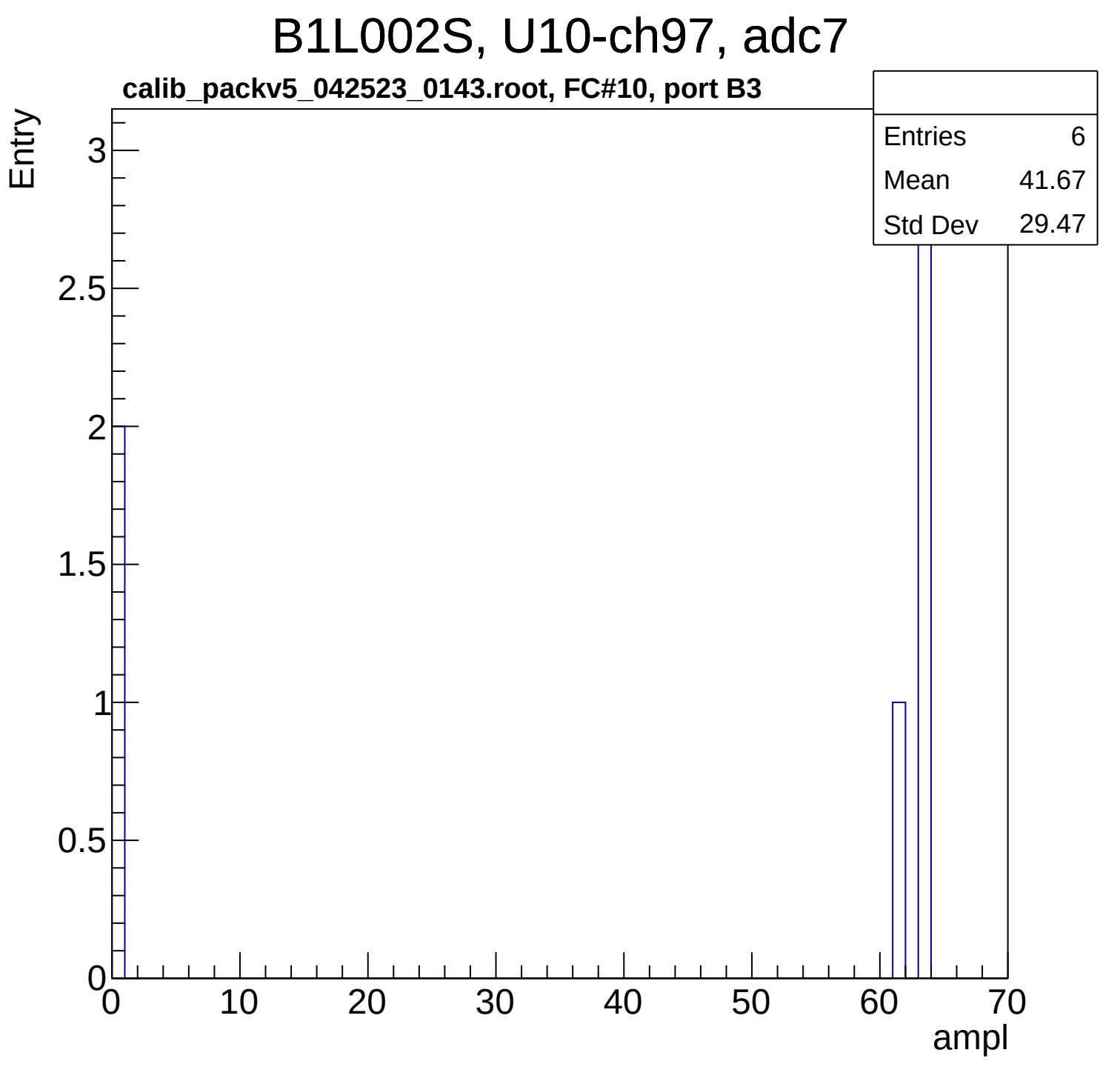

B1L002S, U10-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.17
Std Dev 12.52
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.67
Std Dev 29.47
Entry
ampl
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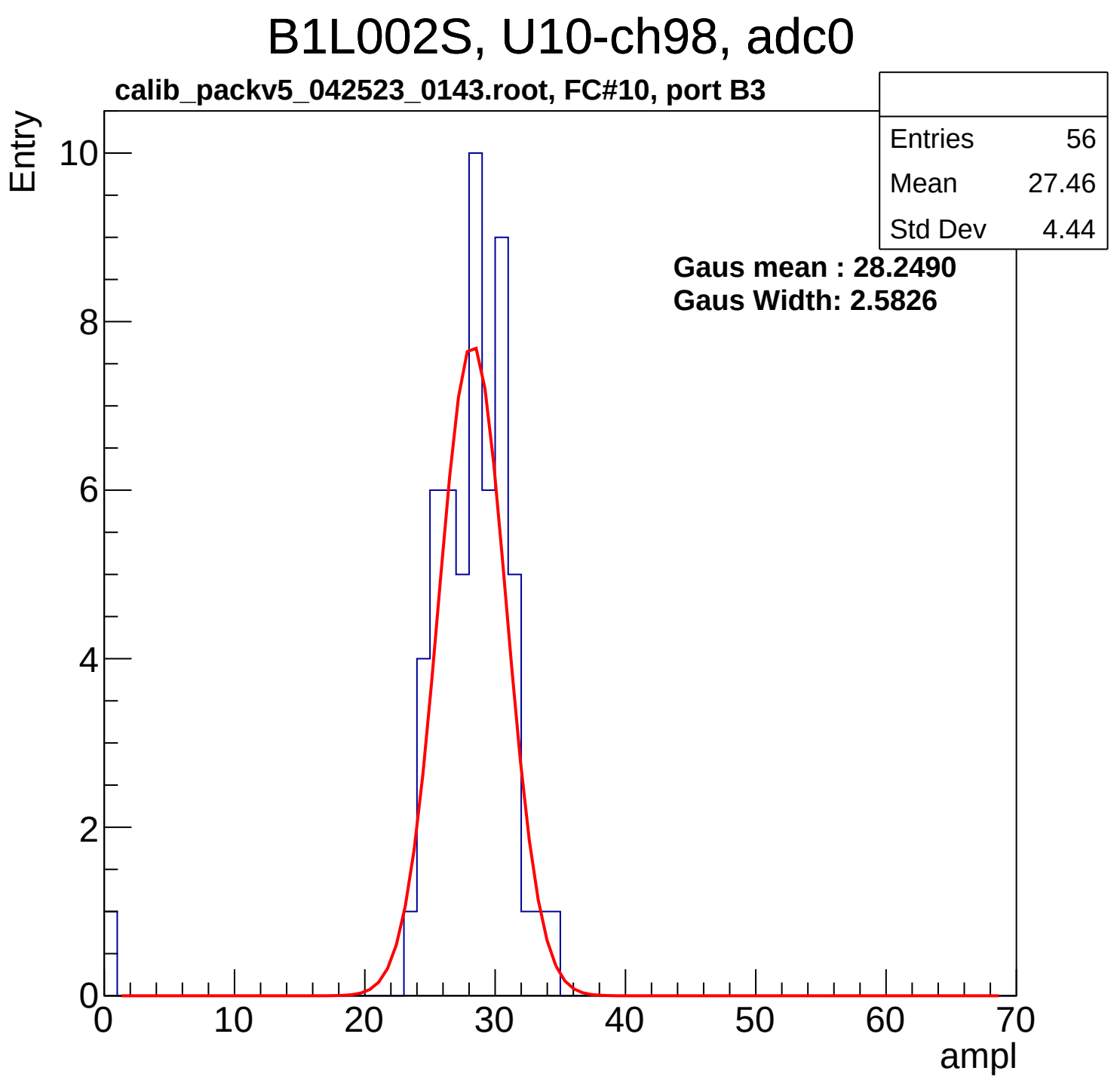
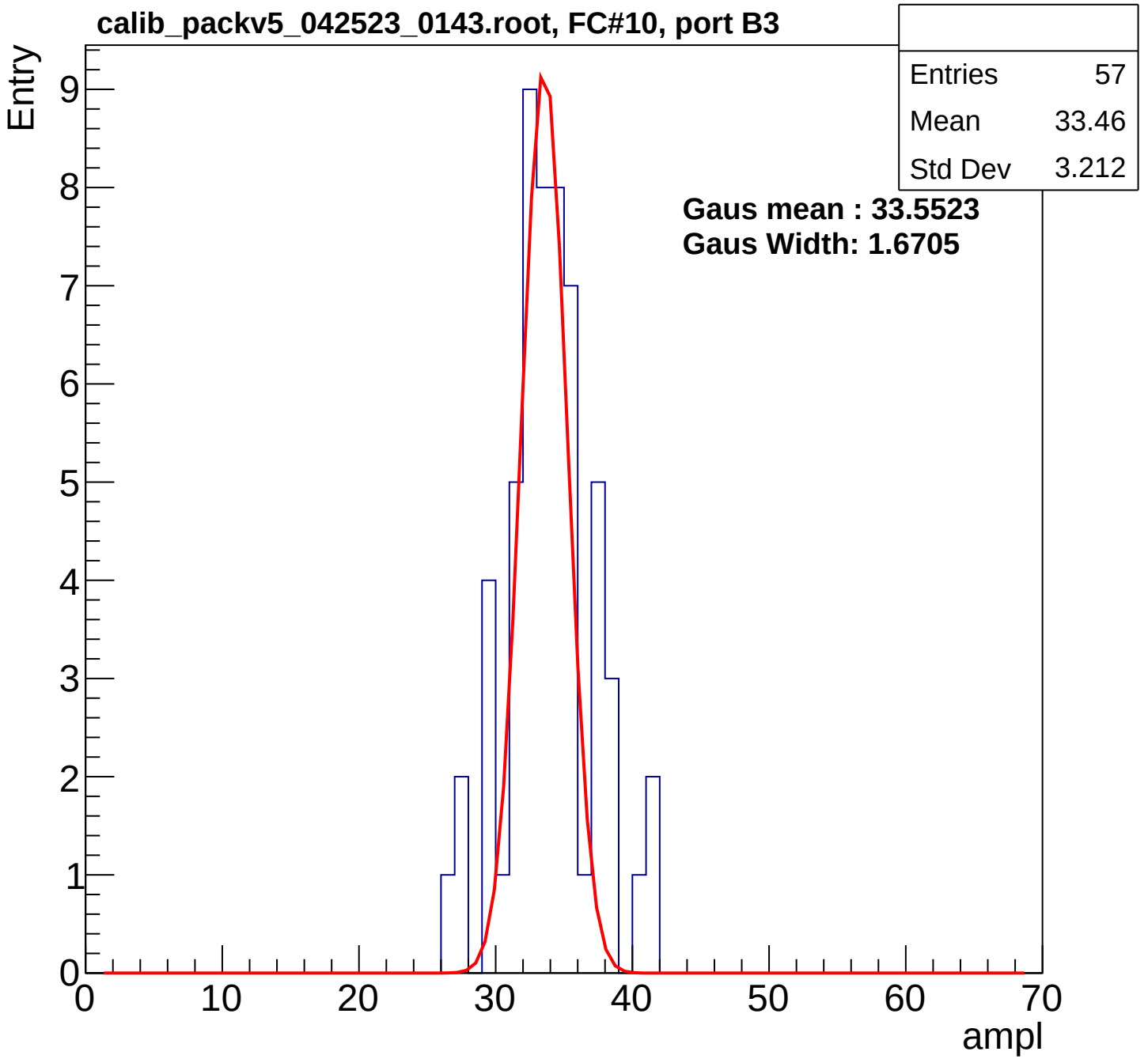
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.46
Std Dev 4.44
Gaus mean : 28.2490
Gaus Width: 2.5826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.46
Std Dev 3.212
Gaus mean : 33.5523
Gaus Width: 1.6705
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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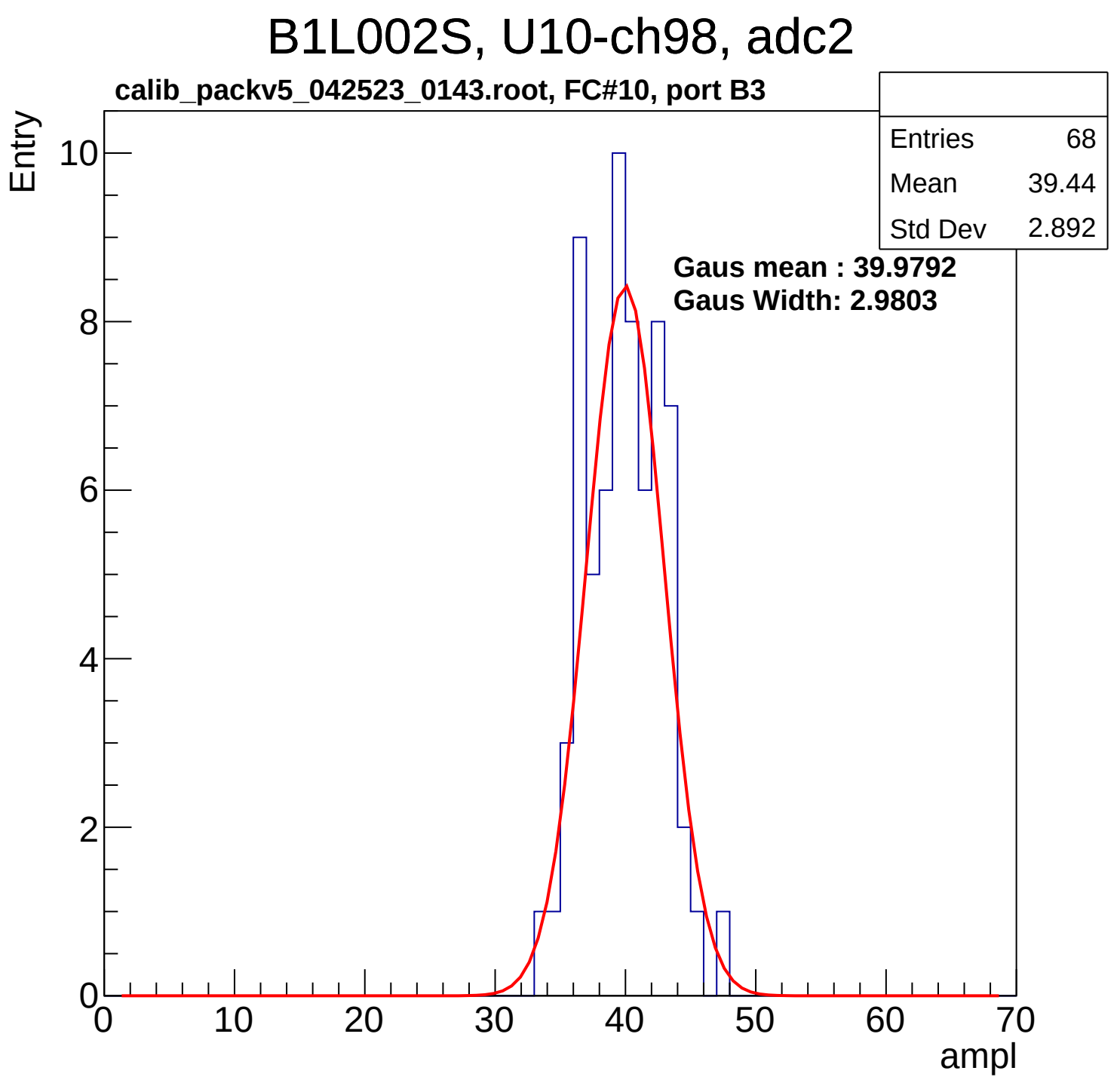
B1L002S, U10-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 39.44
Std Dev 2.892
Gaus mean : 39.9792
Gaus Width: 2.9803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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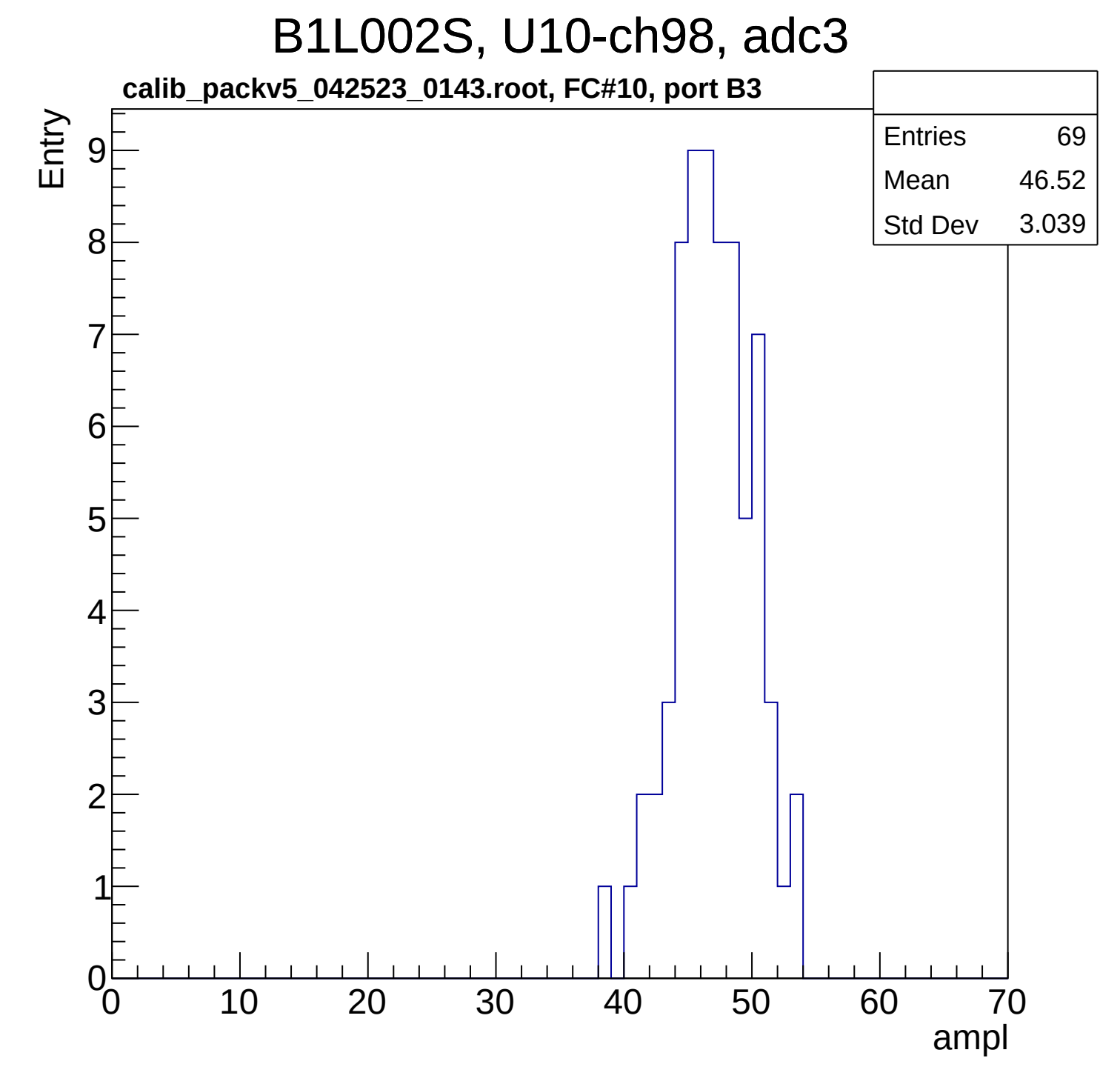
B1L002S, U10-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.52
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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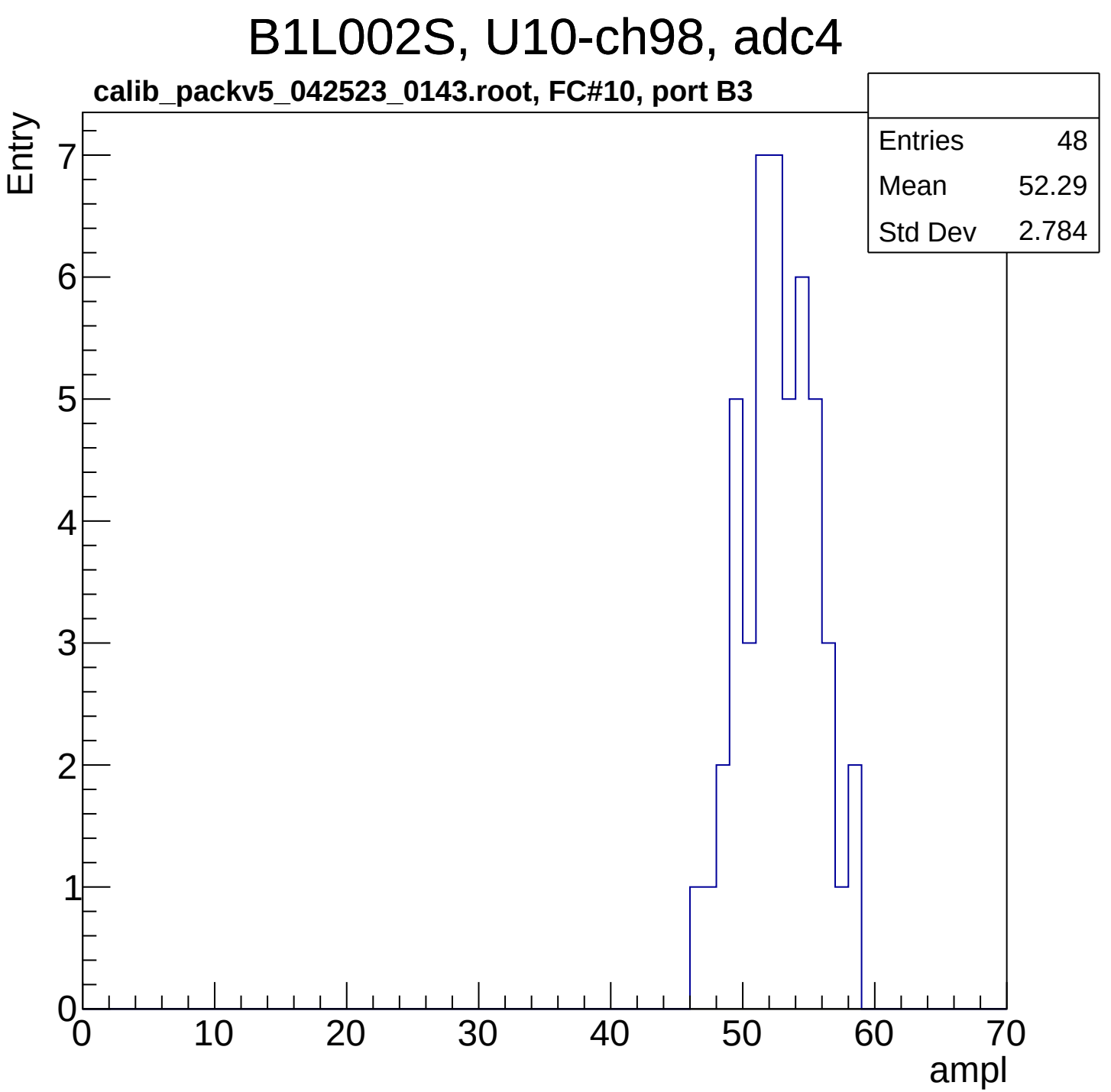
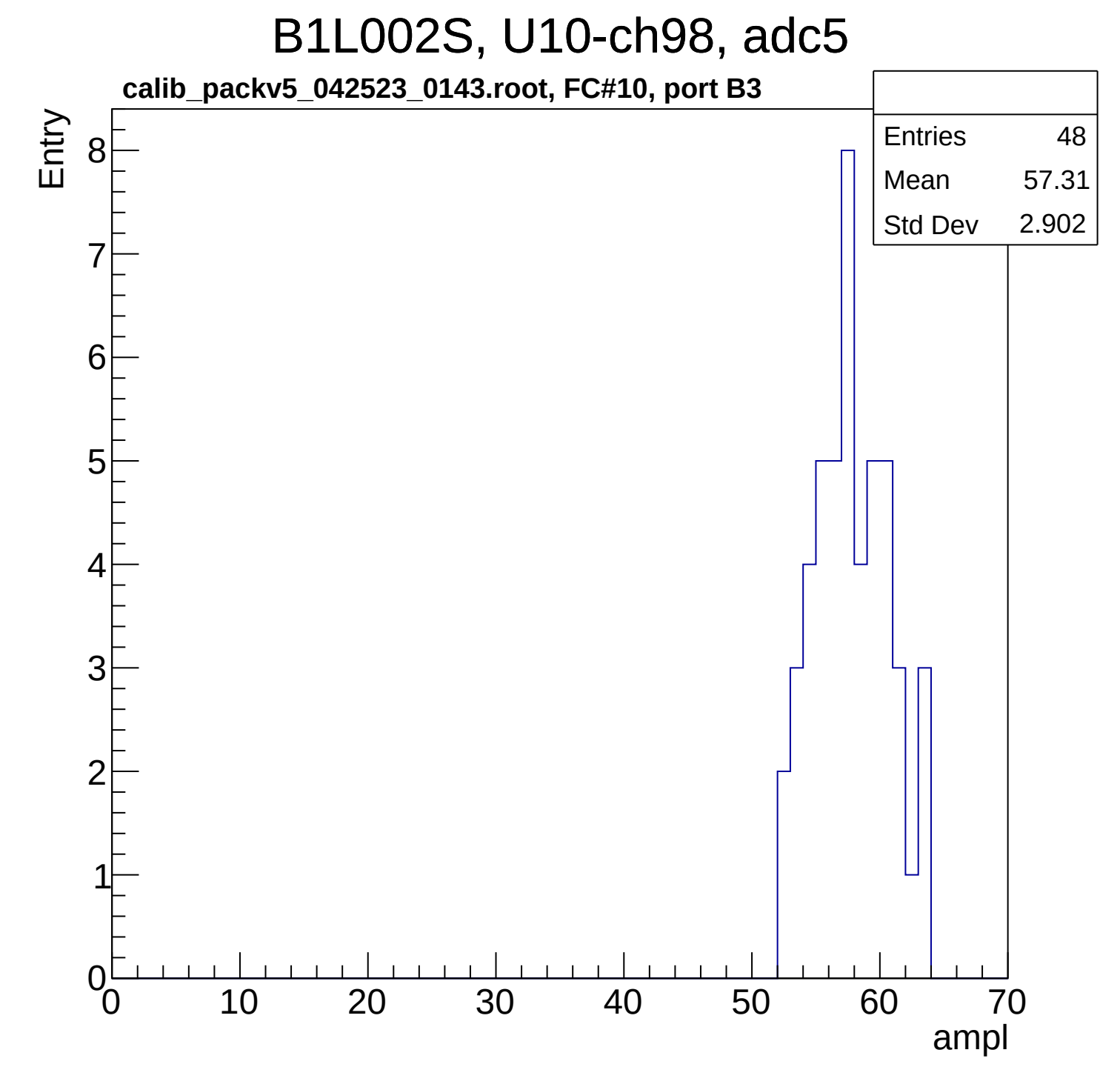

B1L002S, U10-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.29
Std Dev 2.784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.31
Std Dev 2.902
Entry
0
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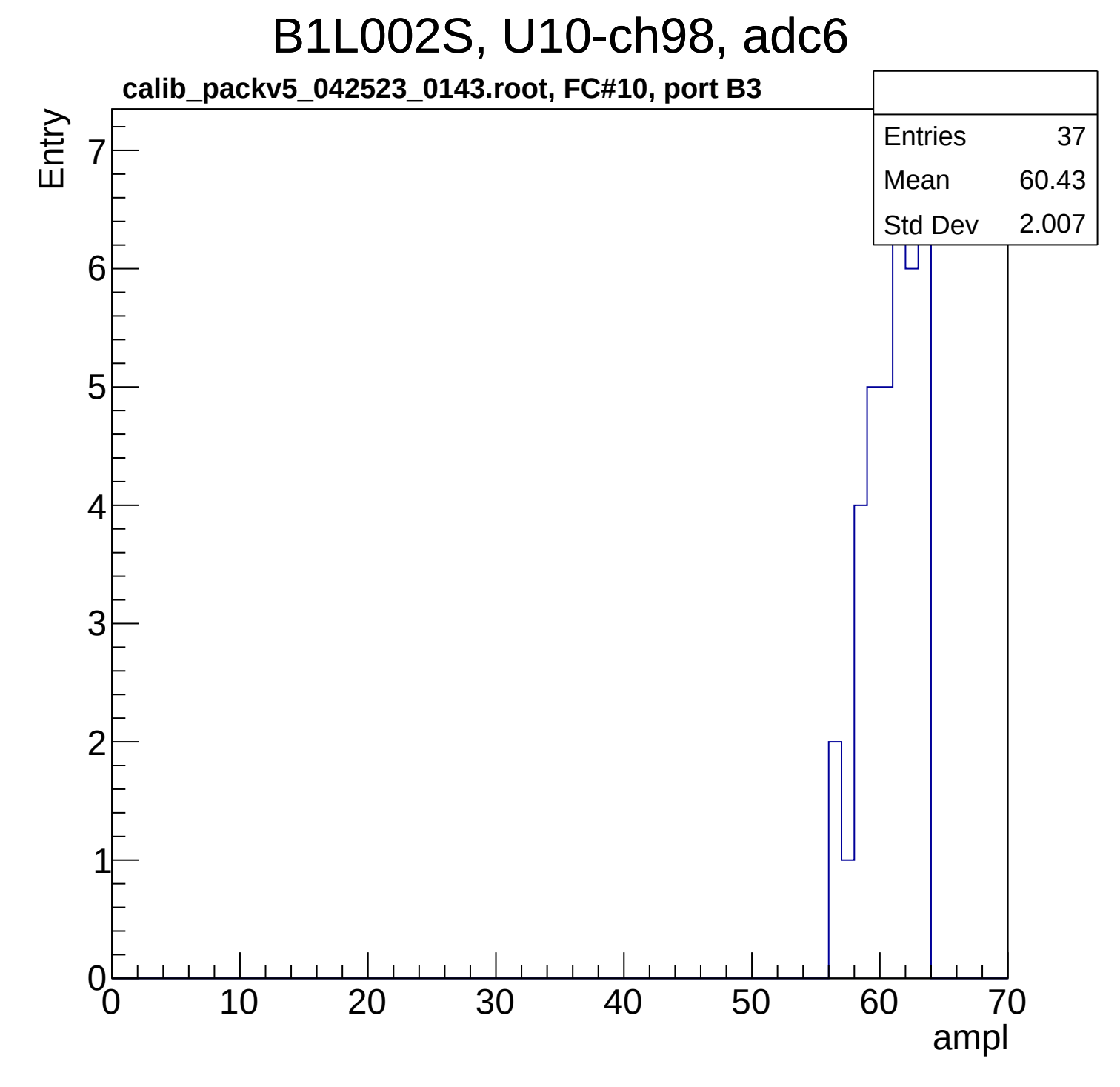
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 60.43
Std Dev 2.007
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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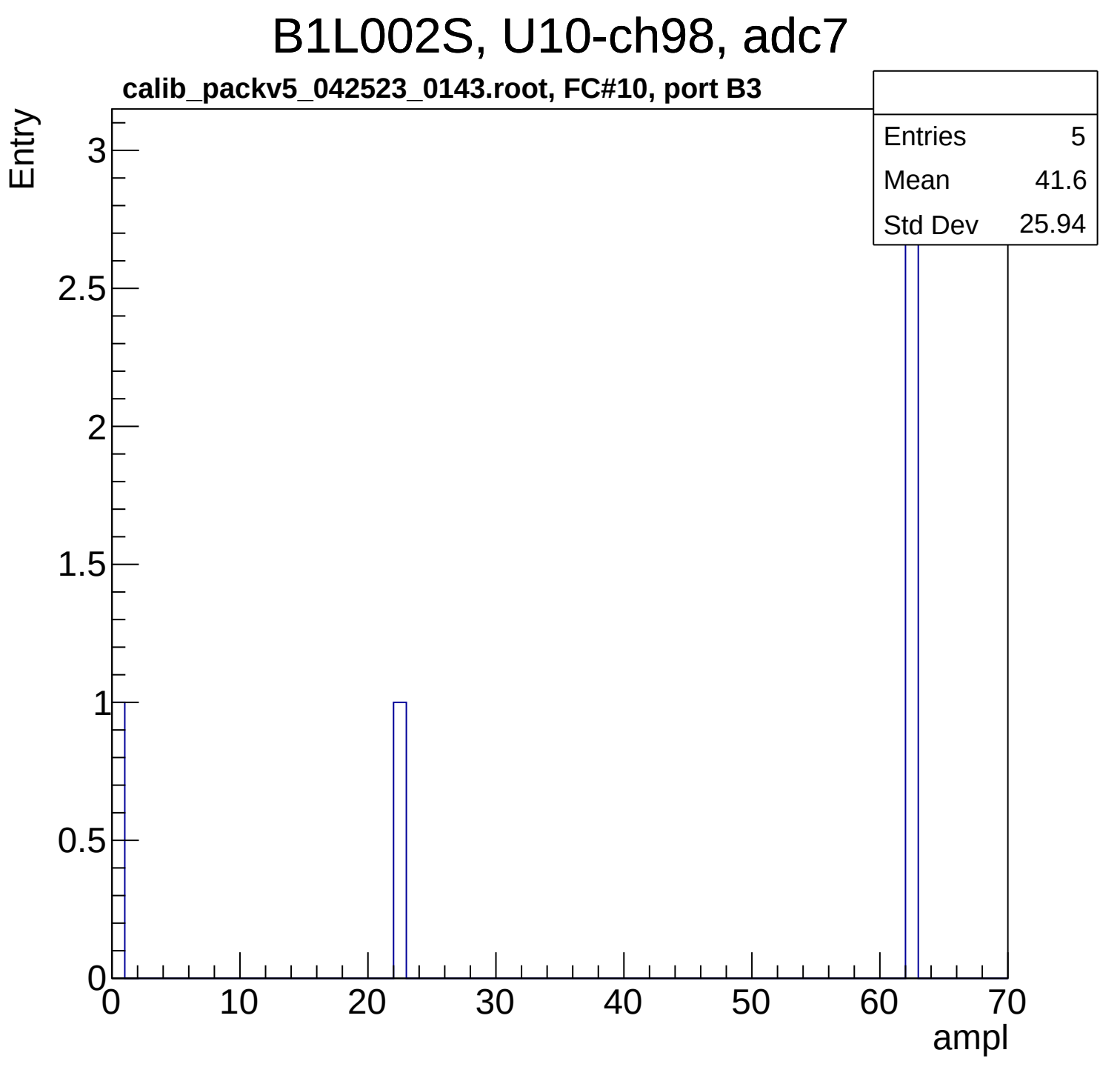
B1L002S, U10-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 41.6
Std Dev 25.94
Entry
ampl
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2.5
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0
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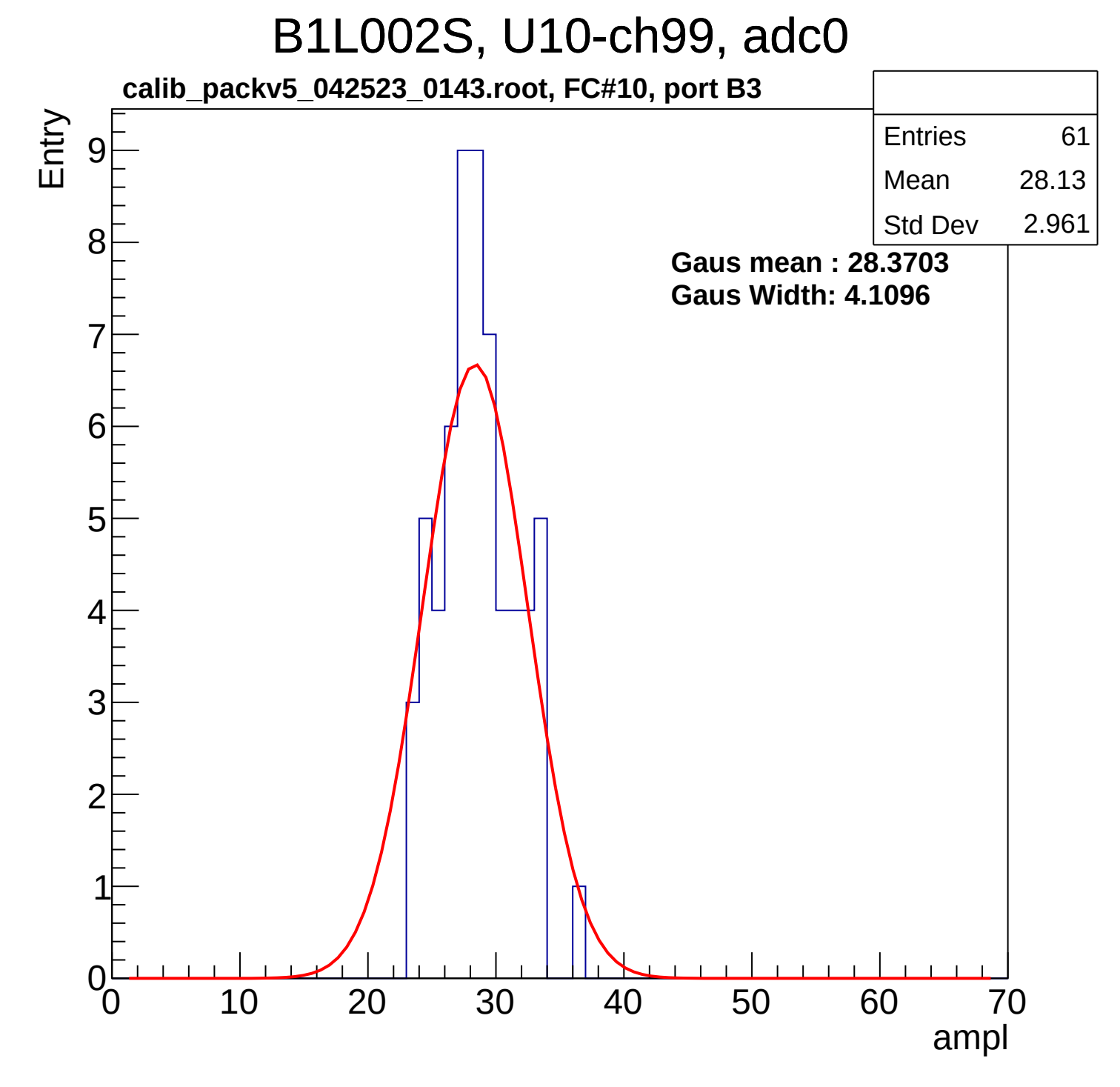
B1L002S, U10-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.13
Std Dev 2.961
Gaus mean : 28.3703
Gaus Width: 4.1096
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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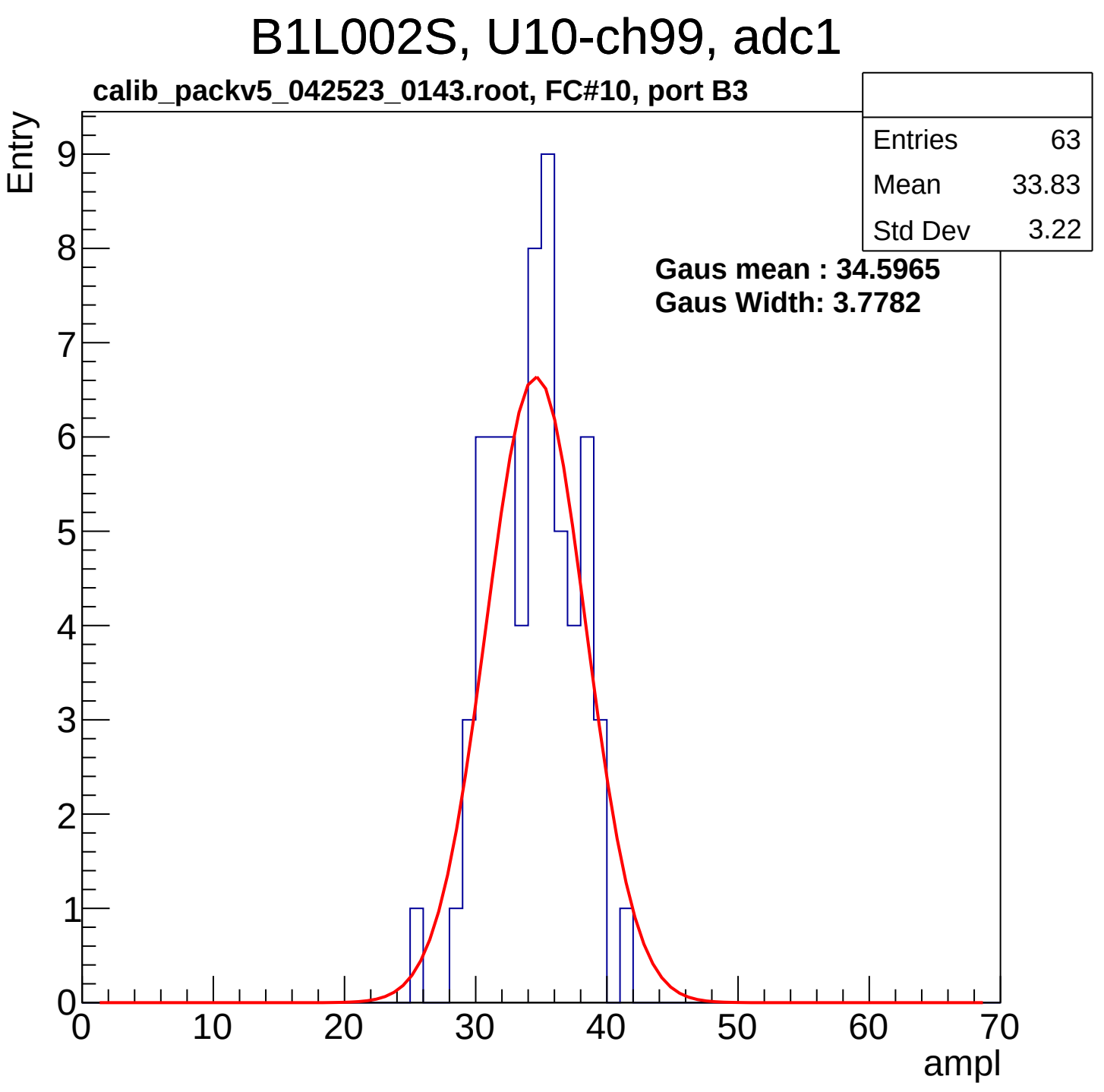
B1L002S, U10-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.83
Std Dev 3.22
Gaus mean : 34.5965
Gaus Width: 3.7782
Entry
ampl
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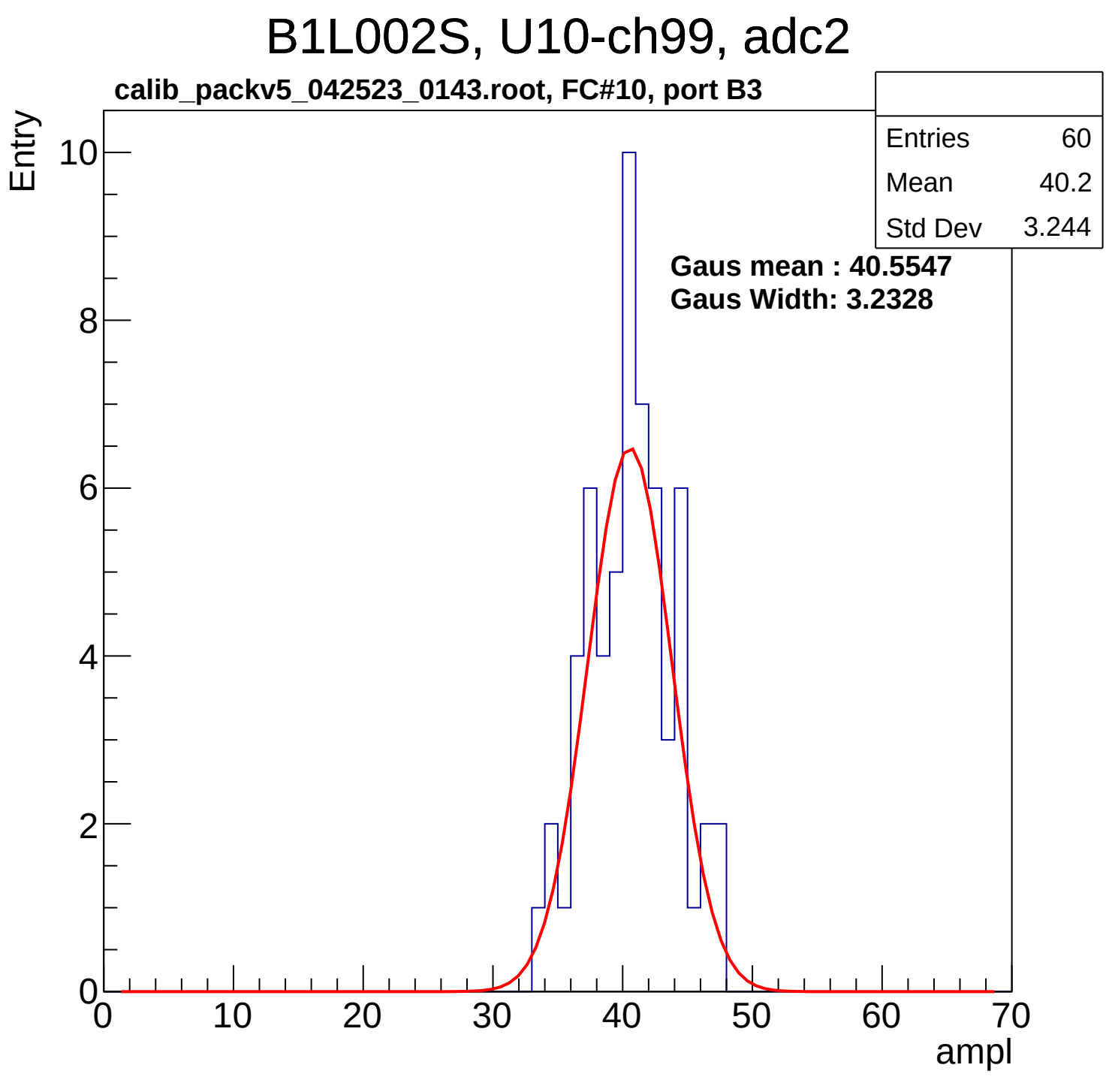
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U10-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.2
Std Dev 3.244
Gaus mean : 40.5547
Gaus Width: 3.2328
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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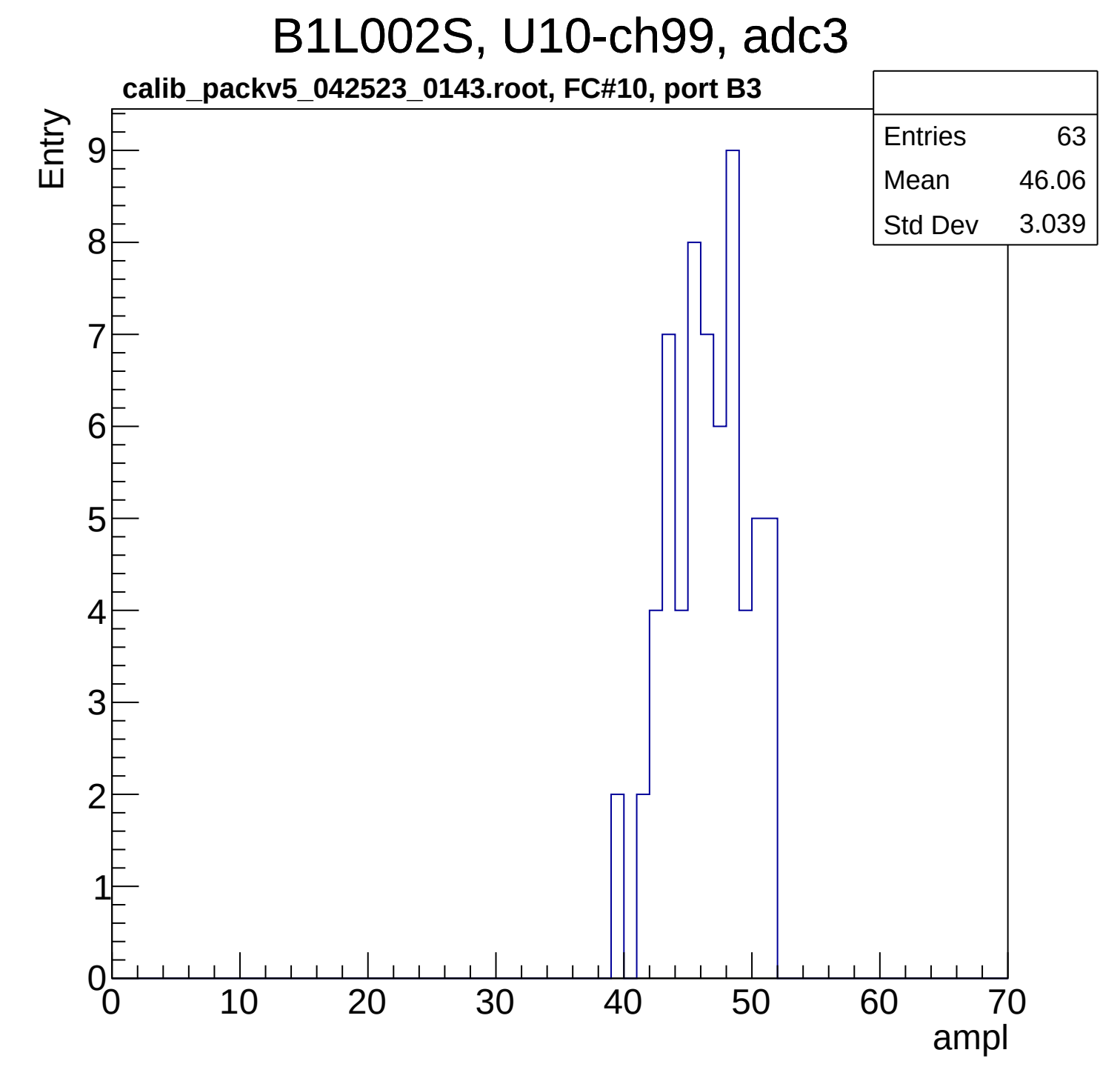
B1L002S, U10-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.06
Std Dev 3.039
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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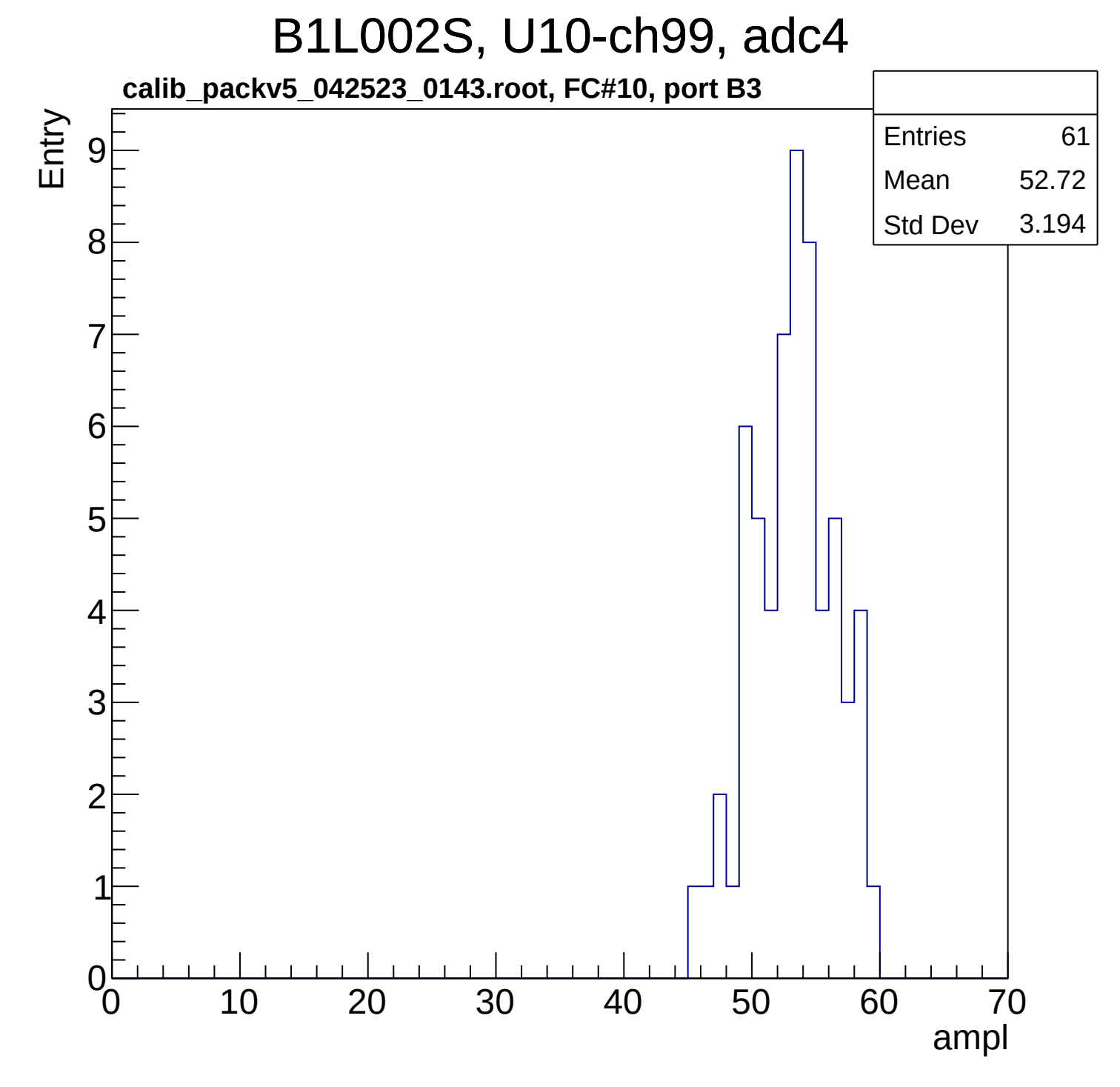
B1L002S, U10-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.72
Std Dev 3.194
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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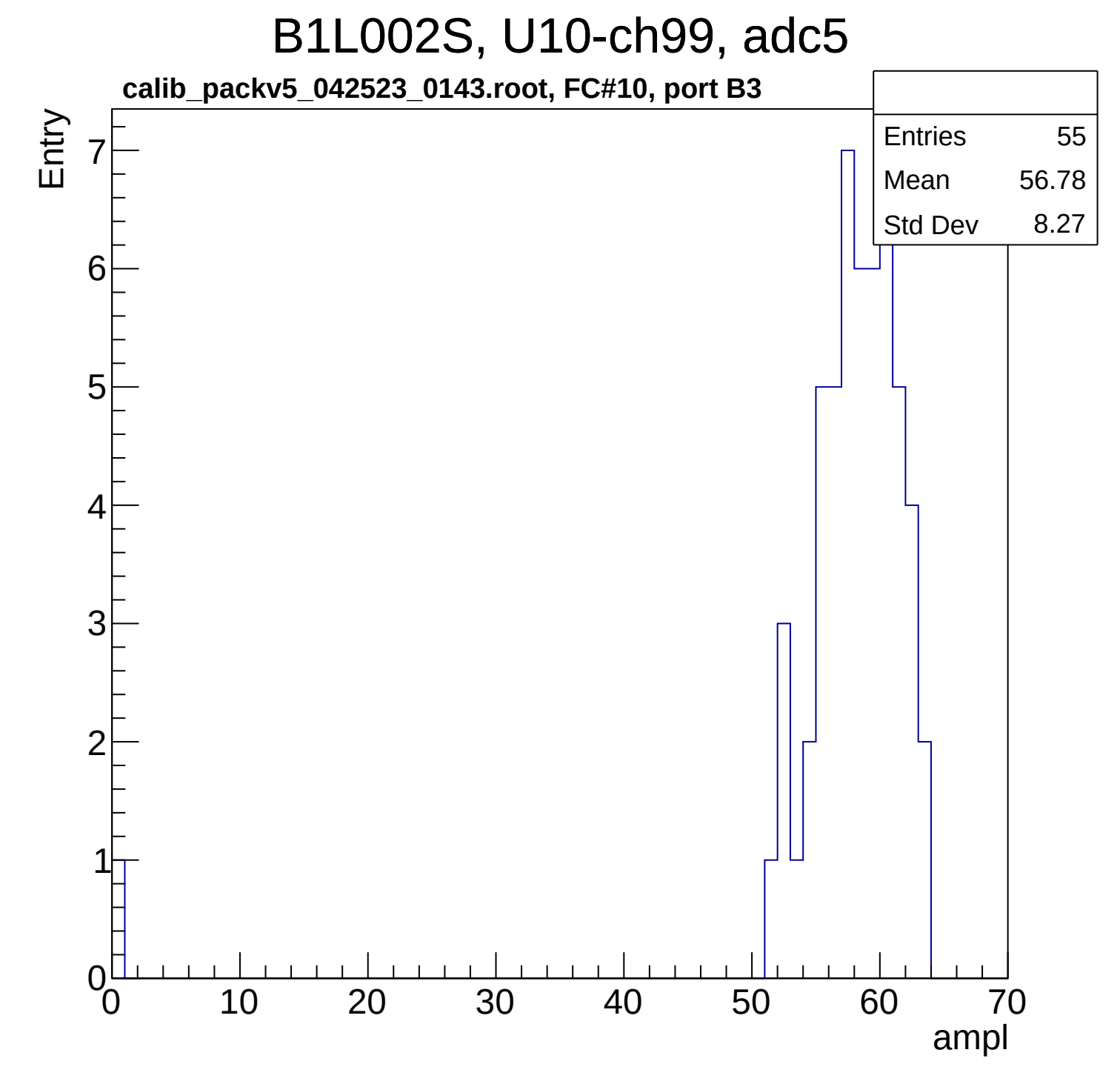
B1L002S, U10-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 56.78
Std Dev 8.27
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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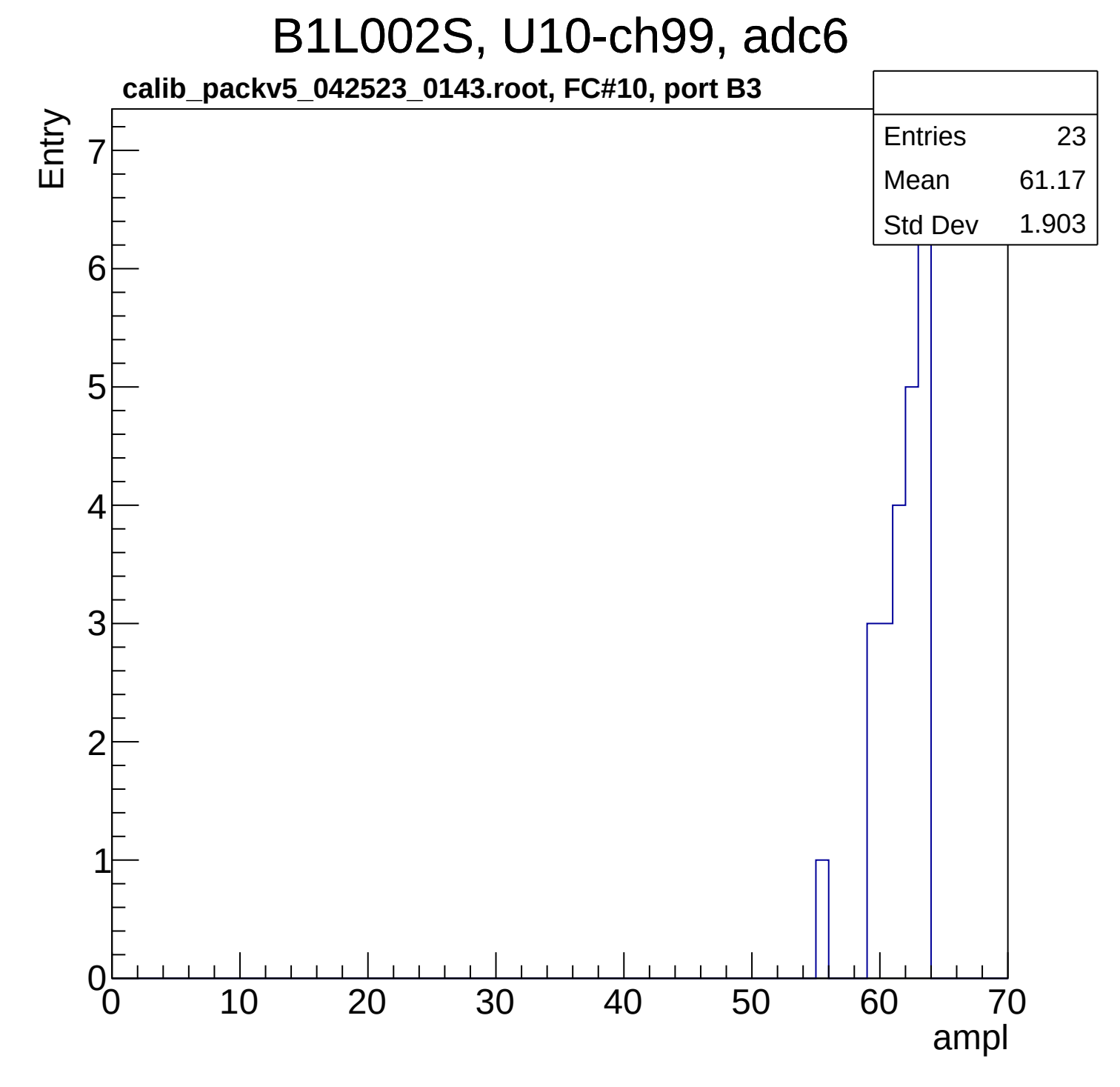
B1L002S, U10-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.17
Std Dev 1.903
Entry
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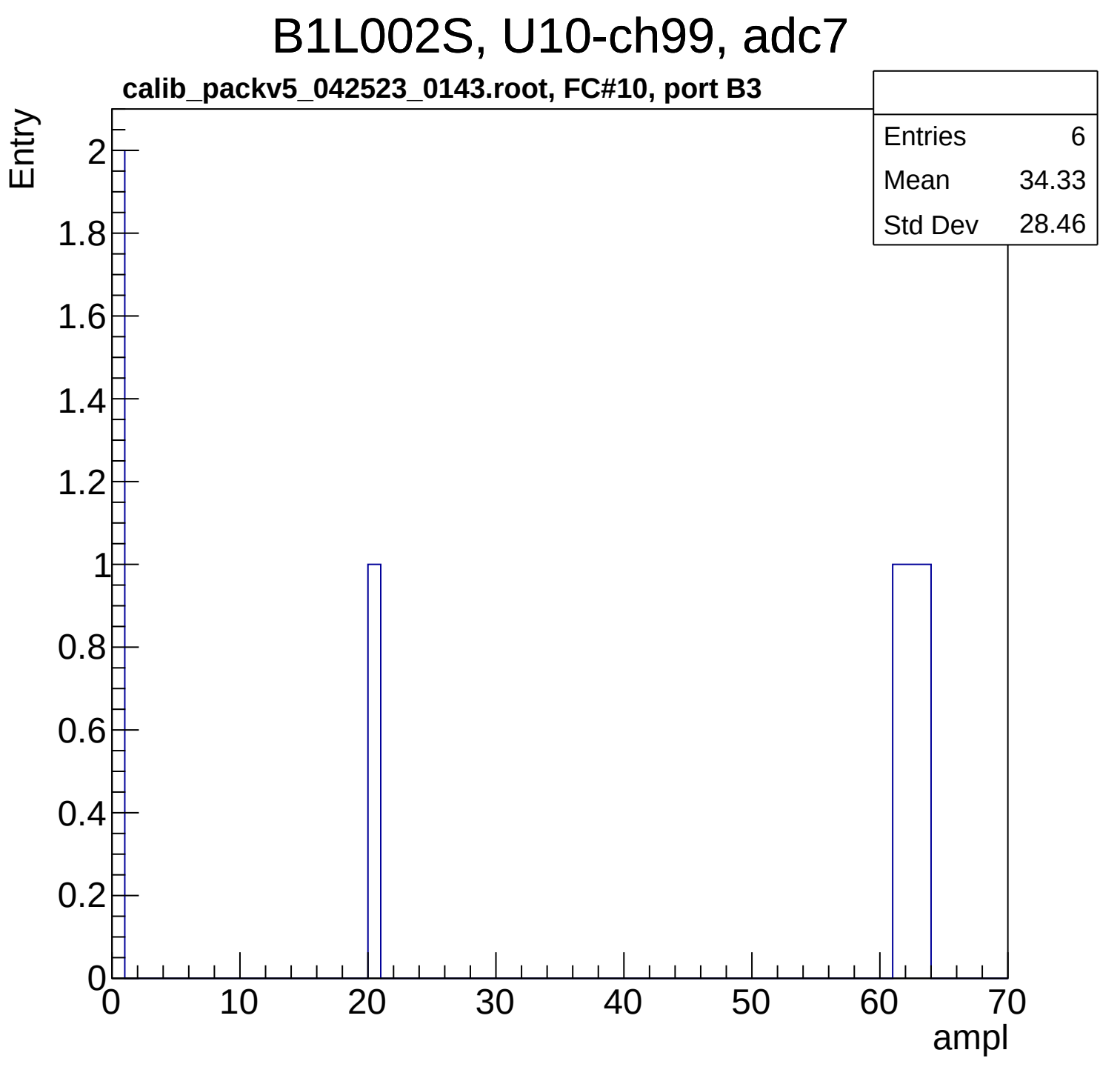
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 34.33
Std Dev 28.46
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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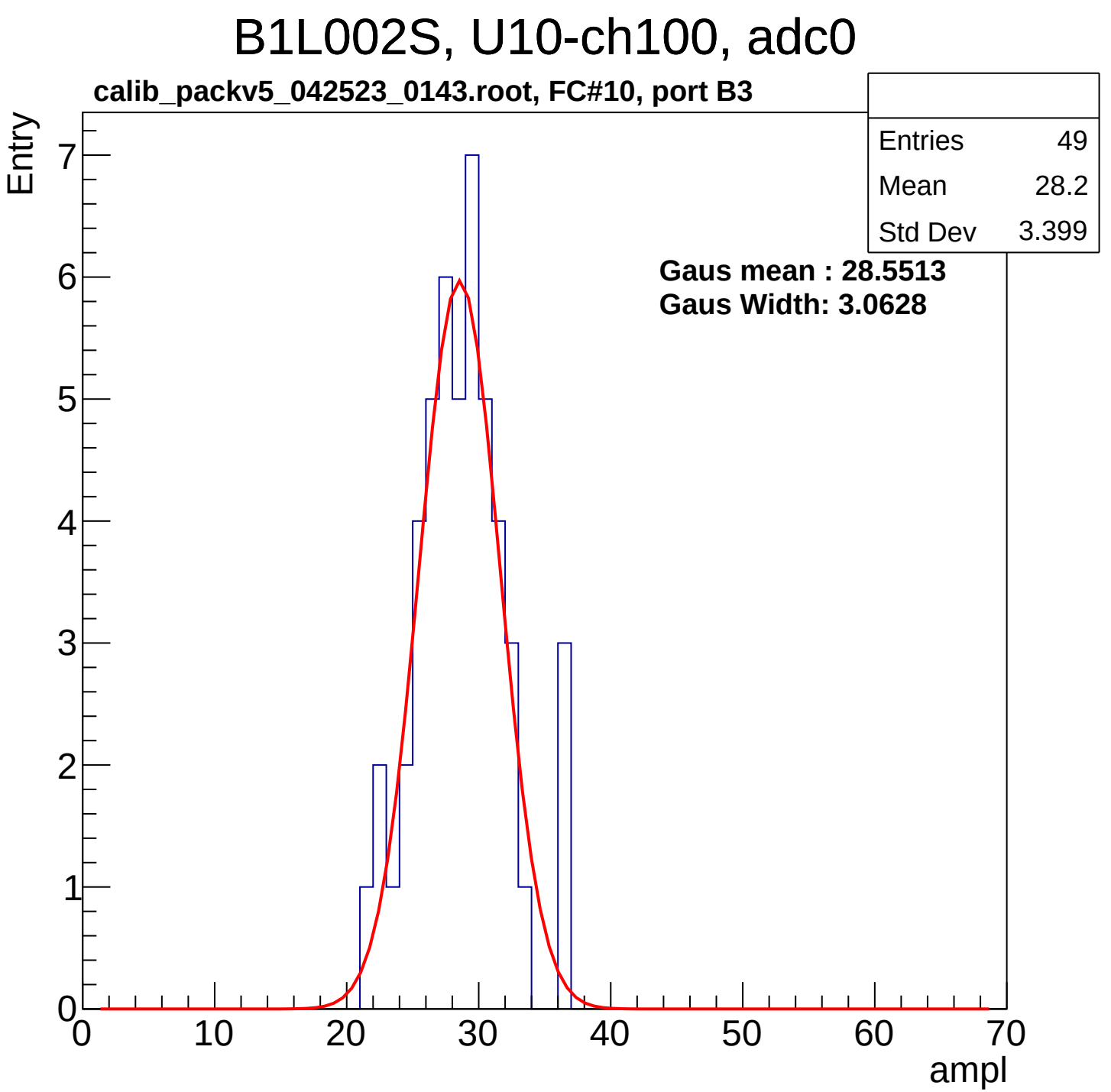
B1L002S, U10-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 28.2
Std Dev 3.399
Gaus mean : 28.5513
Gaus Width: 3.0628
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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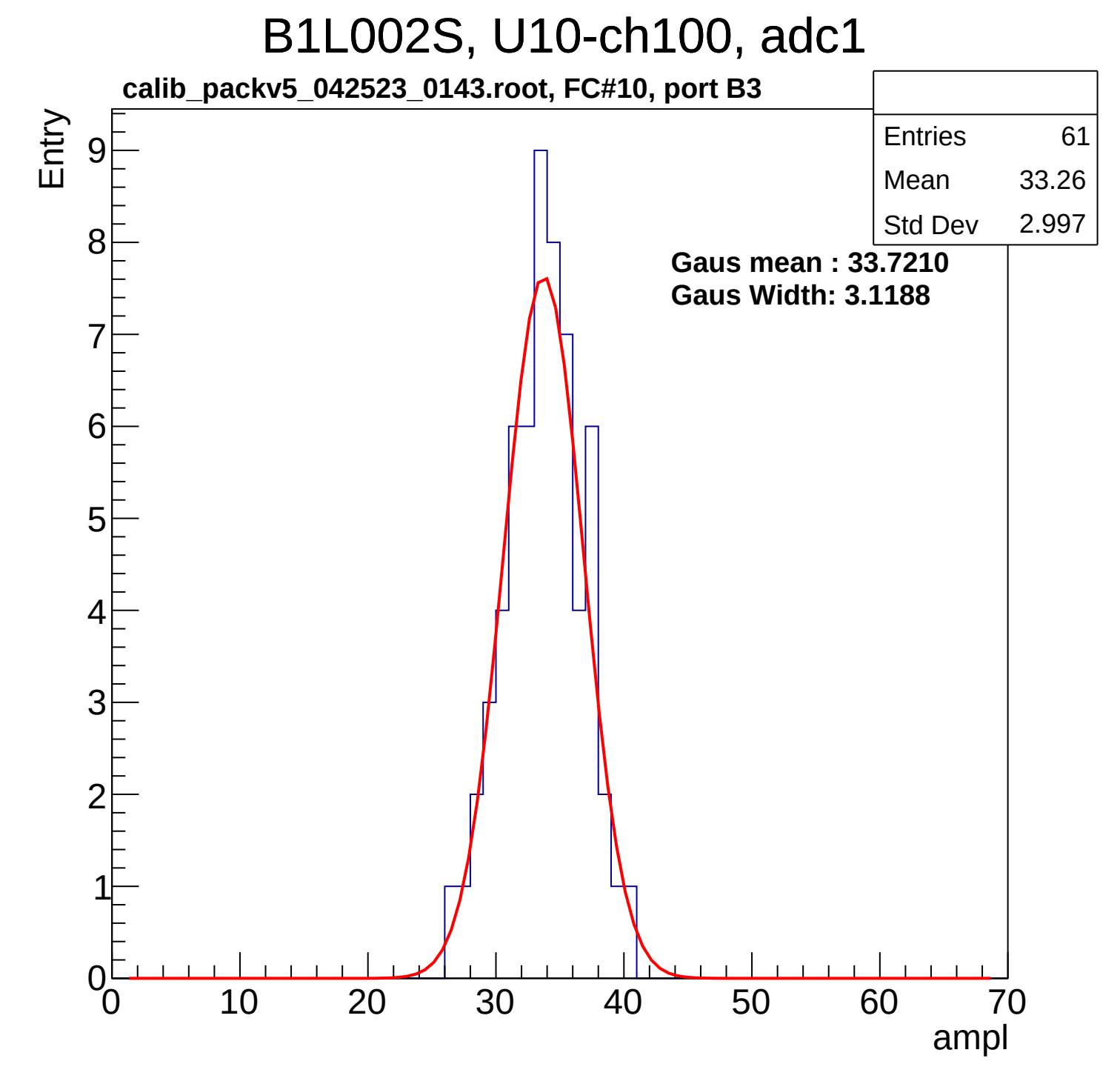
B1L002S, U10-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 33.26
Std Dev 2.997
Gaus mean : 33.7210
Gaus Width: 3.1188
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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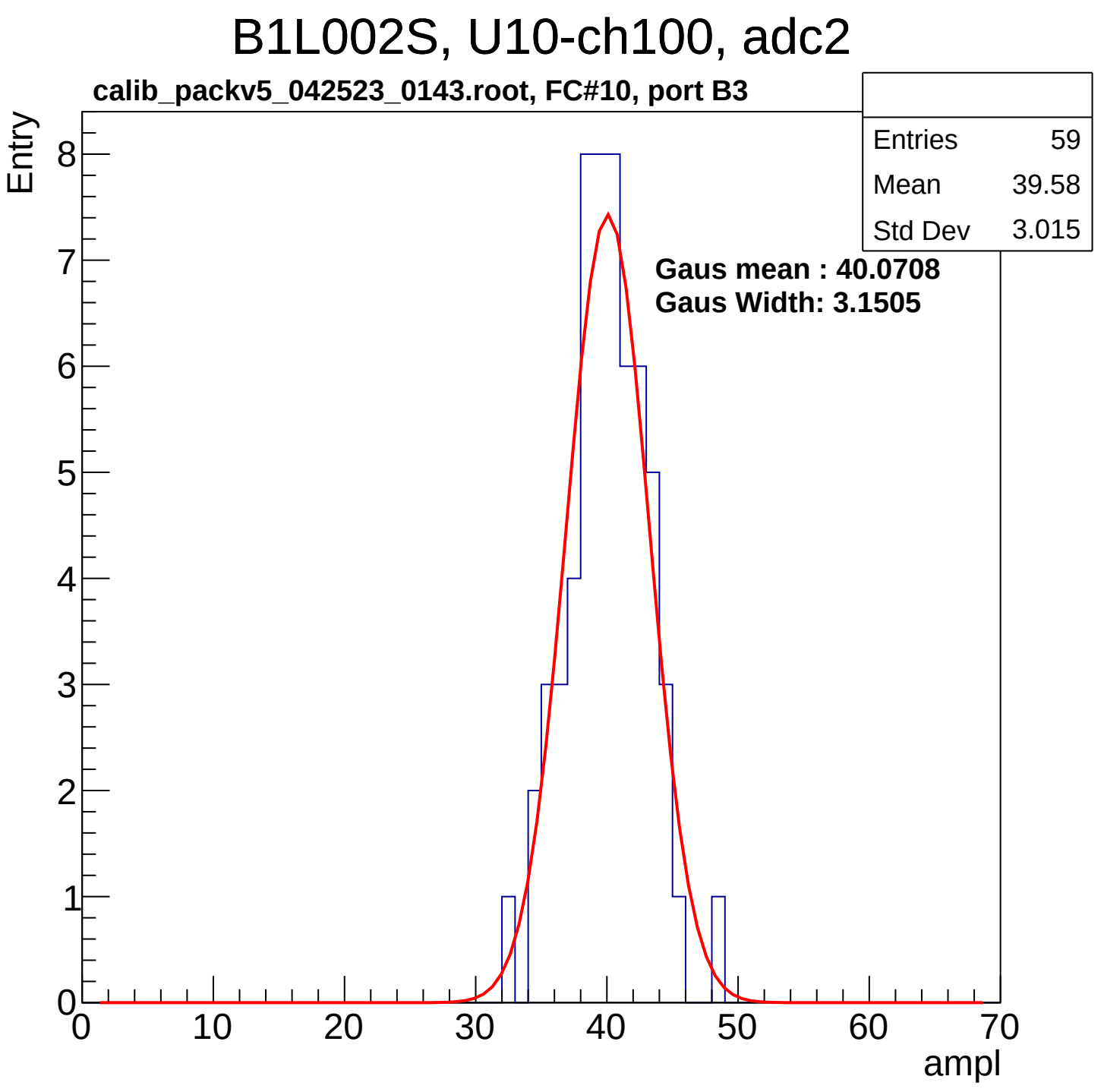
60
70


B1L002S, U10-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.58
Std Dev 3.015
Gaus mean : 40.0708
Gaus Width: 3.1505
Entry
ampl
0
1
2
3
4
5
6
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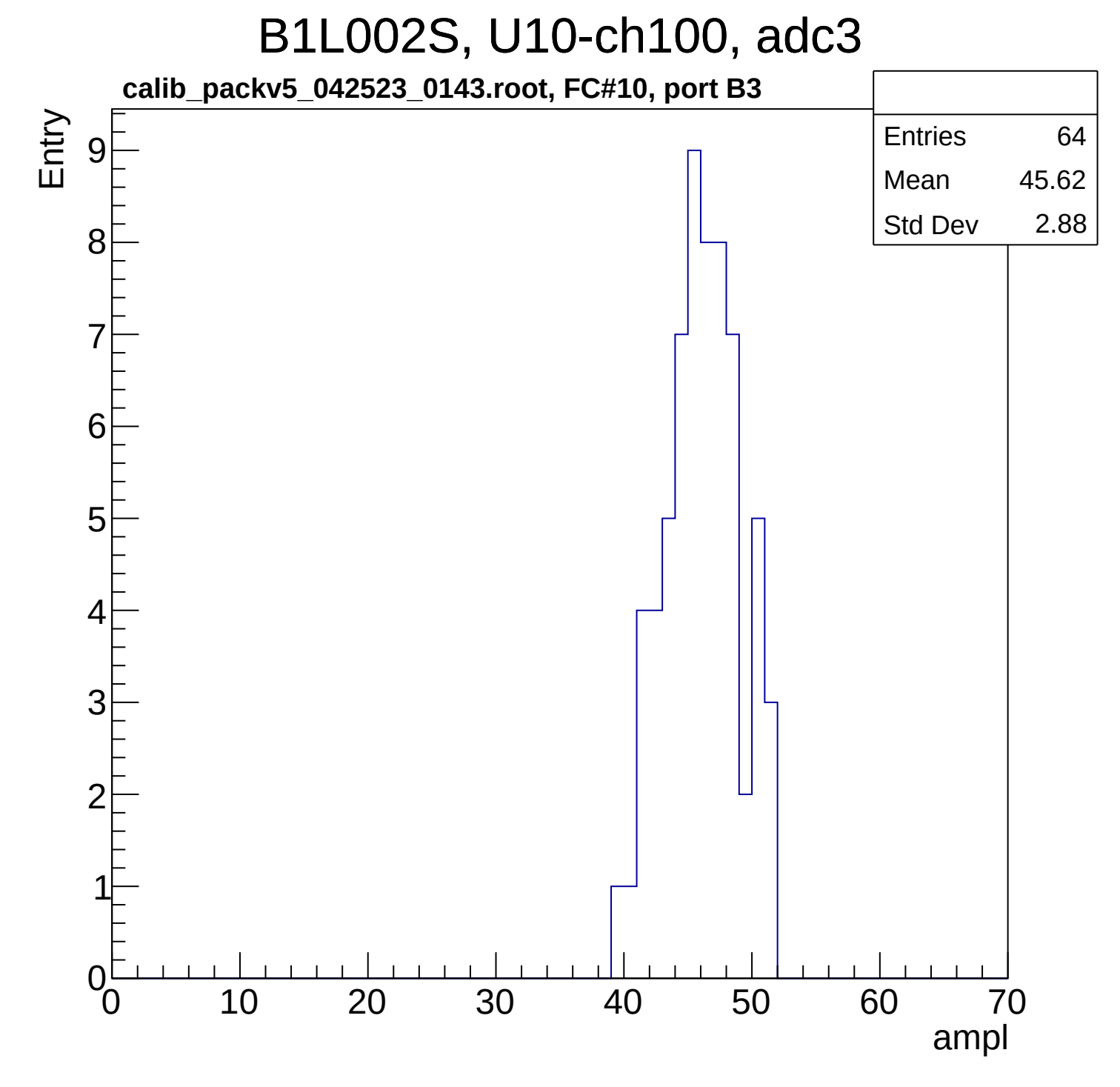
B1L002S, U10-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.62
Std Dev 2.88
Entry
ampl
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2
3
4
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6
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0
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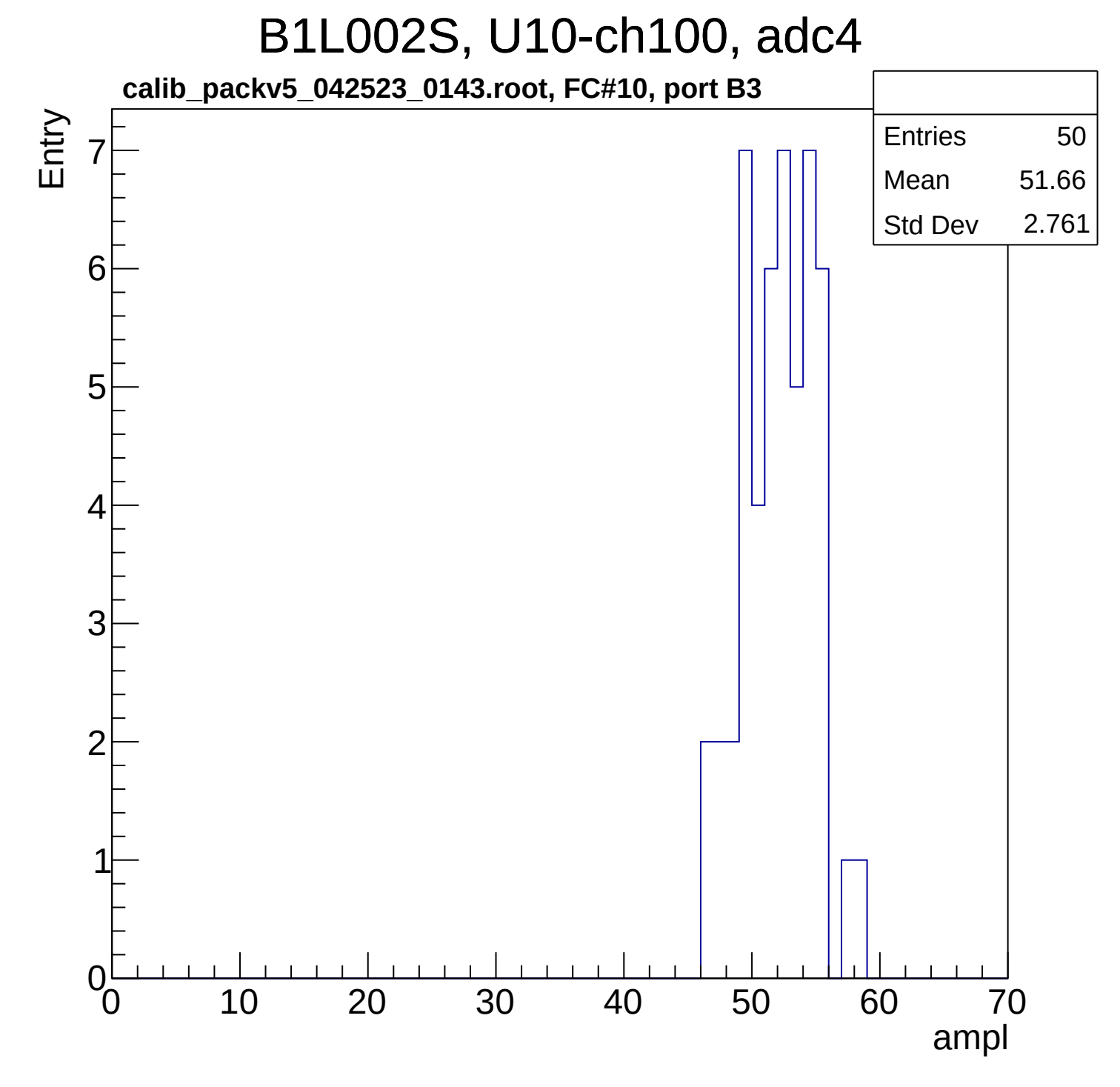
B1L002S, U10-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.66
Std Dev 2.761
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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60
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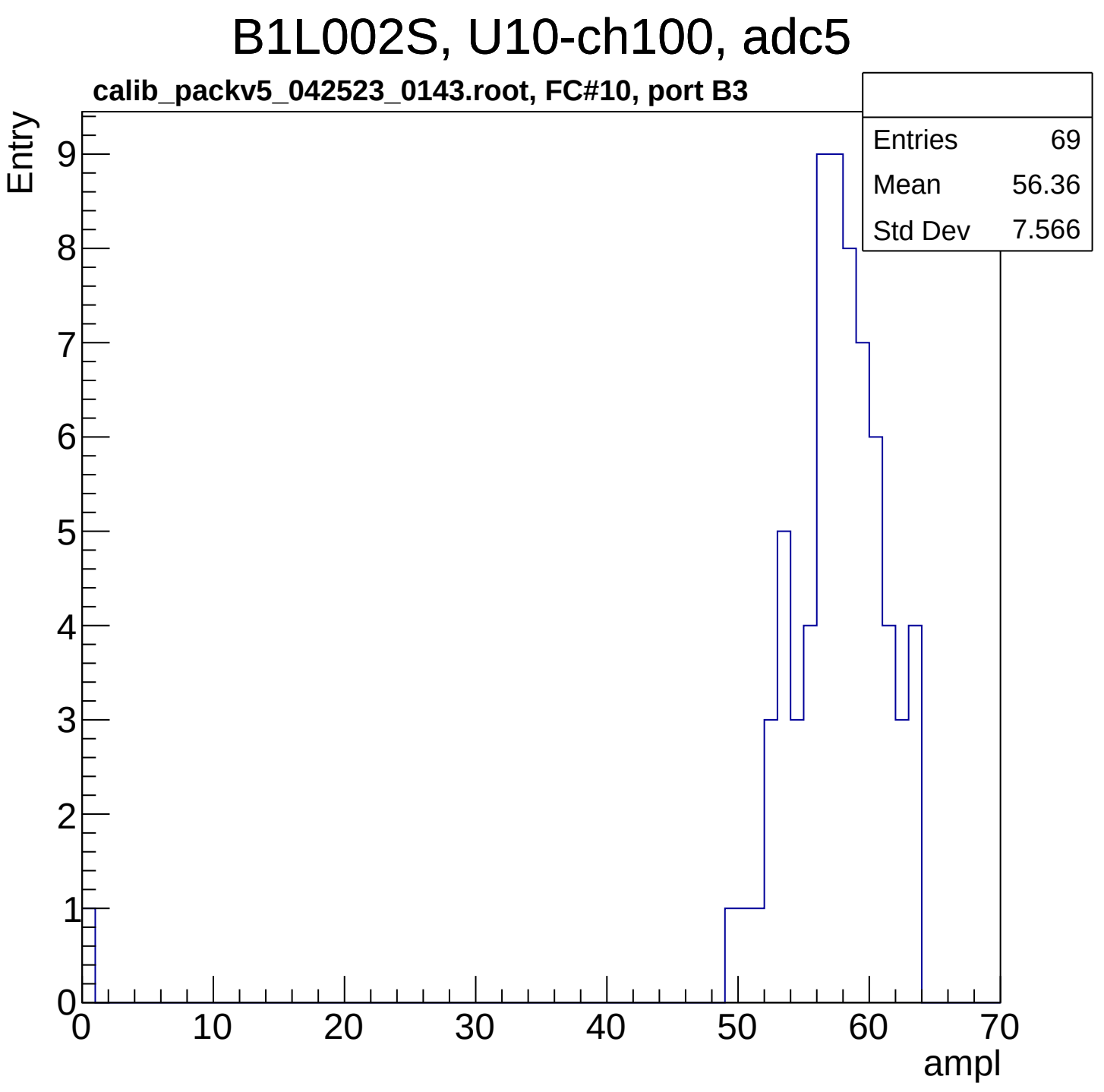
B1L002S, U10-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 56.36
Std Dev 7.566
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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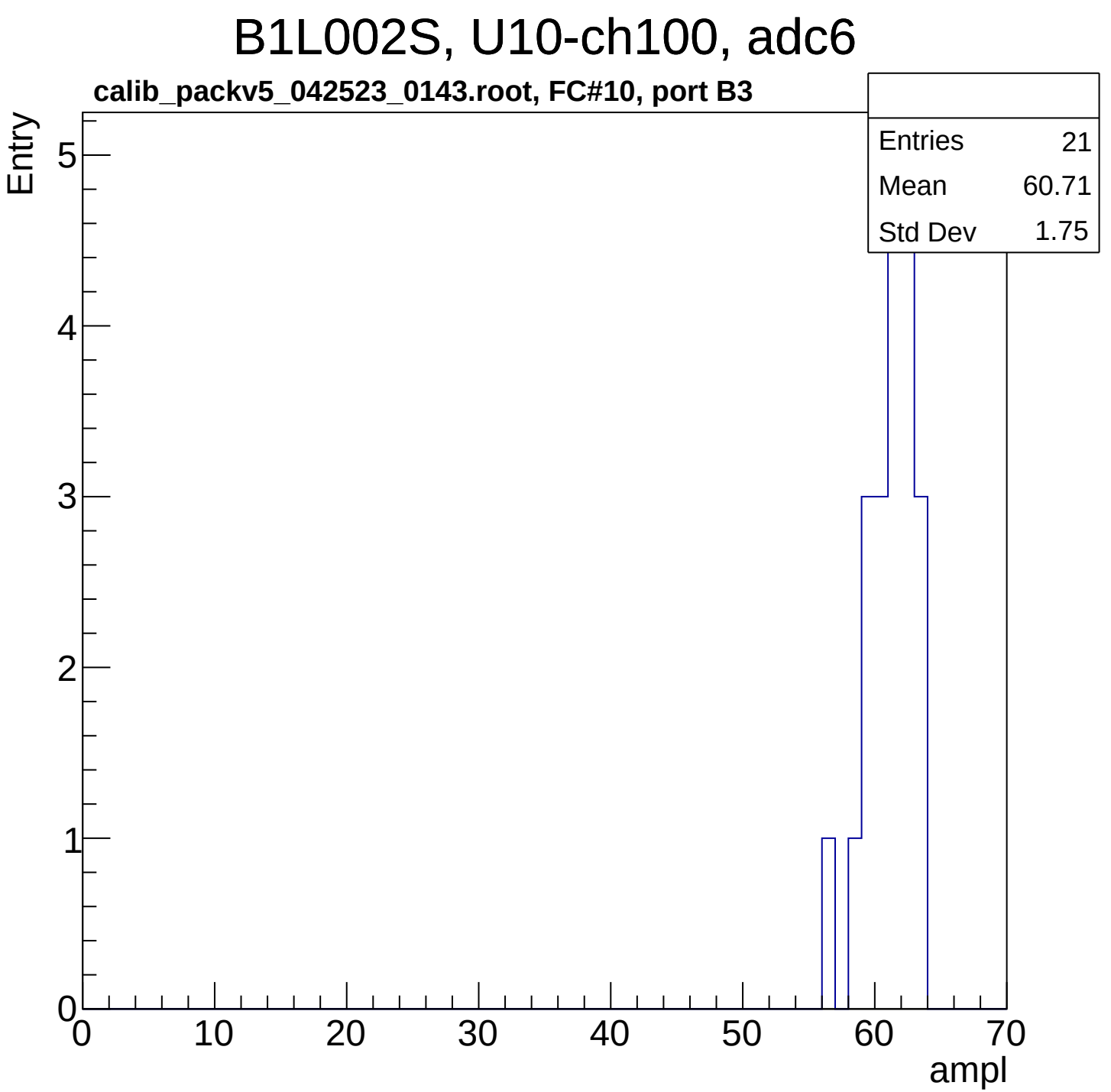
B1L002S, U10-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 60.71
Std Dev 1.75
Entry
0
1
2
3
4
5
0
10
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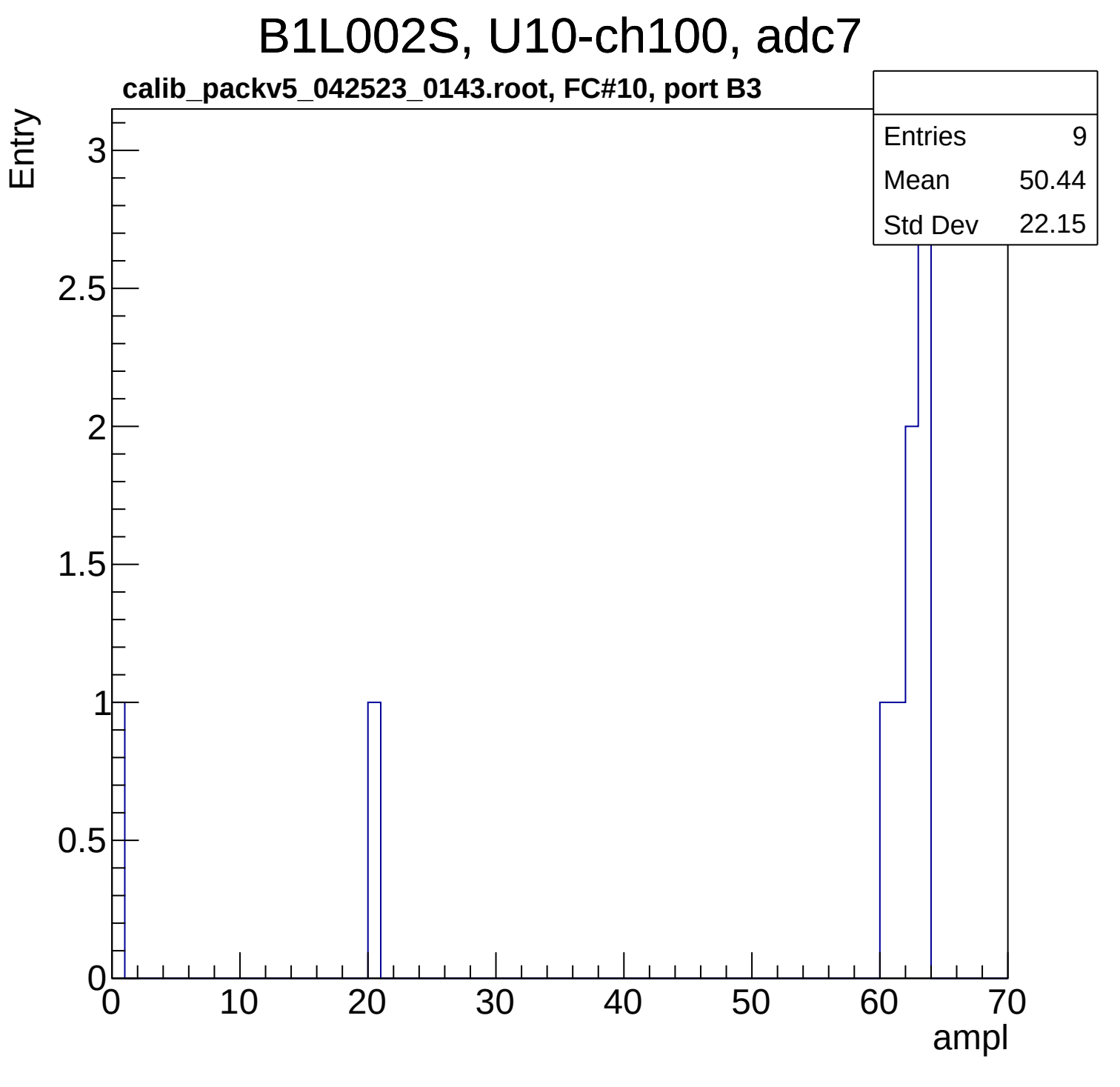
50
60
70
ampl


B1L002S, U10-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 50.44
Std Dev 22.15
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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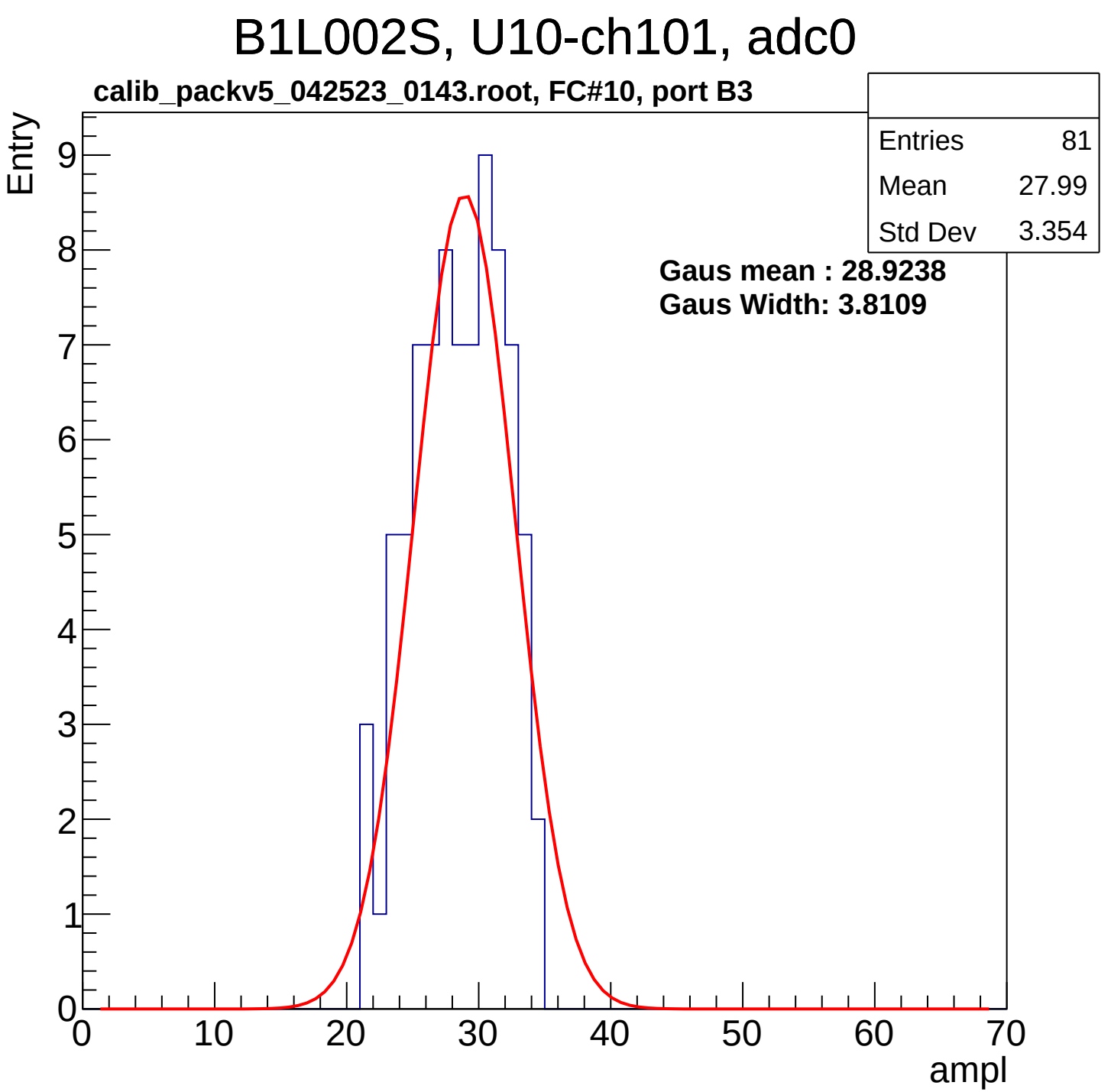
B1L002S, U10-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 27.99
Std Dev 3.354
Gaus mean : 28.9238
Gaus Width: 3.8109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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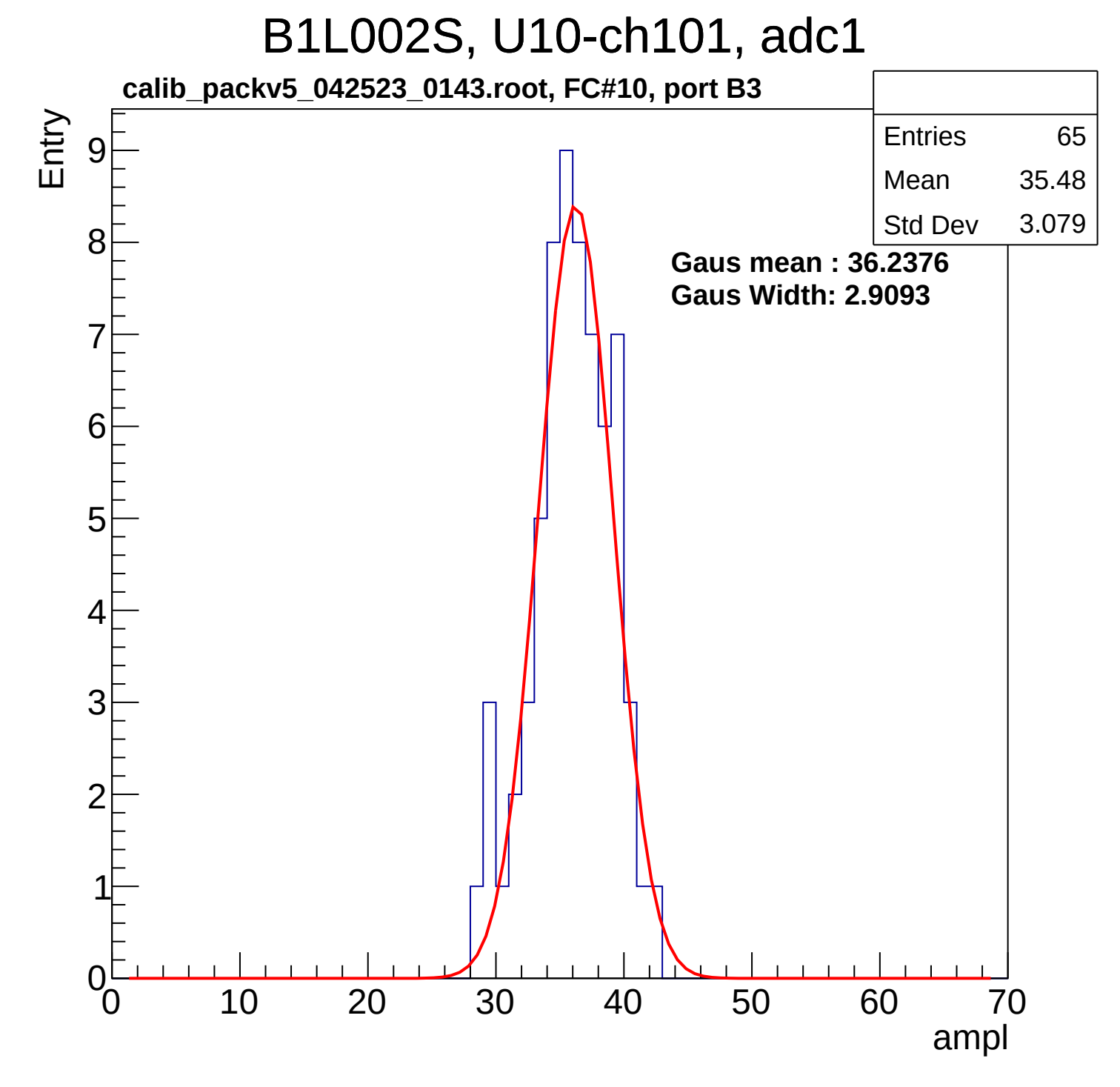
B1L002S, U10-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.48
Std Dev 3.079
Gaus mean : 36.2376
Gaus Width: 2.9093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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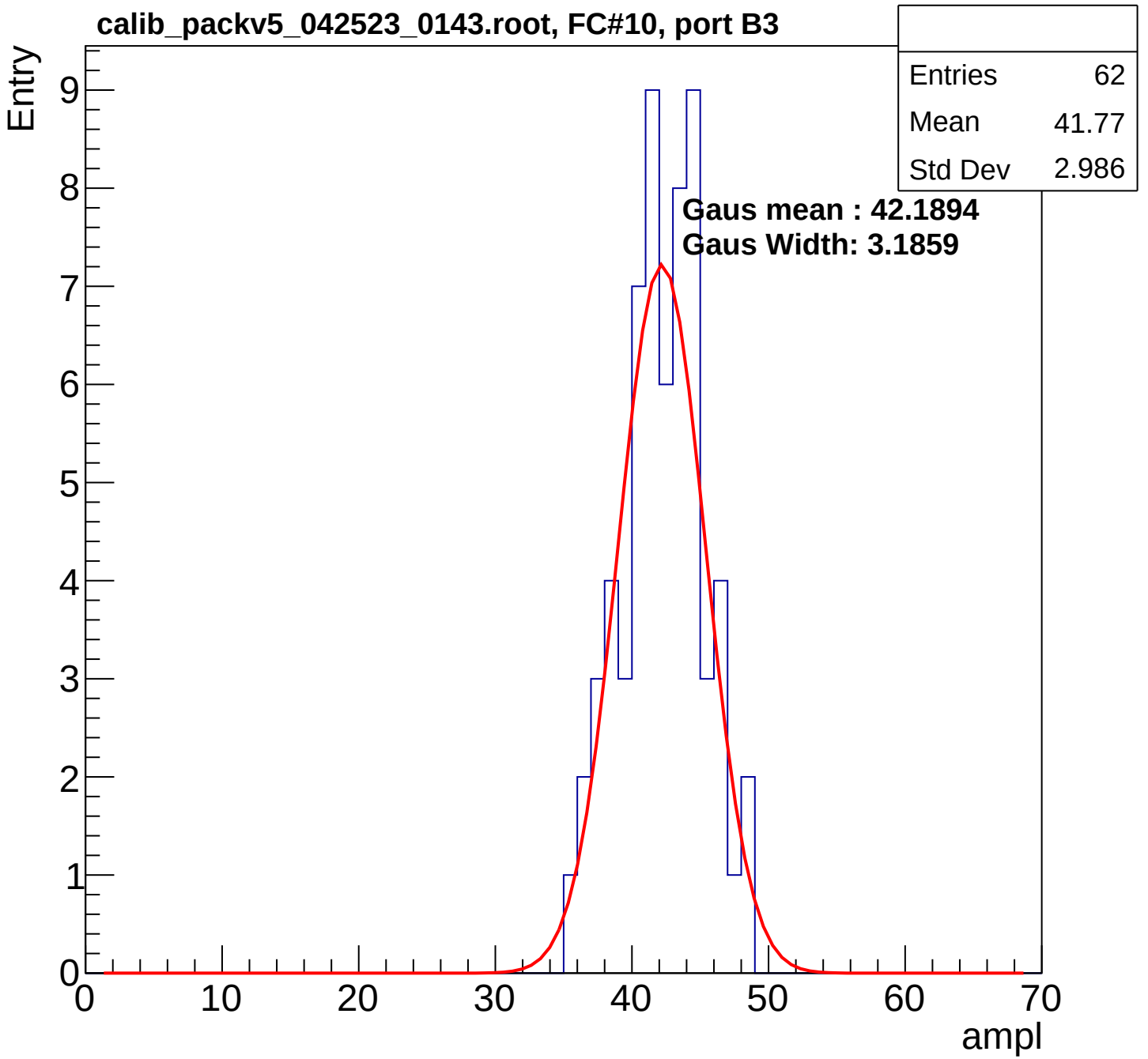
B1L002S, U10-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.77
Std Dev 2.986
Gaus mean : 42.1894
Gaus Width: 3.1859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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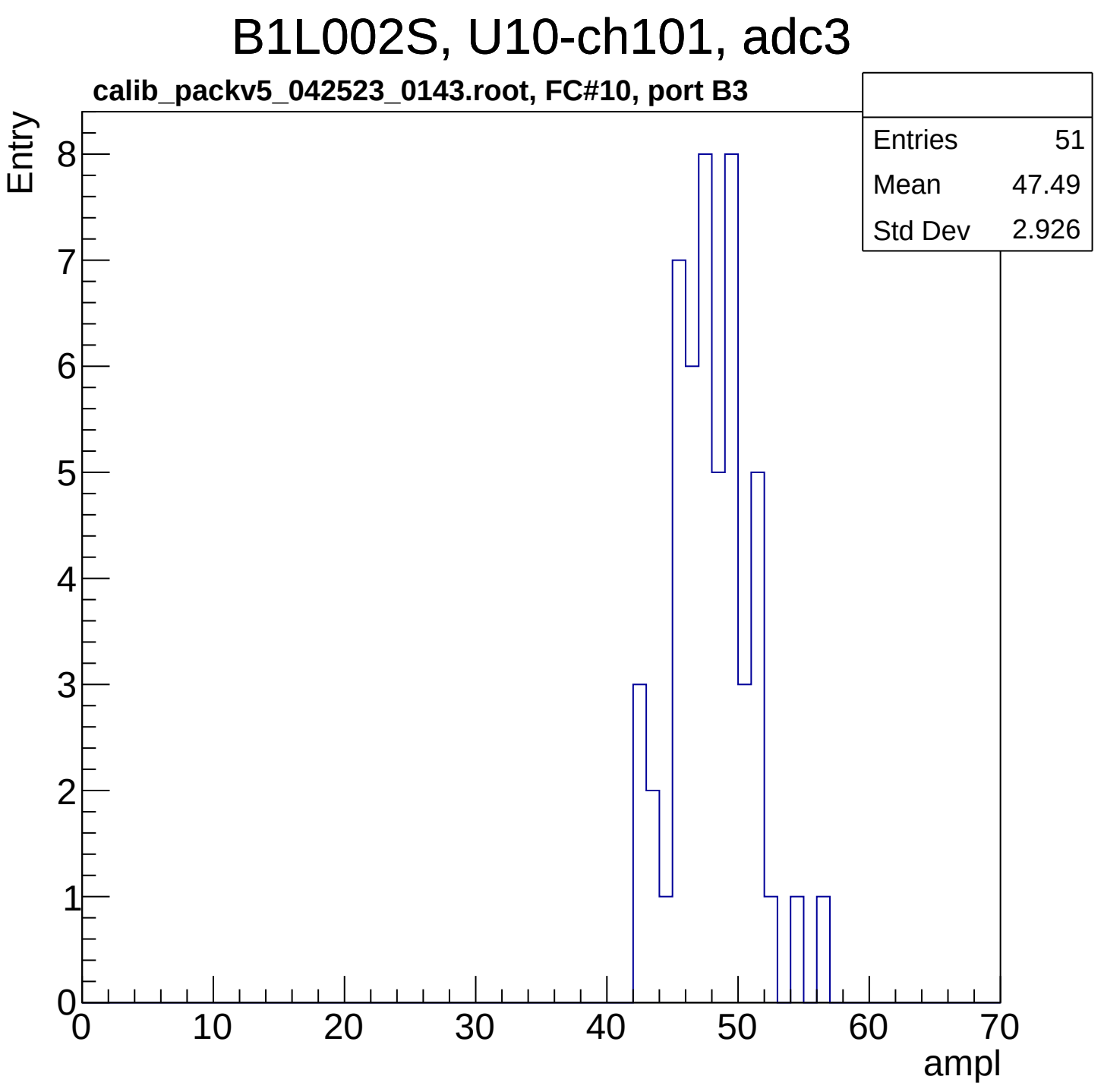
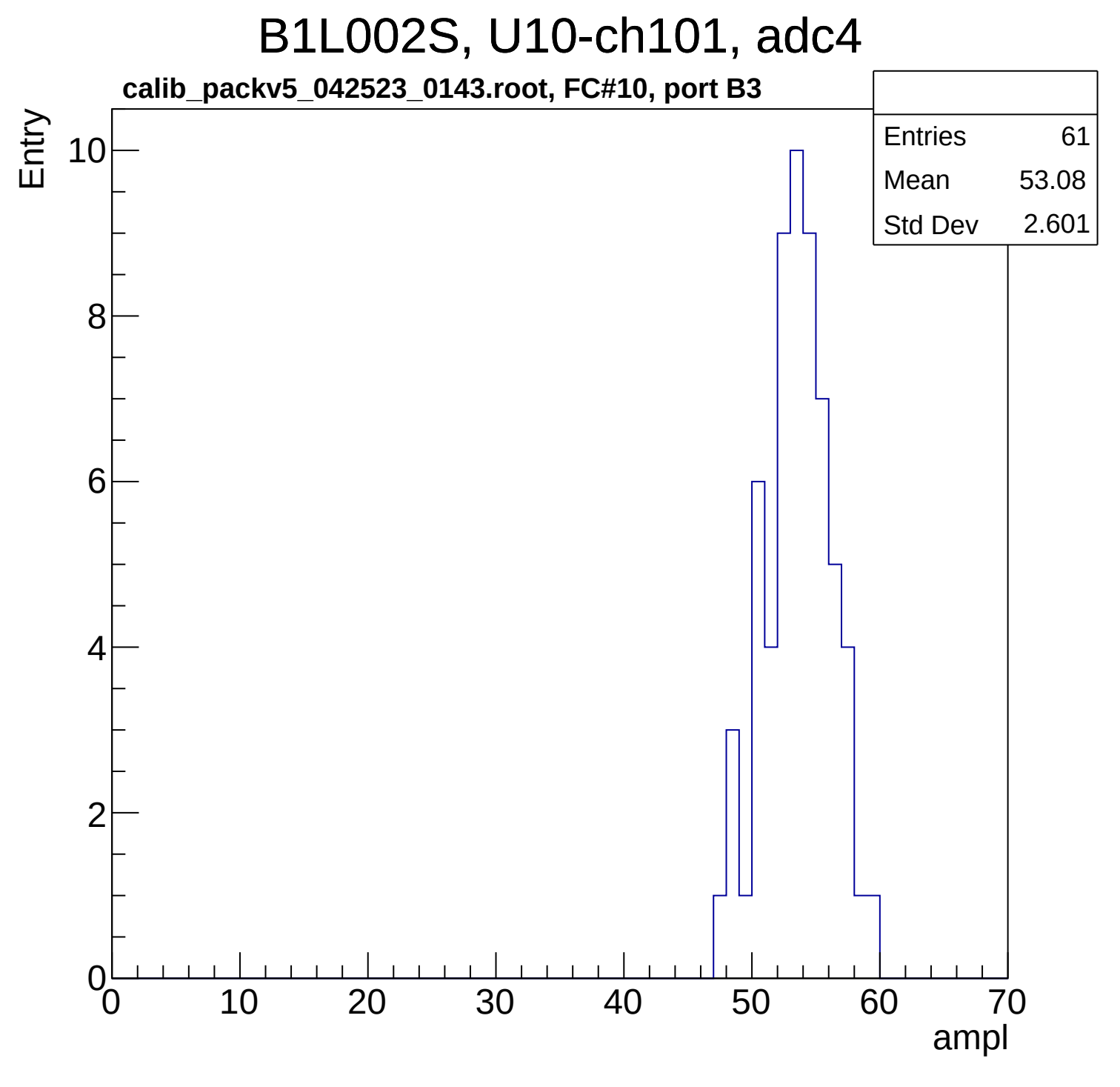
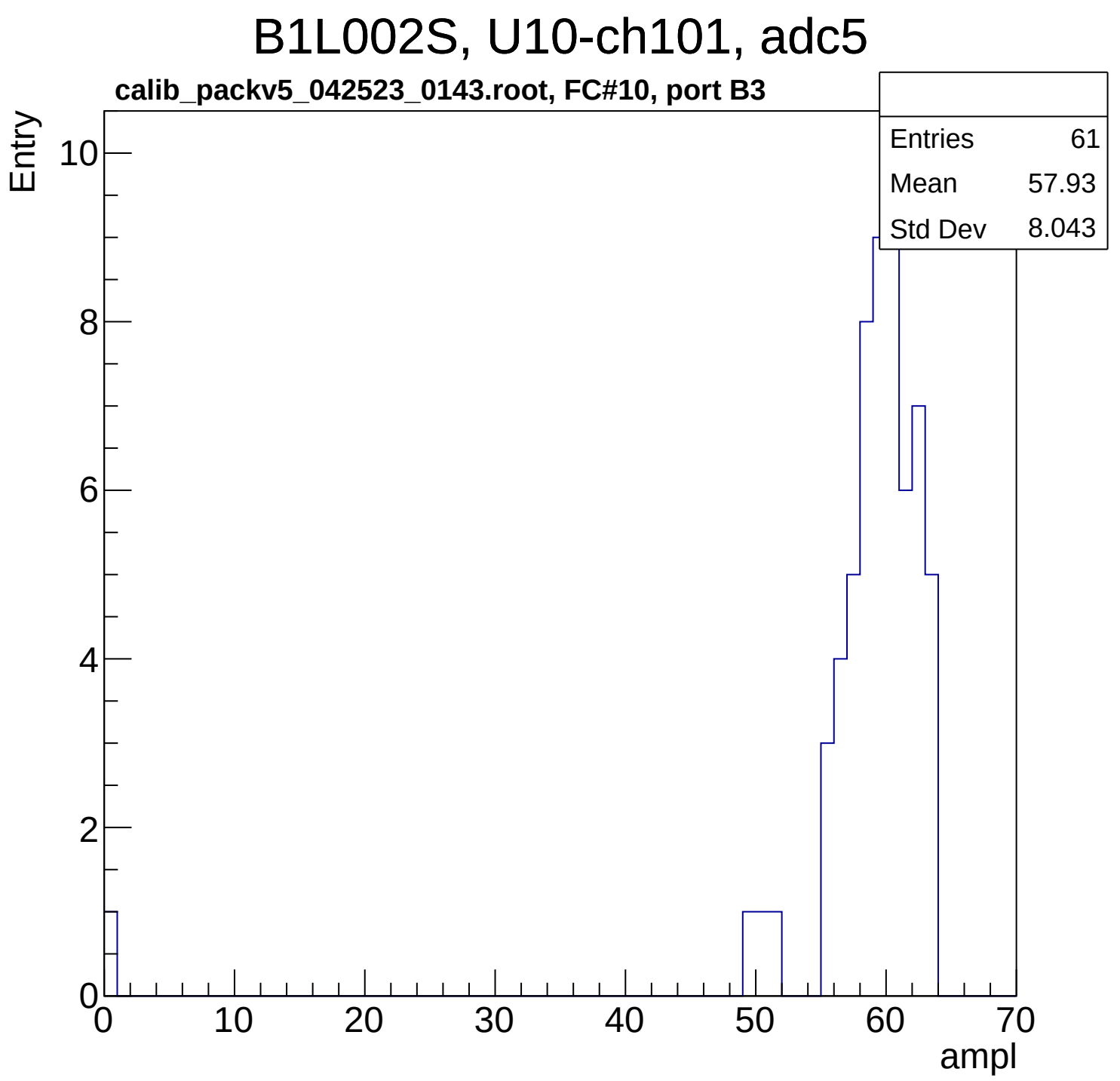
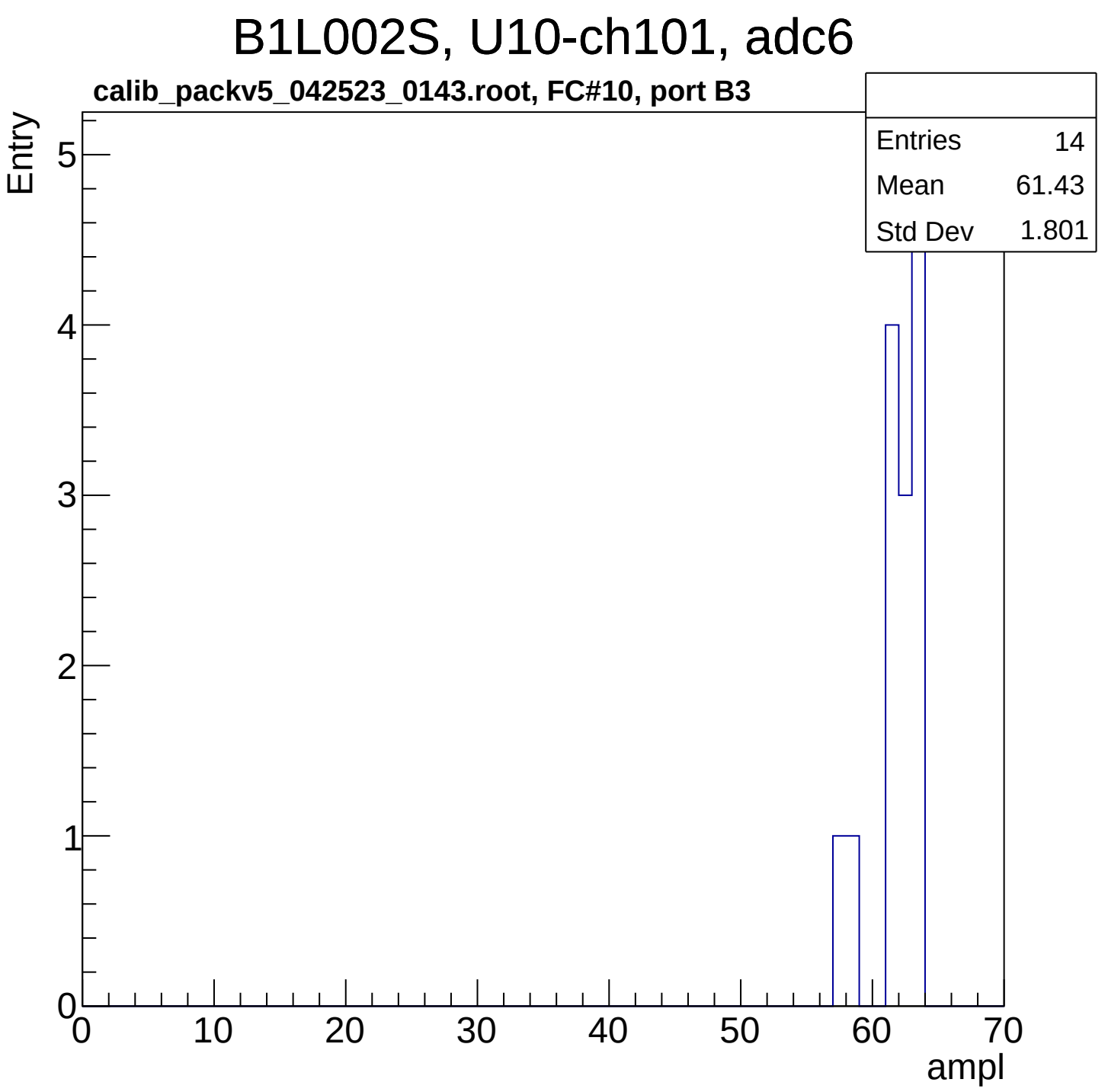
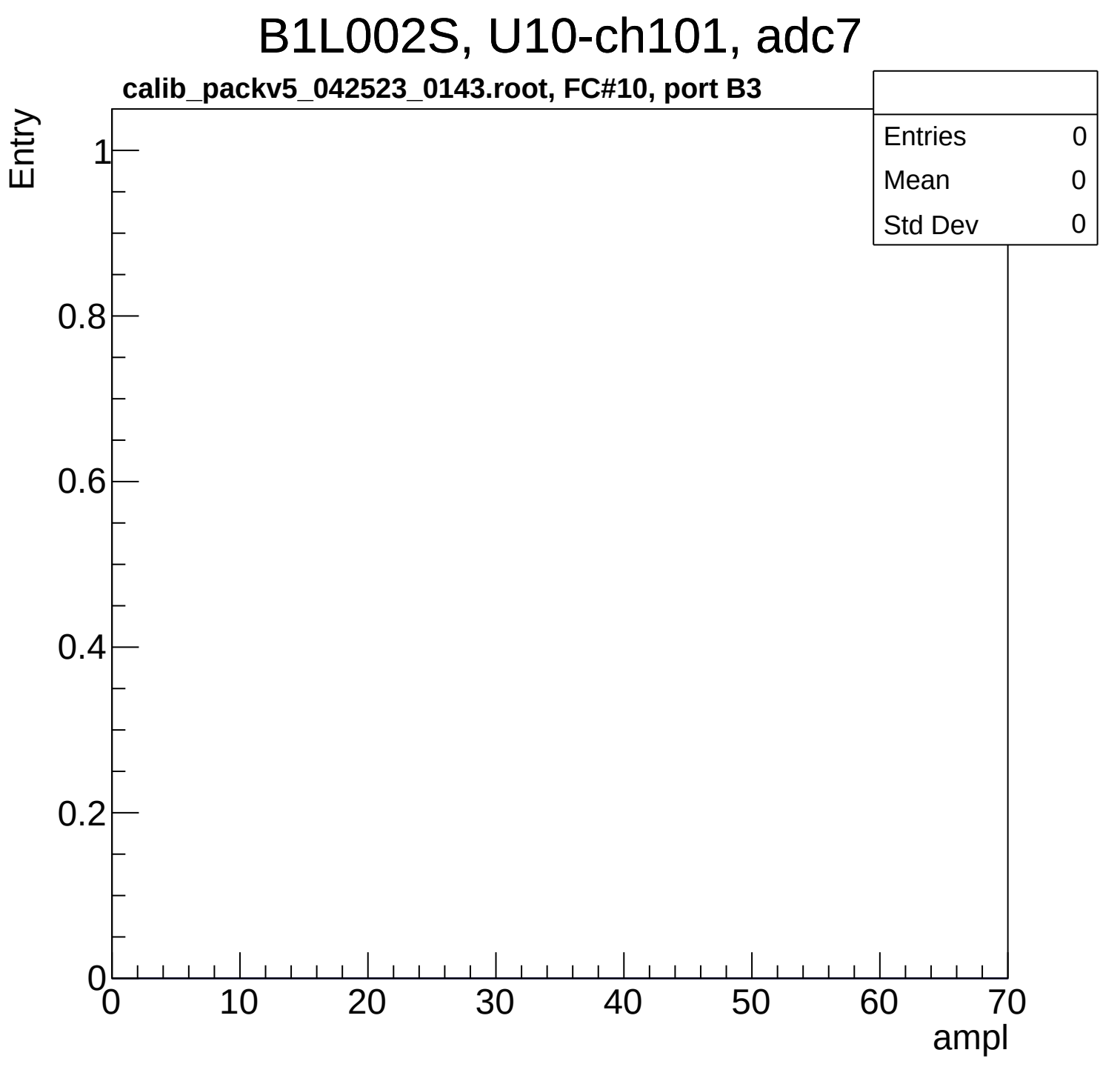
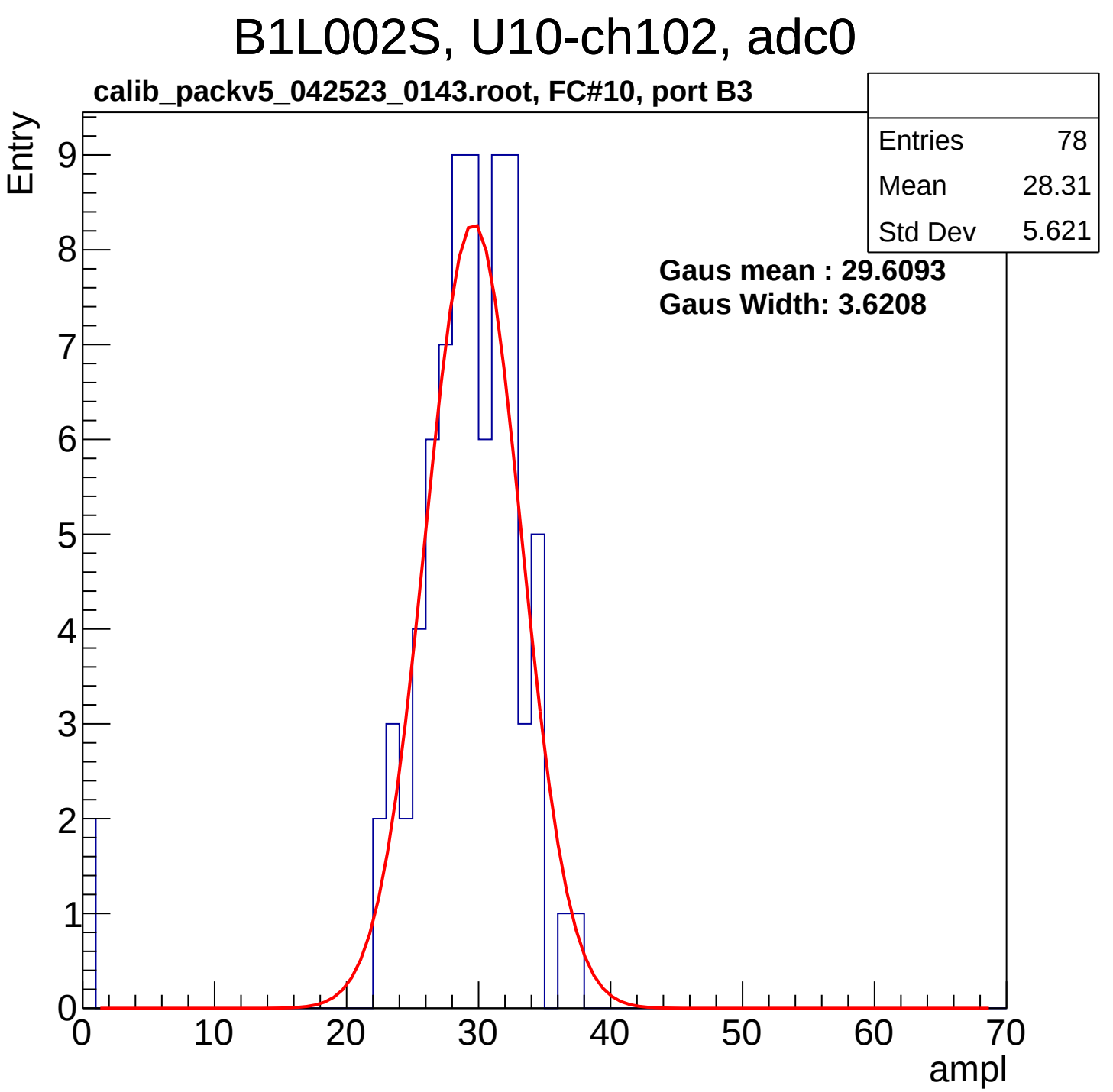

B1L002S, U10-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.49
Std Dev 2.926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

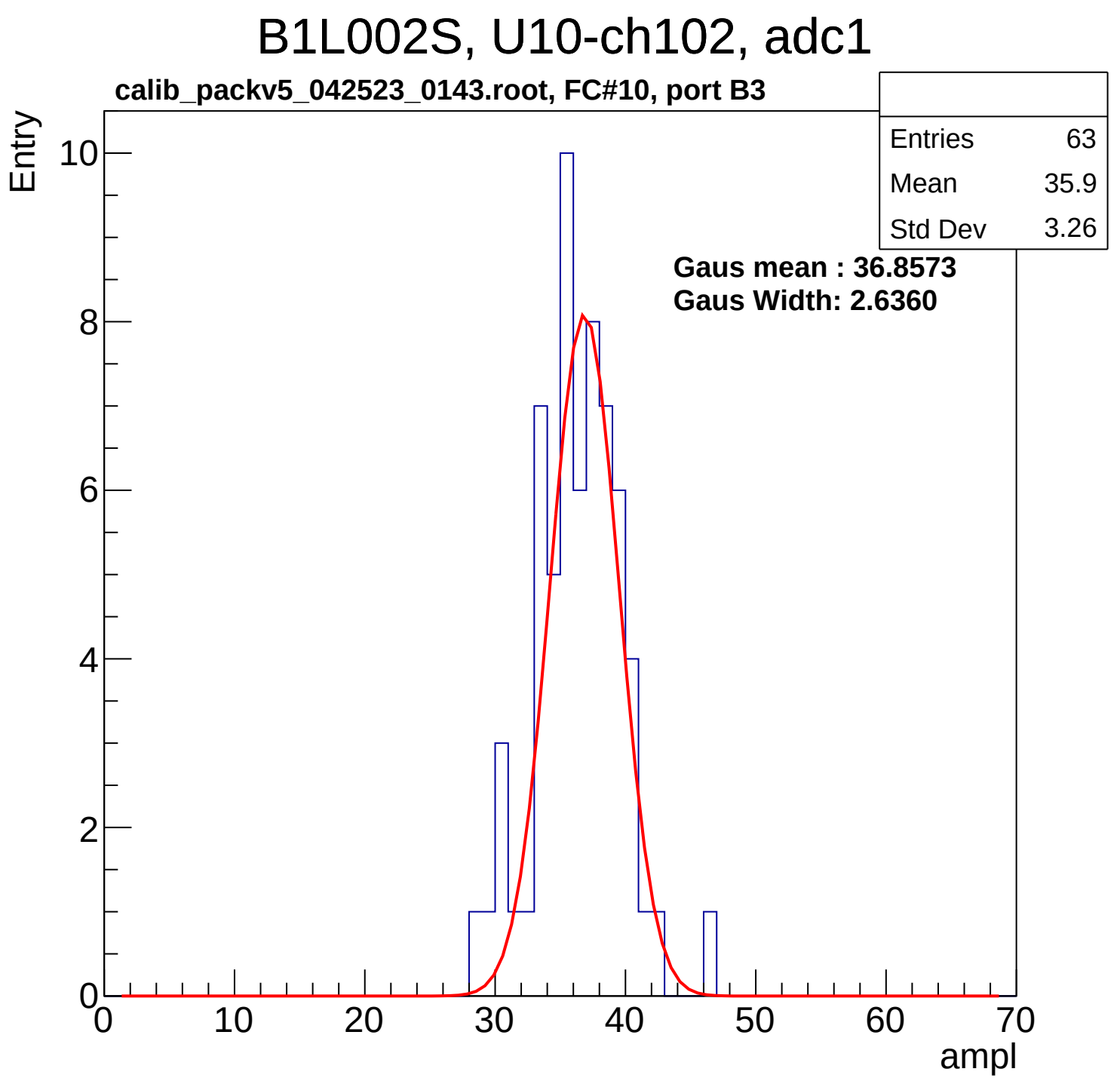

B1L002S, U10-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.08
Std Dev 2.601
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

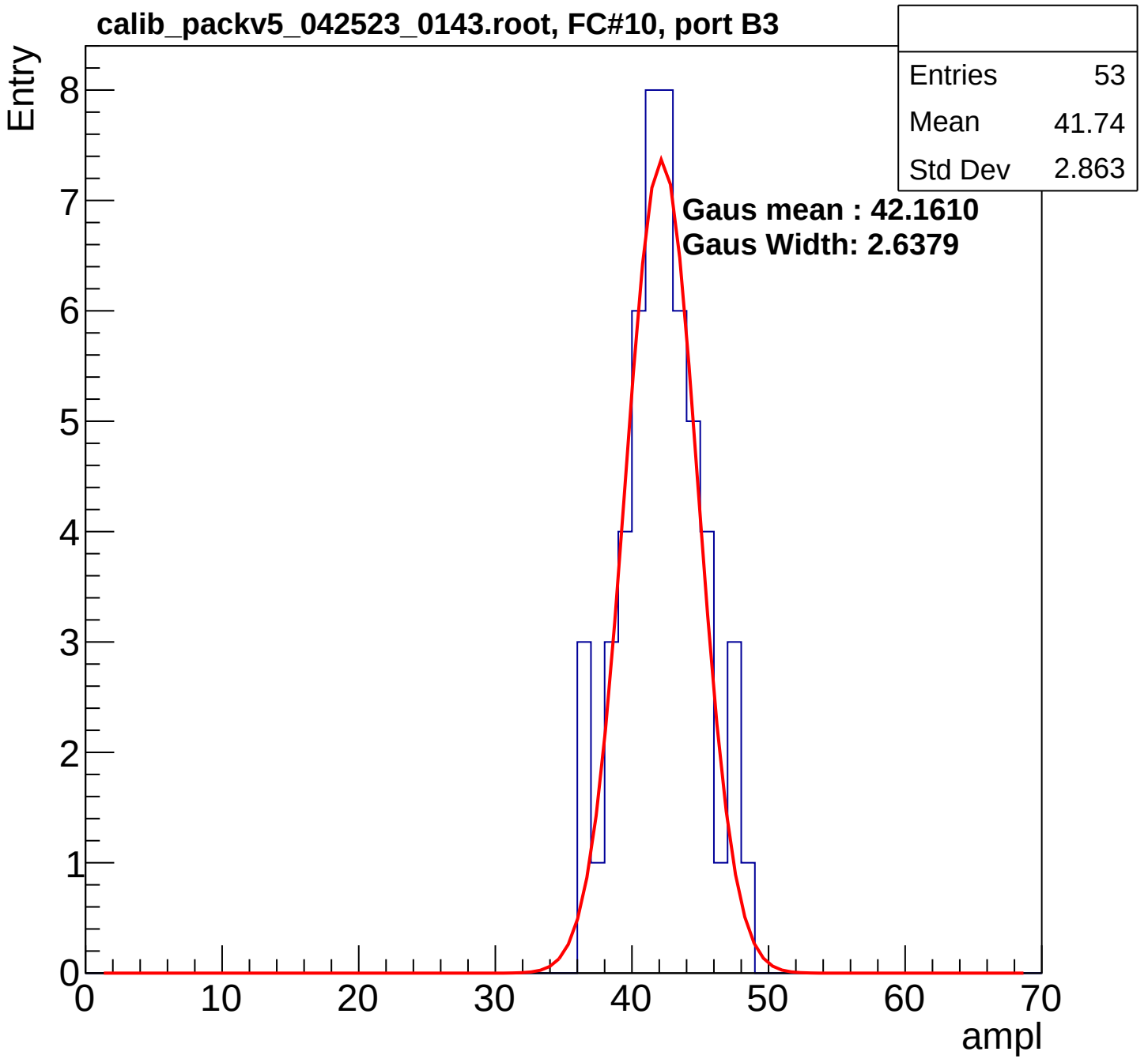

B1L002S, U10-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.93
Std Dev 8.043
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.43
Std Dev 1.801
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.31
Std Dev 5.621
Gaus mean : 29.6093
Gaus Width: 3.6208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.9
Std Dev 3.26
Gaus mean : 36.8573
Gaus Width: 2.6360
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.74
Std Dev 2.863
Gaus mean : 42.1610
Gaus Width: 2.6379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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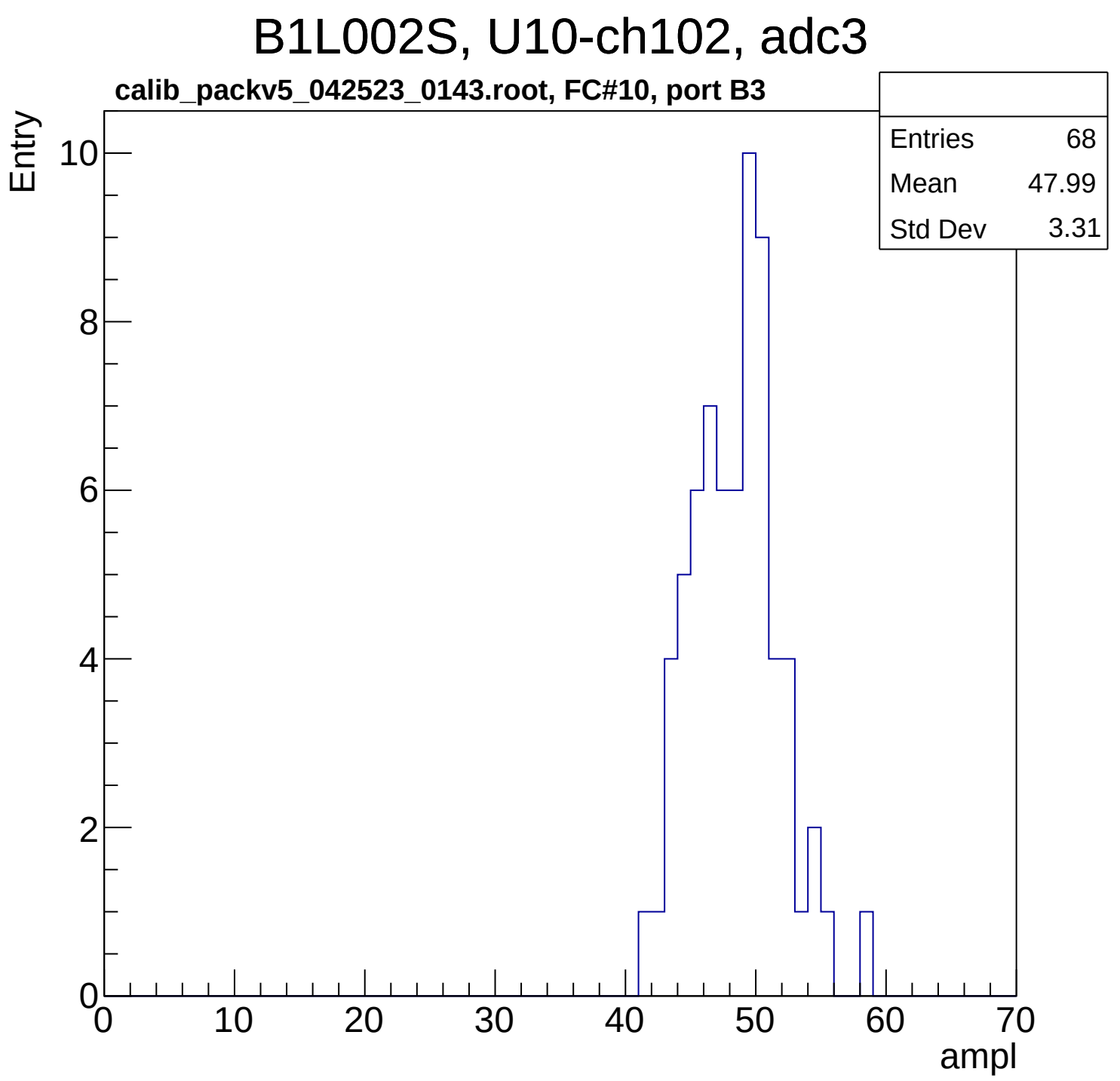
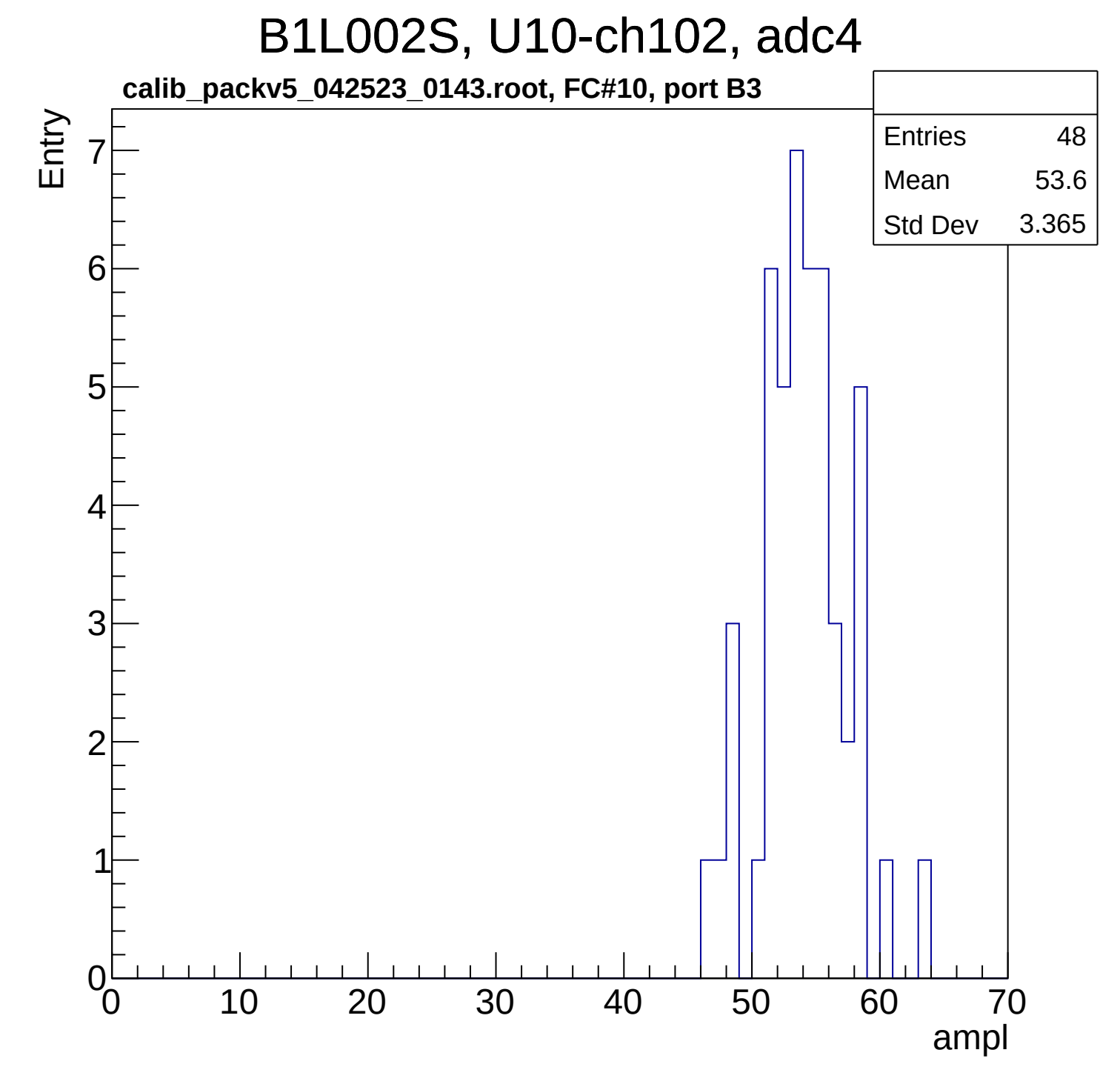
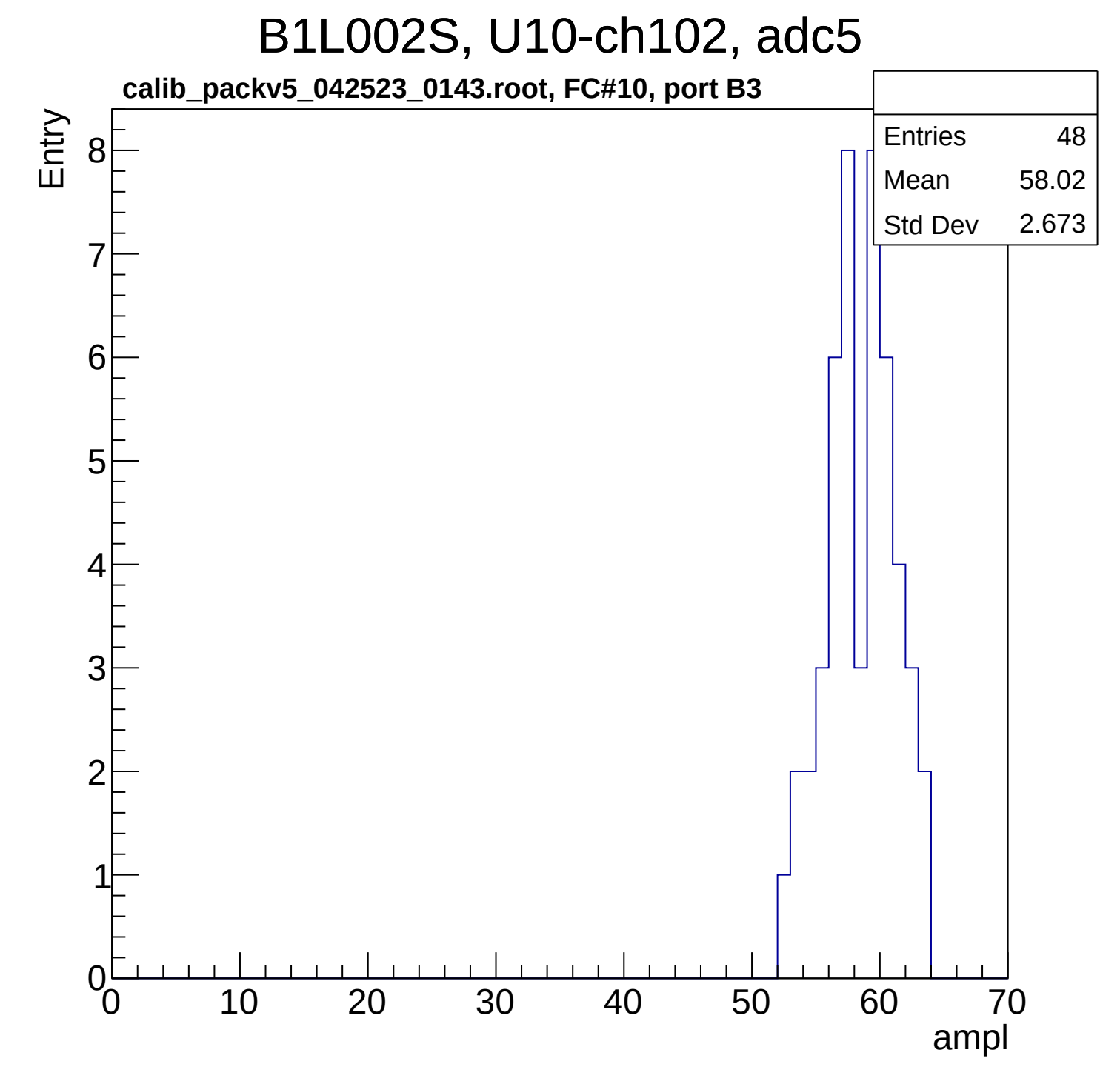

B1L002S, U10-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.99
Std Dev 3.31
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.6
Std Dev 3.365
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.02
Std Dev 2.673
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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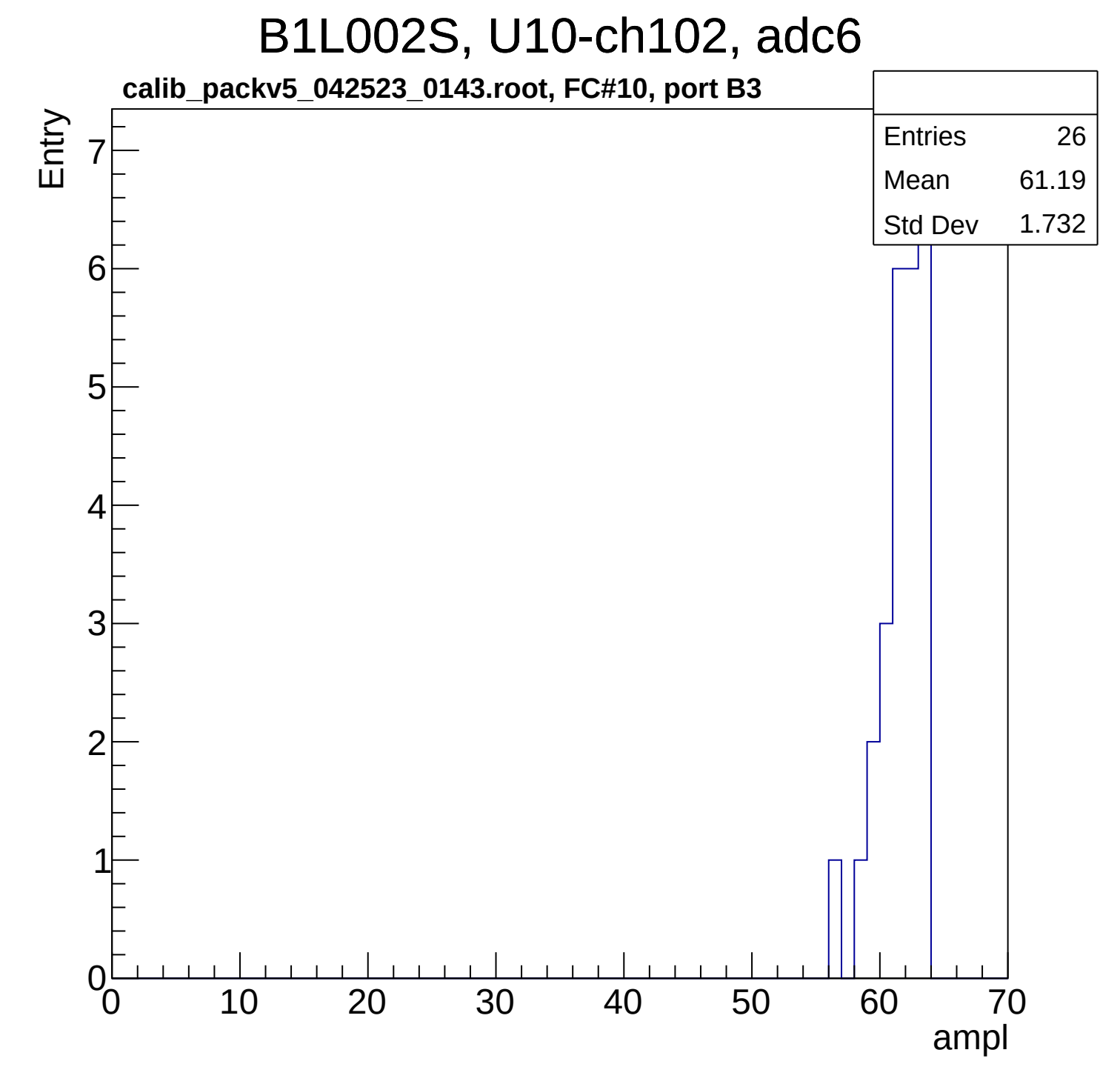
B1L002S, U10-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.19
Std Dev 1.732
Entry
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1
2
3
4
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10
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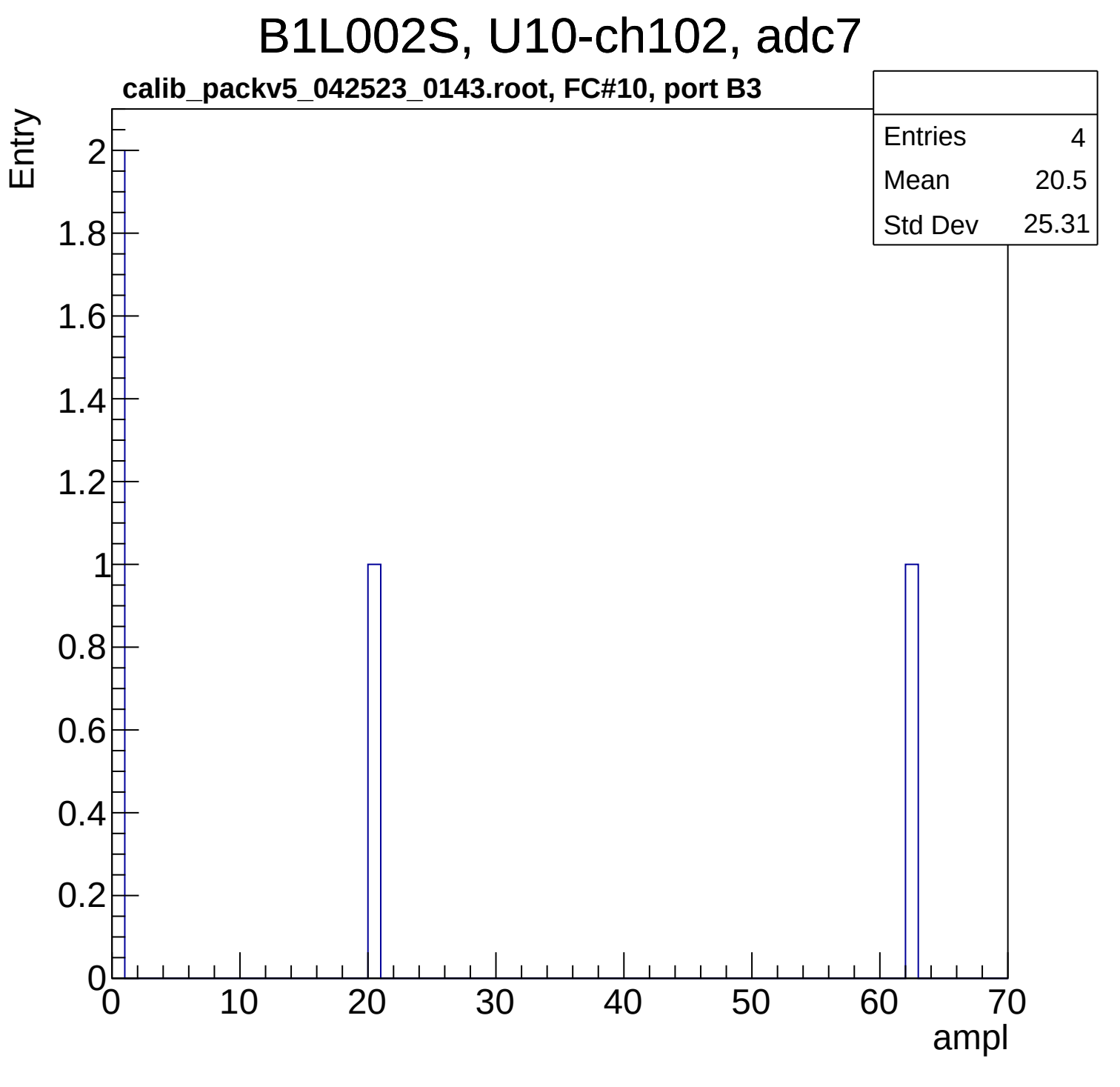
50
60
70
ampl


B1L002S, U10-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.5
Std Dev 25.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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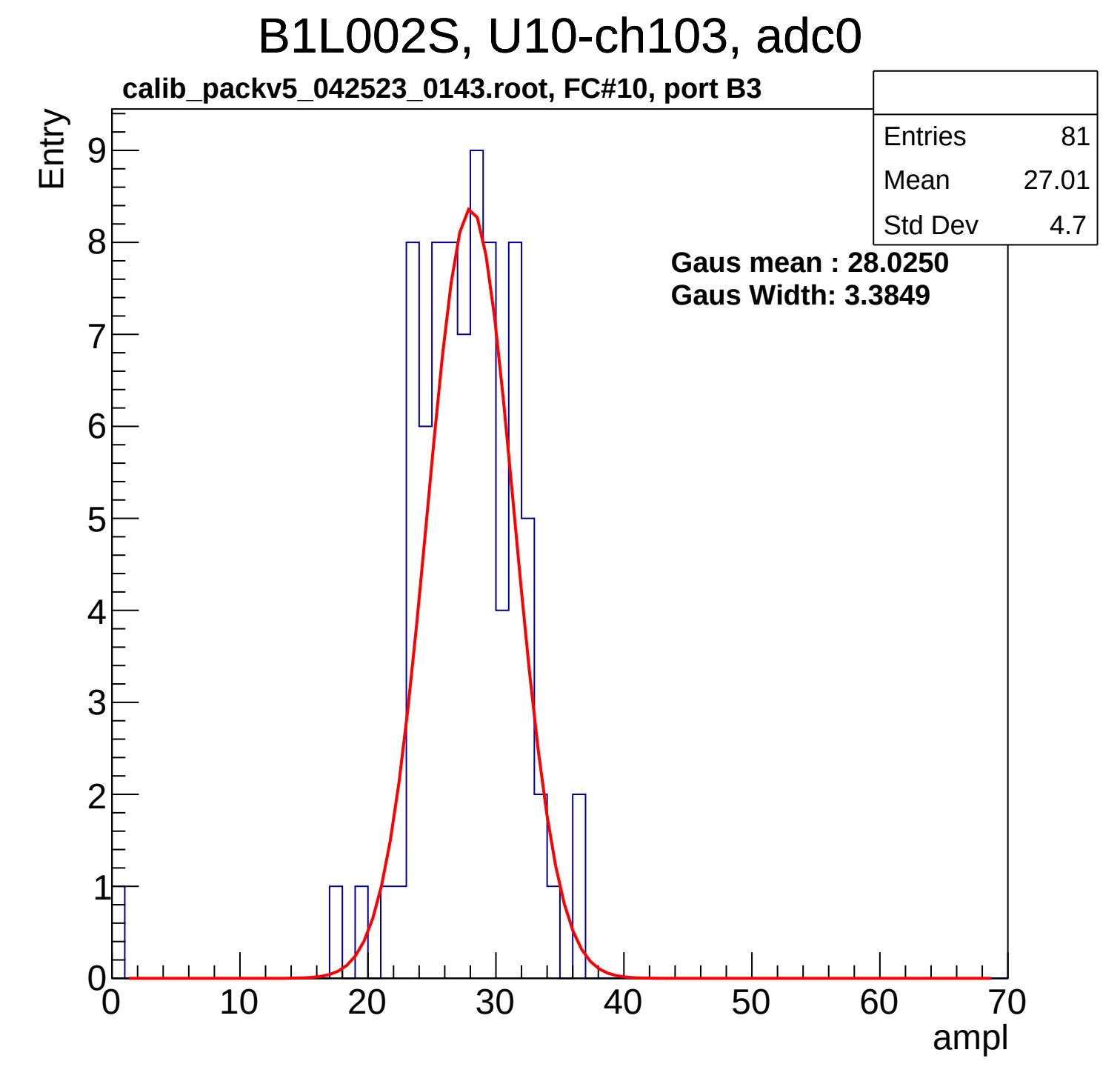
B1L002S, U10-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 27.01
Std Dev 4.7
Gaus mean : 28.0250
Gaus Width: 3.3849
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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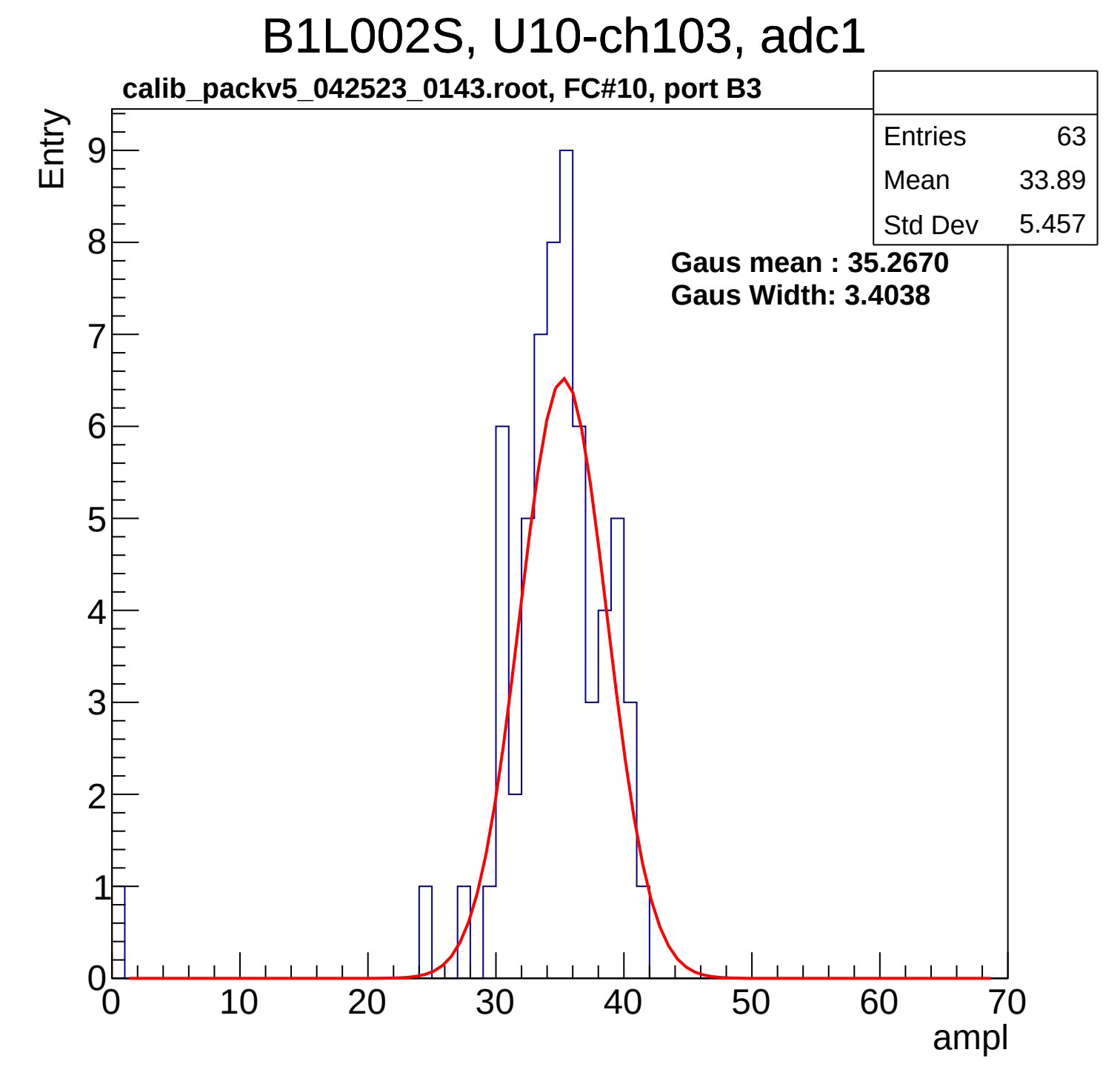
40
50
60
70


B1L002S, U10-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.89
Std Dev 5.457
Gaus mean : 35.2670
Gaus Width: 3.4038
Entry
ampl
0
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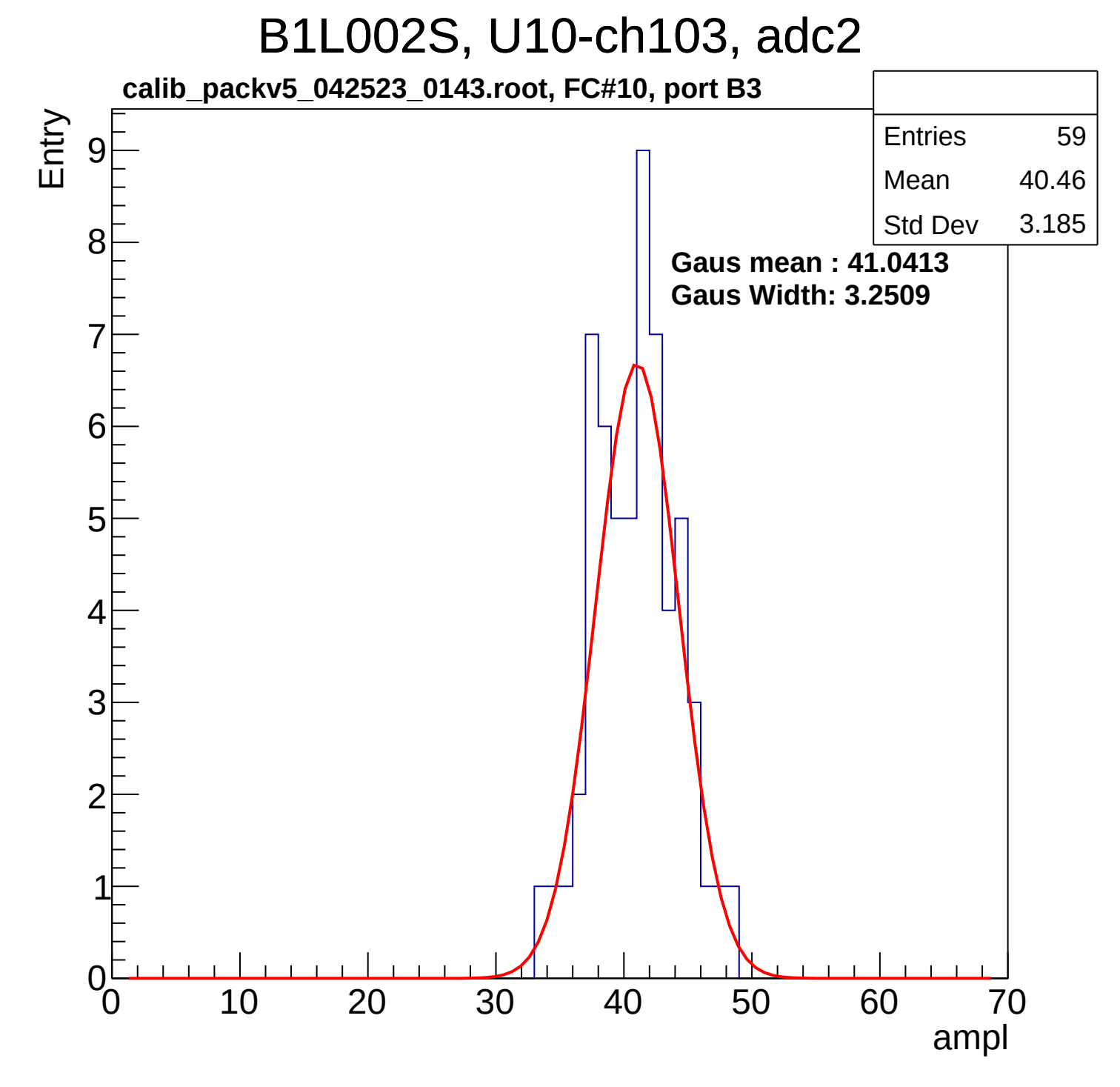
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.46
Std Dev 3.185
Gaus mean : 41.0413
Gaus Width: 3.2509
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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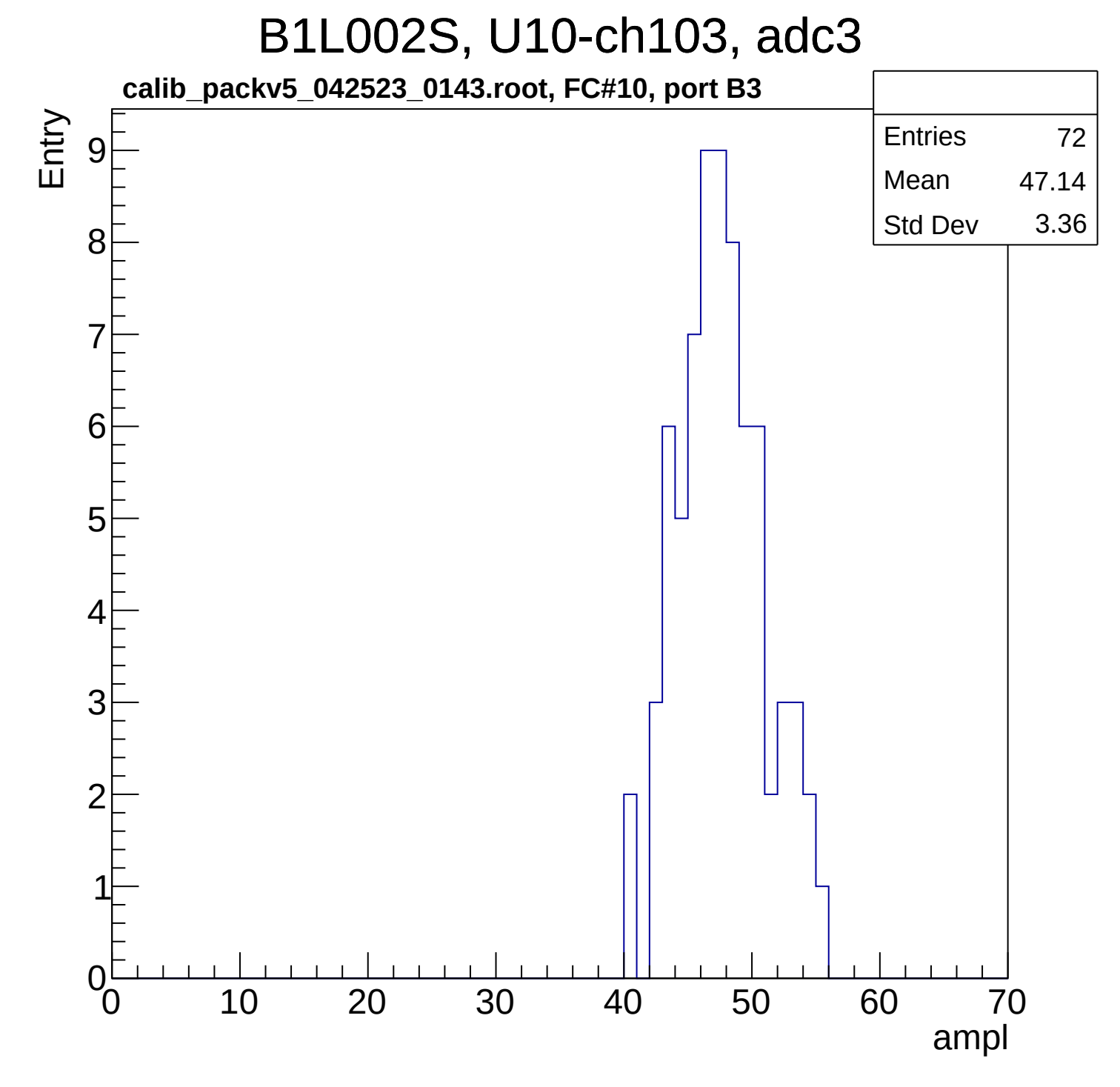
B1L002S, U10-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.14
Std Dev 3.36
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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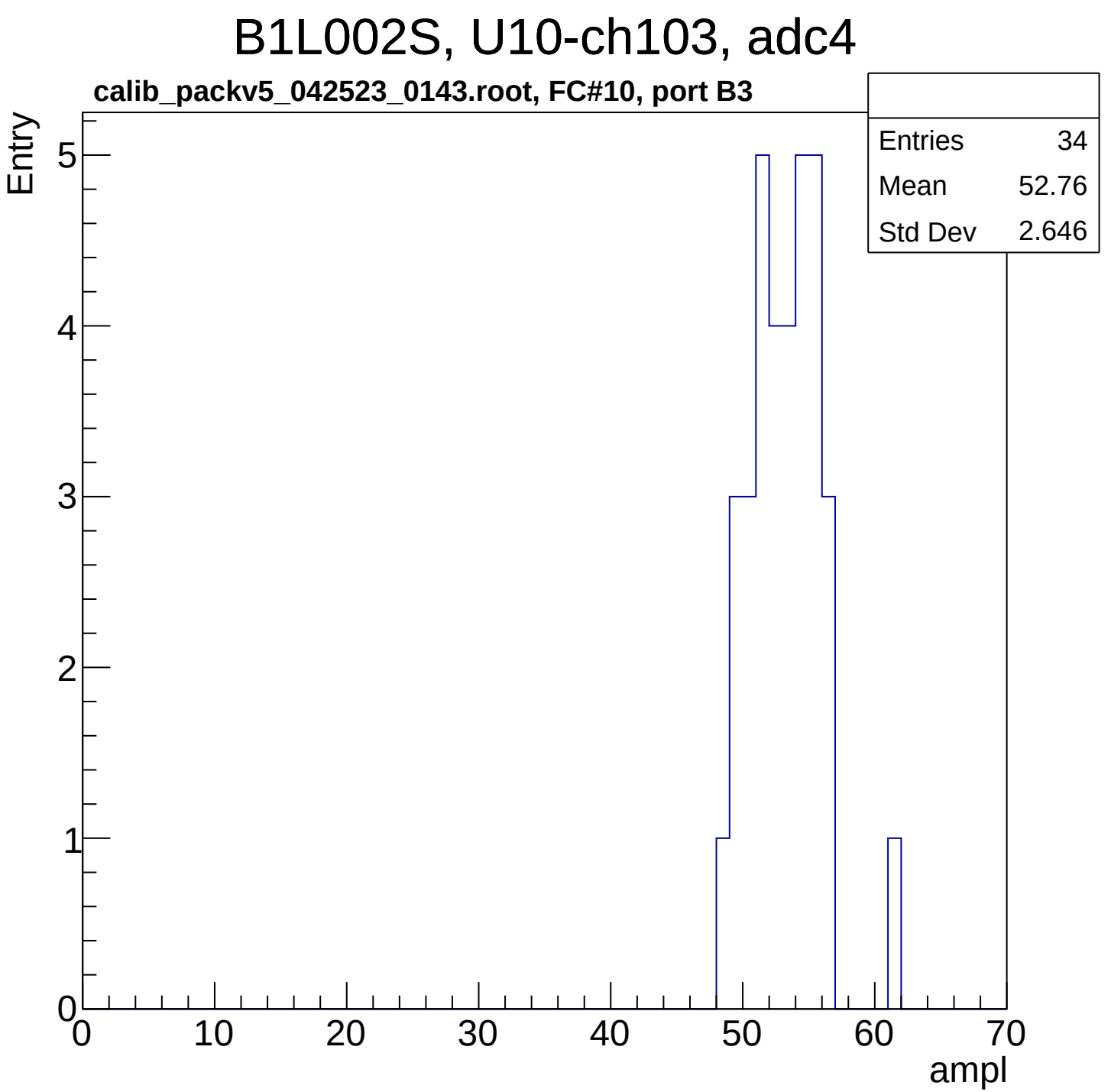
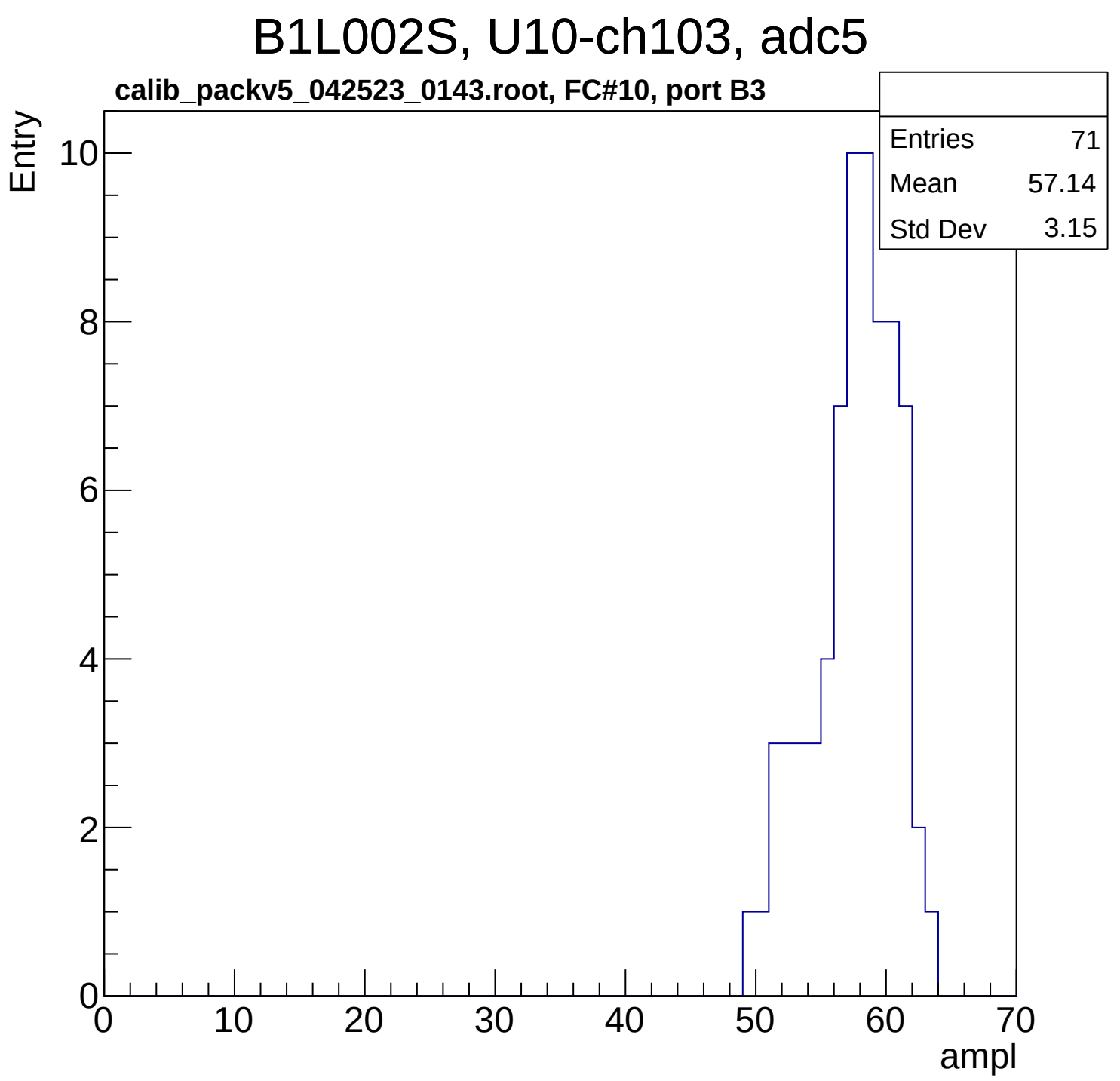
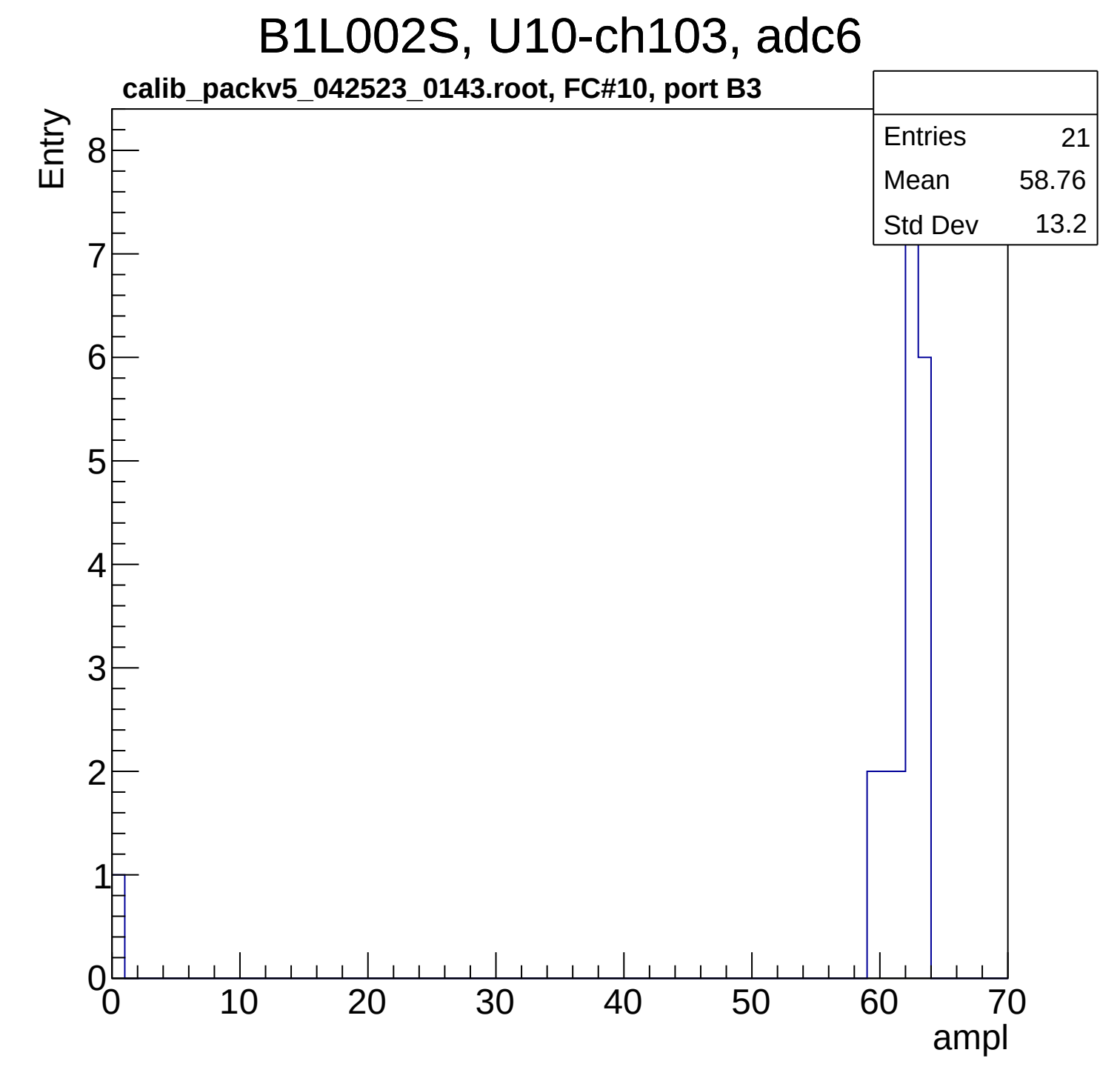

B1L002S, U10-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 52.76
Std Dev 2.646
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.14
Std Dev 3.15
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.76
Std Dev 13.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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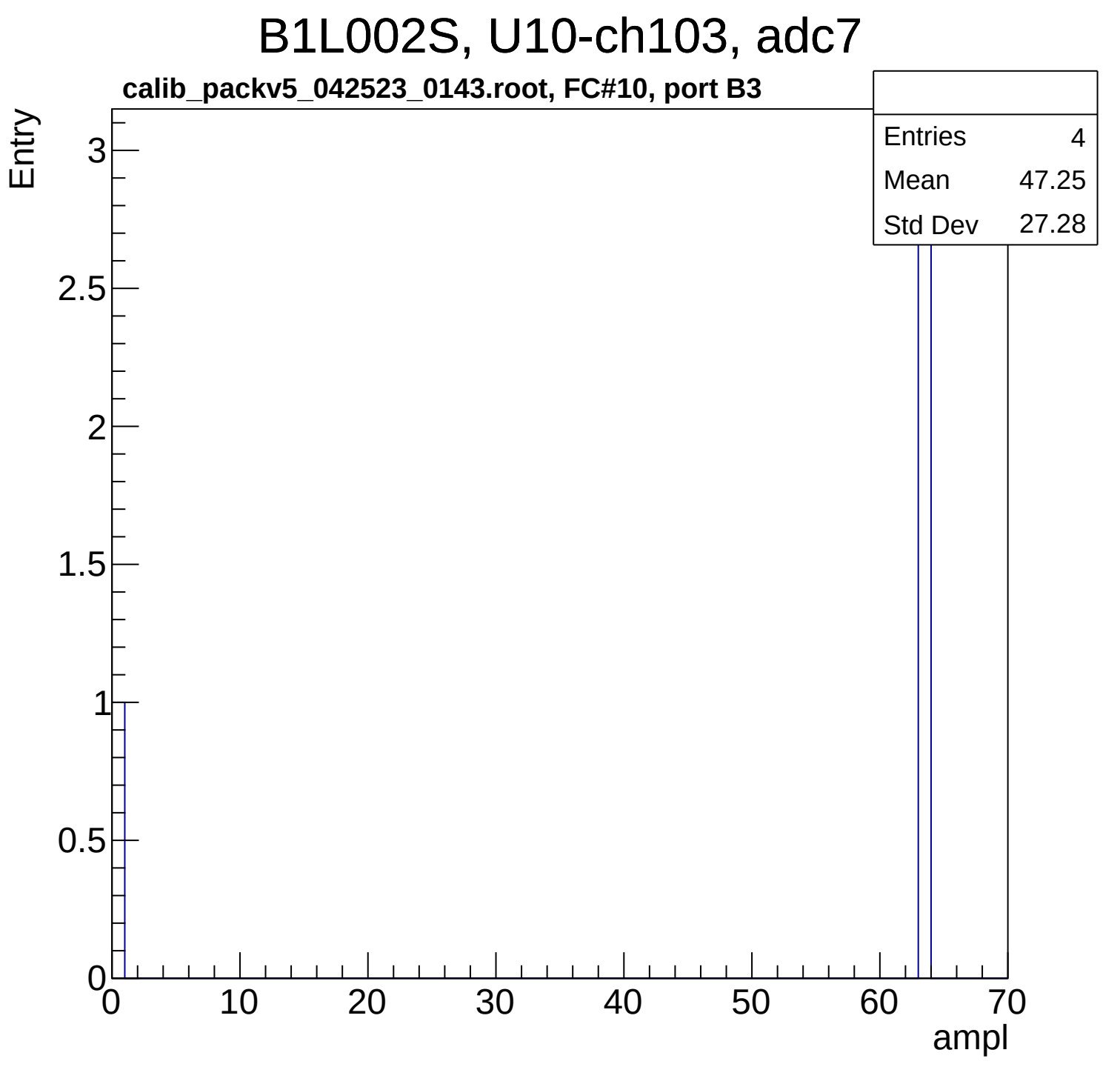
B1L002S, U10-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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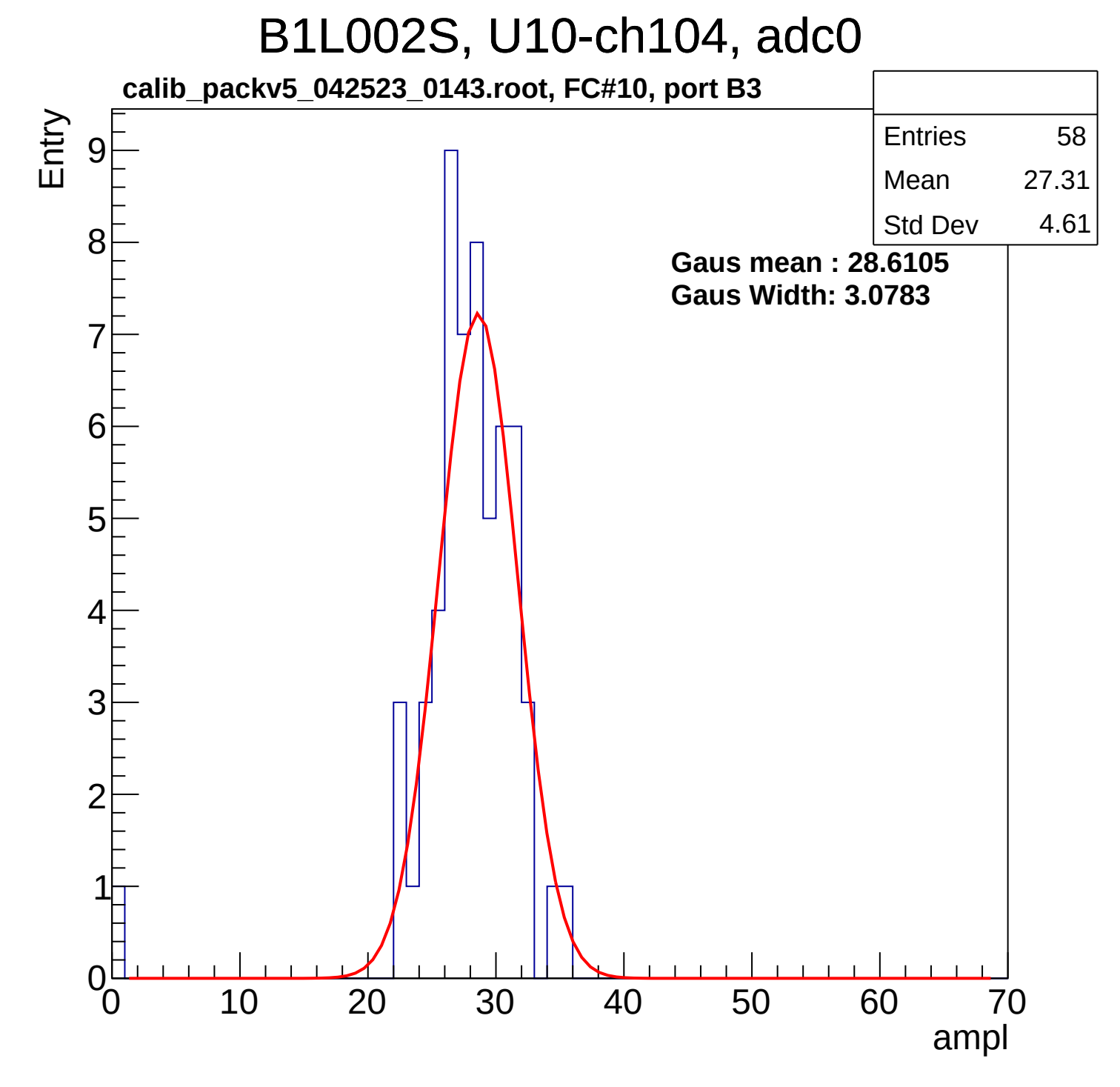
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.31
Std Dev 4.61
Gaus mean : 28.6105
Gaus Width: 3.0783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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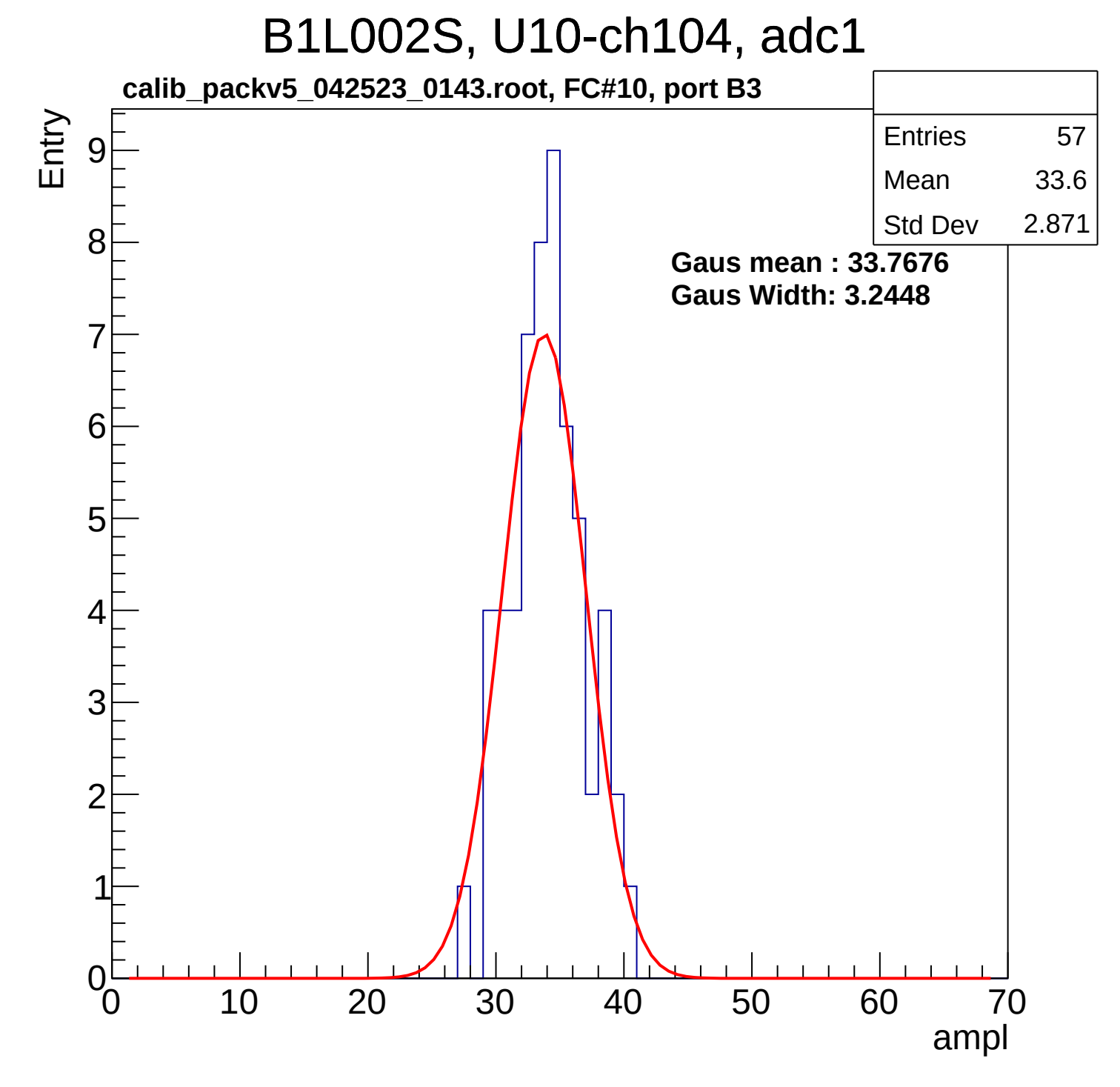
B1L002S, U10-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.6
Std Dev 2.871
Gaus mean : 33.7676
Gaus Width: 3.2448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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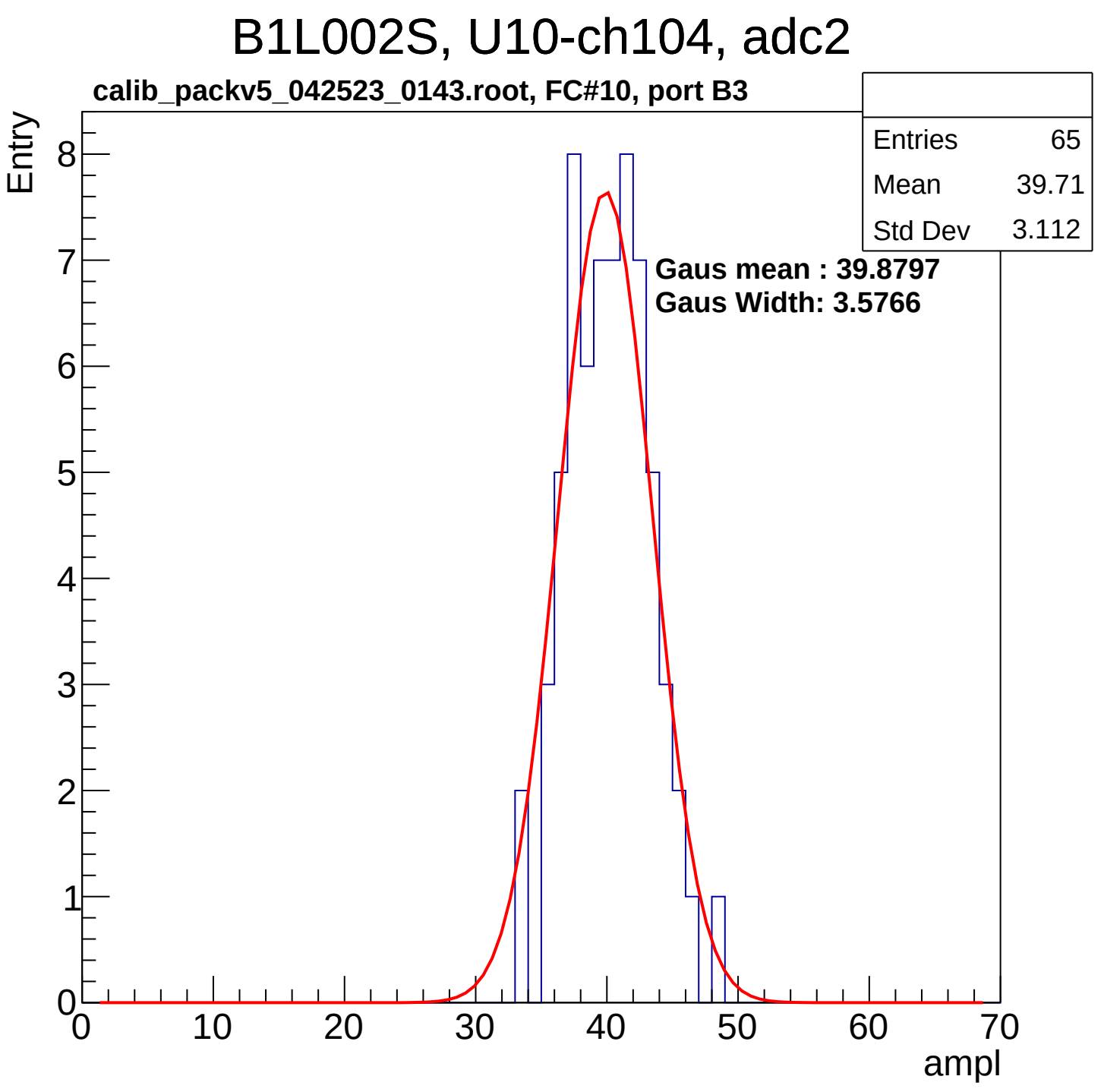
B1L002S, U10-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 39.71
Std Dev 3.112
Gaus mean : 39.8797
Gaus Width: 3.5766
Entry
ampl
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2
3
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0
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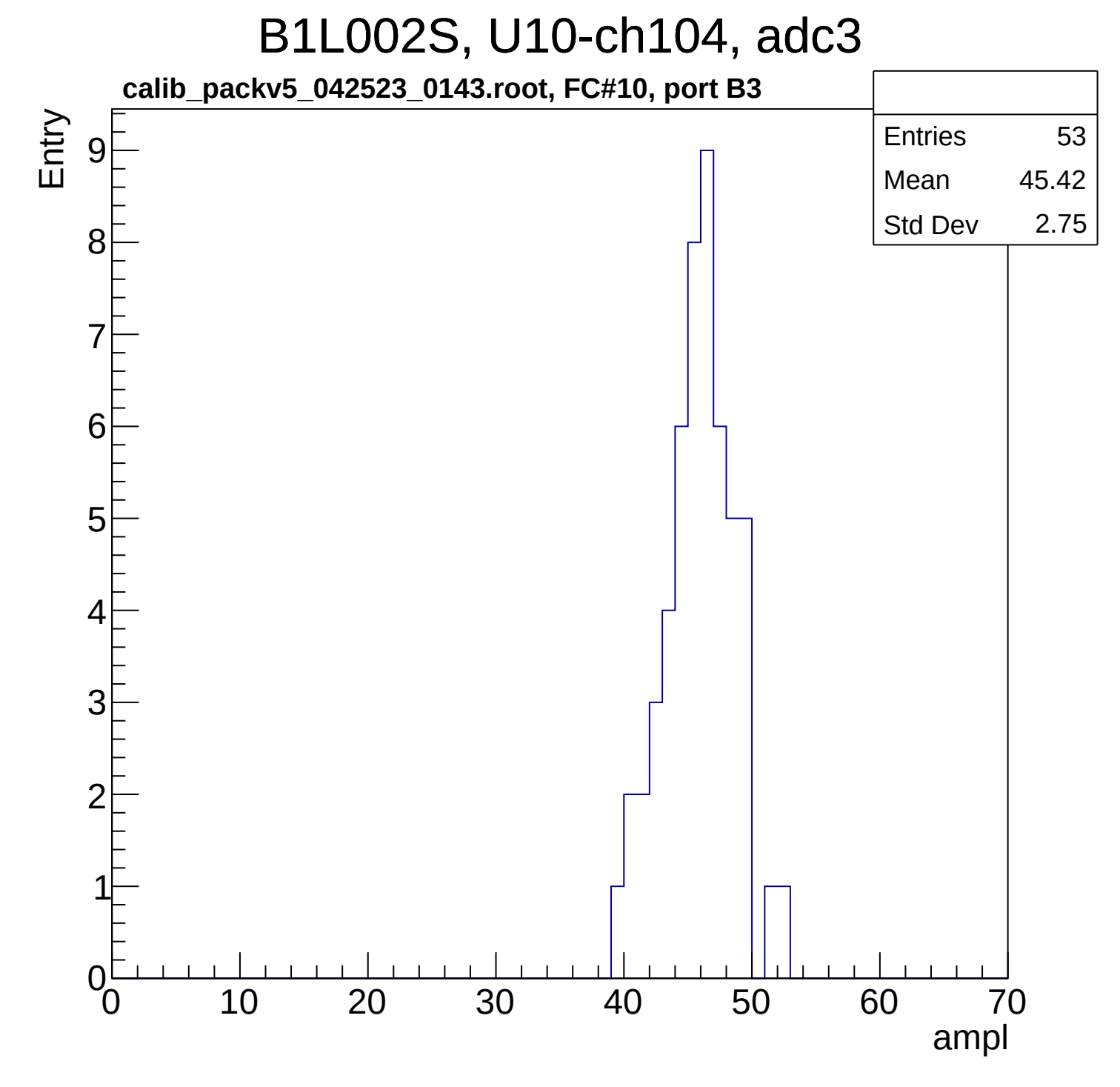
B1L002S, U10-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.42
Std Dev 2.75
Entry
ampl
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6
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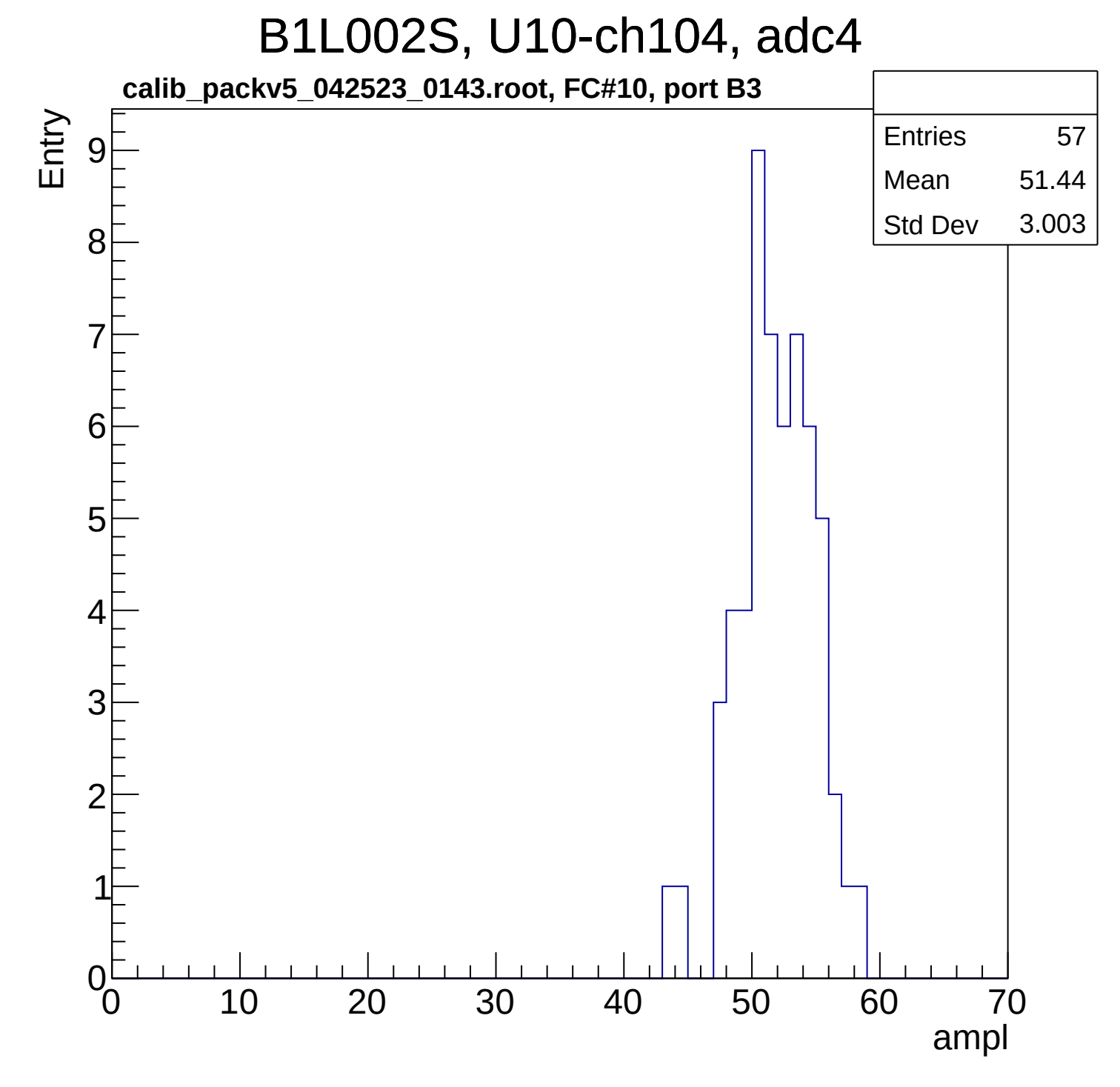
B1L002S, U10-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.44
Std Dev 3.003
Entry
ampl
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3
4
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6
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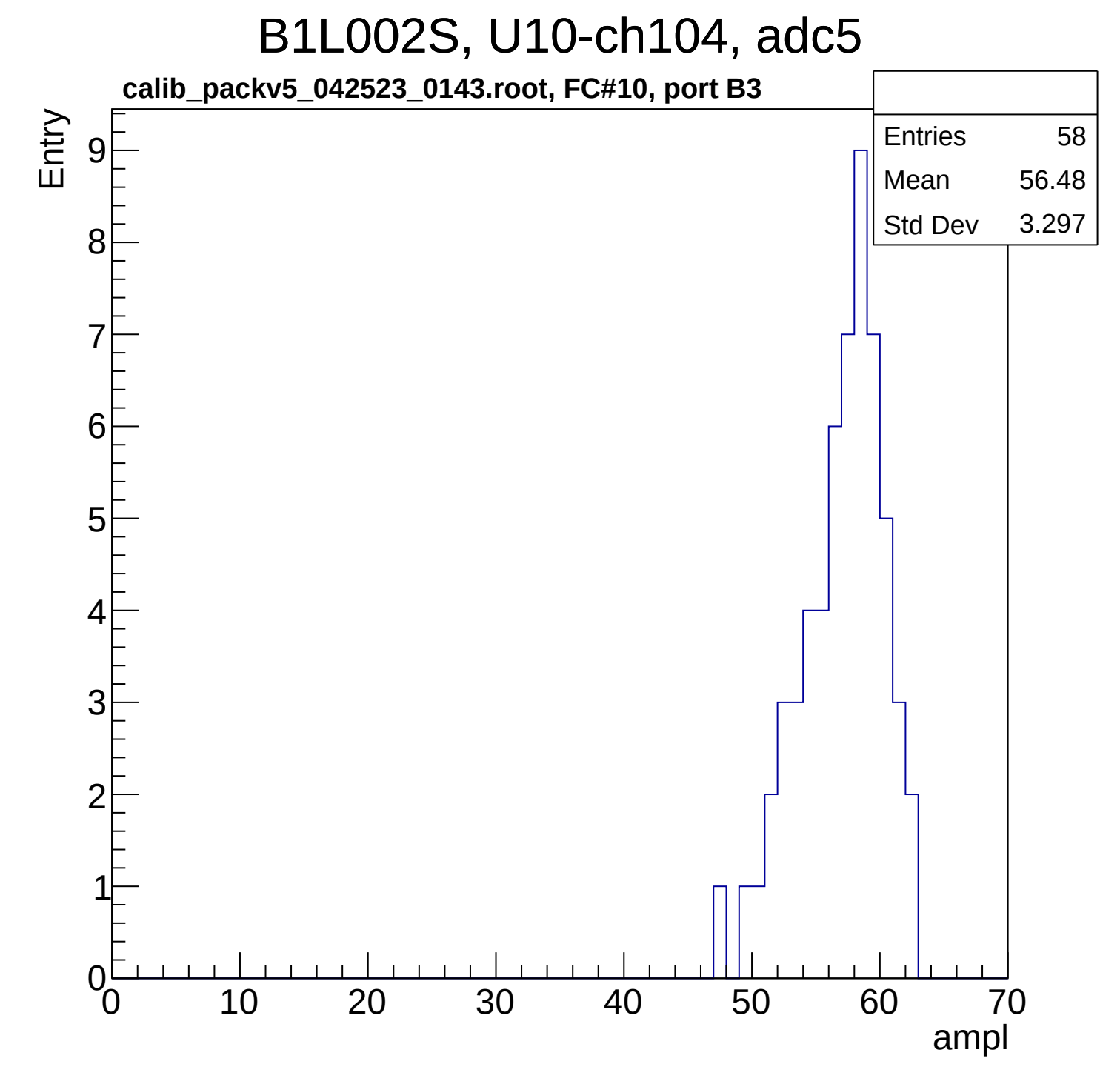
B1L002S, U10-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 56.48
Std Dev 3.297
Entry
ampl
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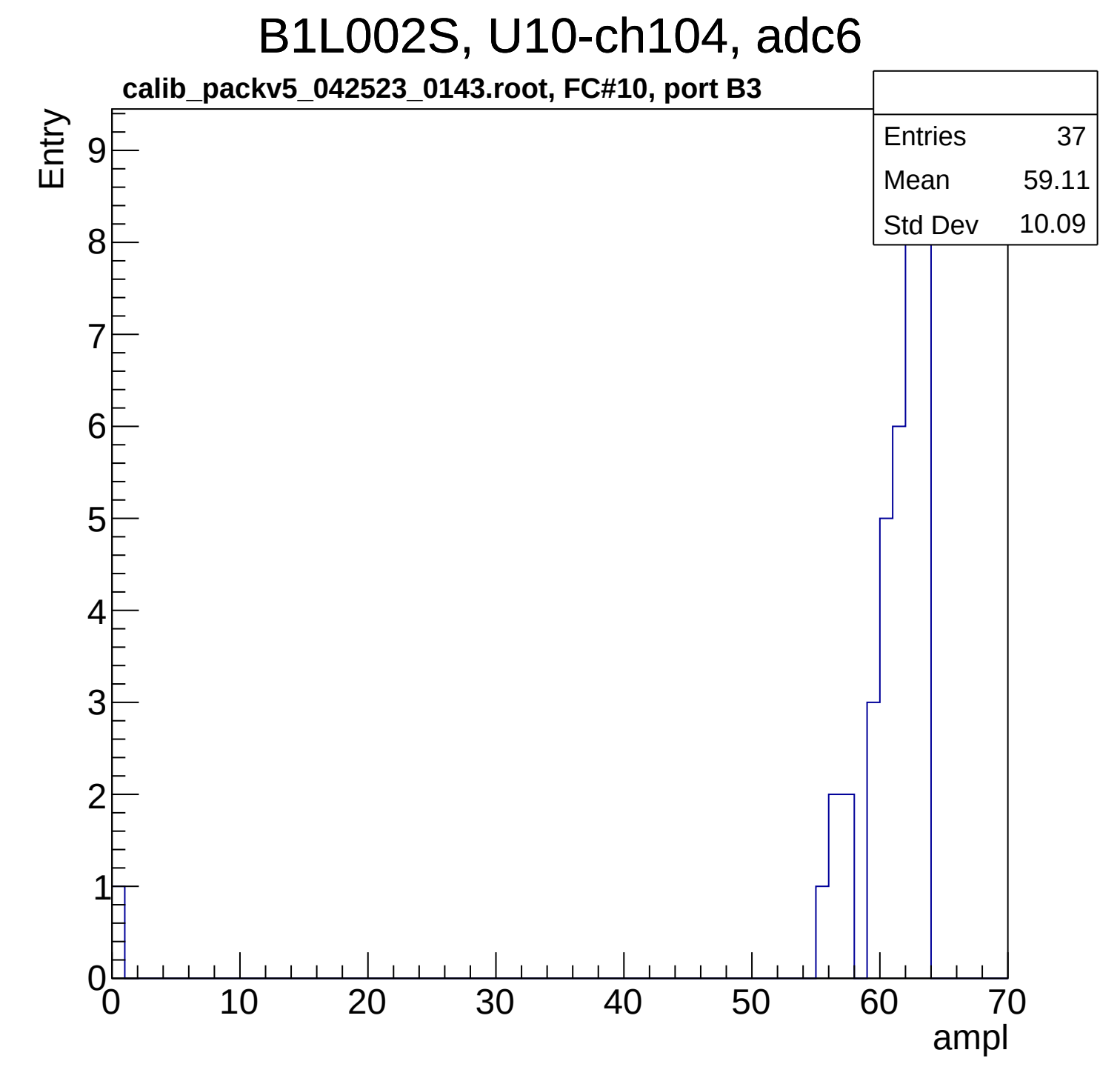
B1L002S, U10-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 59.11
Std Dev 10.09
Entry
ampl
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2
3
4
5
6
7
8
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0
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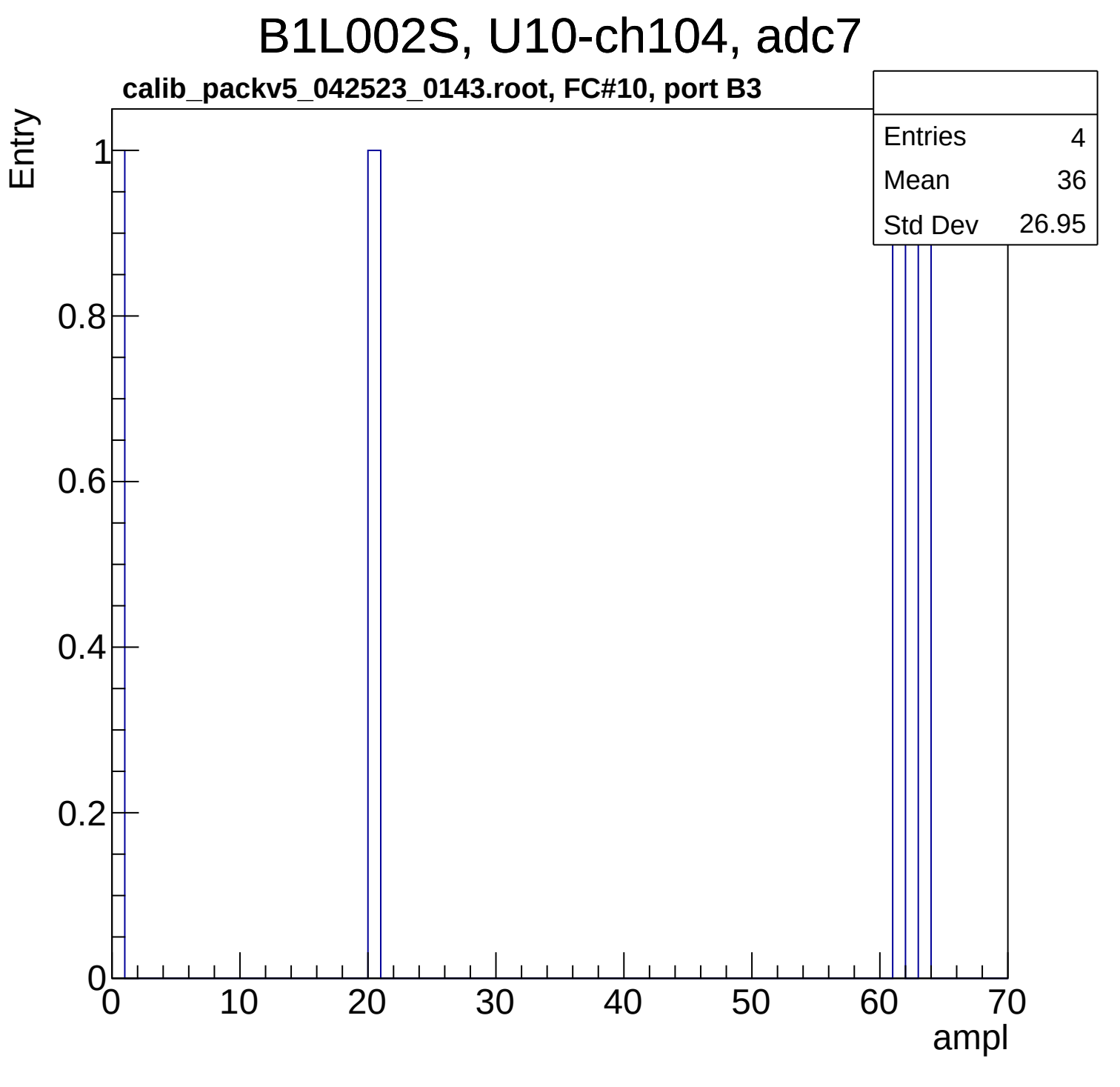
B1L002S, U10-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36
Std Dev 26.95
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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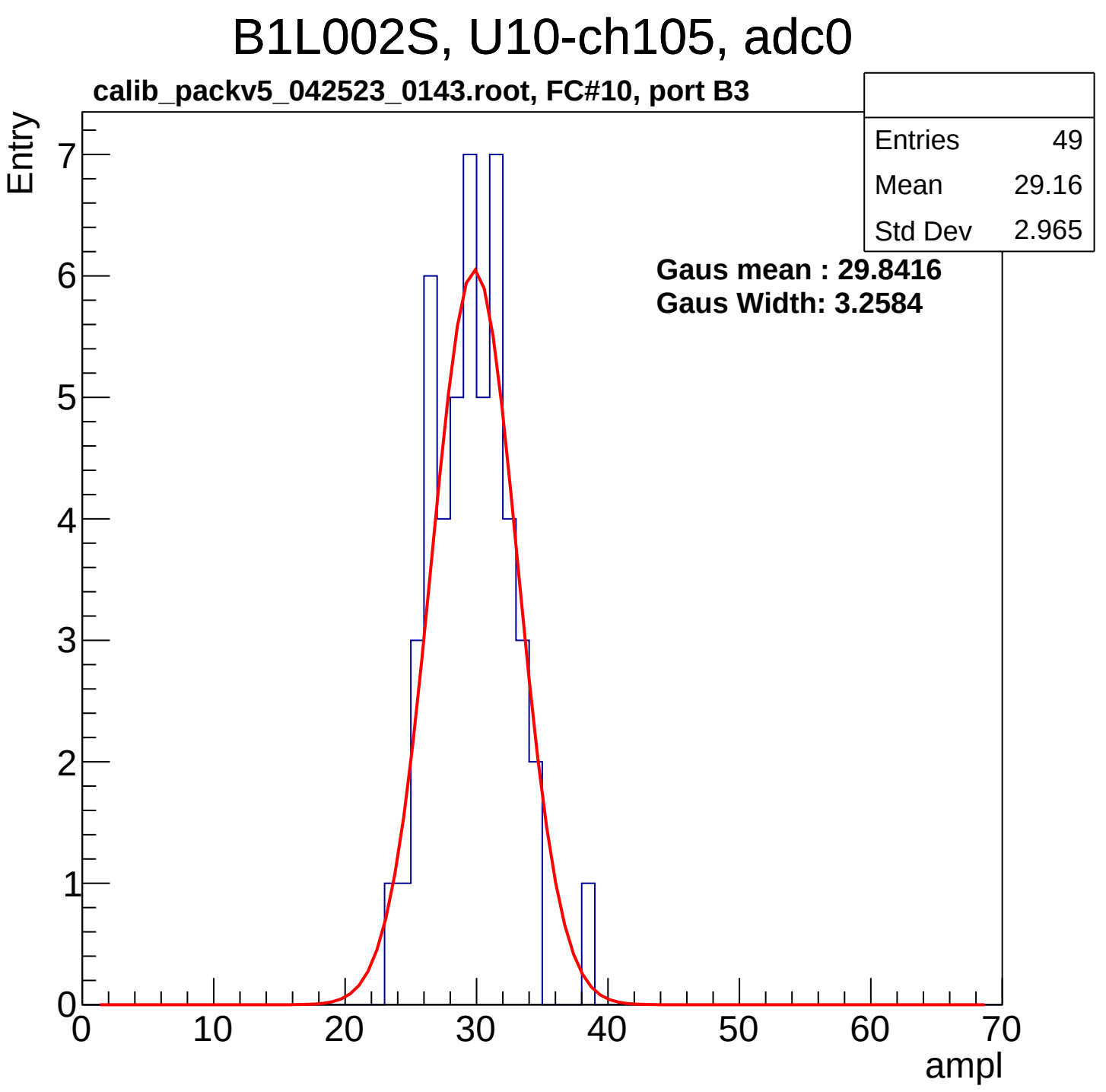
B1L002S, U10-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 29.16
Std Dev 2.965
Gaus mean : 29.8416
Gaus Width: 3.2584
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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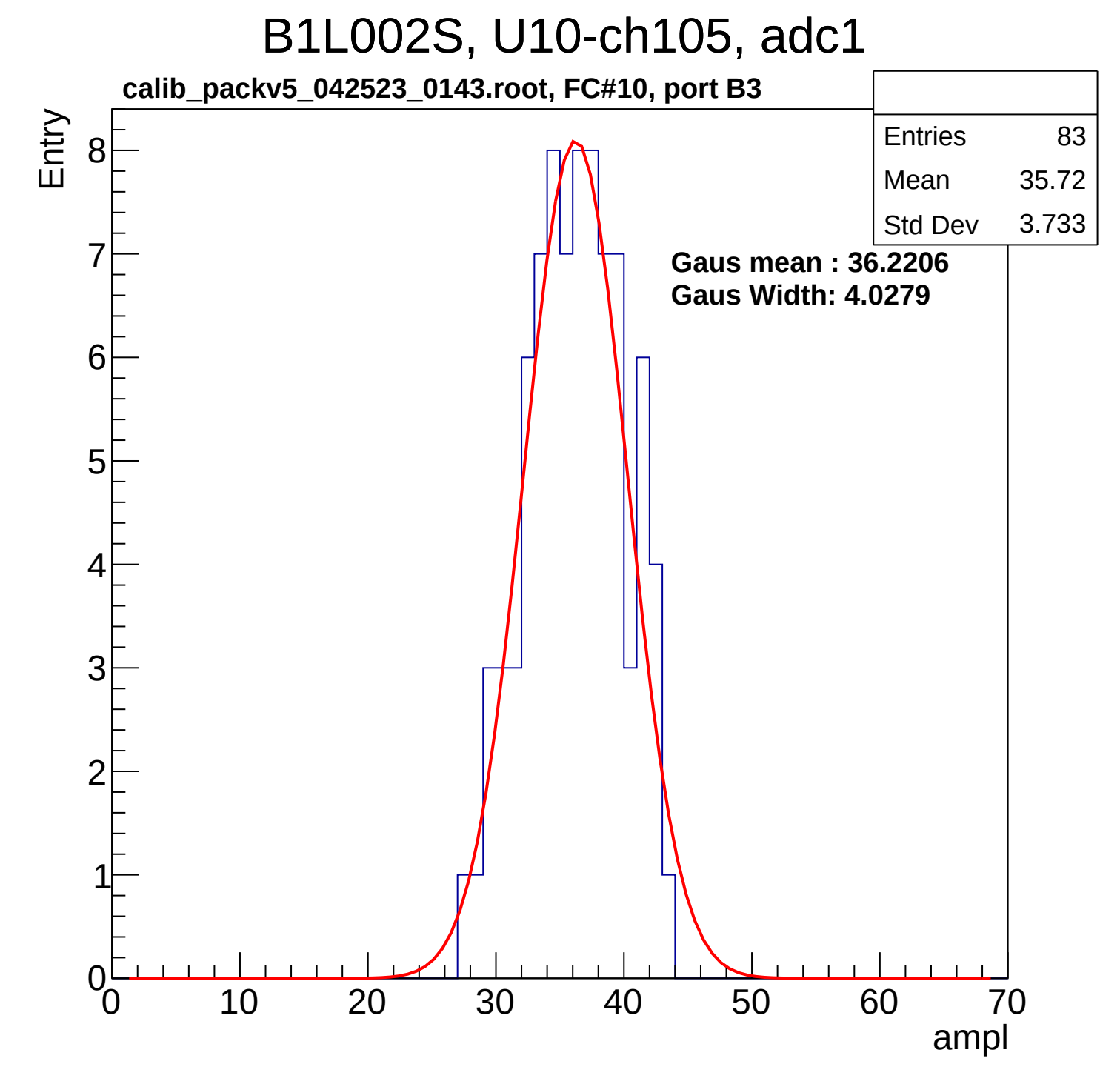
B1L002S, U10-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 35.72
Std Dev 3.733
Gaus mean : 36.2206
Gaus Width: 4.0279
Entry
ampl
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1
2
3
4
5
6
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0
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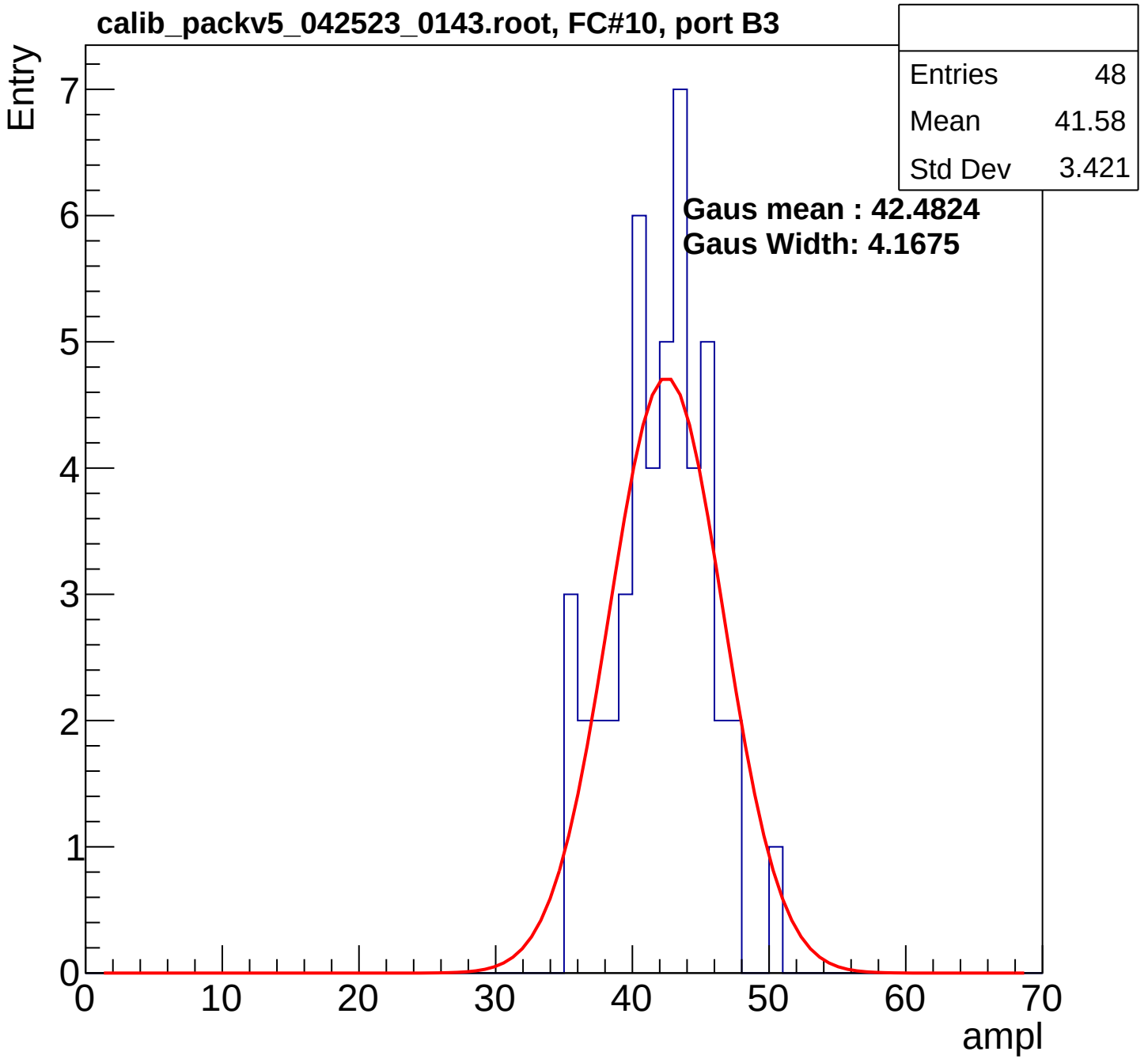
B1L002S, U10-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.58
Std Dev 3.421
Gaus mean : 42.4824
Gaus Width: 4.1675
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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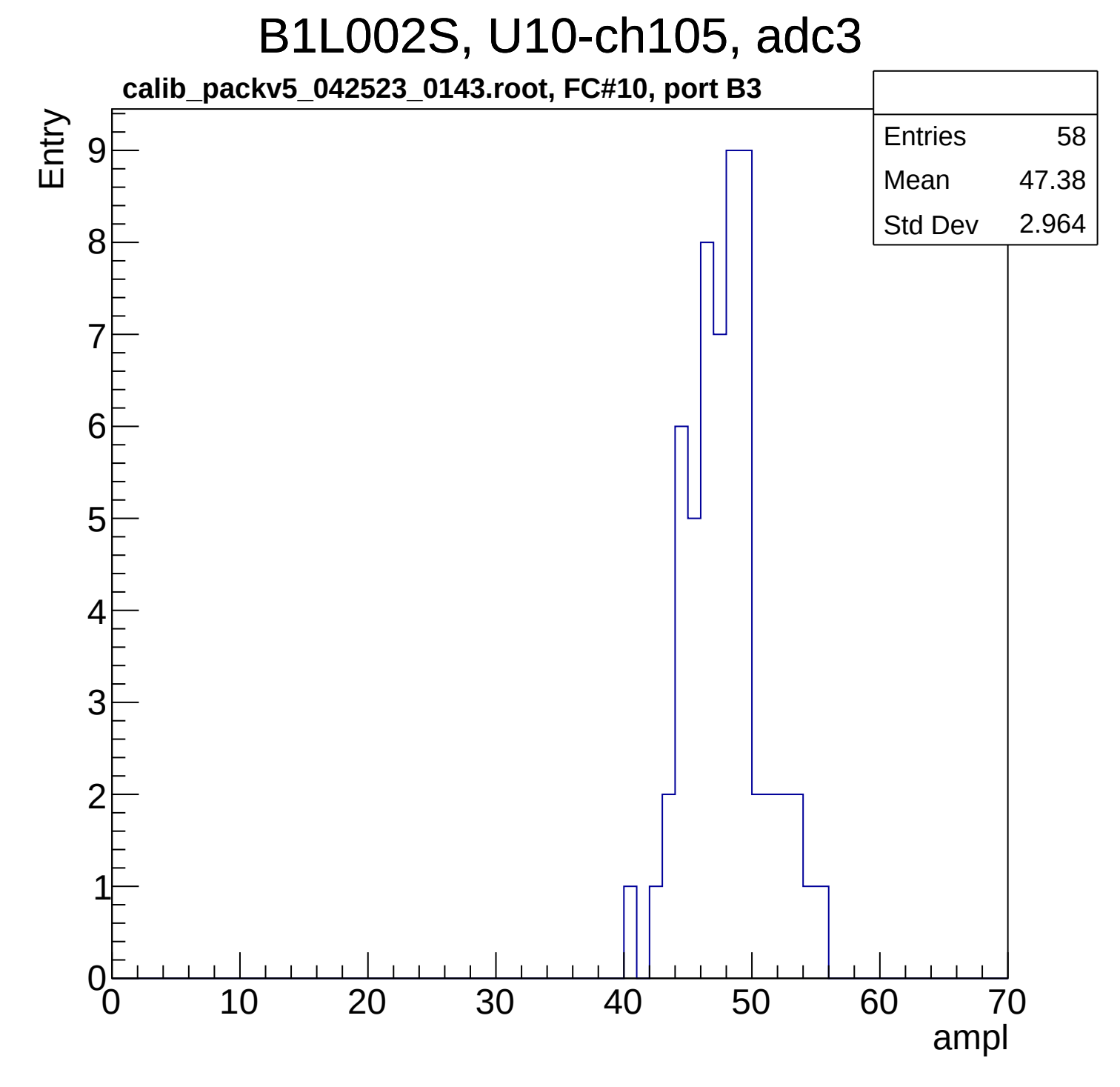
B1L002S, U10-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.38
Std Dev 2.964
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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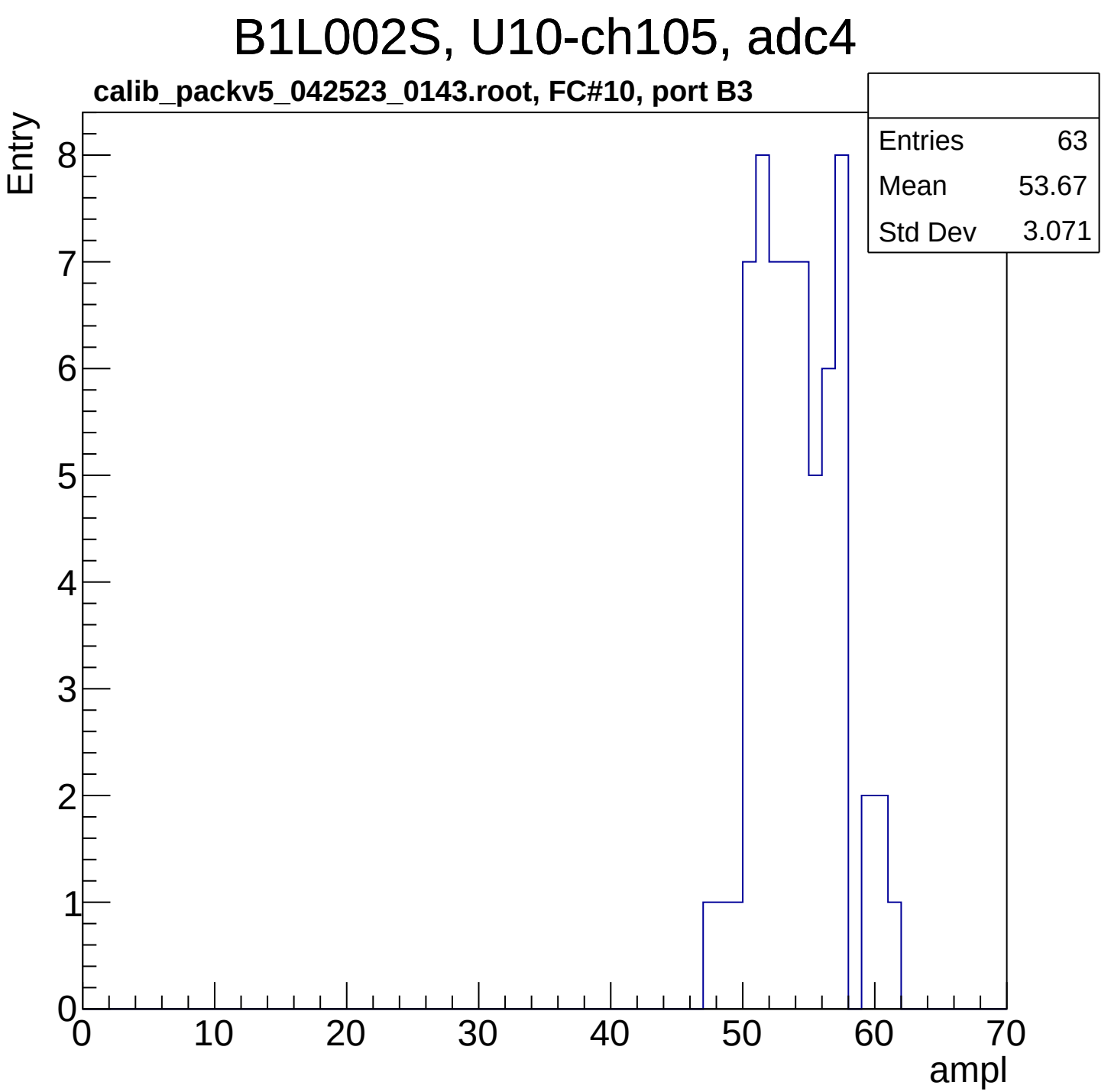
B1L002S, U10-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.67
Std Dev 3.071
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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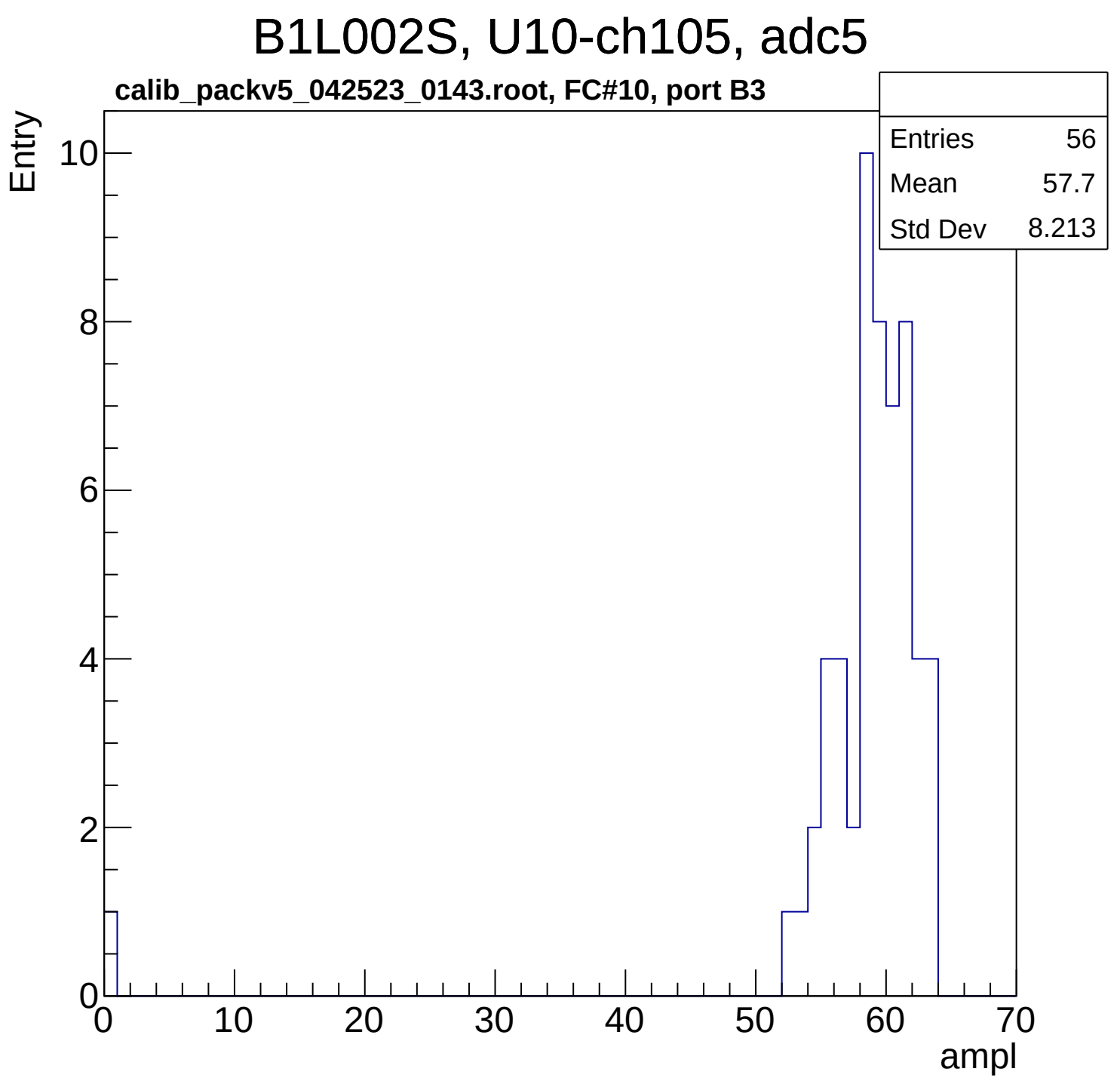
B1L002S, U10-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.7
Std Dev 8.213
Entry
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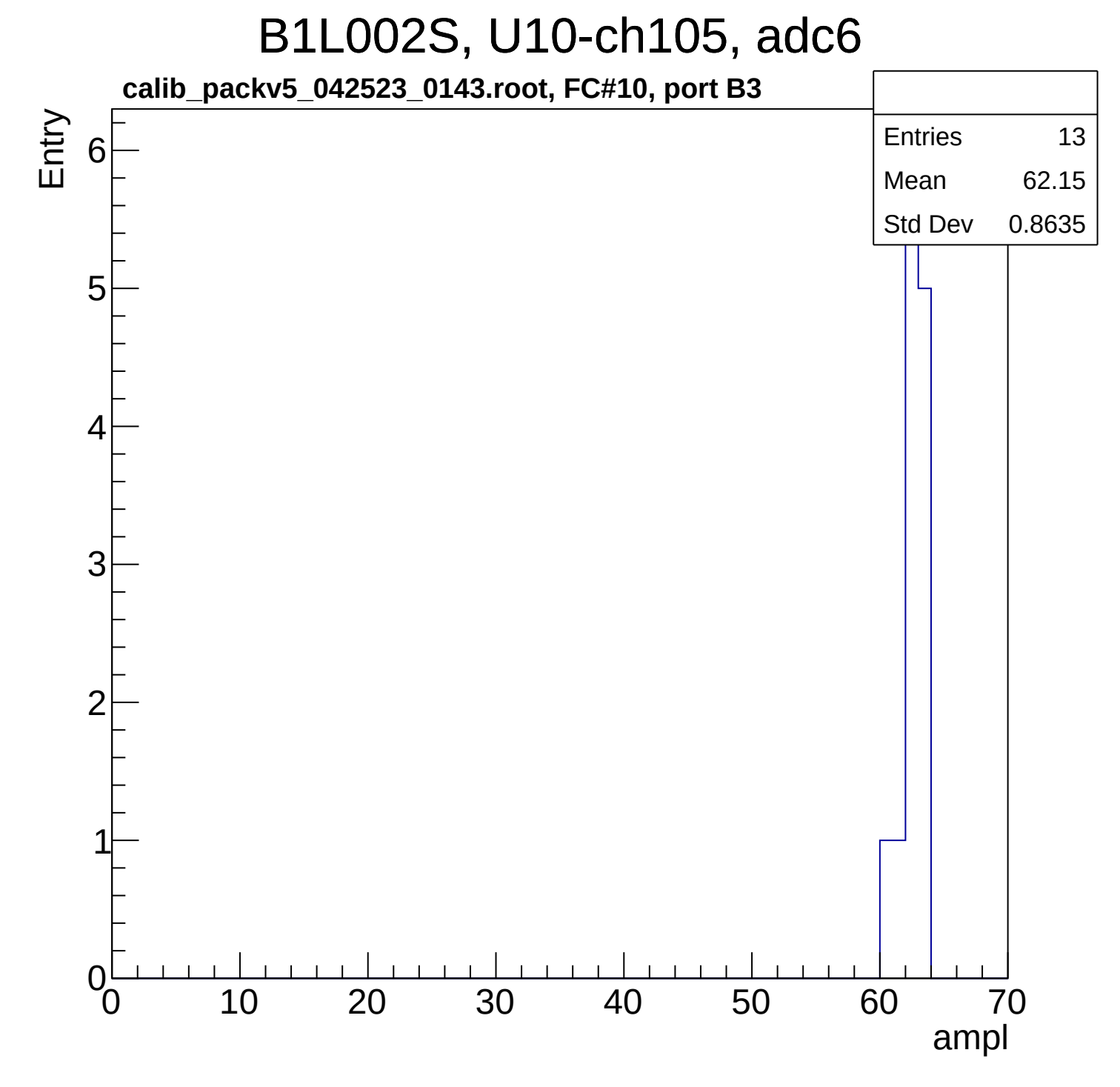
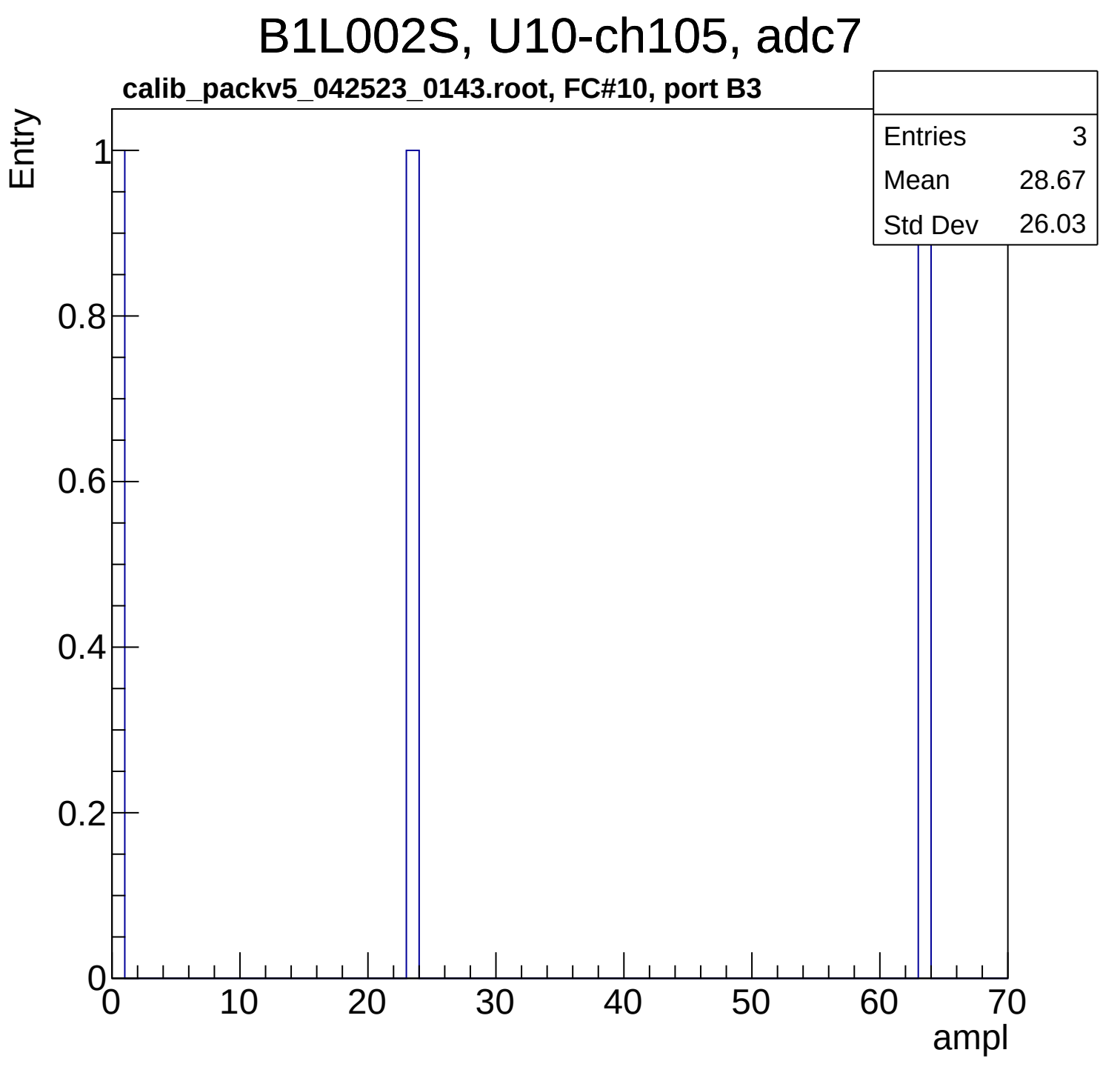
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 62.15
Std Dev 0.8635
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
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60
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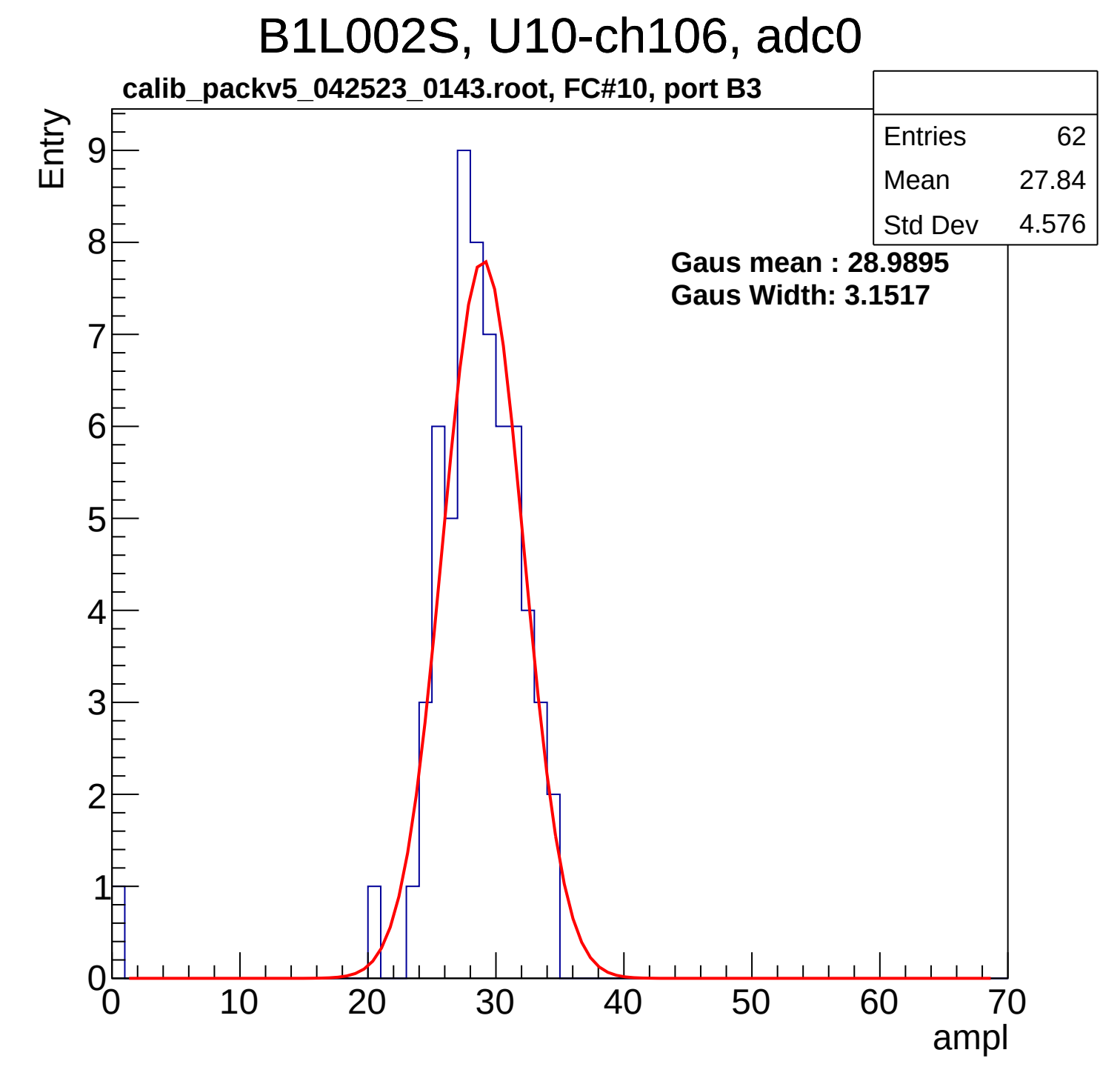
B1L002S, U10-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.84
Std Dev 4.576
Gaus mean : 28.9895
Gaus Width: 3.1517
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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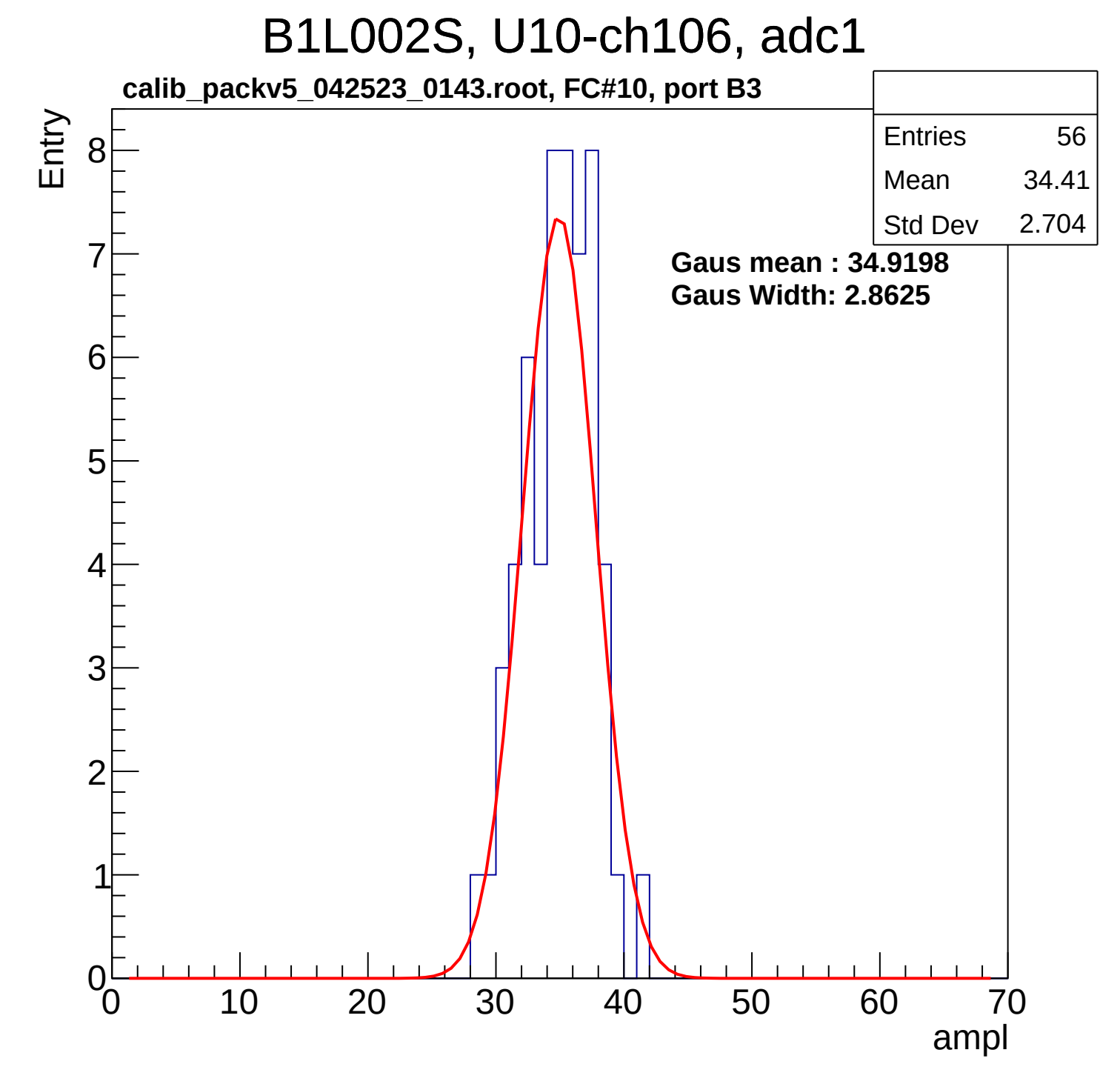
B1L002S, U10-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.41
Std Dev 2.704
Gaus mean : 34.9198
Gaus Width: 2.8625
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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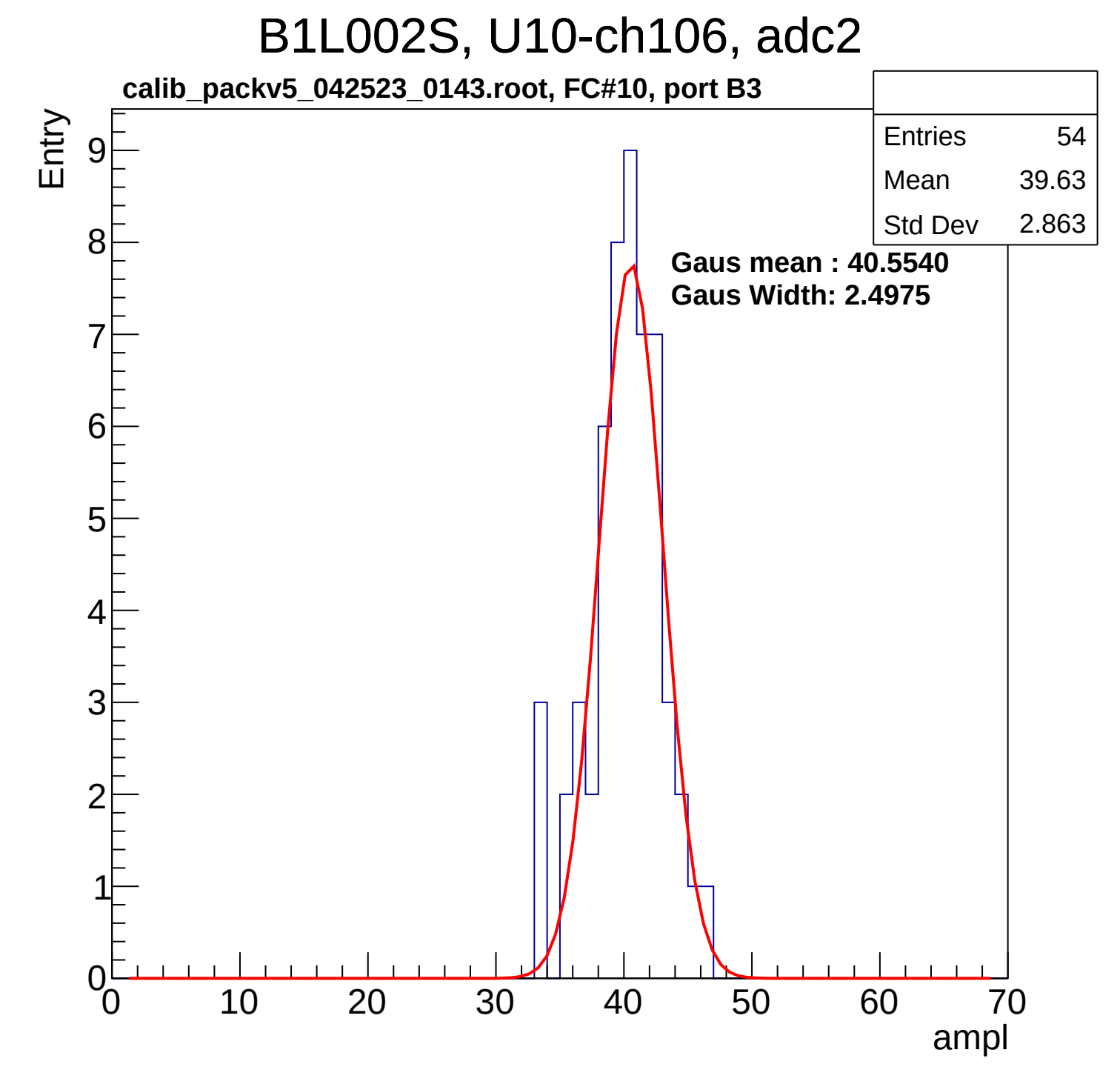
B1L002S, U10-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 39.63
Std Dev 2.863
Gaus mean : 40.5540
Gaus Width: 2.4975
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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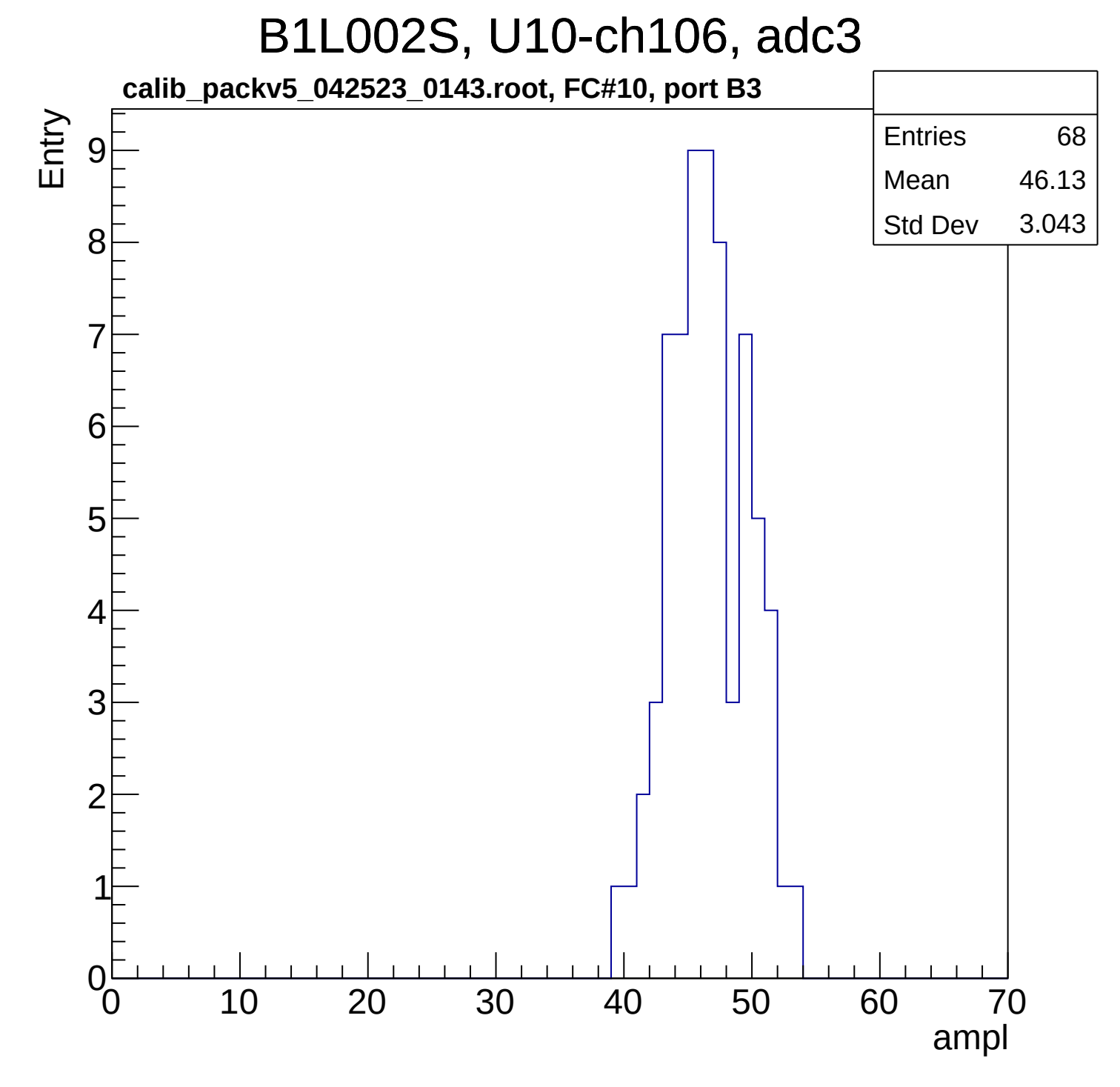
60
70


B1L002S, U10-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.13
Std Dev 3.043
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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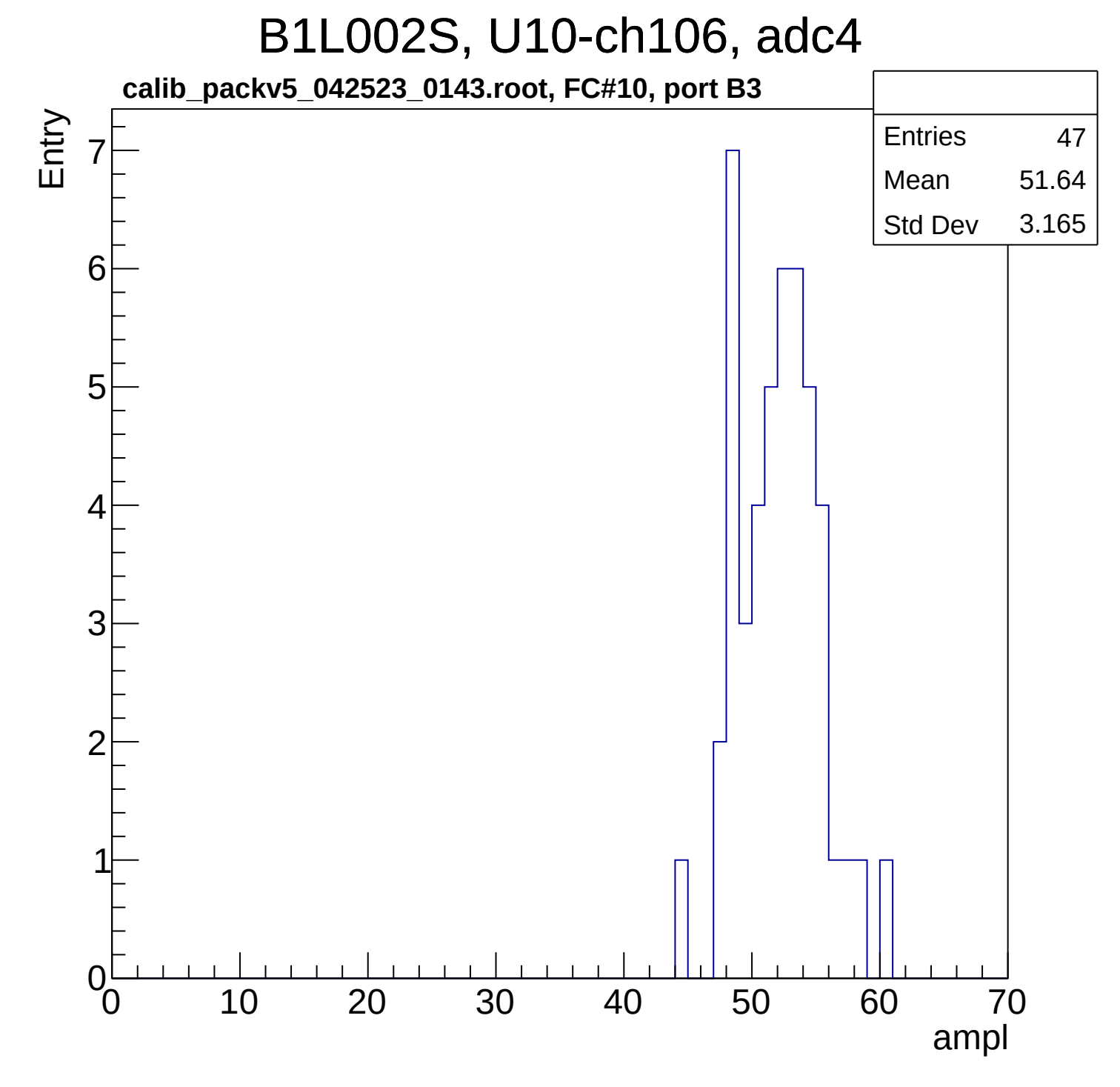
B1L002S, U10-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 51.64
Std Dev 3.165
Entry
ampl
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3
4
5
6
7
0
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50
60
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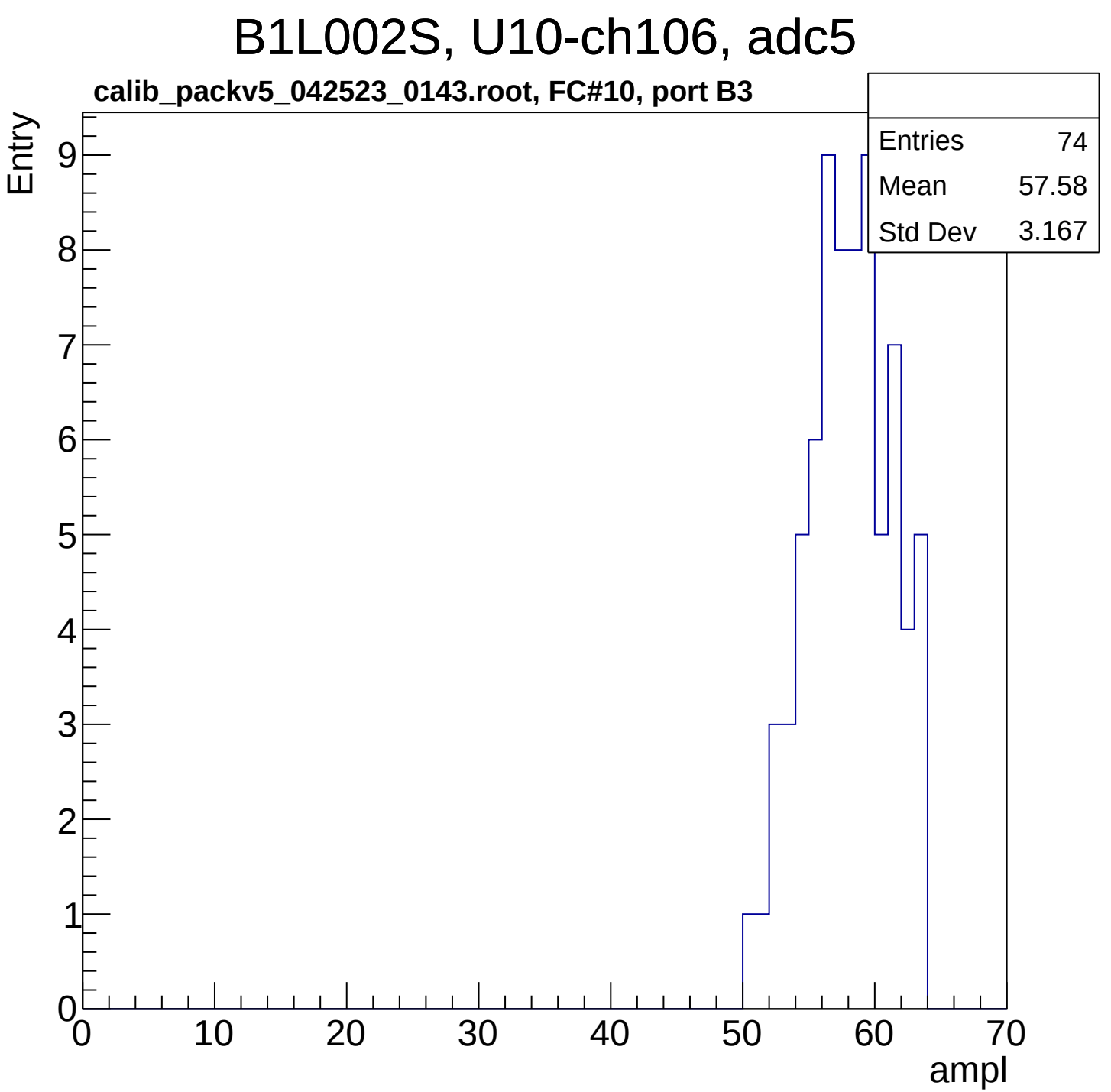
B1L002S, U10-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 57.58
Std Dev 3.167
Entry
ampl
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0
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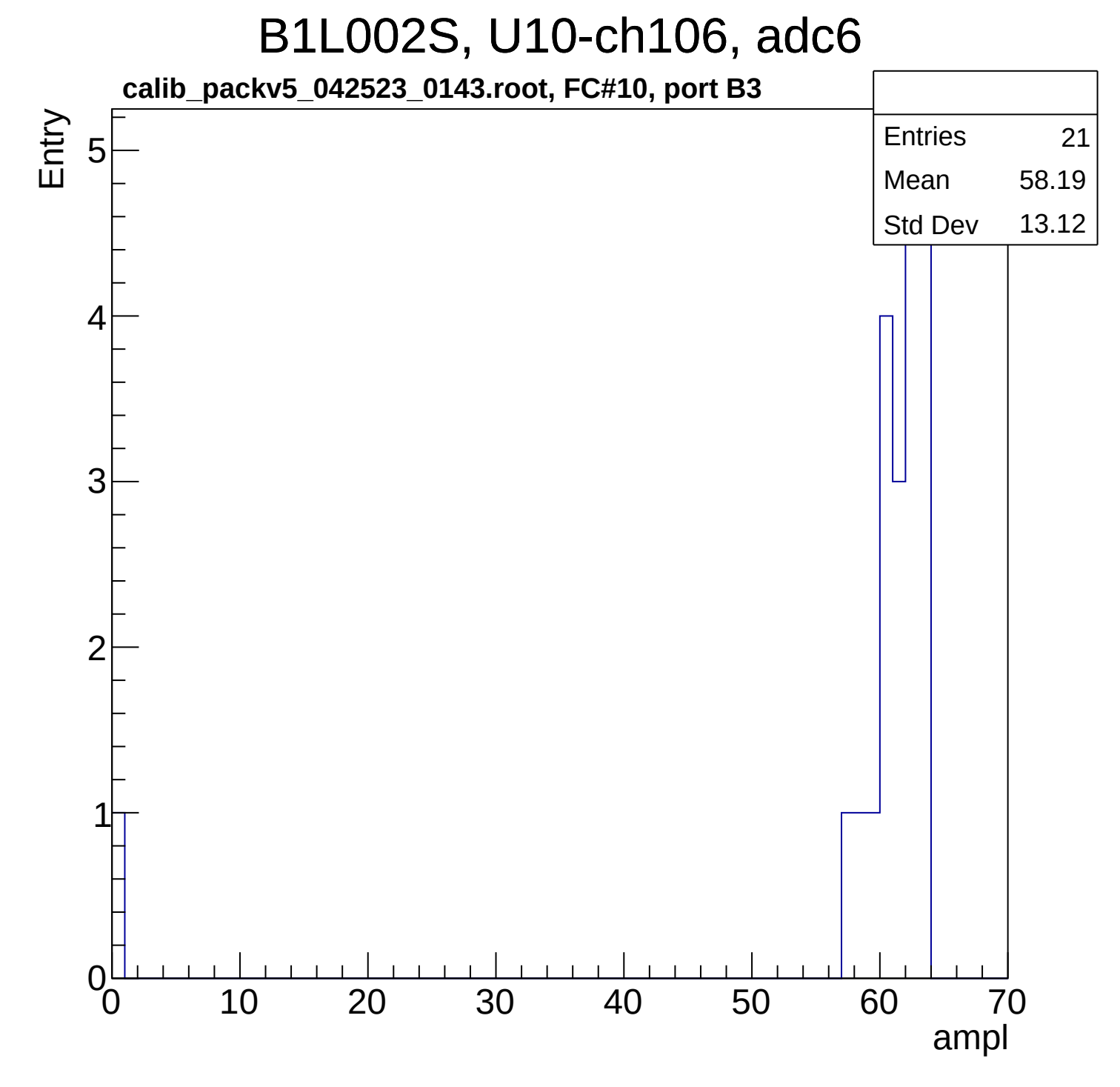
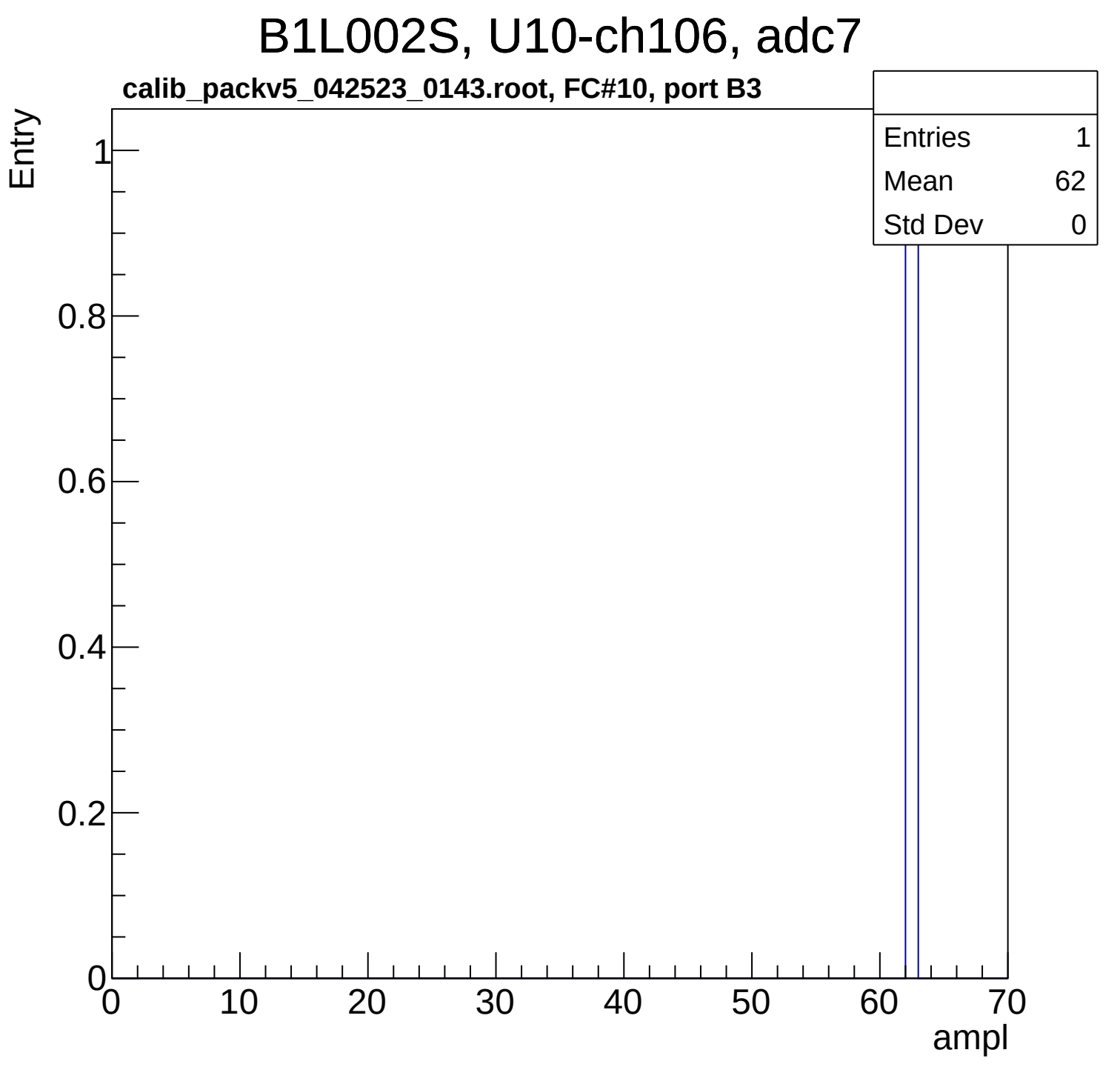

B1L002S, U10-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.19
Std Dev 13.12
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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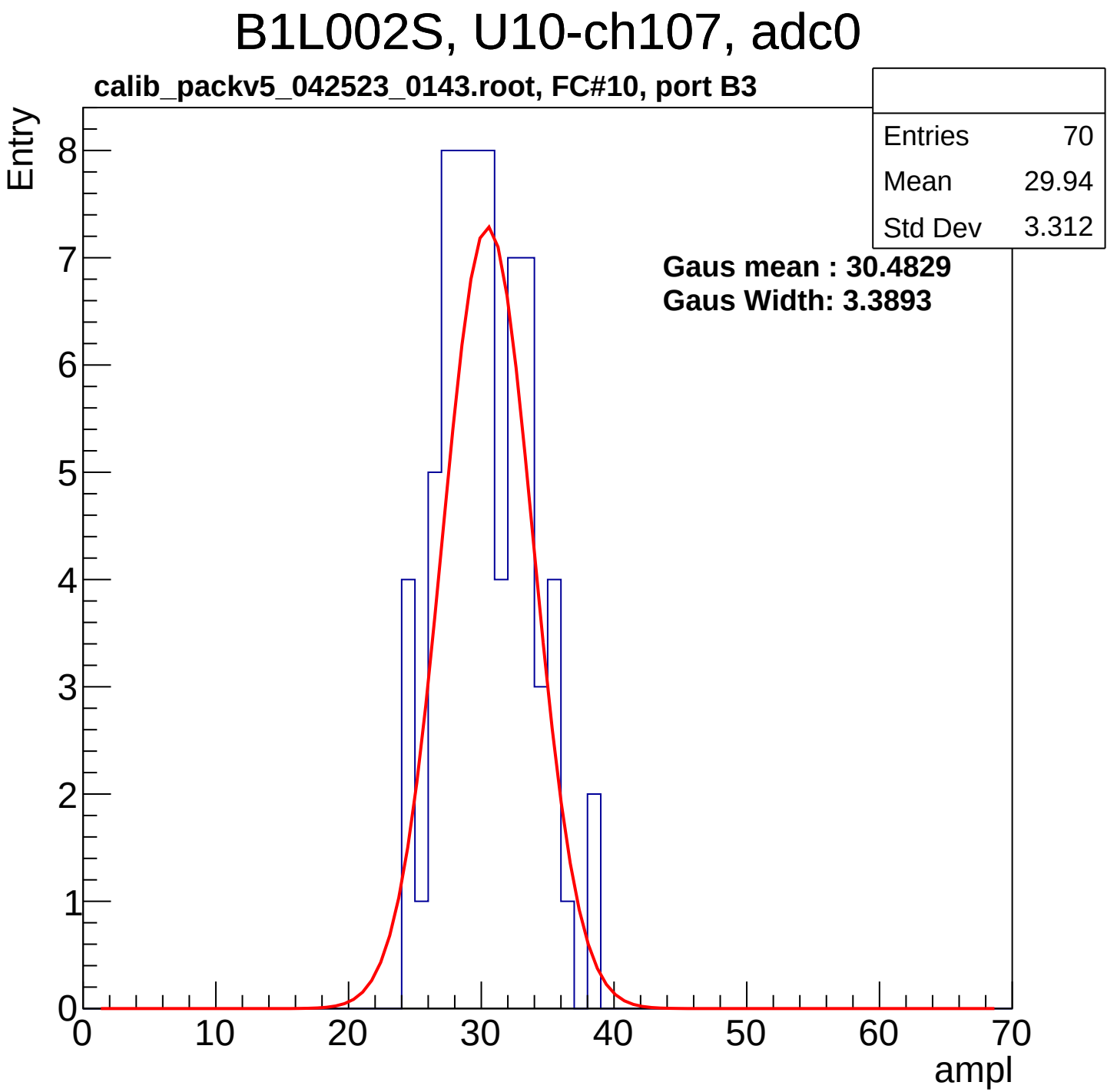
30
40
50
60
70
ampl


B1L002S, U10-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.94
Std Dev 3.312
Gaus mean : 30.4829
Gaus Width: 3.3893
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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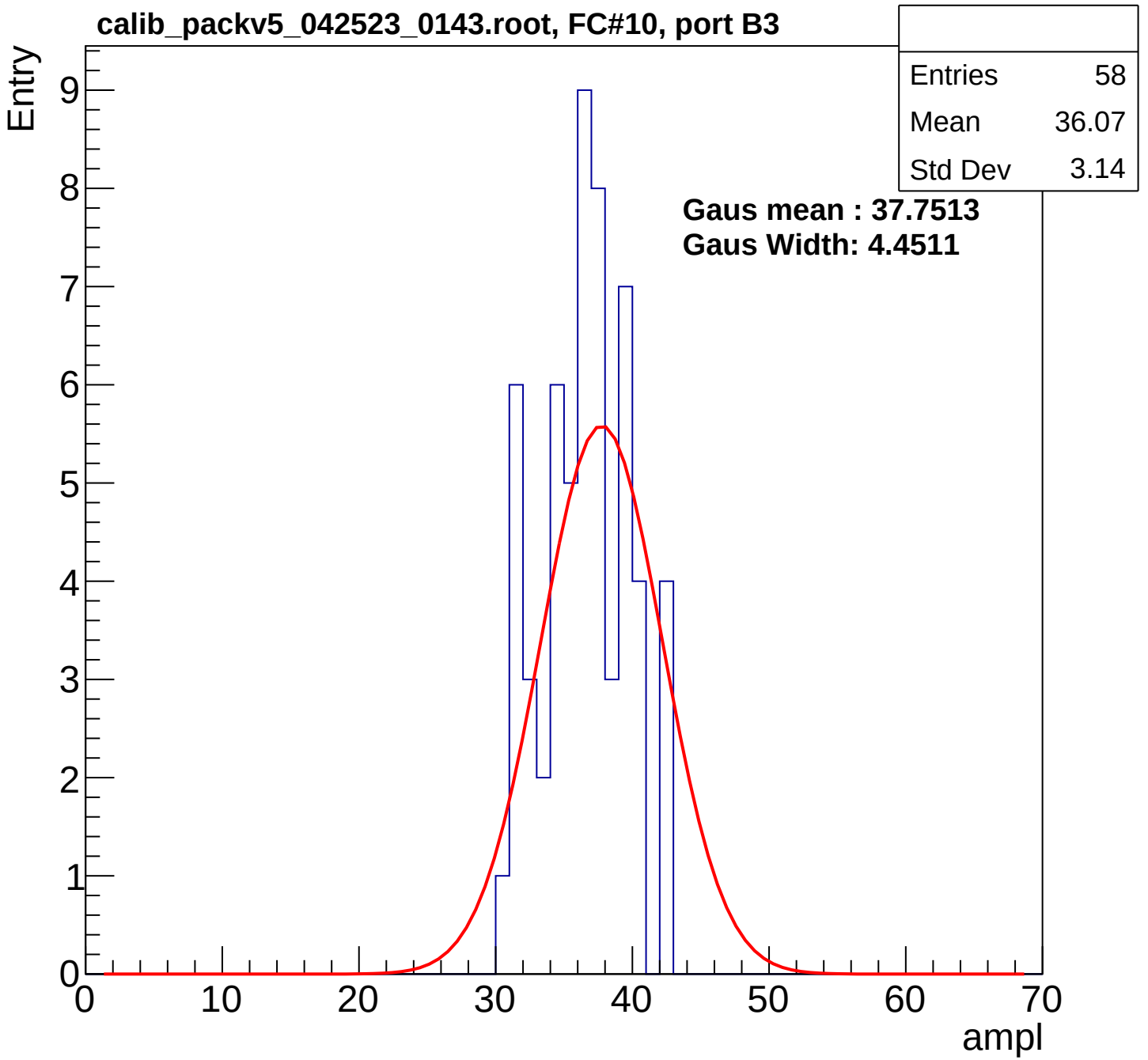
B1L002S, U10-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 36.07
Std Dev 3.14
Gaus mean : 37.7513
Gaus Width: 4.4511
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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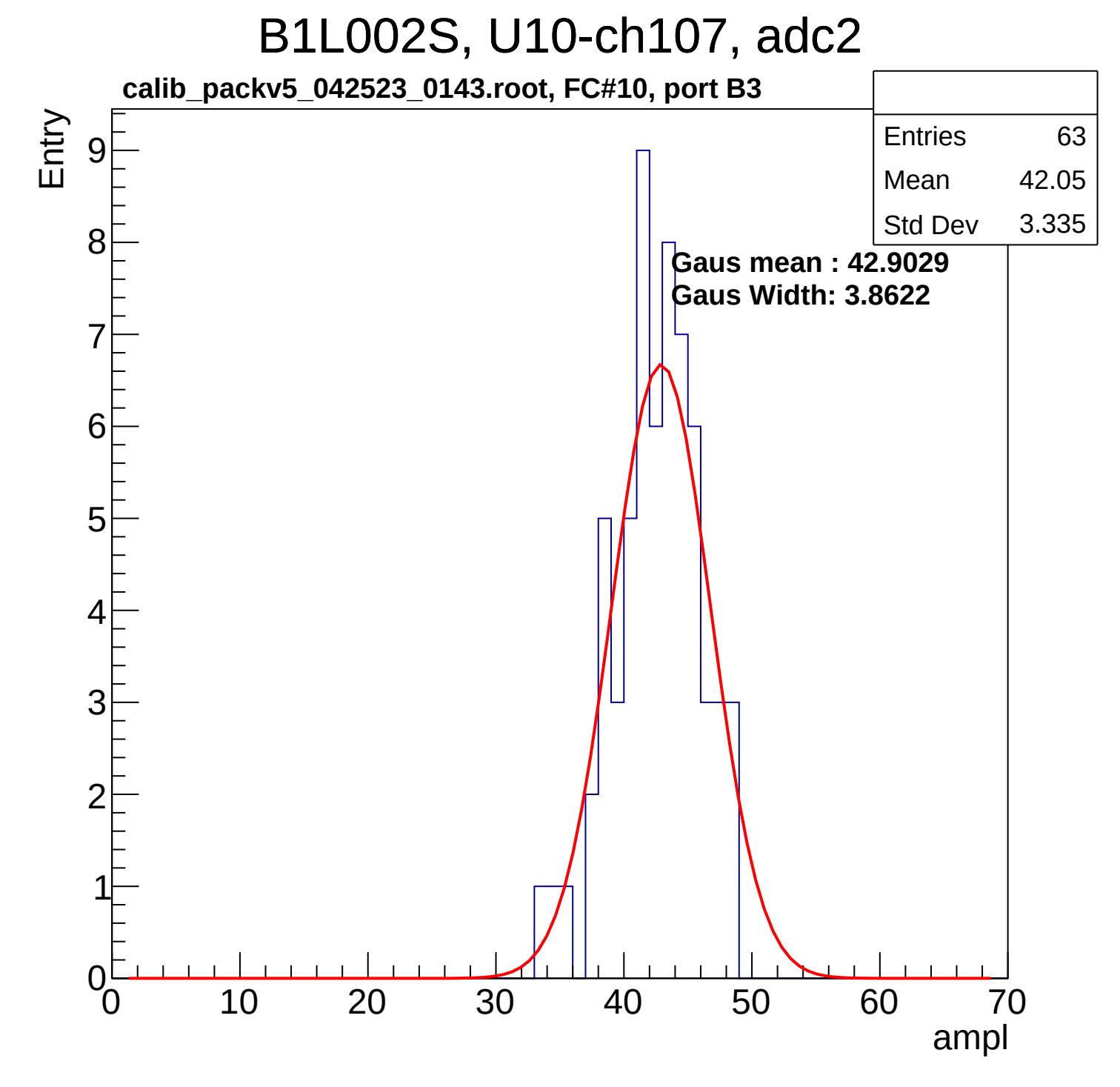
B1L002S, U10-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.05
Std Dev 3.335
Gaus mean : 42.9029
Gaus Width: 3.8622
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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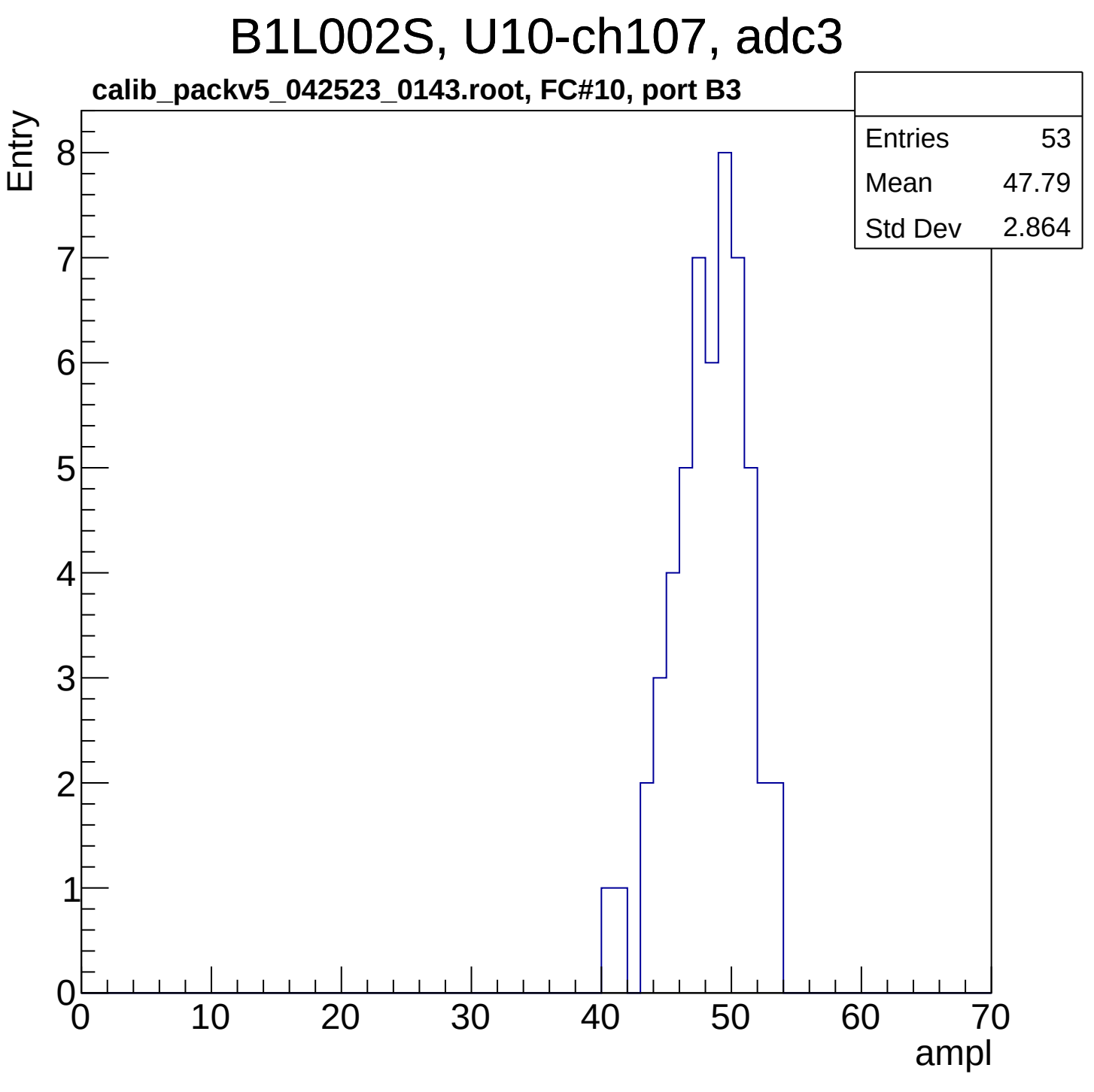
B1L002S, U10-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.79
Std Dev 2.864
Entry
ampl
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3
4
5
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7
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0
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40
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60
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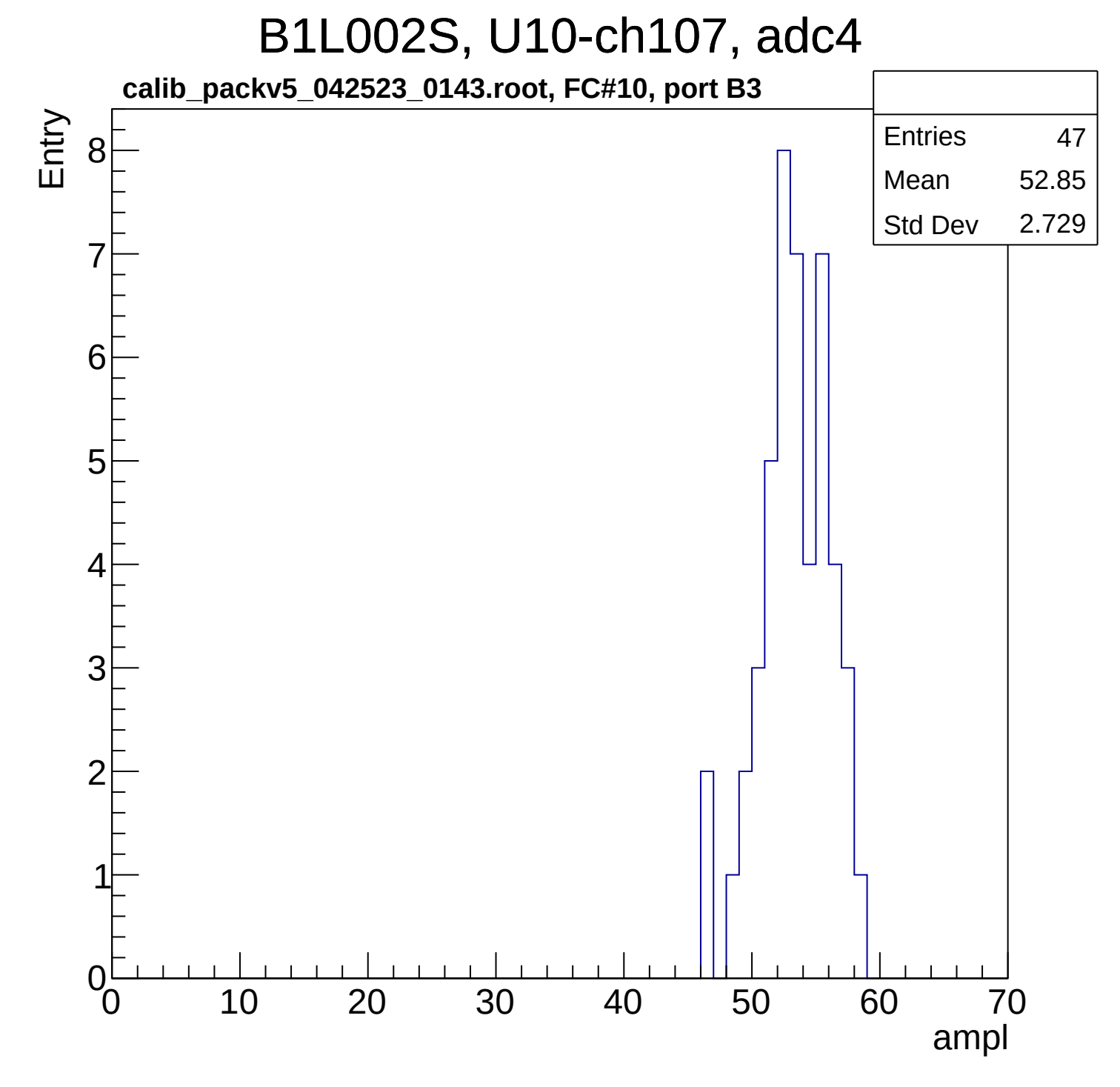
B1L002S, U10-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 52.85
Std Dev 2.729
Entry
ampl
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3
4
5
6
7
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0
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60
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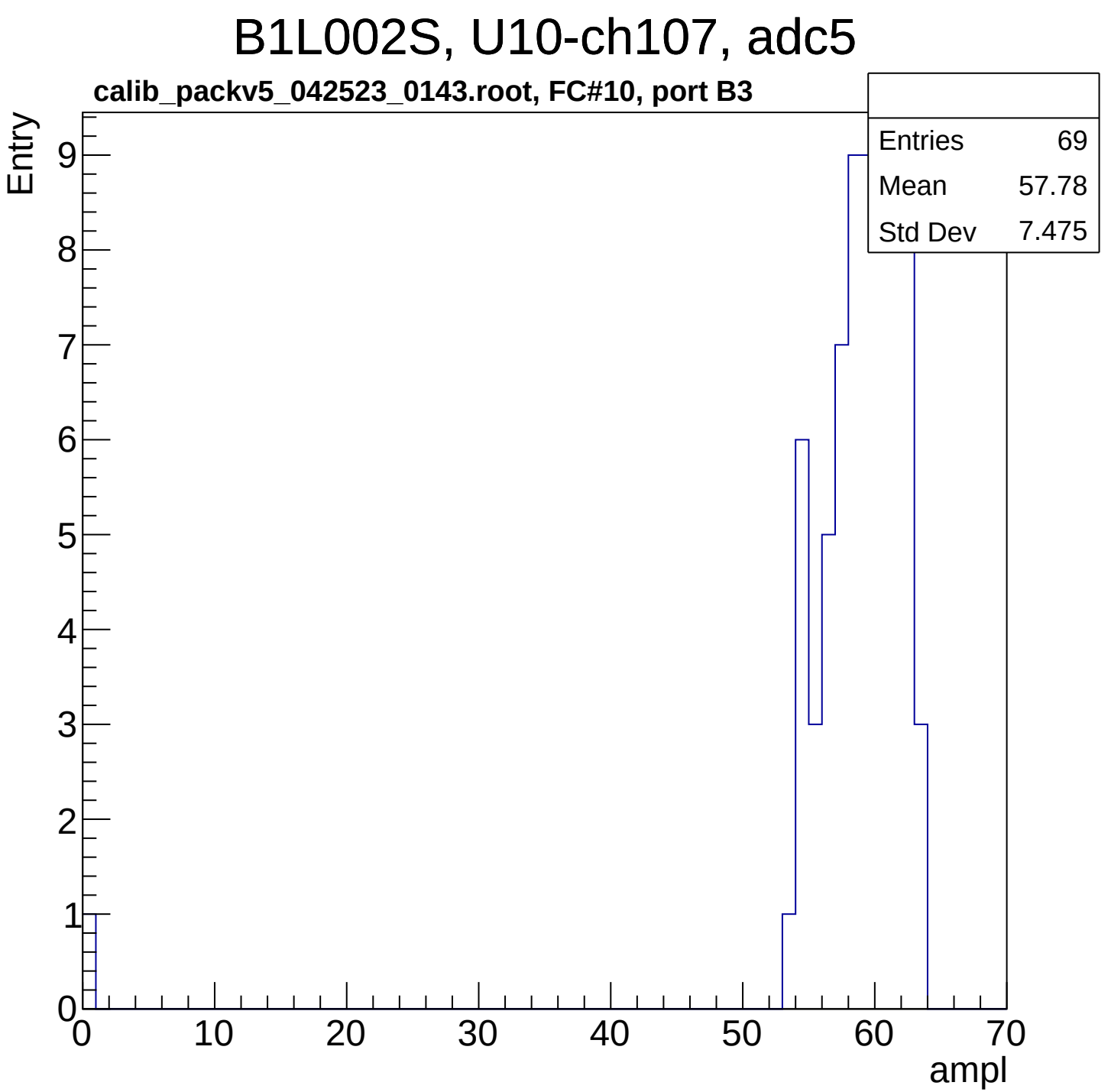
B1L002S, U10-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.78
Std Dev 7.475
Entry
ampl
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3
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5
6
7
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0
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40
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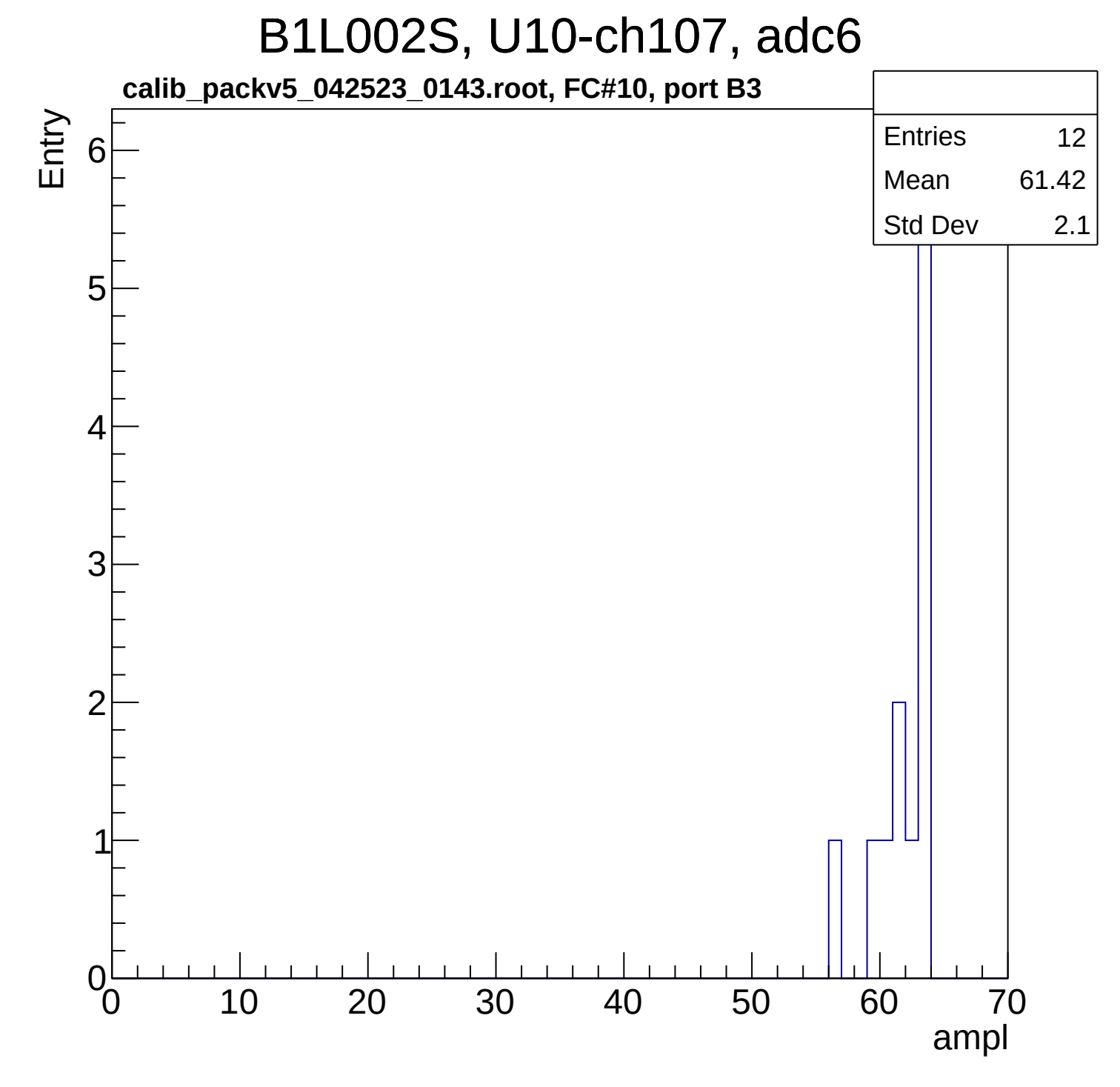
B1L002S, U10-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.42
Std Dev 2.1
Entry
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3
4
5
6
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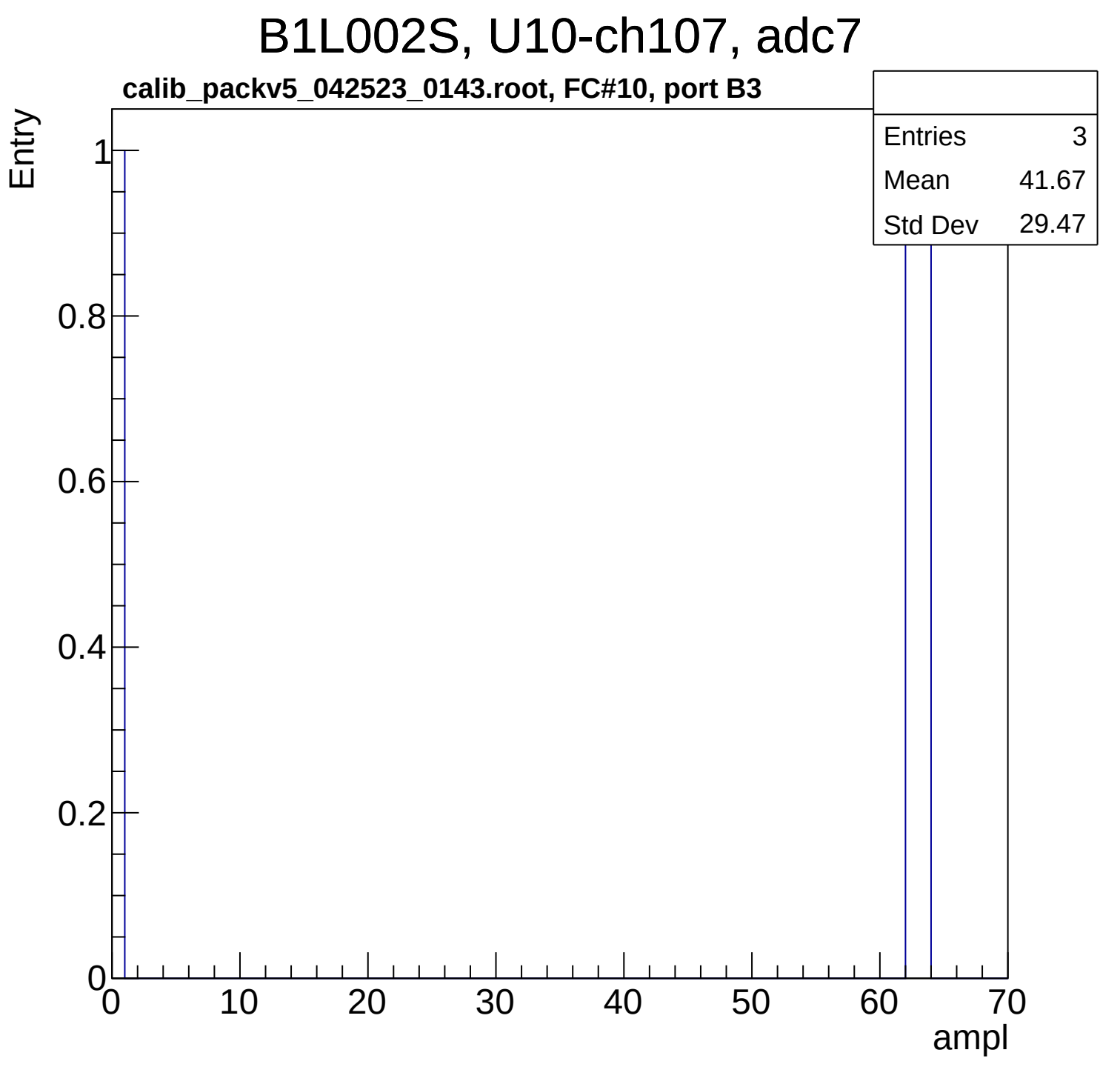
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.4
0.2
0
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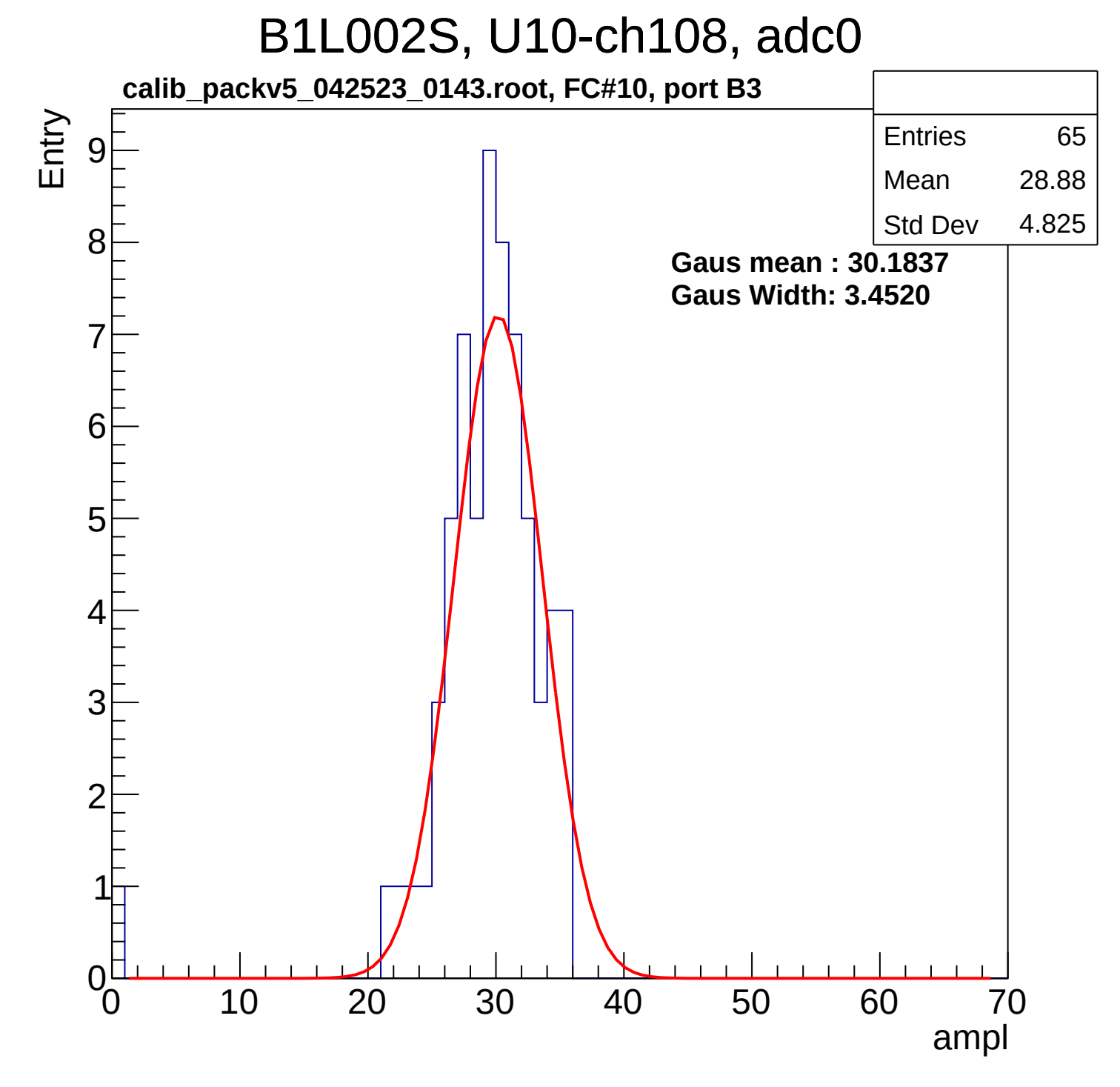
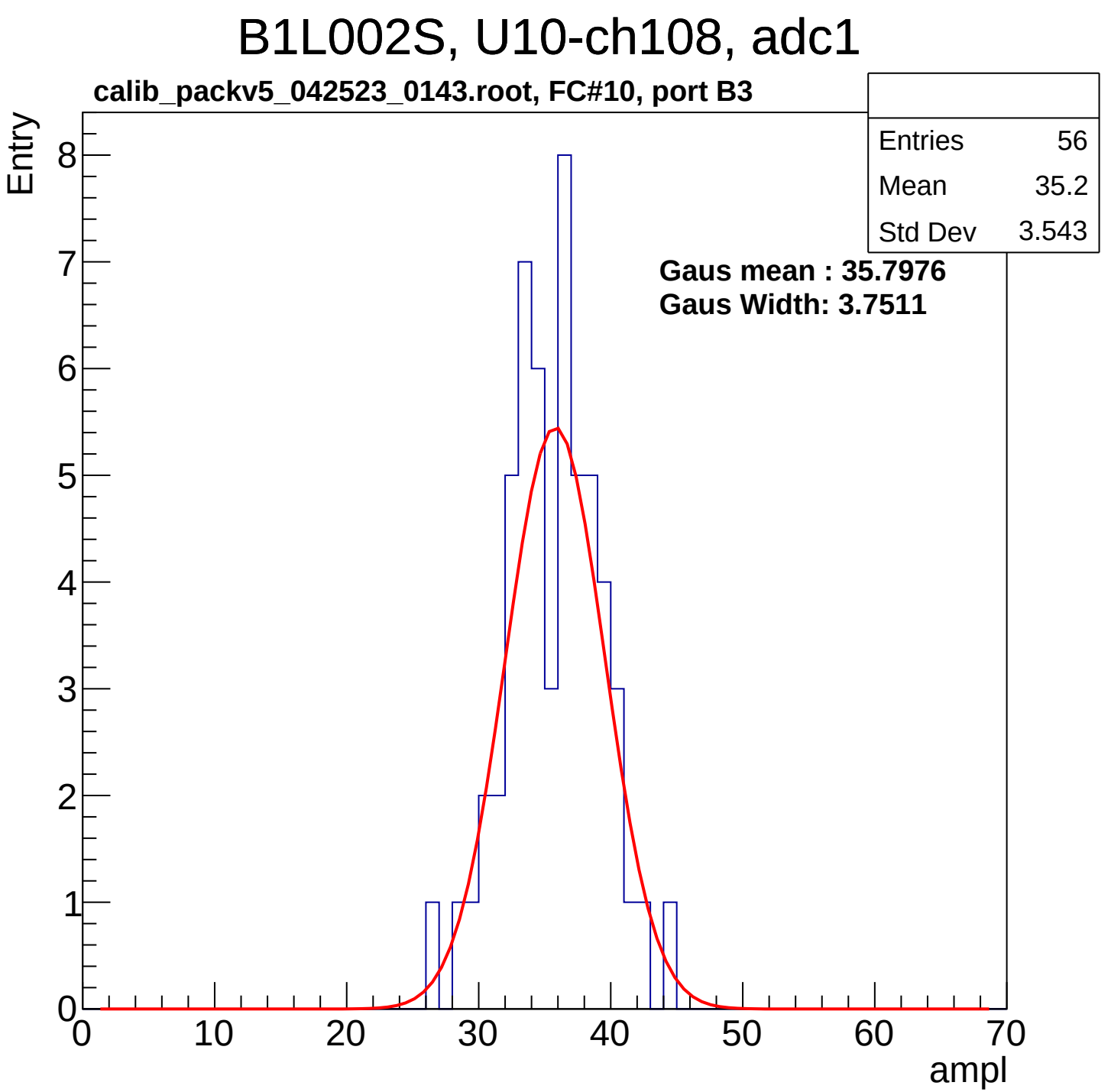
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.88
Std Dev 4.825
Gaus mean : 30.1837
Gaus Width: 3.4520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.2
Std Dev 3.543
Gaus mean : 35.7976
Gaus Width: 3.7511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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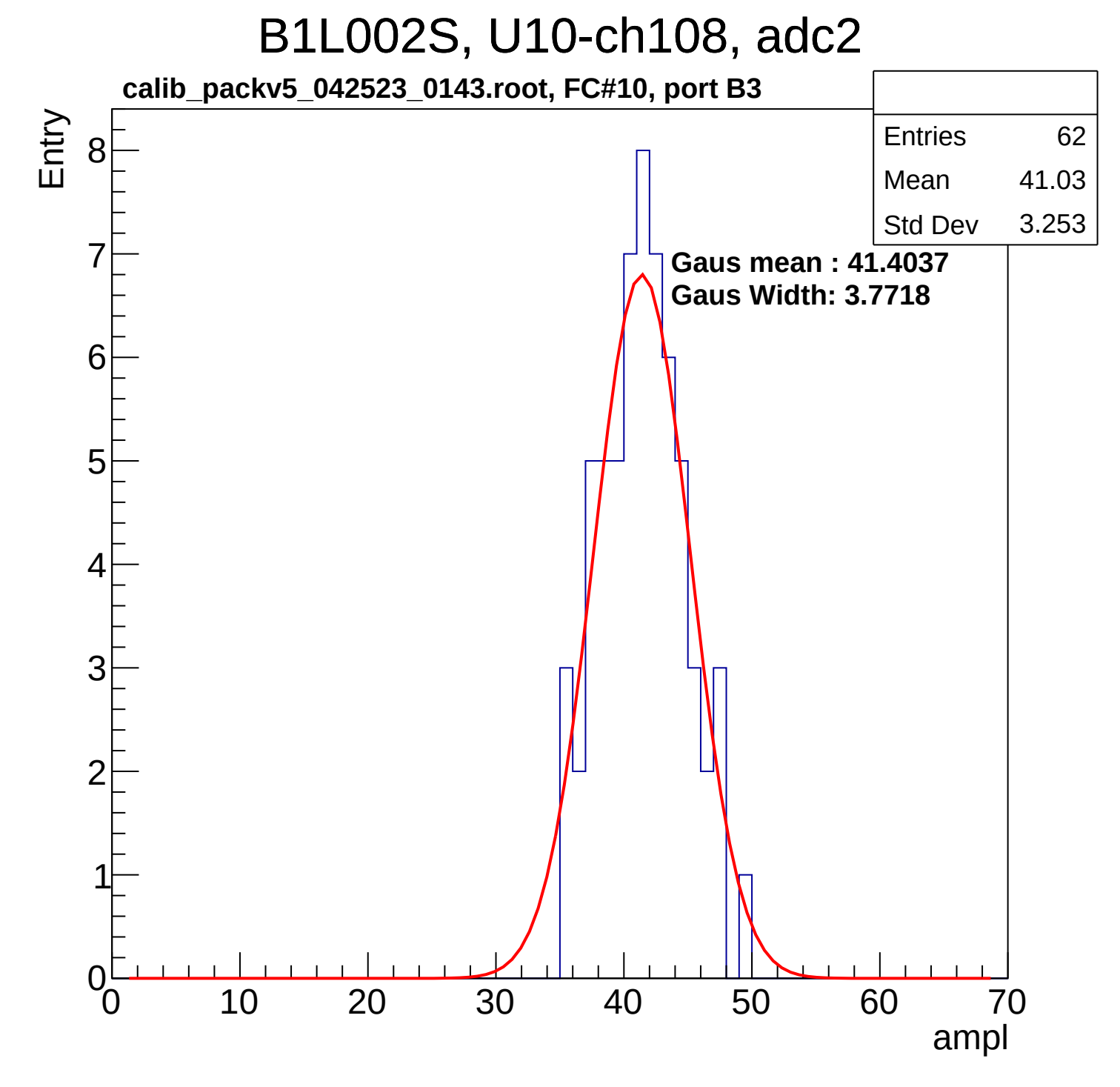
B1L002S, U10-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.03
Std Dev 3.253
Gaus mean : 41.4037
Gaus Width: 3.7718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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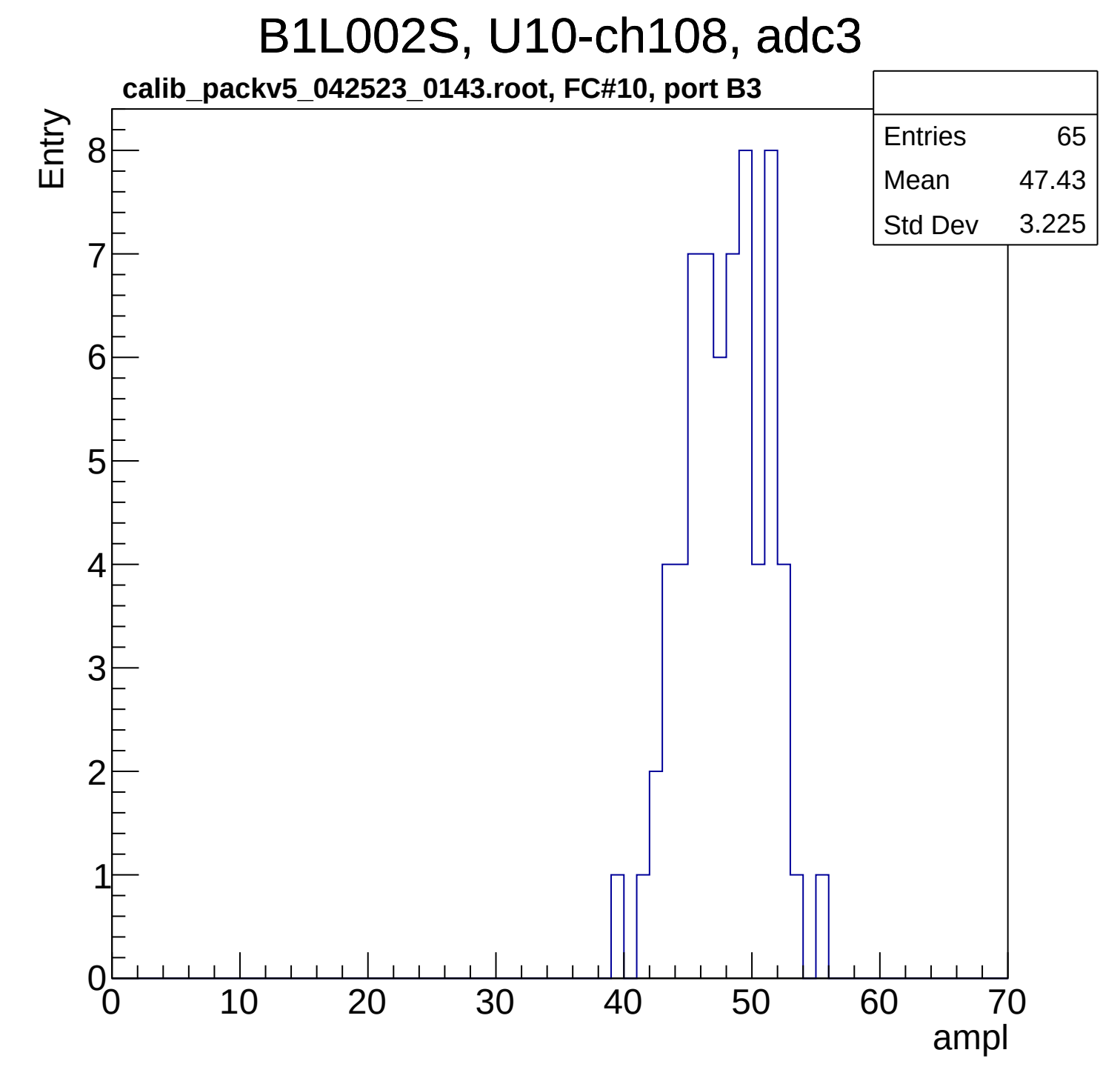
B1L002S, U10-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.43
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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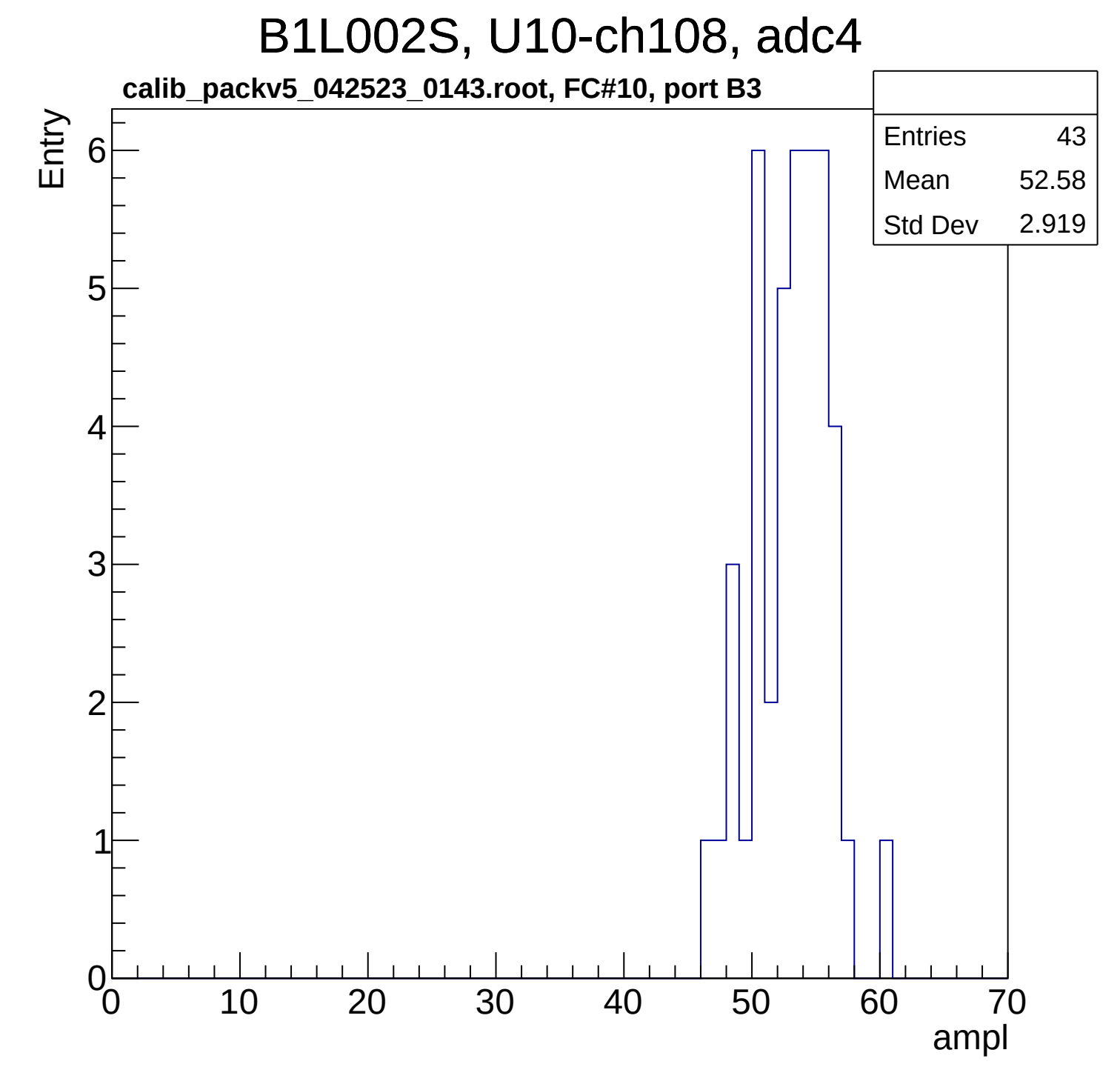
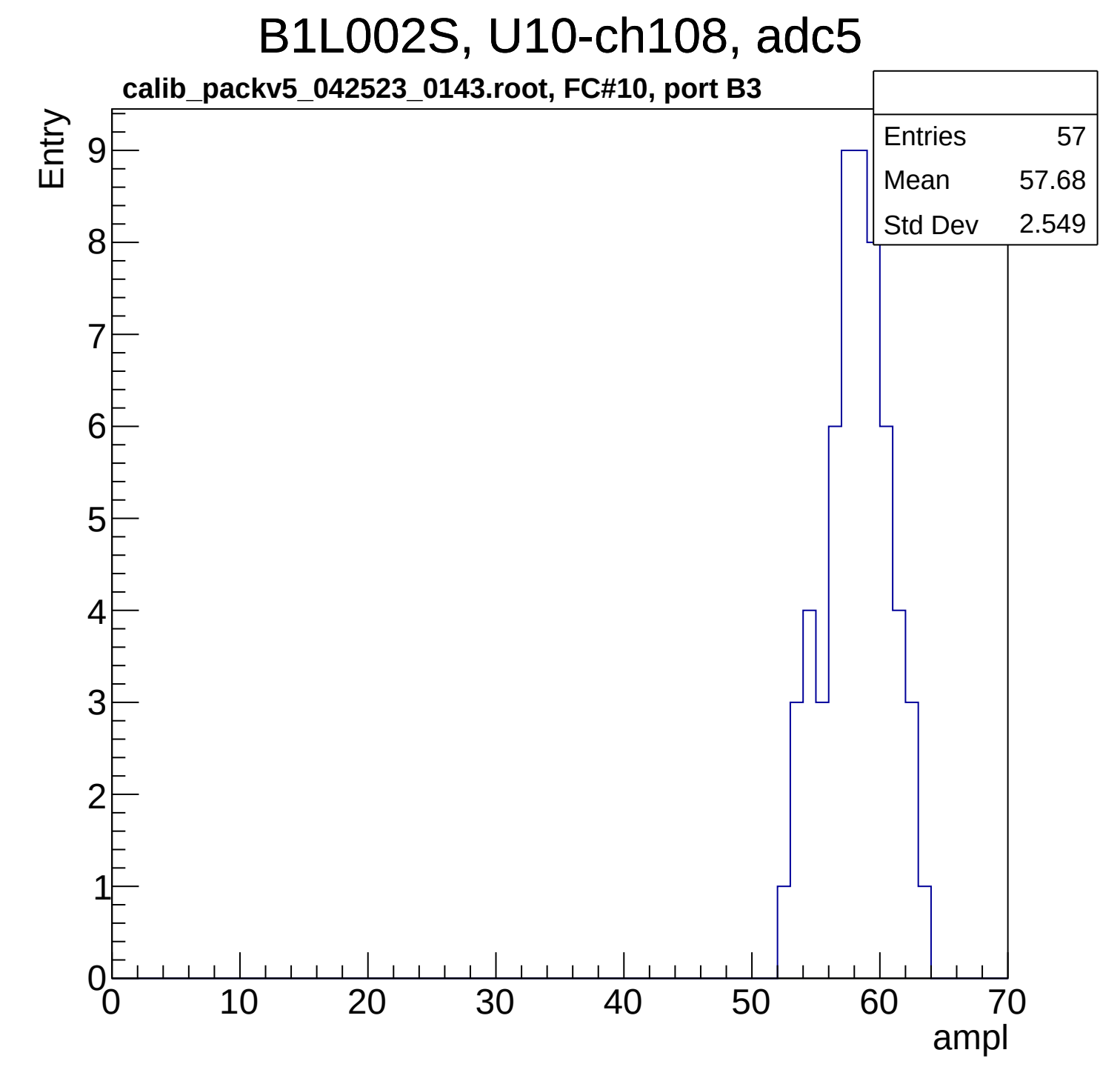

B1L002S, U10-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 52.58
Std Dev 2.919
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.68
Std Dev 2.549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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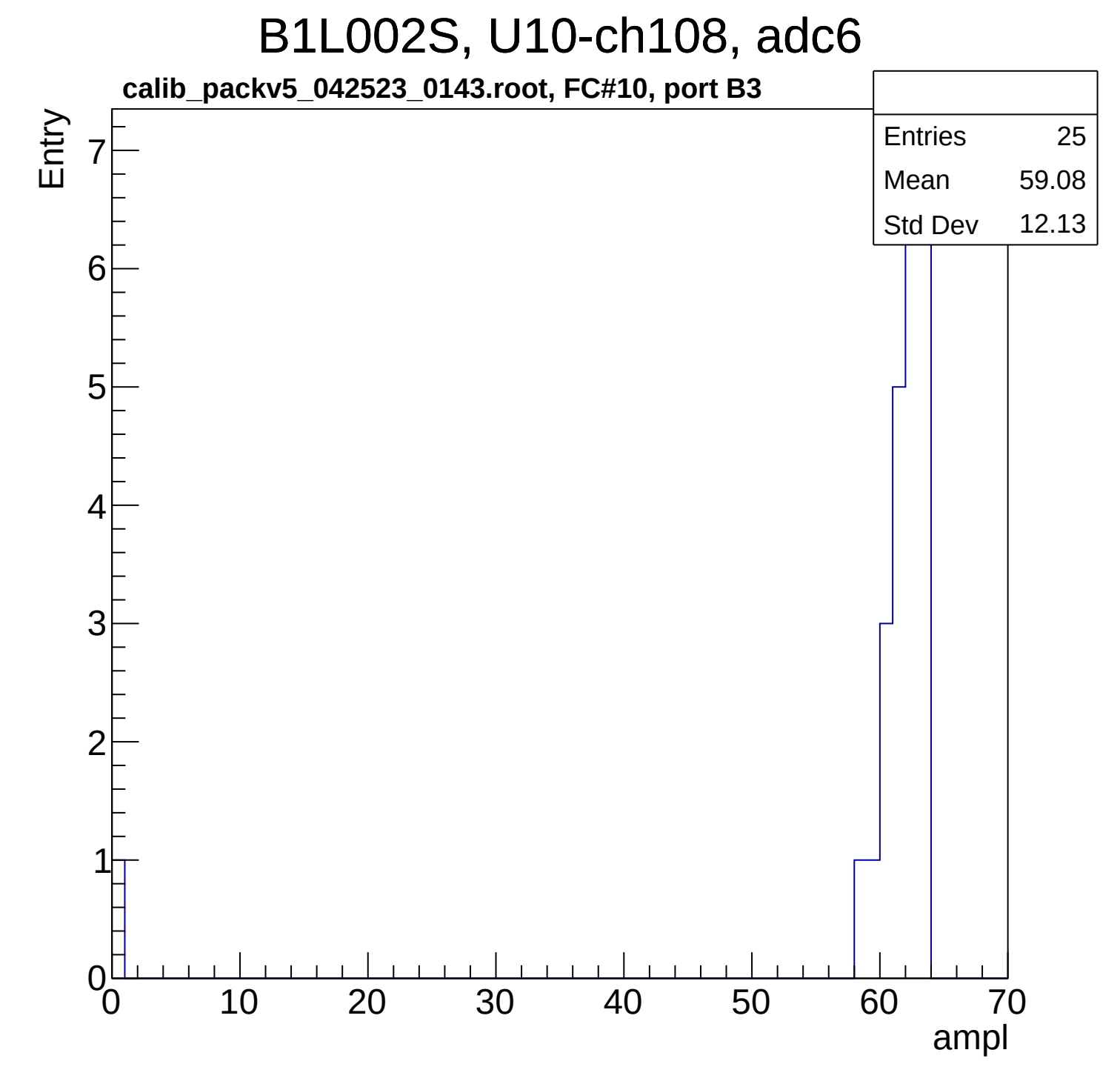
B1L002S, U10-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59.08
Std Dev 12.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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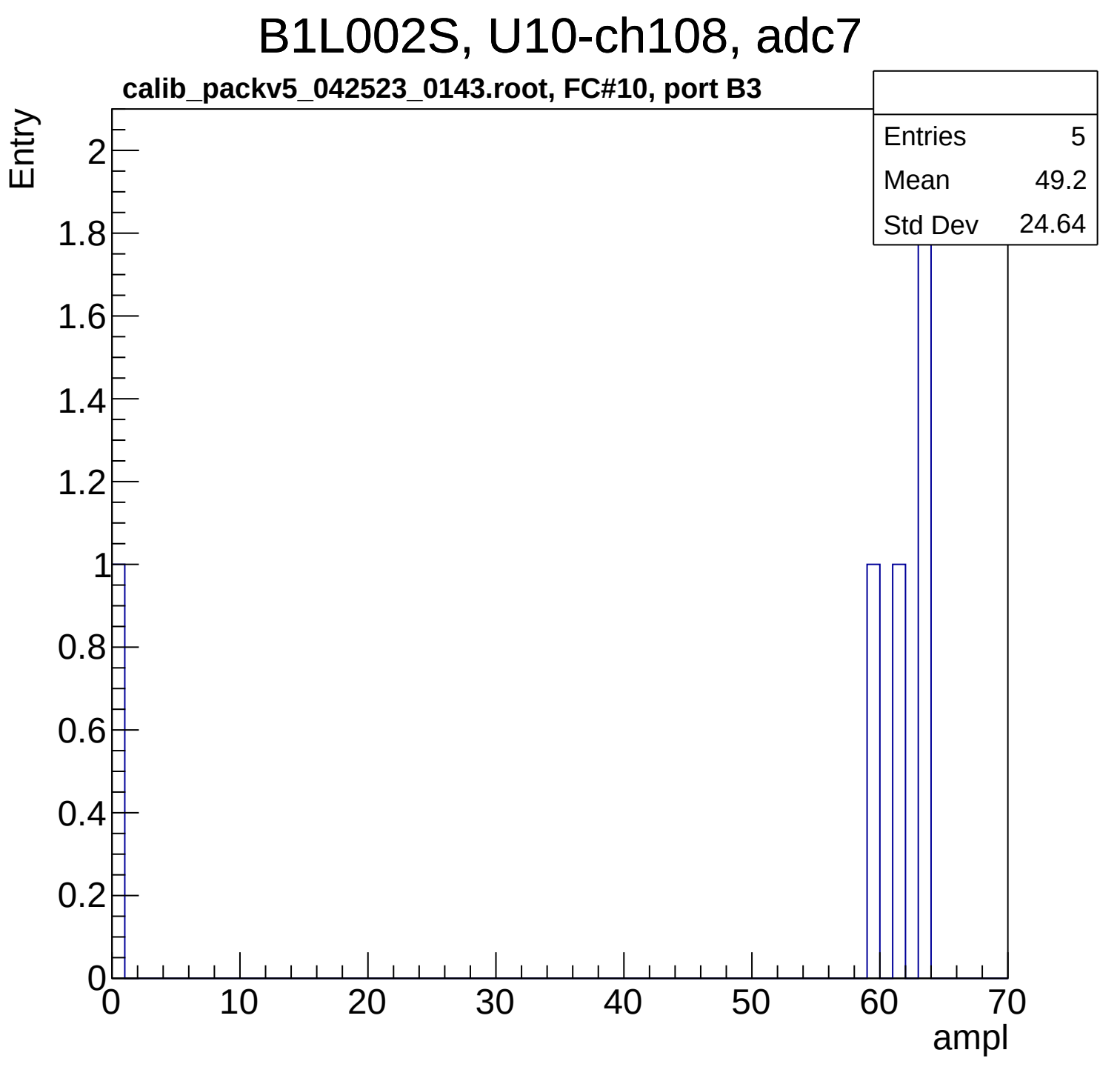
B1L002S, U10-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.2
Std Dev 24.64
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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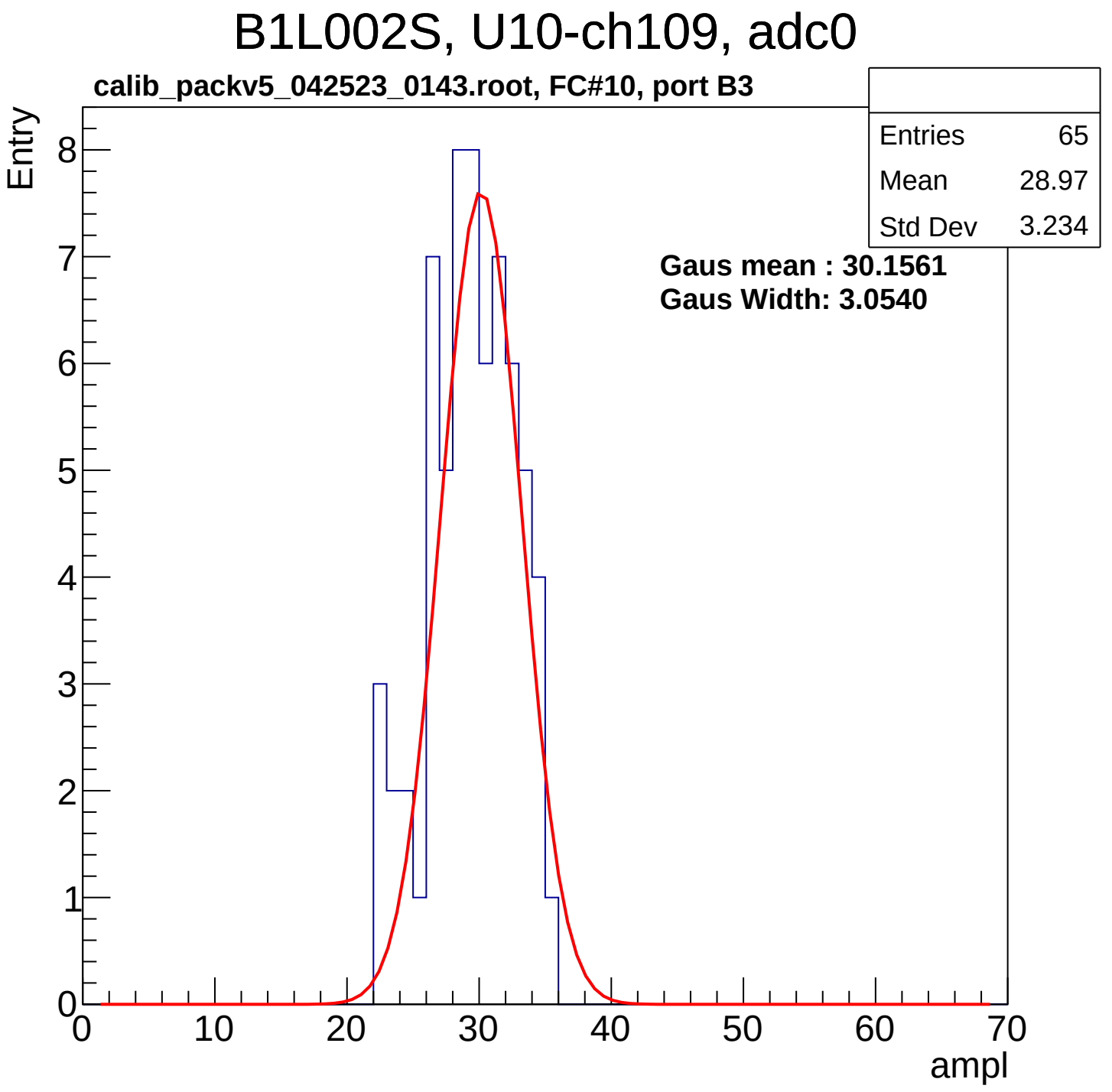
B1L002S, U10-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.97
Std Dev 3.234
Gaus mean : 30.1561
Gaus Width: 3.0540
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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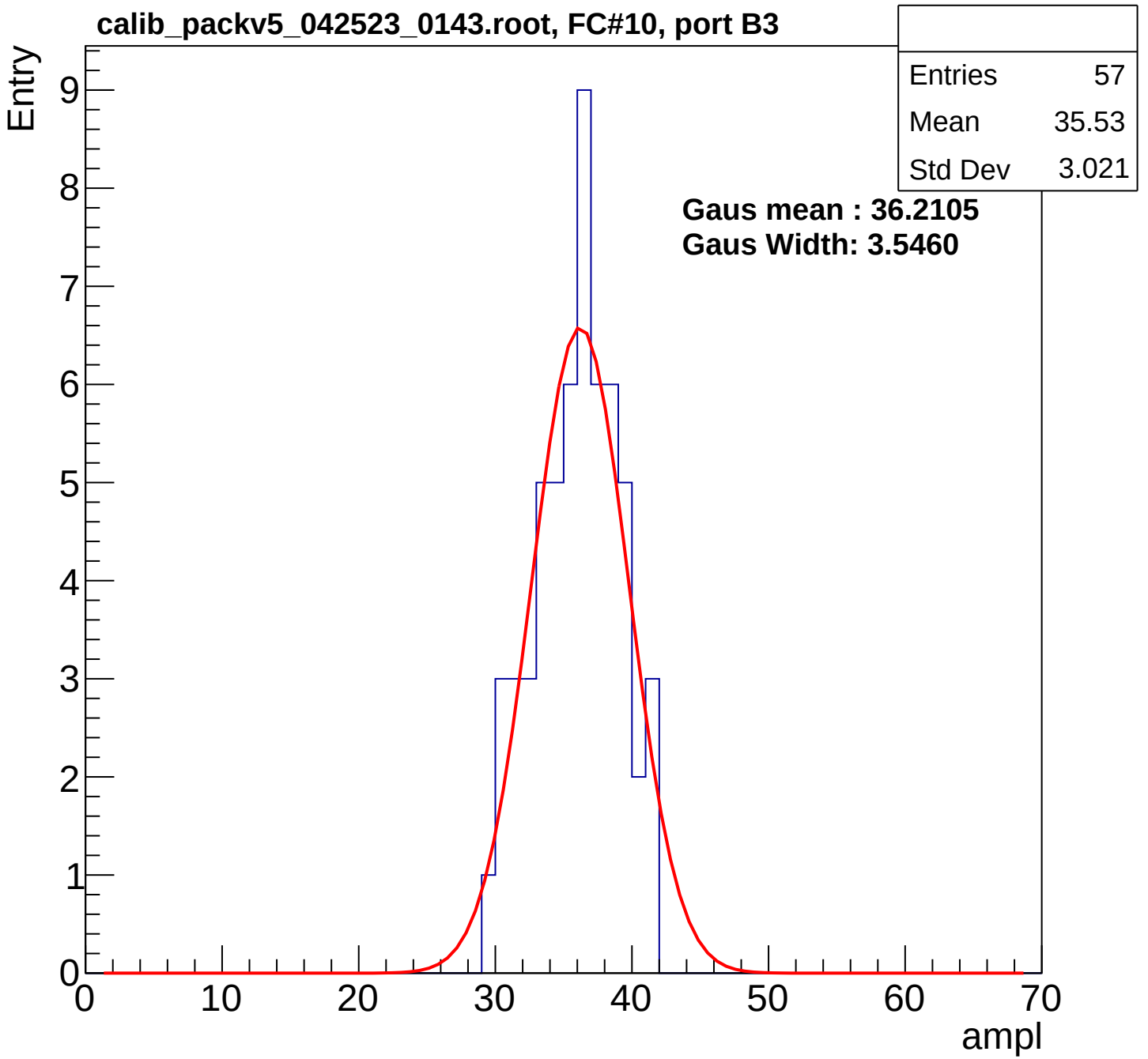
B1L002S, U10-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.53
Std Dev 3.021
Gaus mean : 36.2105
Gaus Width: 3.5460
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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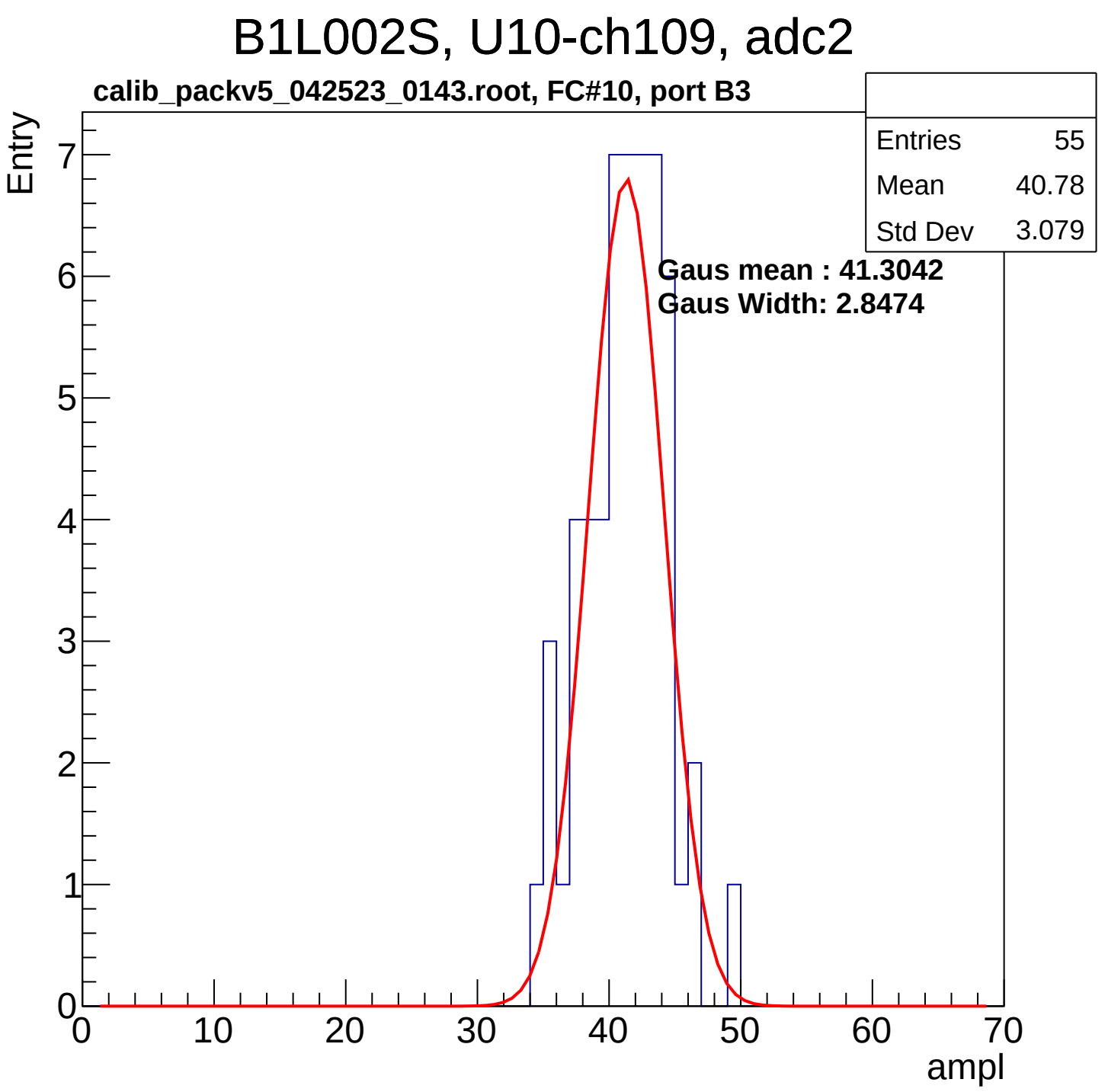
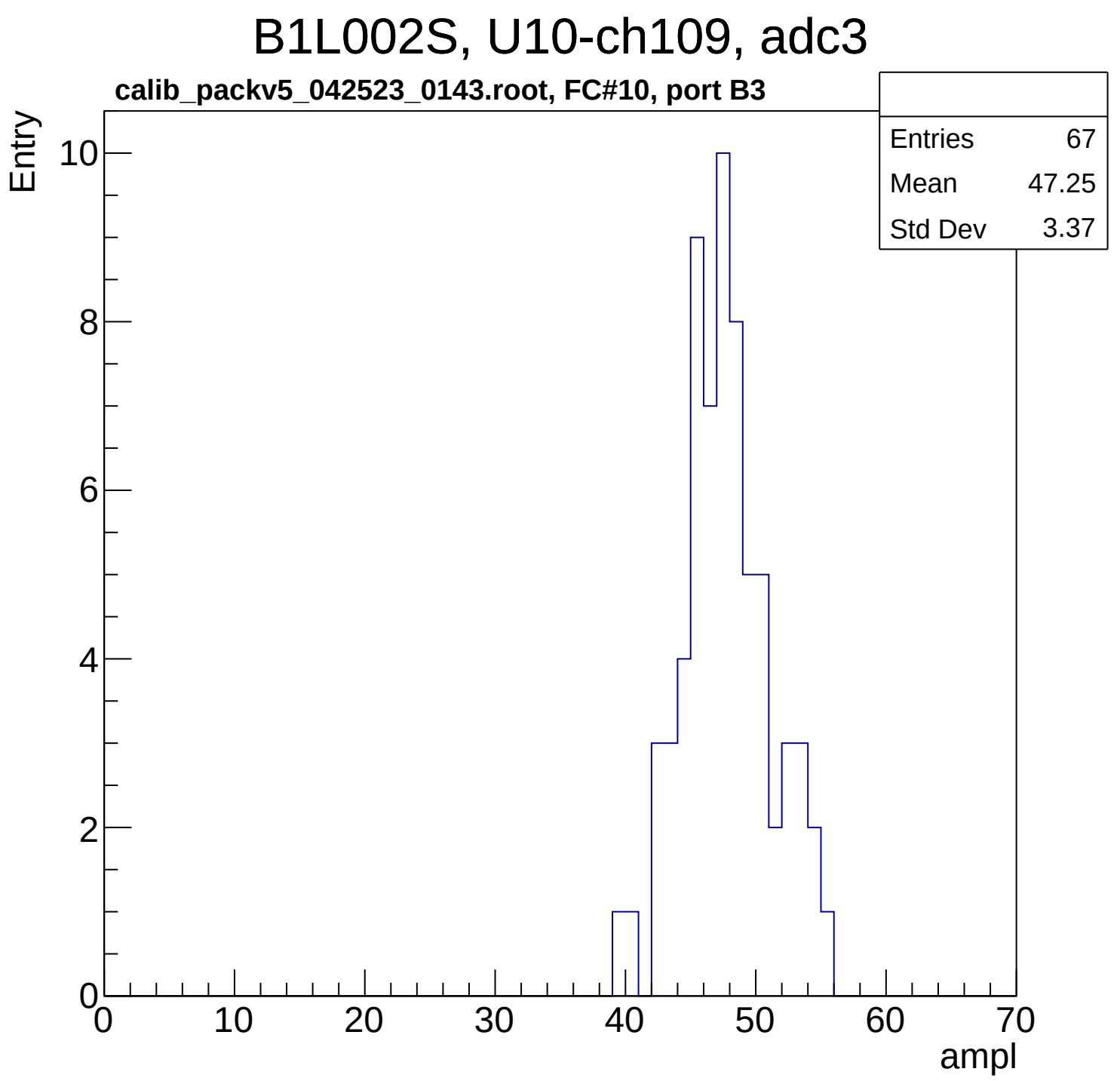
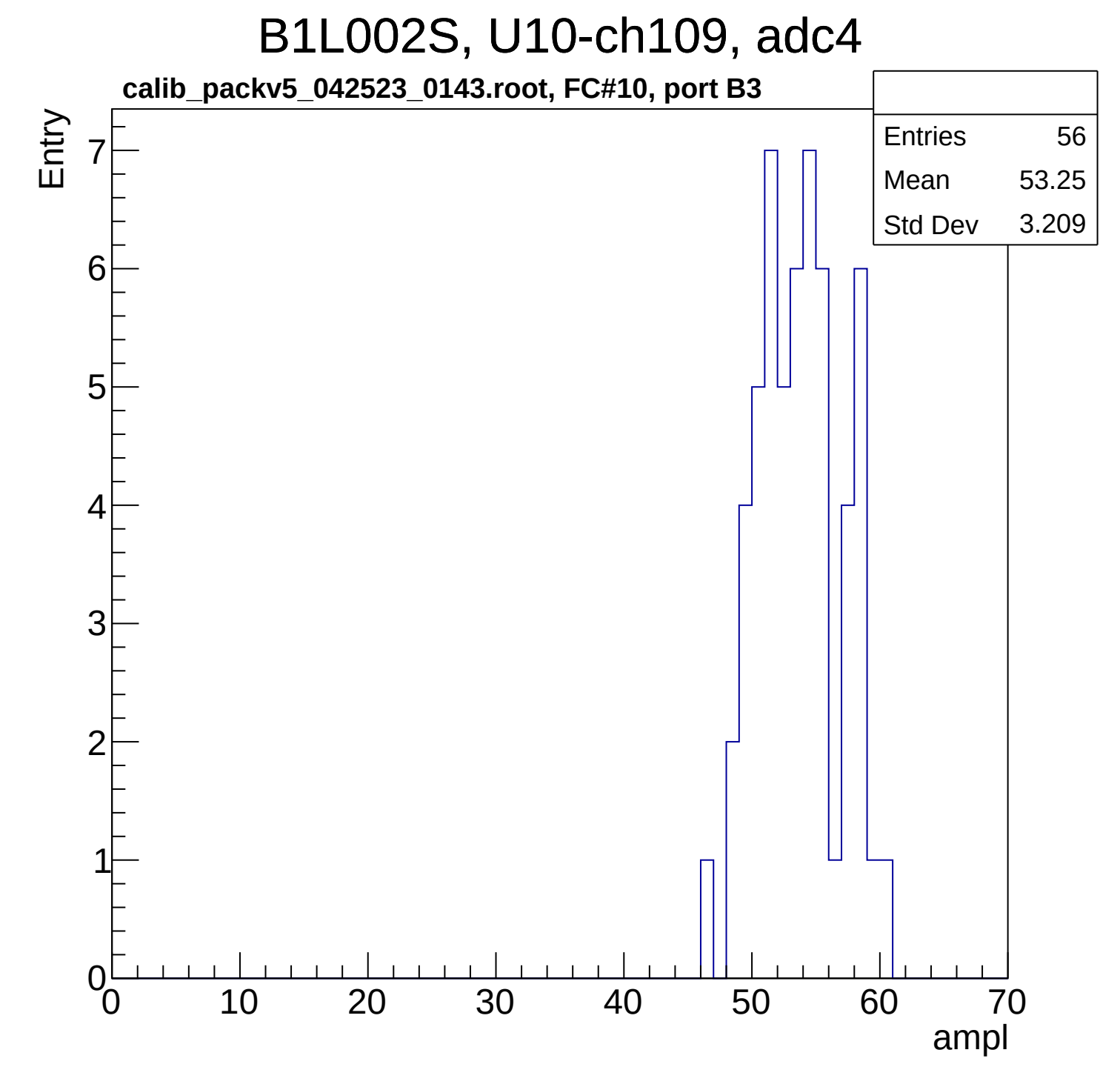
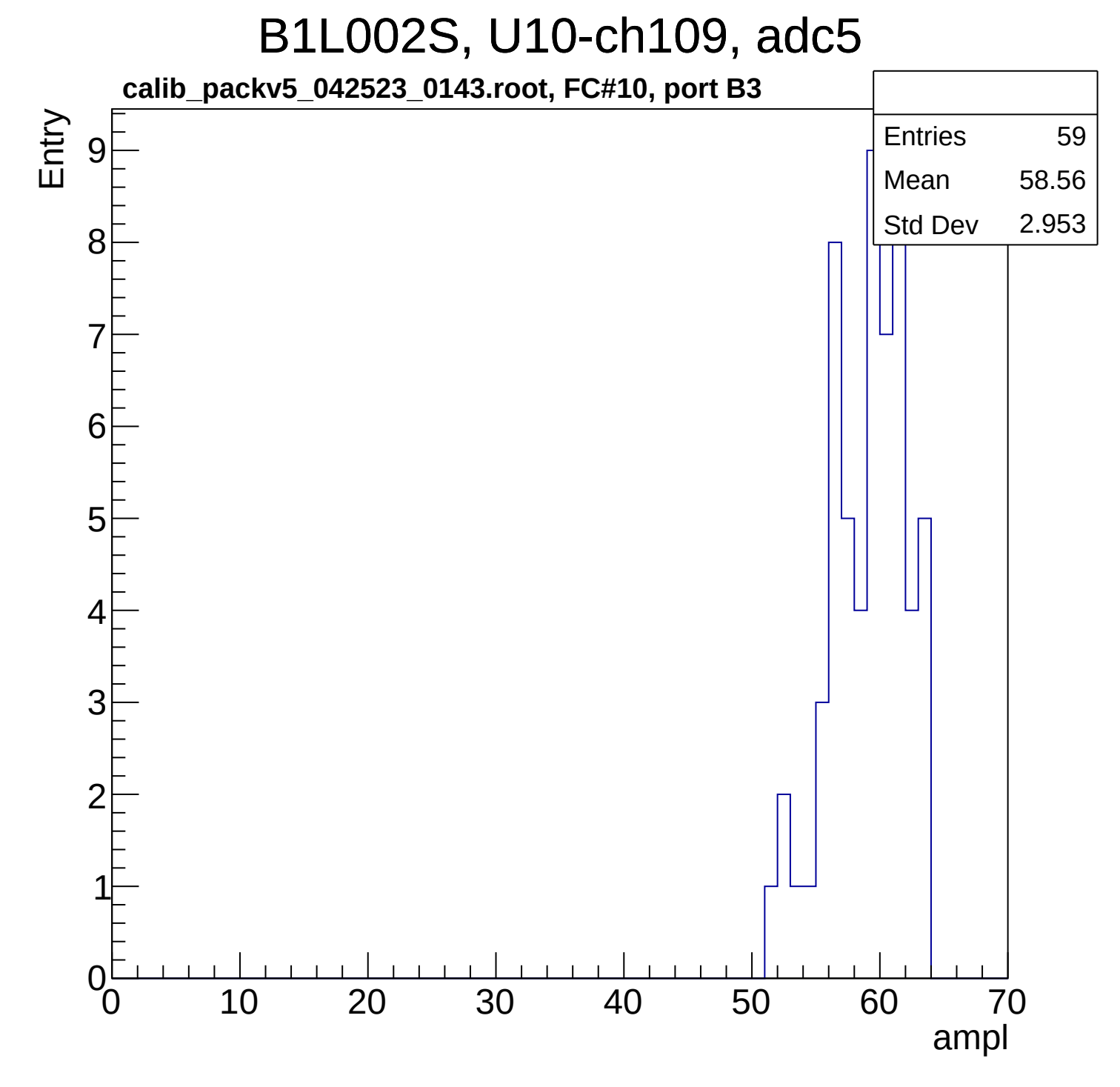
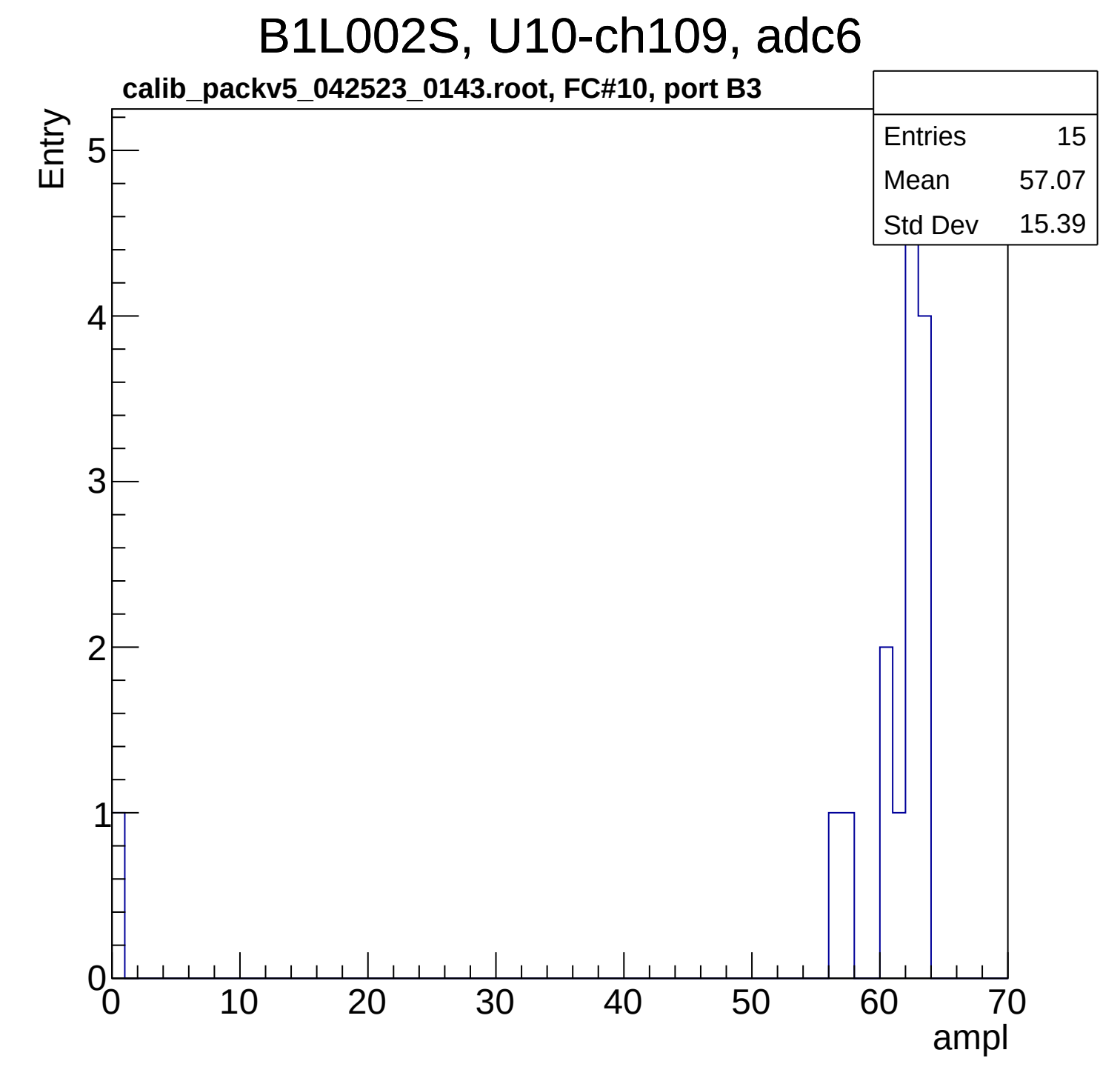
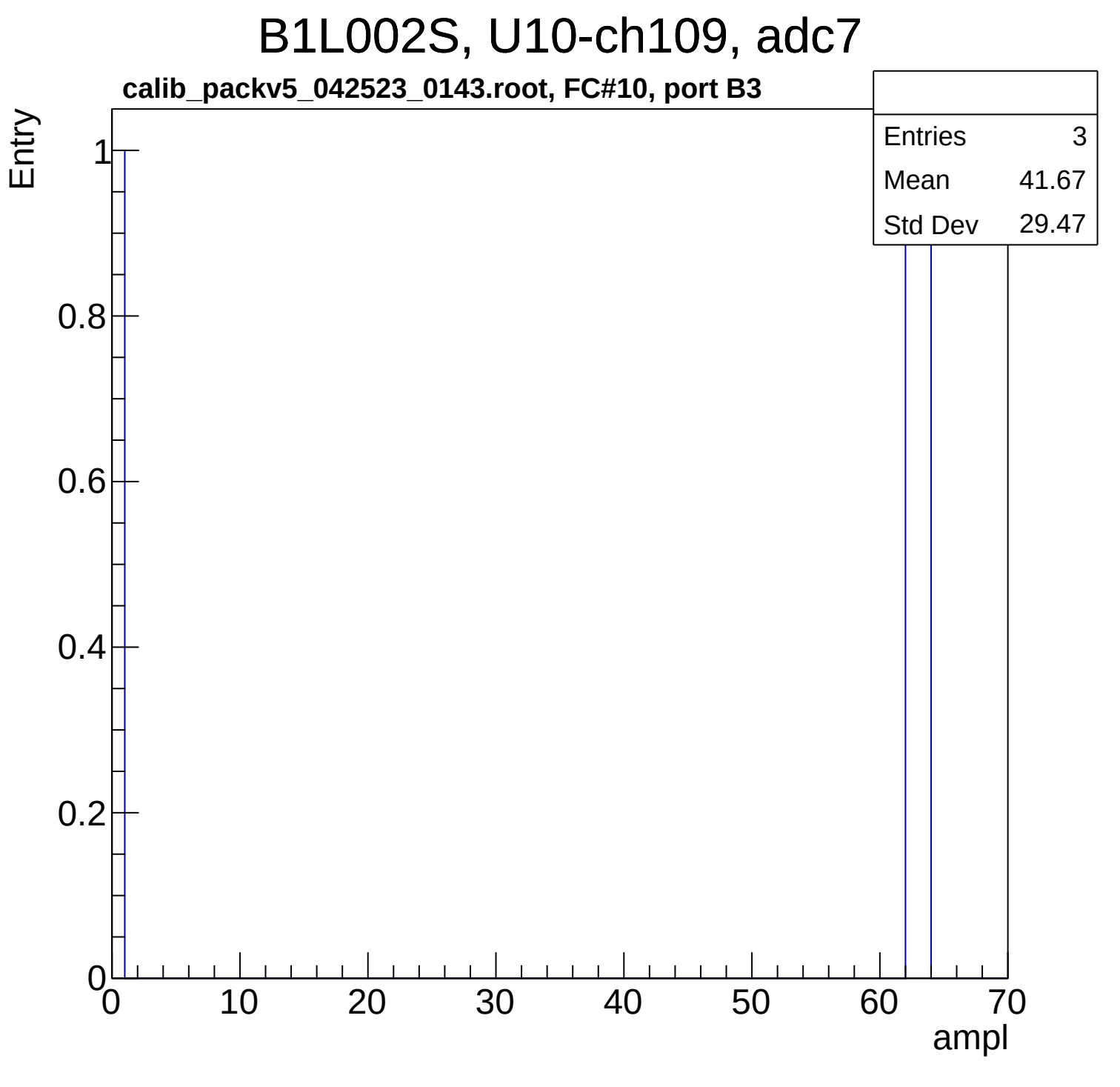

B1L002S, U10-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.78
Std Dev 3.079
Gaus mean : 41.3042
Gaus Width: 2.8474
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

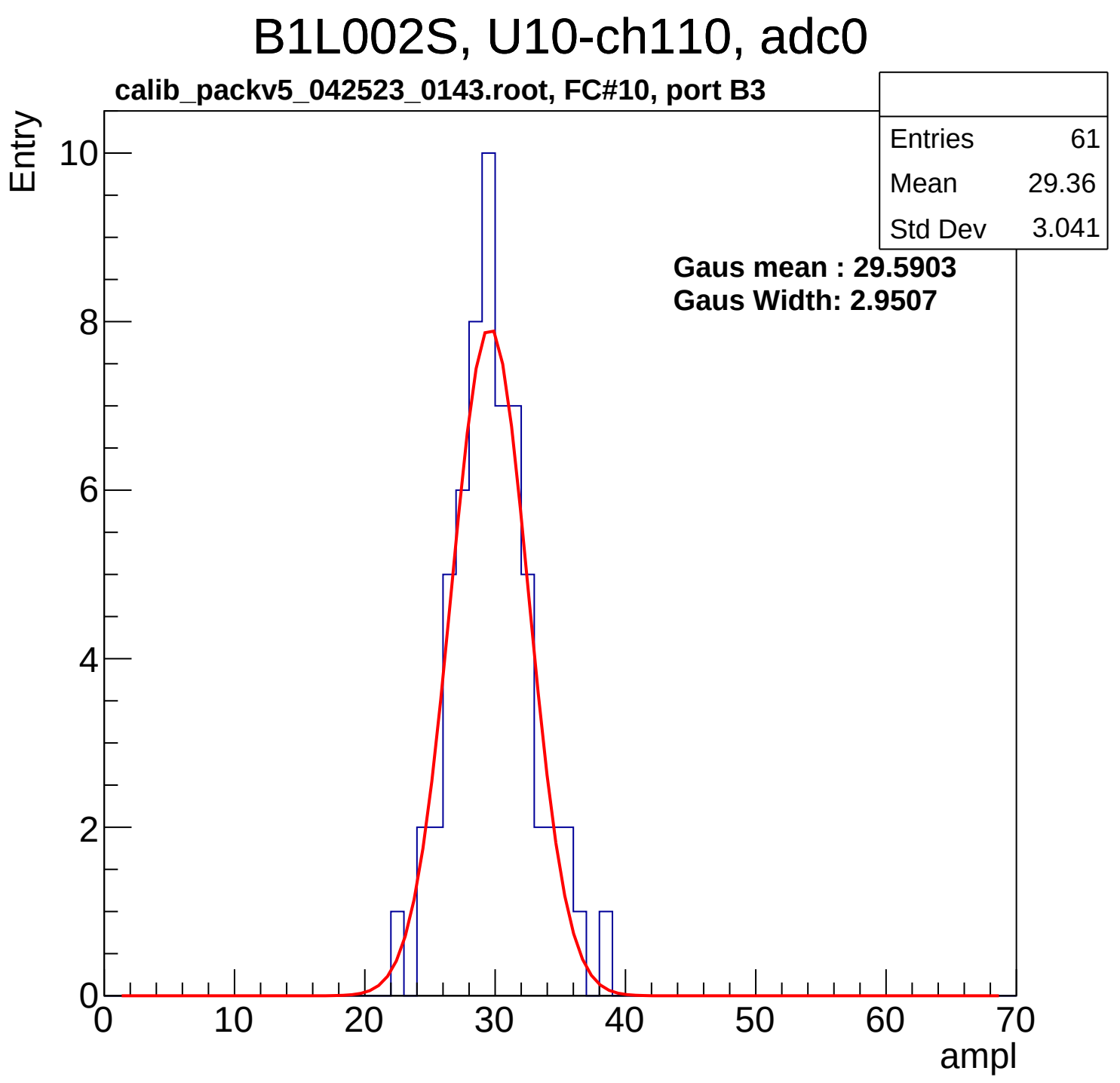

B1L002S, U10-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.25
Std Dev 3.37
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

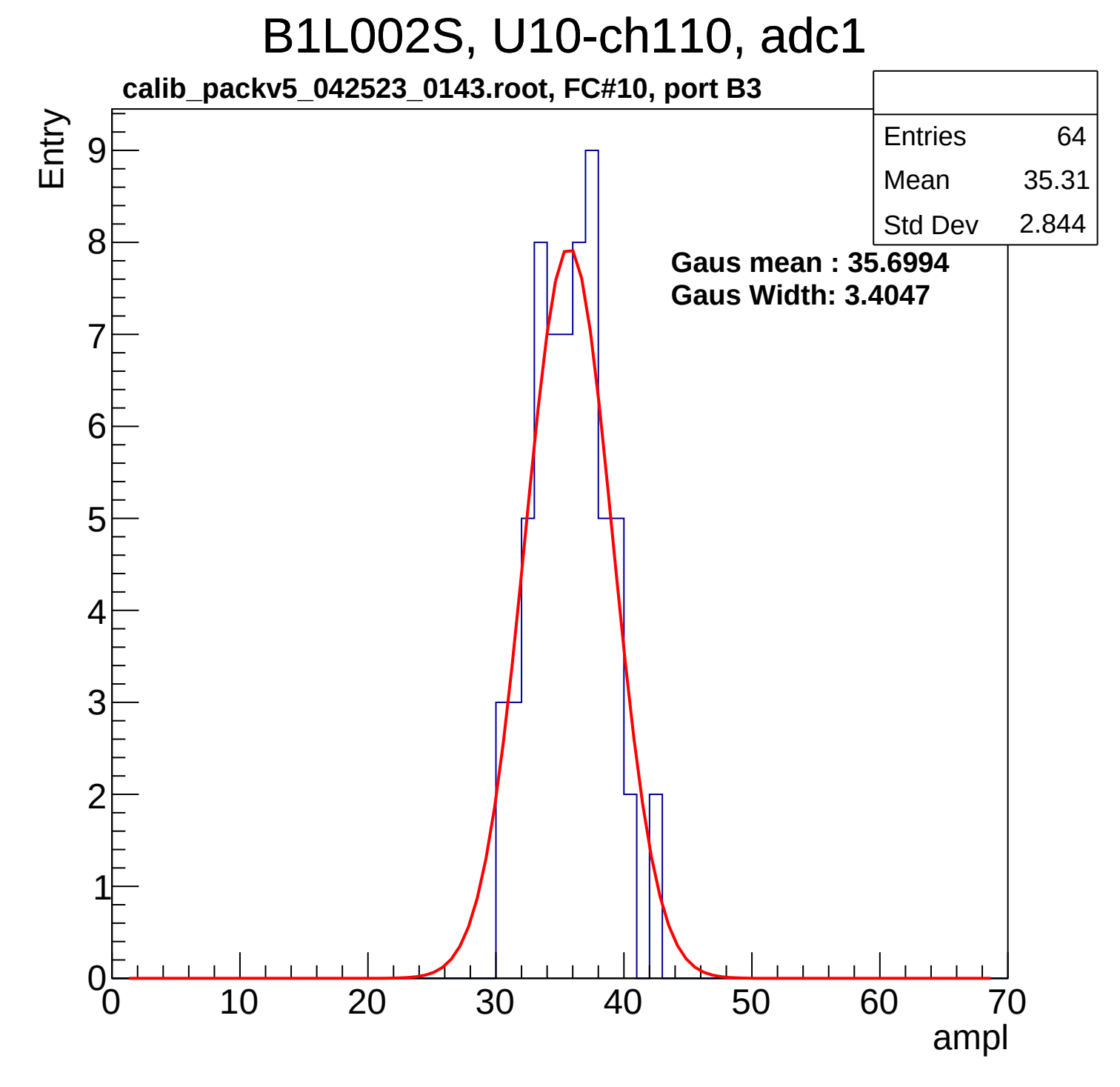

B1L002S, U10-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.25
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

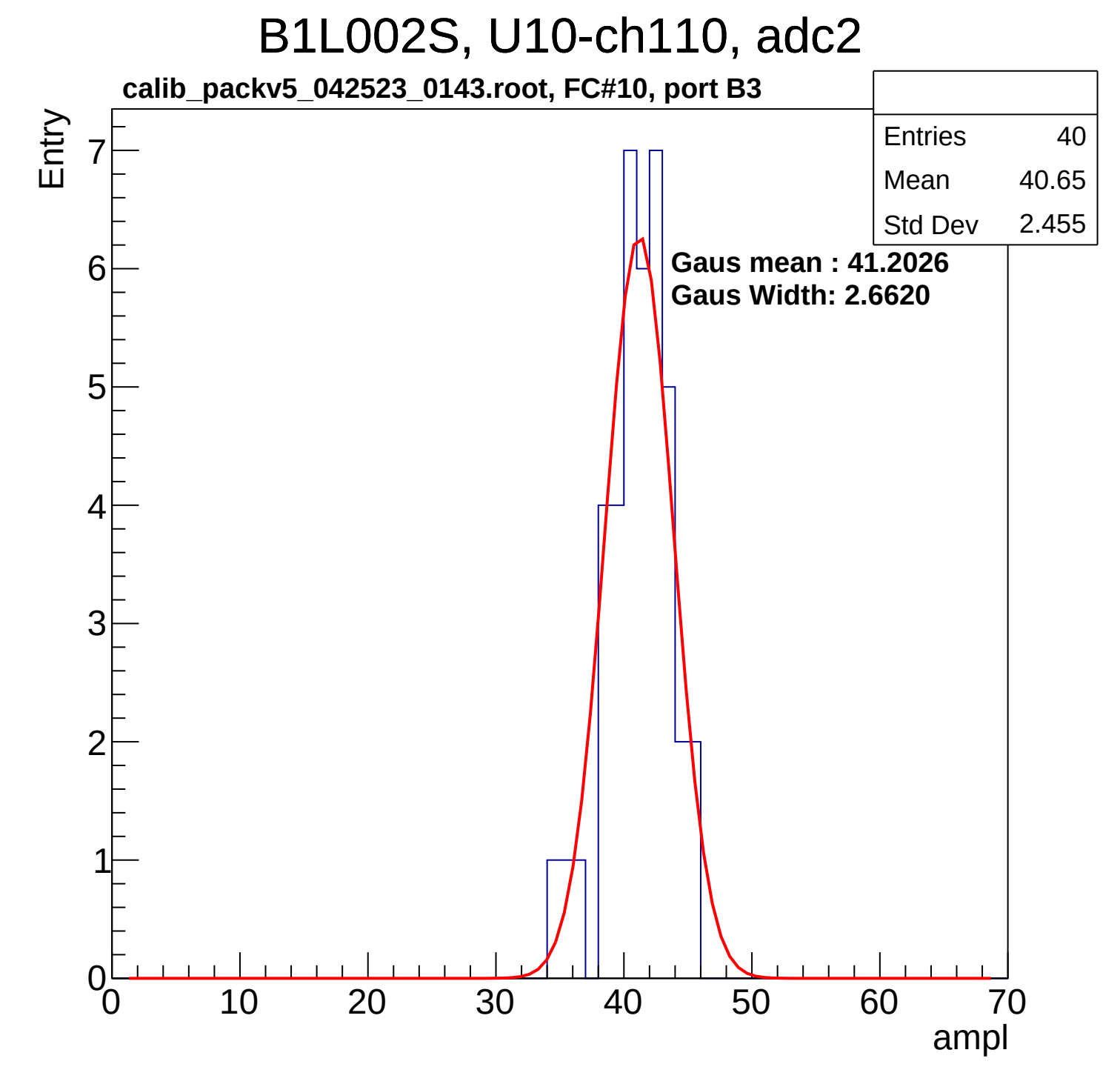

B1L002S, U10-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.56
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

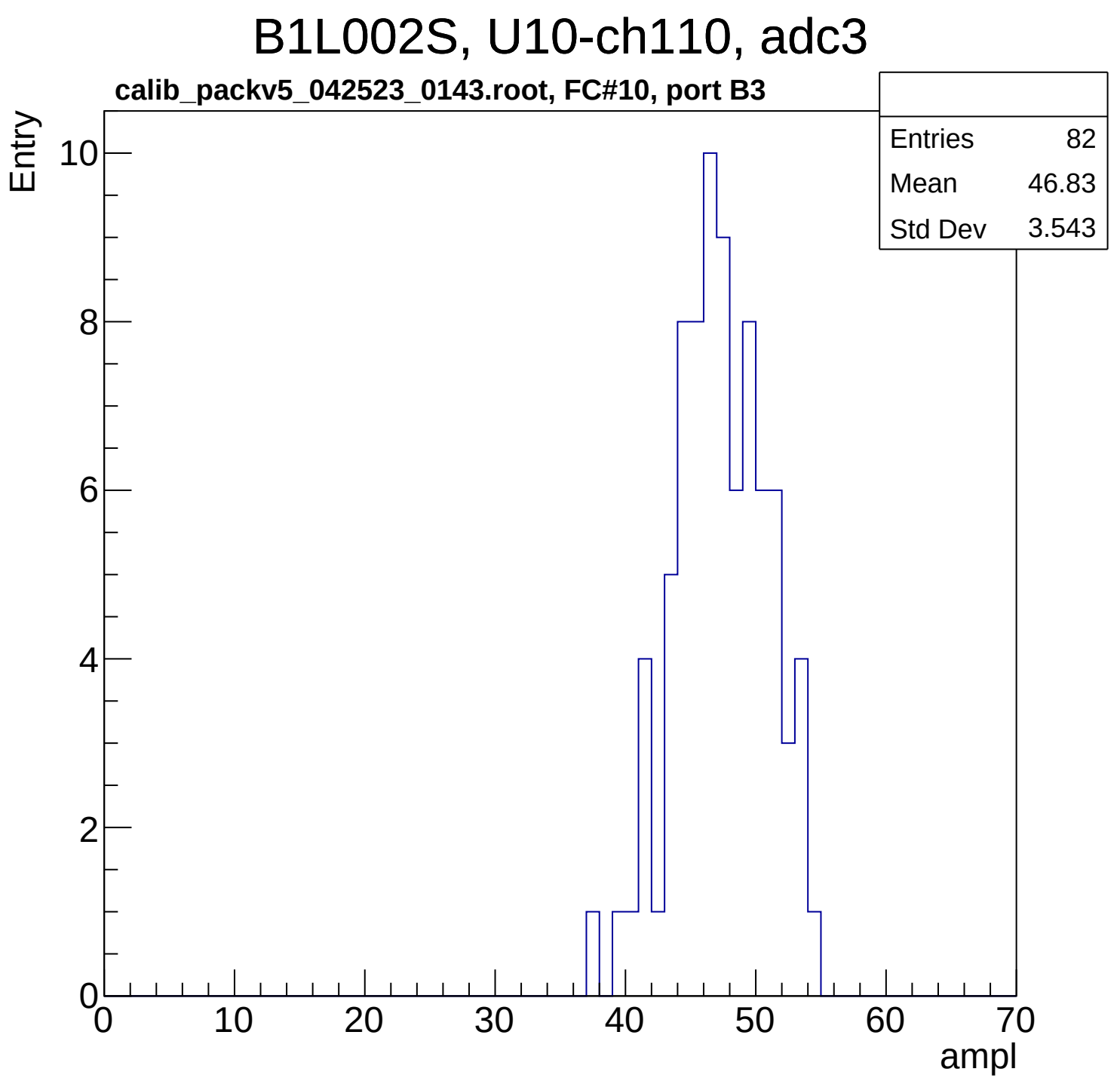

B1L002S, U10-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.07
Std Dev 15.39
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

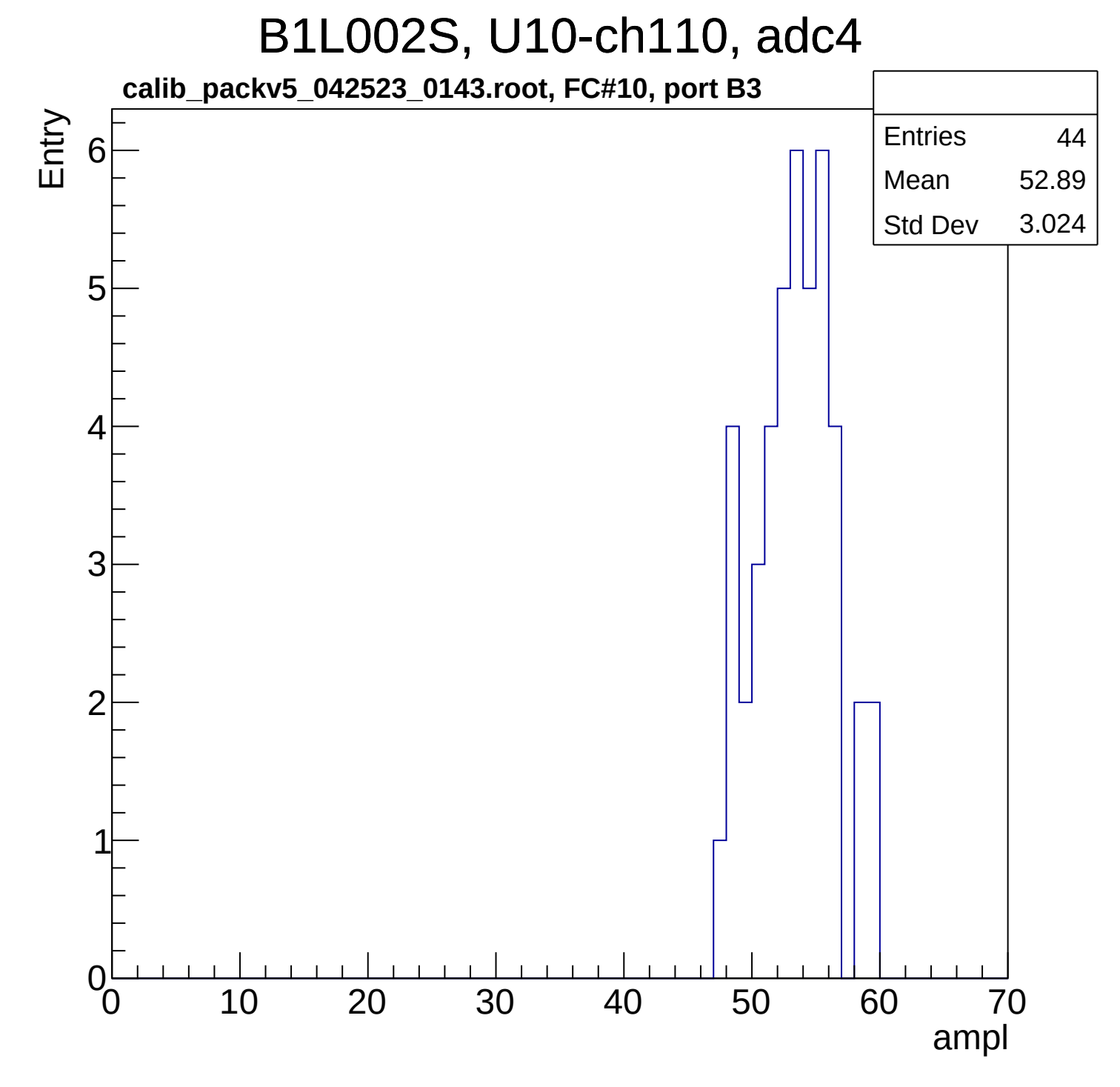

B1L002S, U10-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

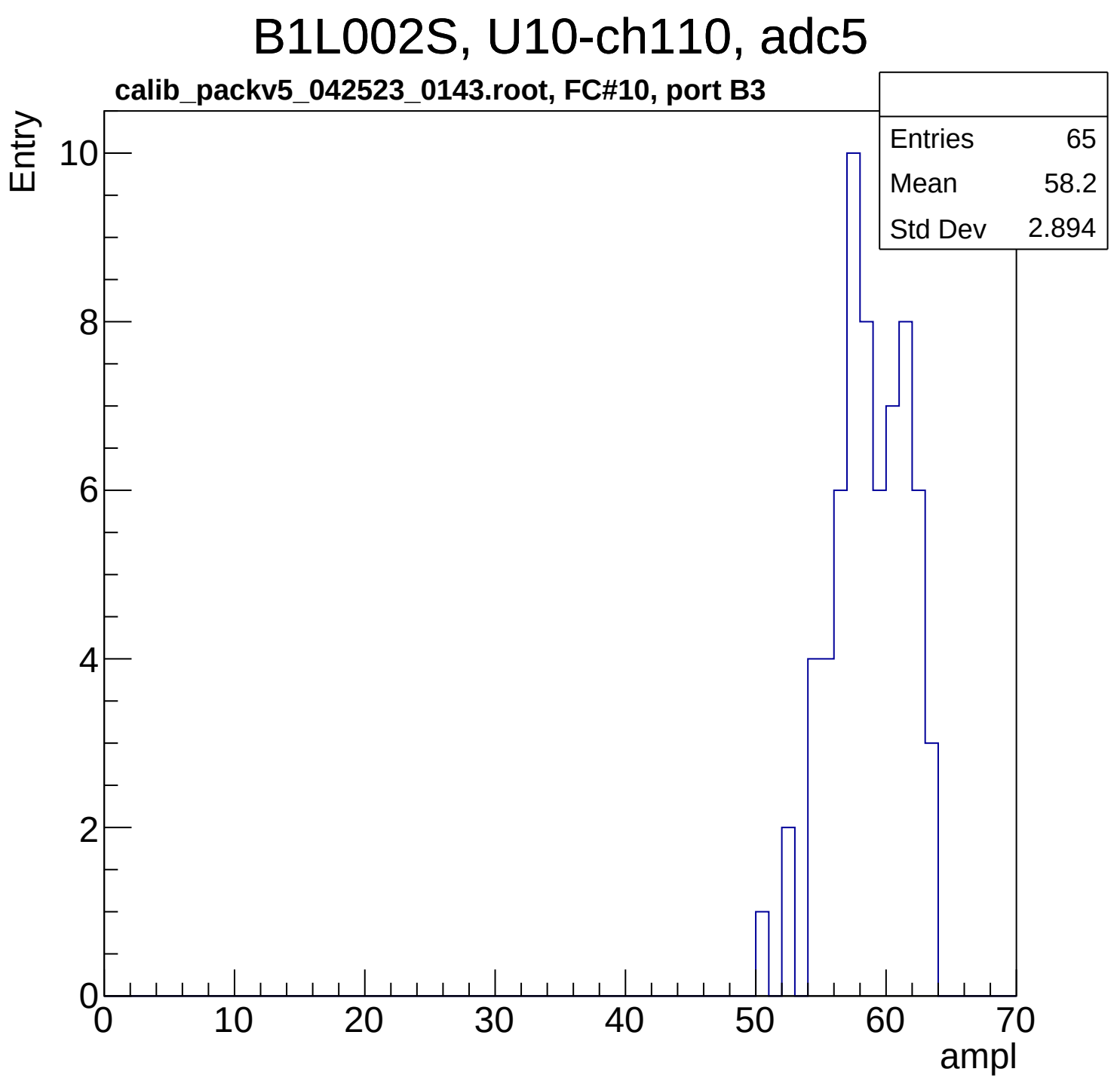

B1L002S, U10-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.36
Std Dev 3.041
Gaus mean : 29.5903
Gaus Width: 2.9507
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

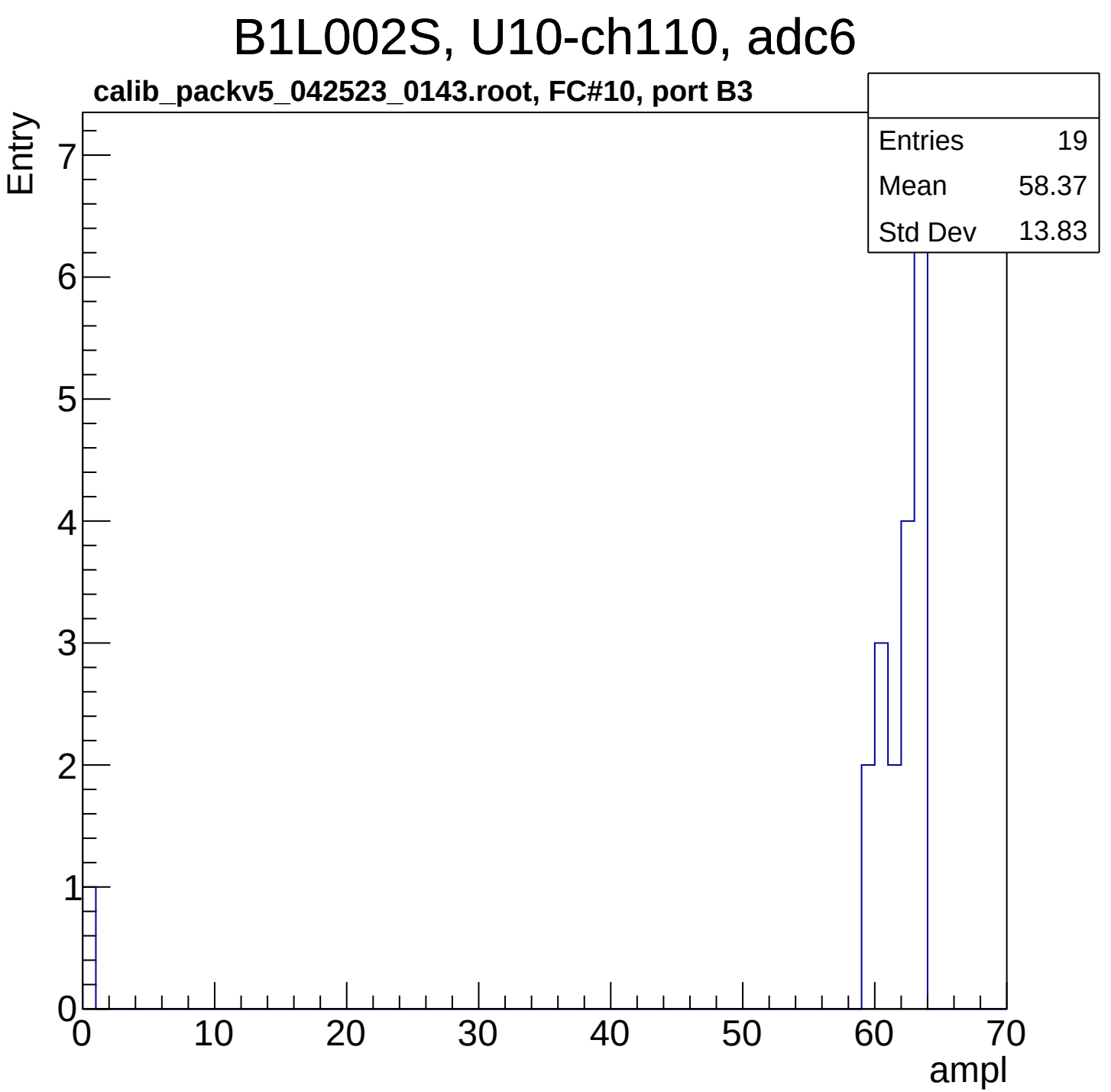

B1L002S, U10-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.31
Std Dev 2.844
Gaus mean : 35.6994
Gaus Width: 3.4047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 40.65
Std Dev 2.455
Gaus mean : 41.2026
Gaus Width: 2.6620
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 46.83
Std Dev 3.543
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.89
Std Dev 3.024
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.2
Std Dev 2.894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.37
Std Dev 13.83
Entry
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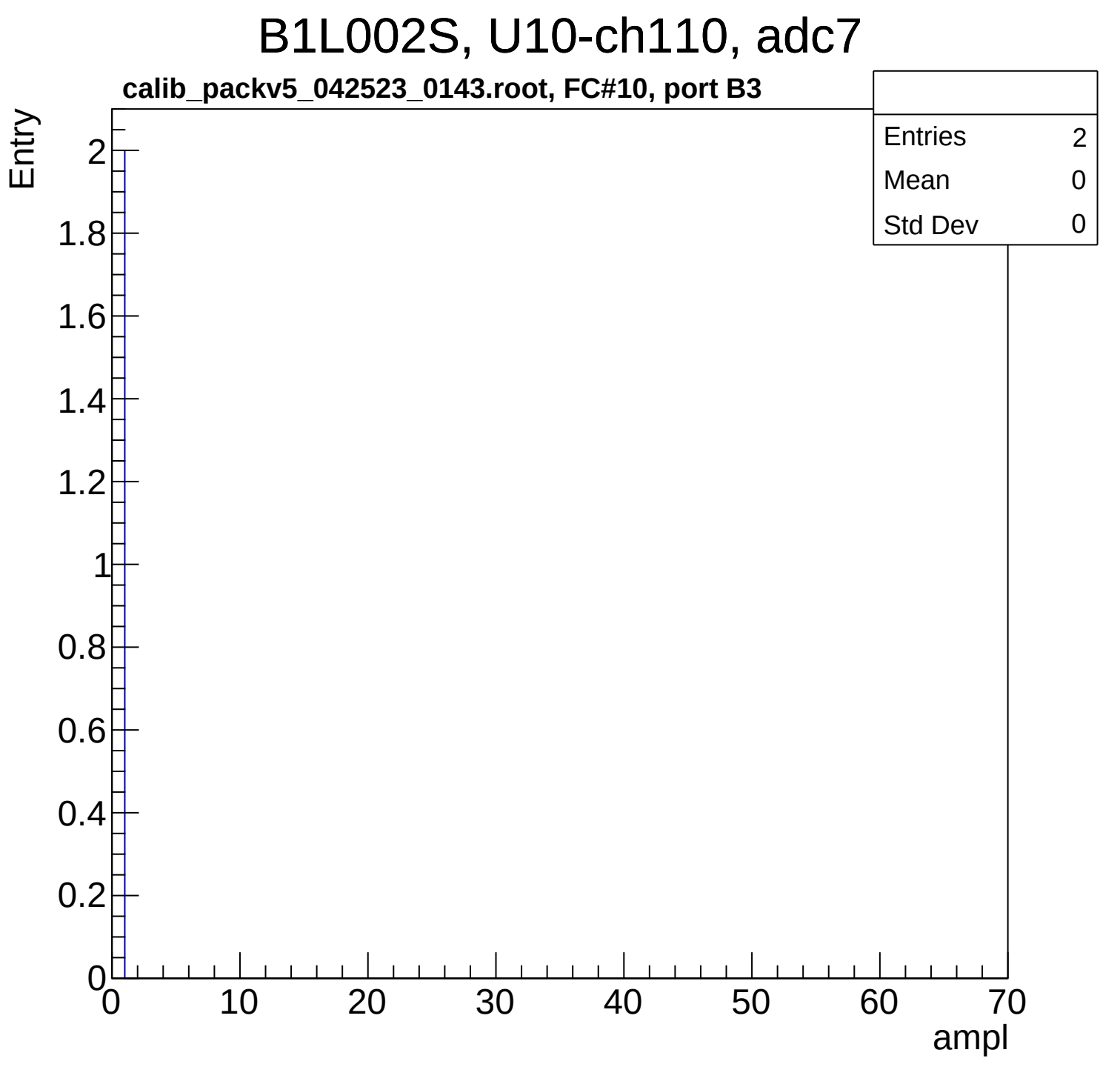
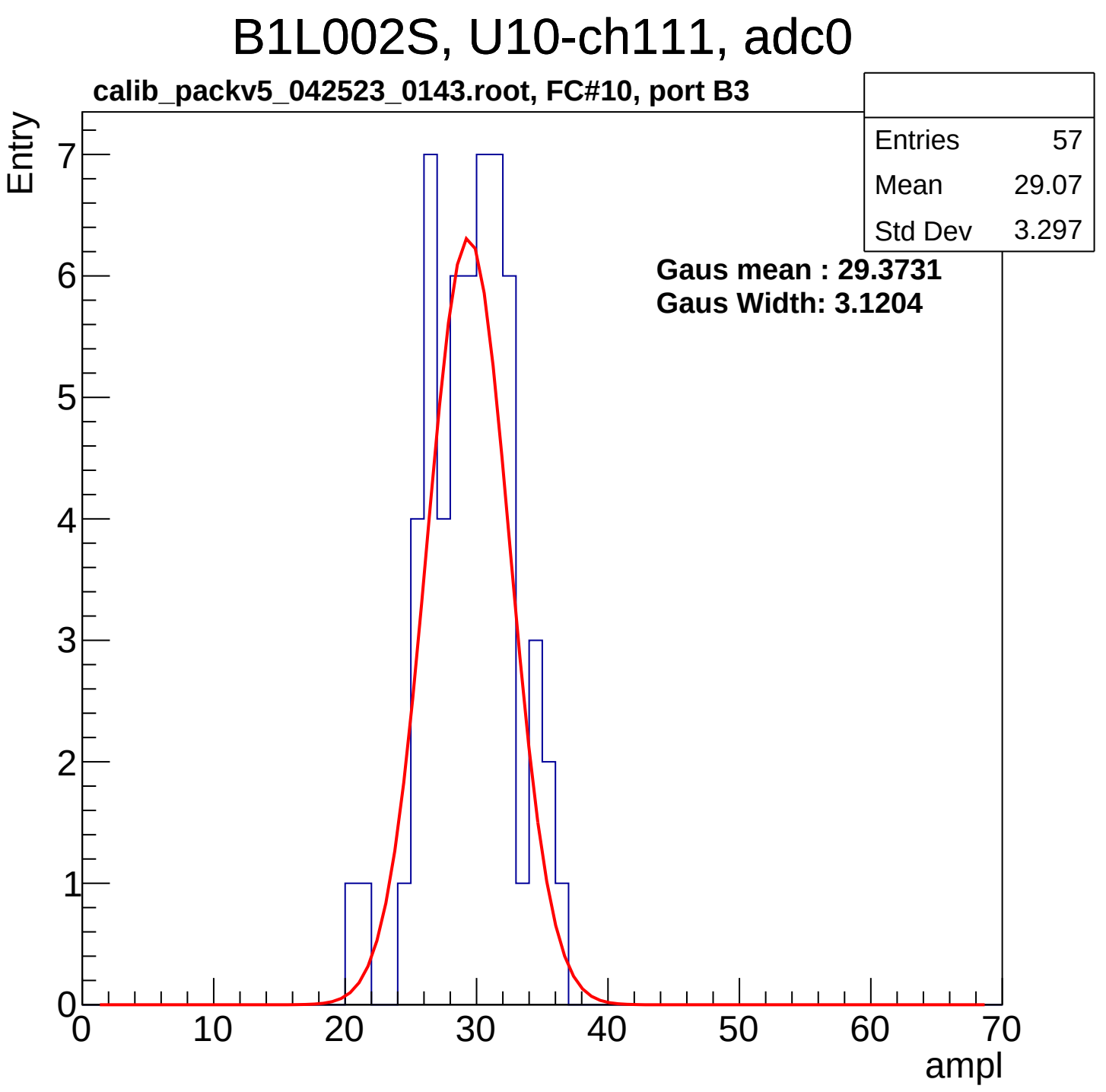
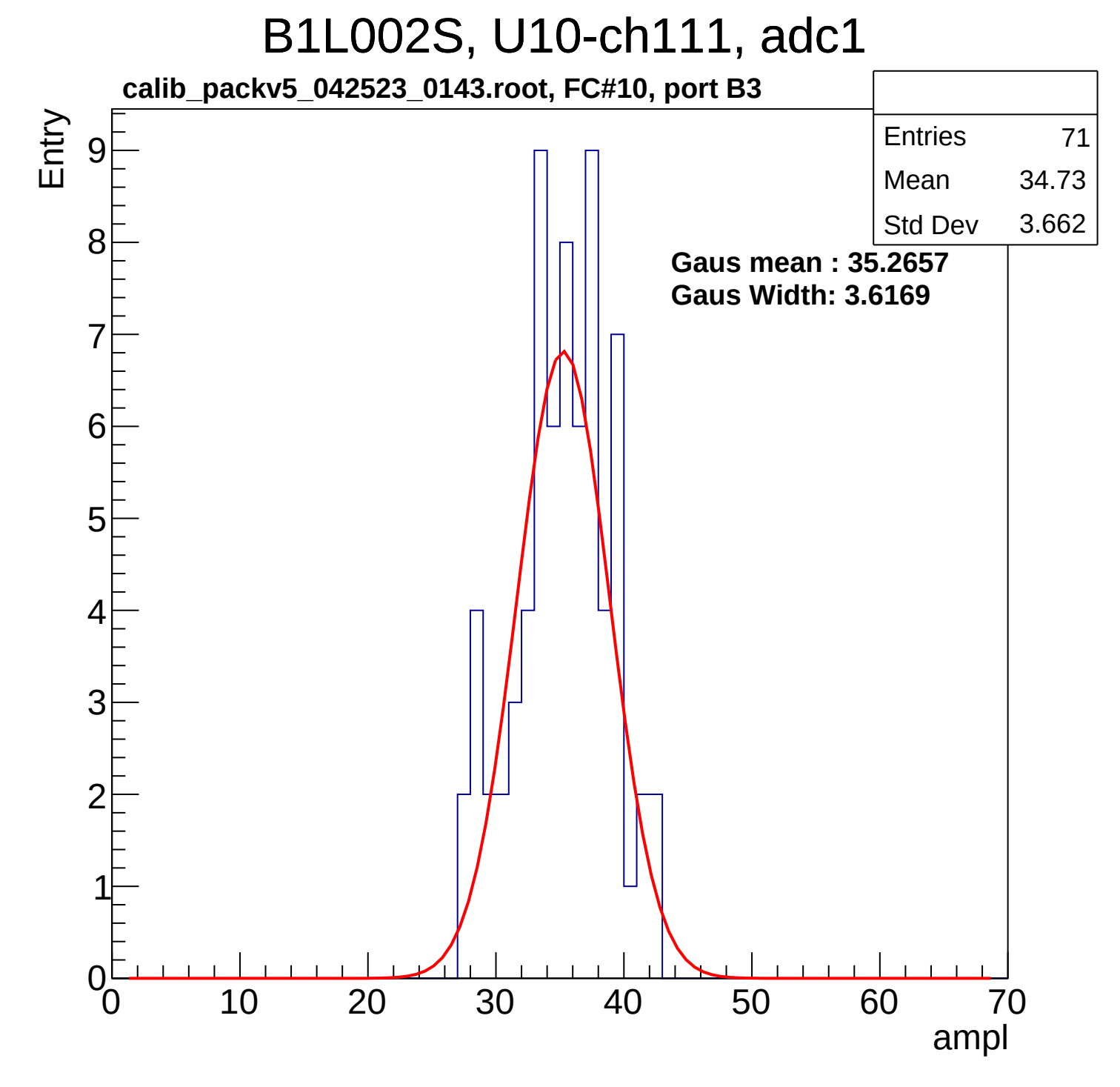
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.07
Std Dev 3.297
Gaus mean : 29.3731
Gaus Width: 3.1204
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.73
Std Dev 3.662
Gaus mean : 35.2657
Gaus Width: 3.6169
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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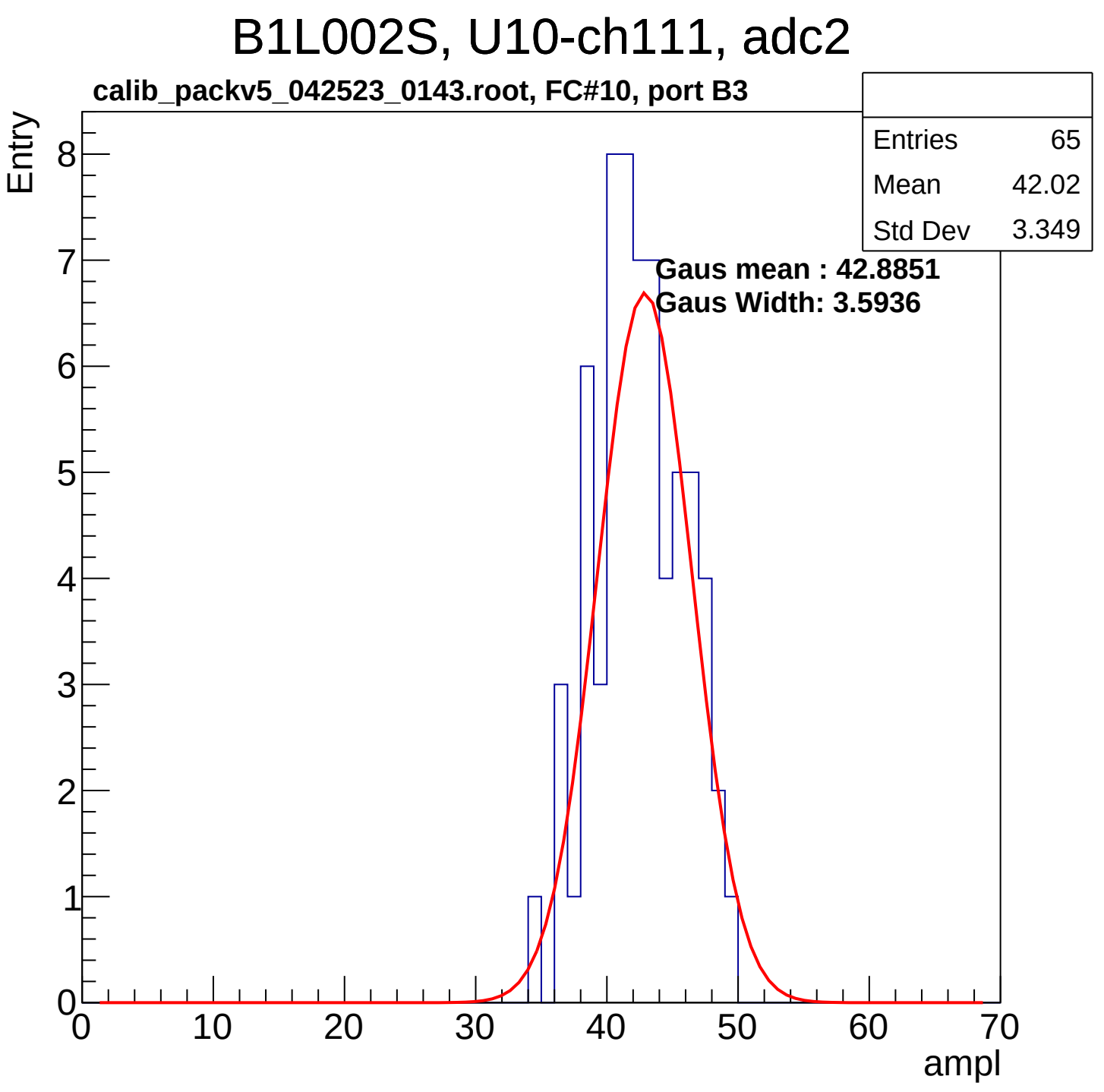
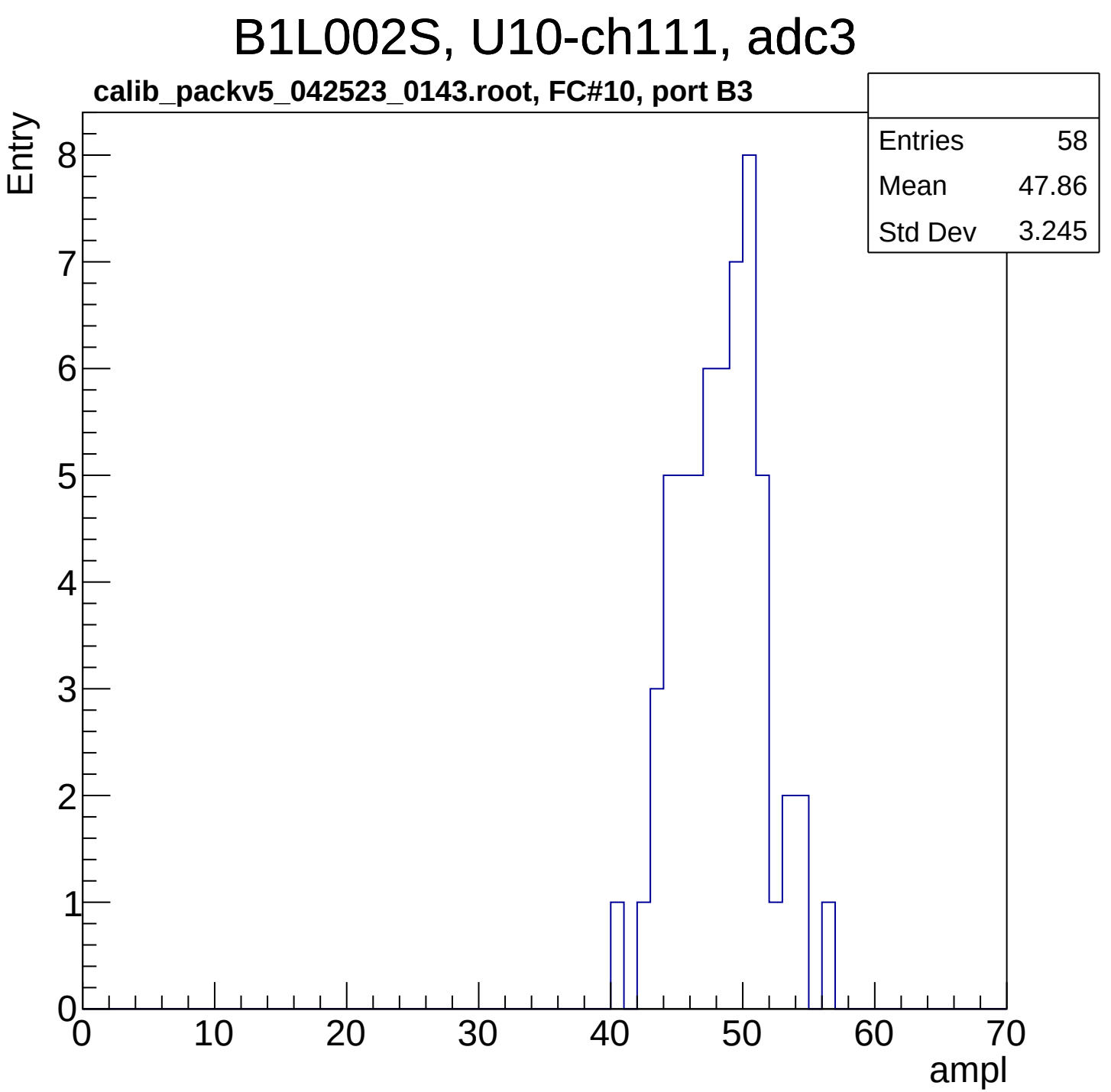

B1L002S, U10-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 42.02
Std Dev 3.349
Gaus mean : 42.8851
Gaus Width: 3.5936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.86
Std Dev 3.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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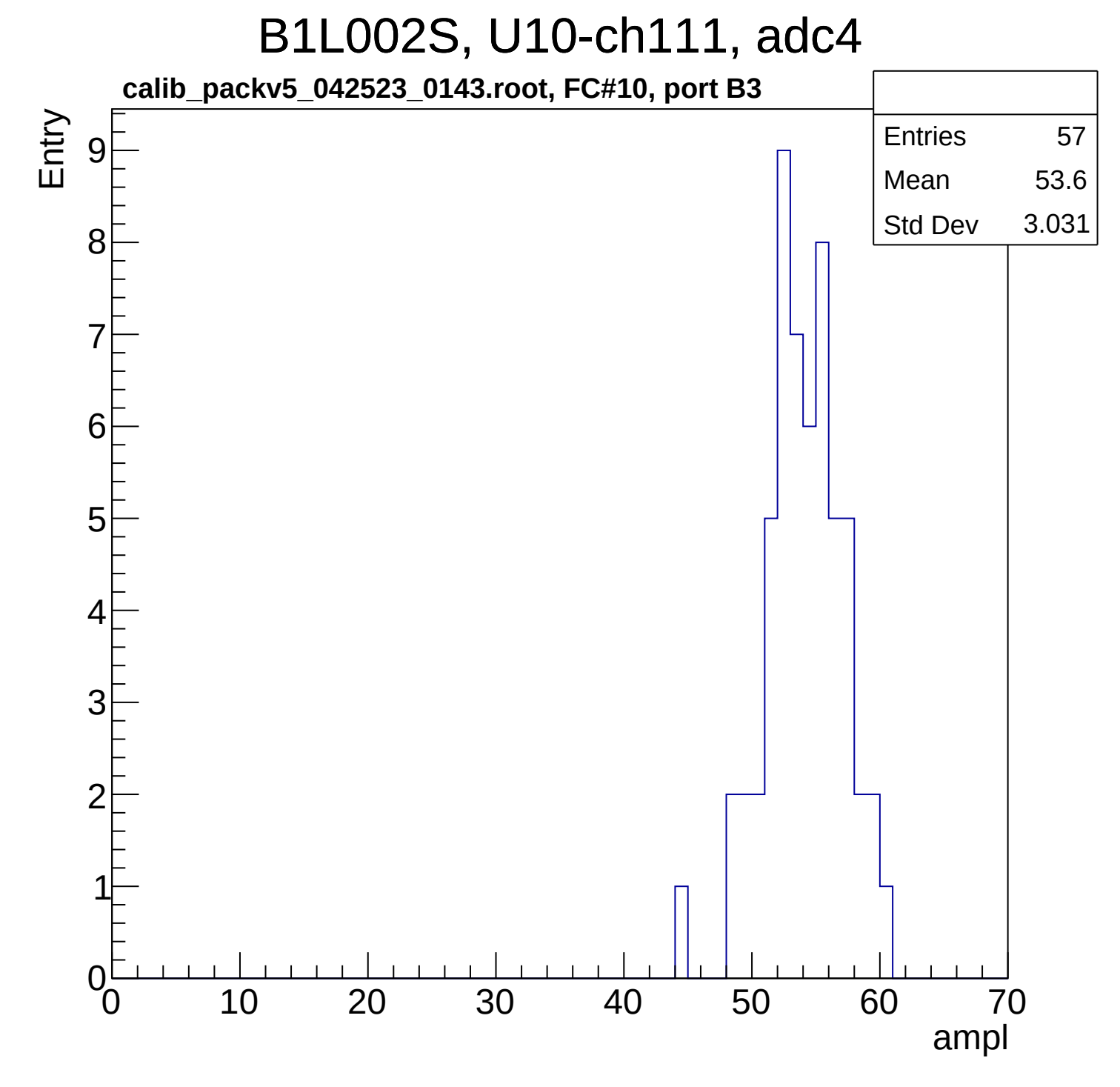
40
50
60
70


B1L002S, U10-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.6
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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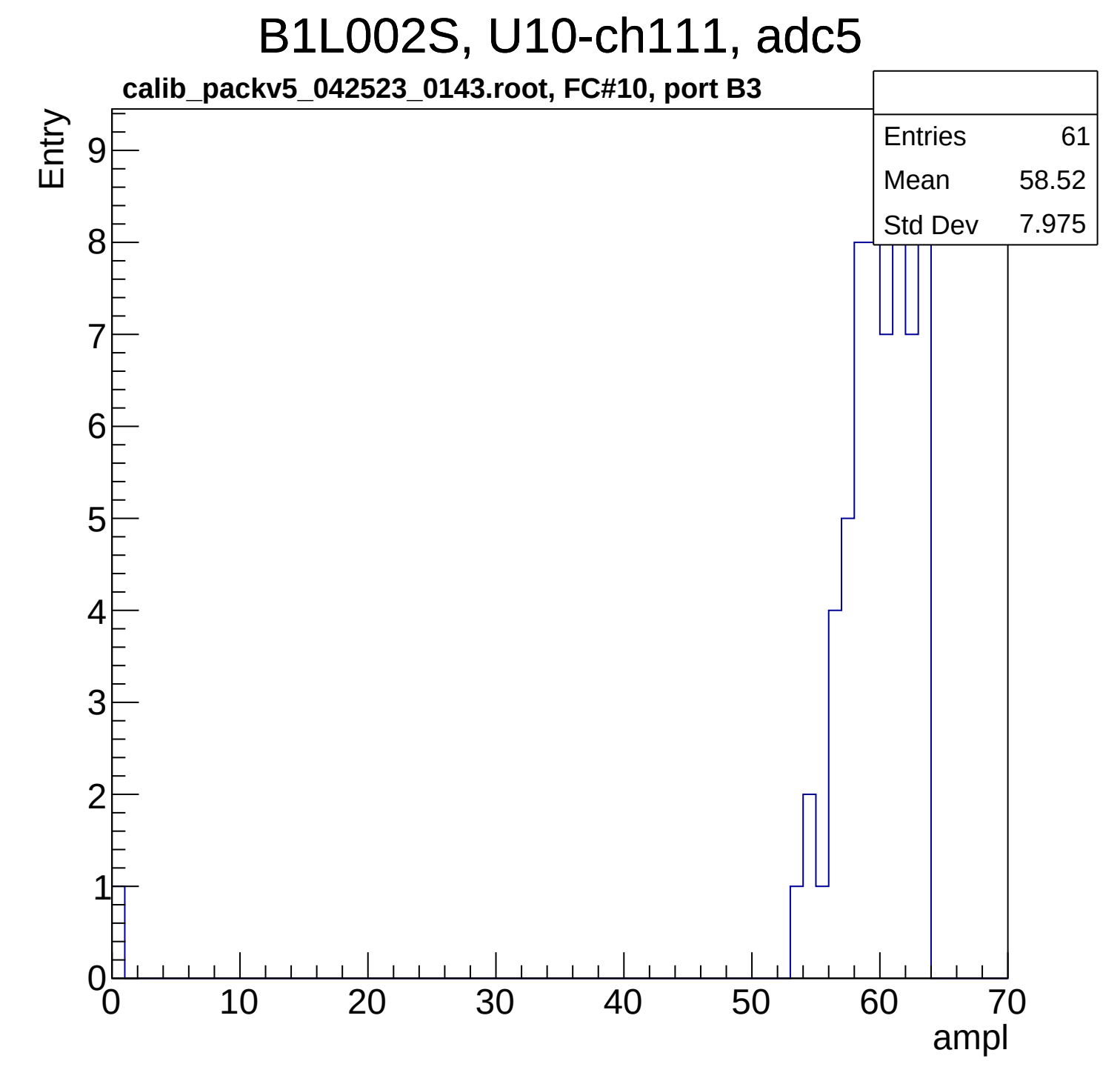
B1L002S, U10-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.52
Std Dev 7.975
Entry
ampl
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3
4
5
6
7
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0
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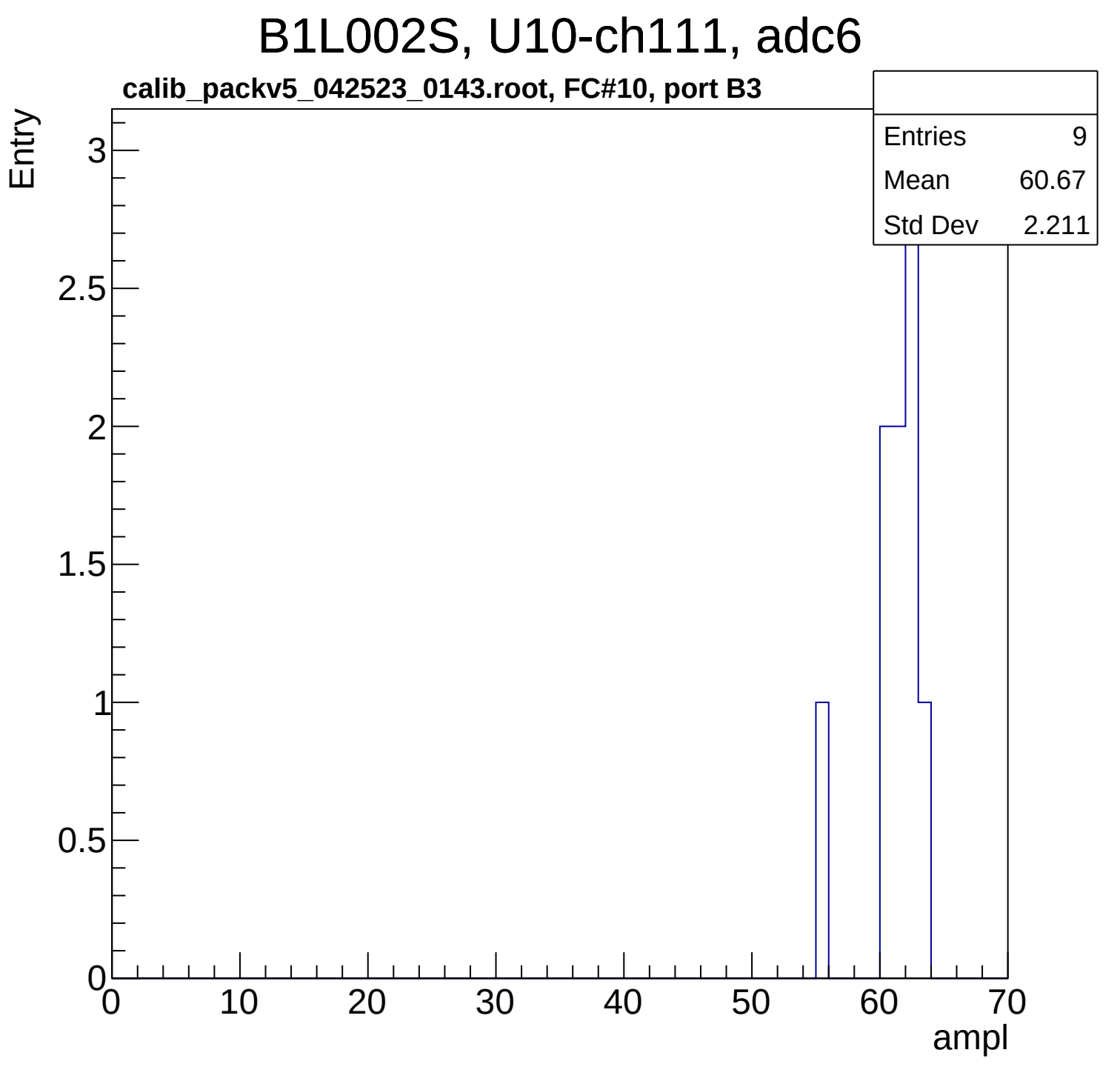
B1L002S, U10-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 60.67
Std Dev 2.211
Entry
ampl
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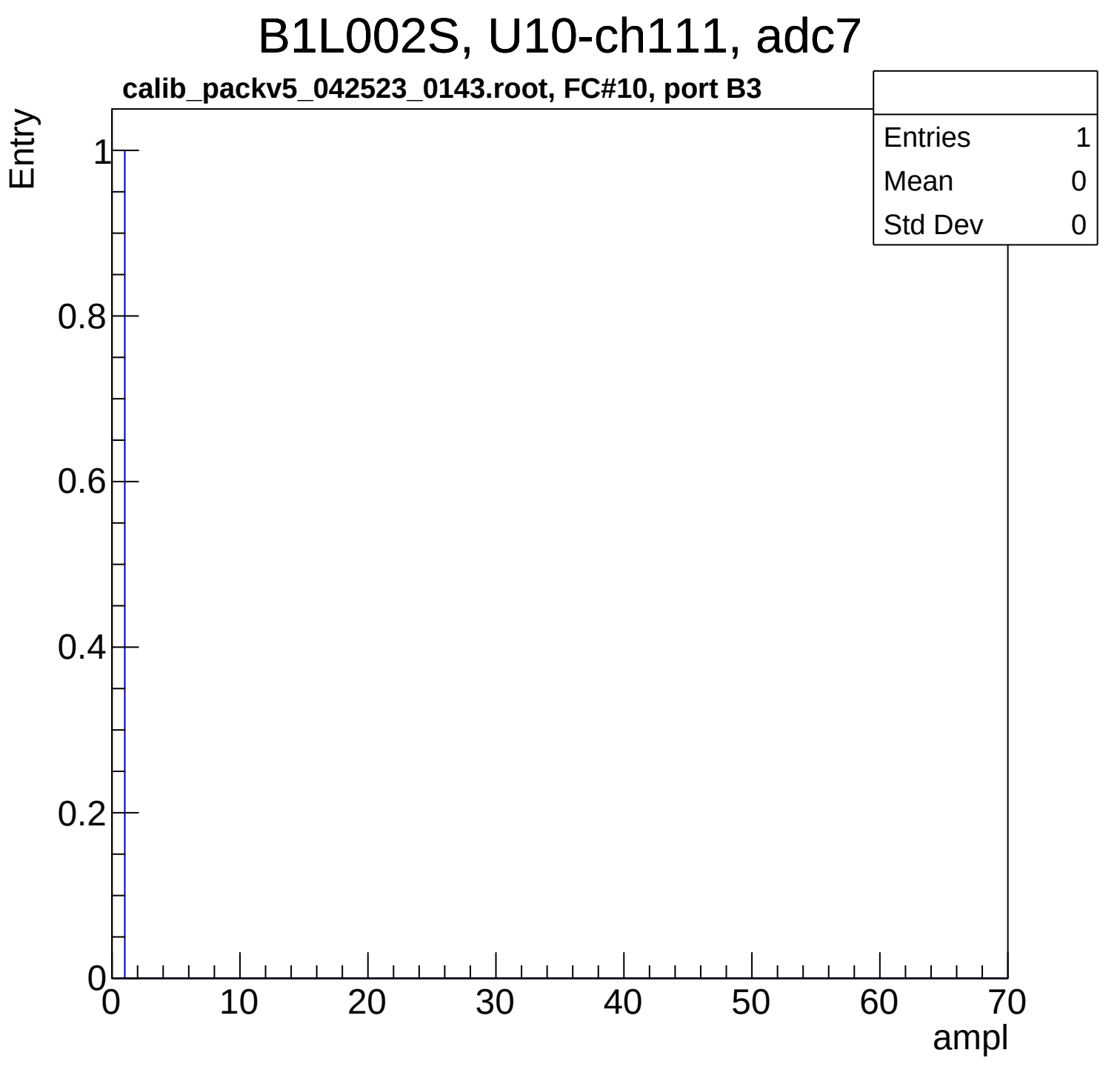
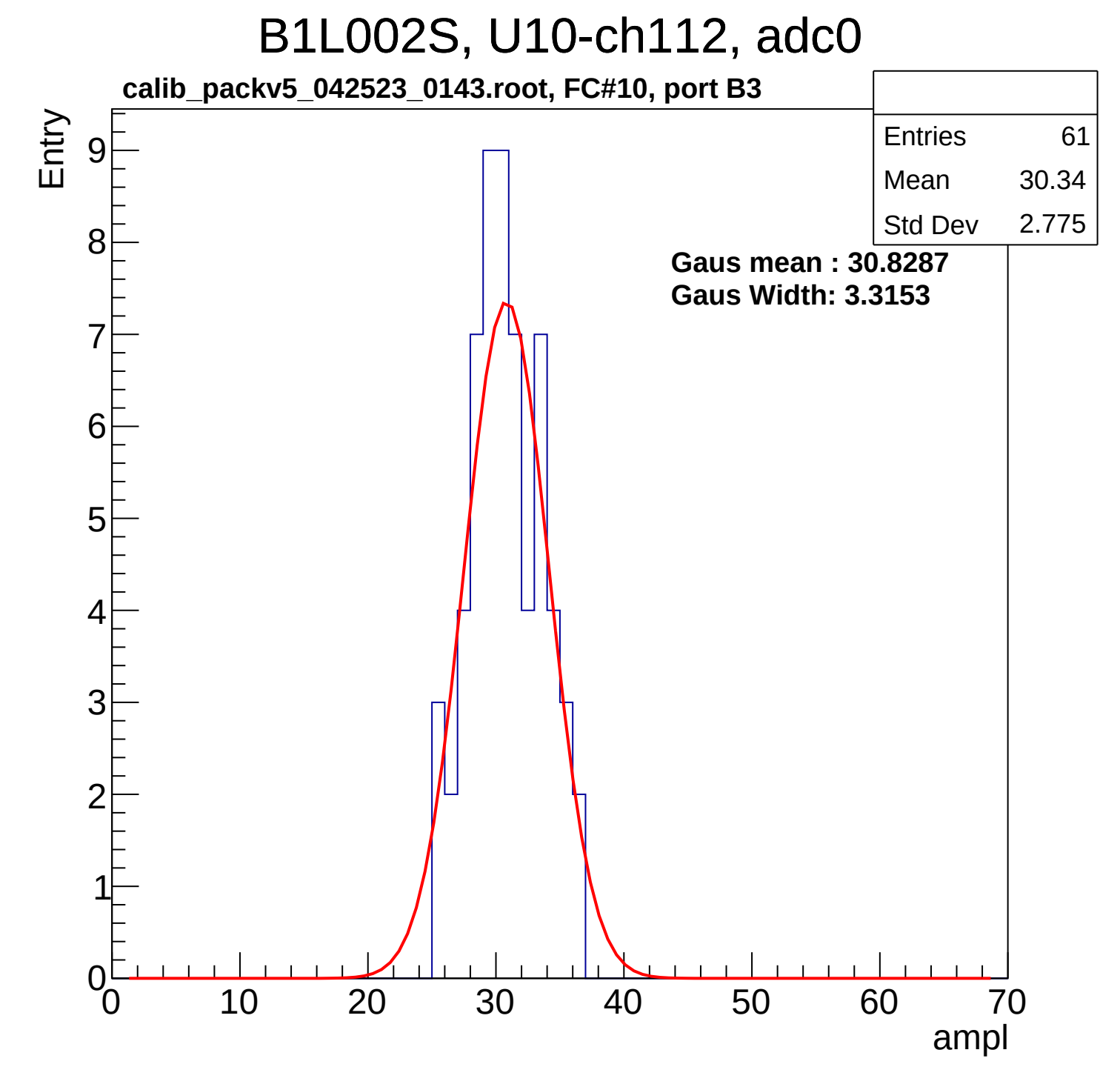
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 30.34
Std Dev 2.775
Gaus mean : 30.8287
Gaus Width: 3.3153
Entry
ampl
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2
3
4
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6
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8
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0
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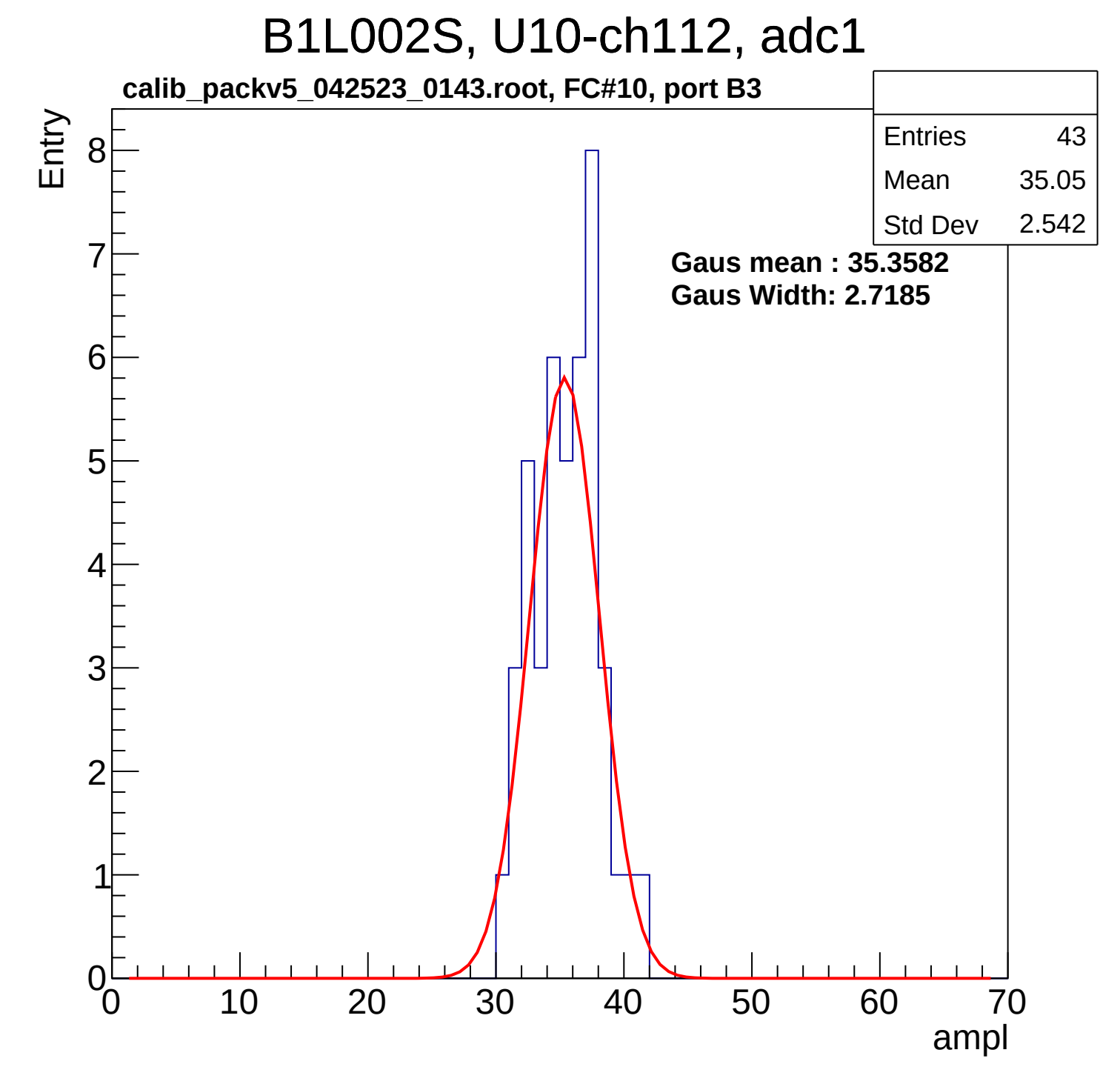
B1L002S, U10-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 35.05
Std Dev 2.542
Gaus mean : 35.3582
Gaus Width: 2.7185
Entry
ampl
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2
3
4
5
6
7
8
0
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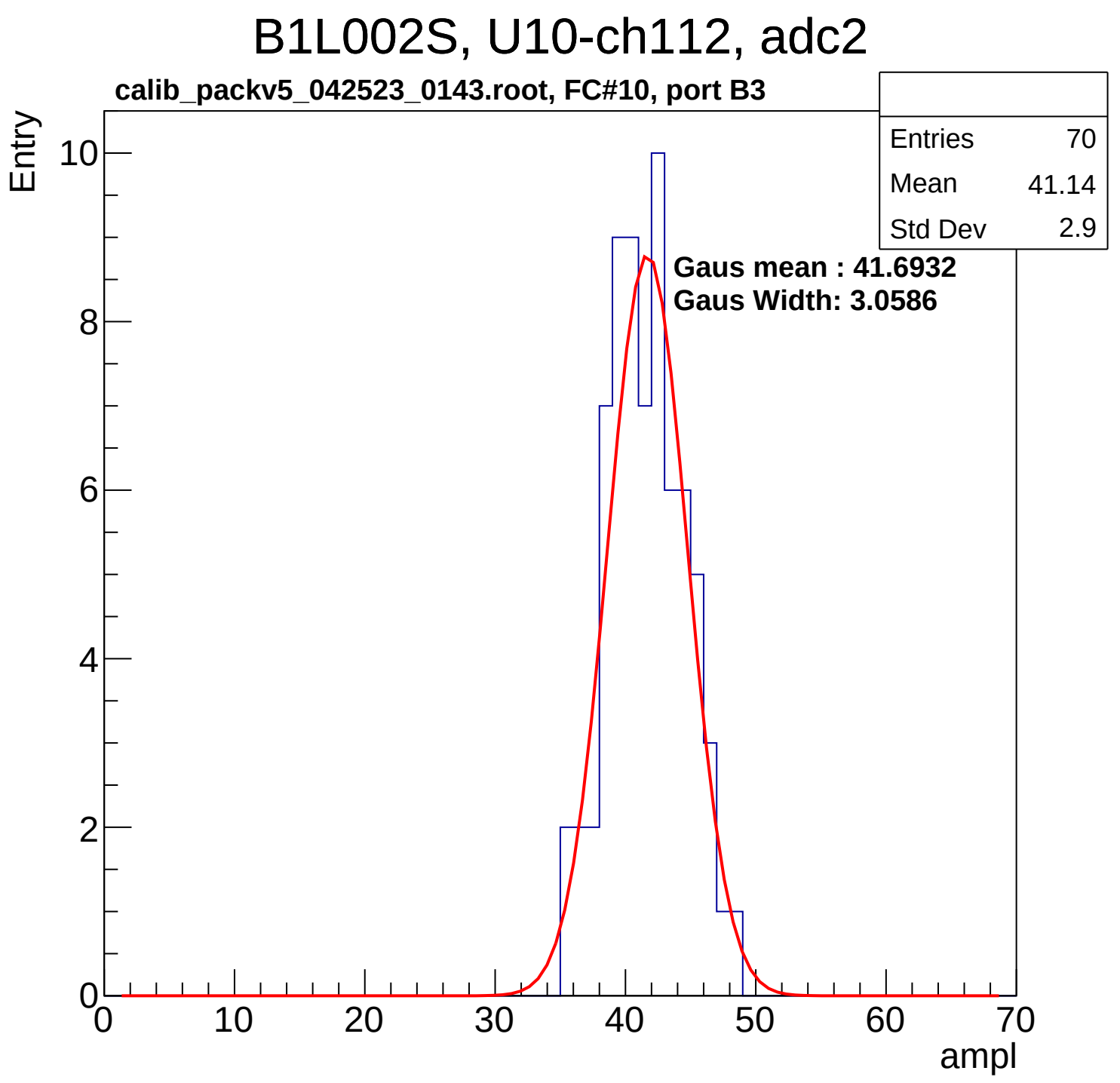
B1L002S, U10-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.14
Std Dev 2.9
Gaus mean : 41.6932
Gaus Width: 3.0586
Entry
ampl
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2
4
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0
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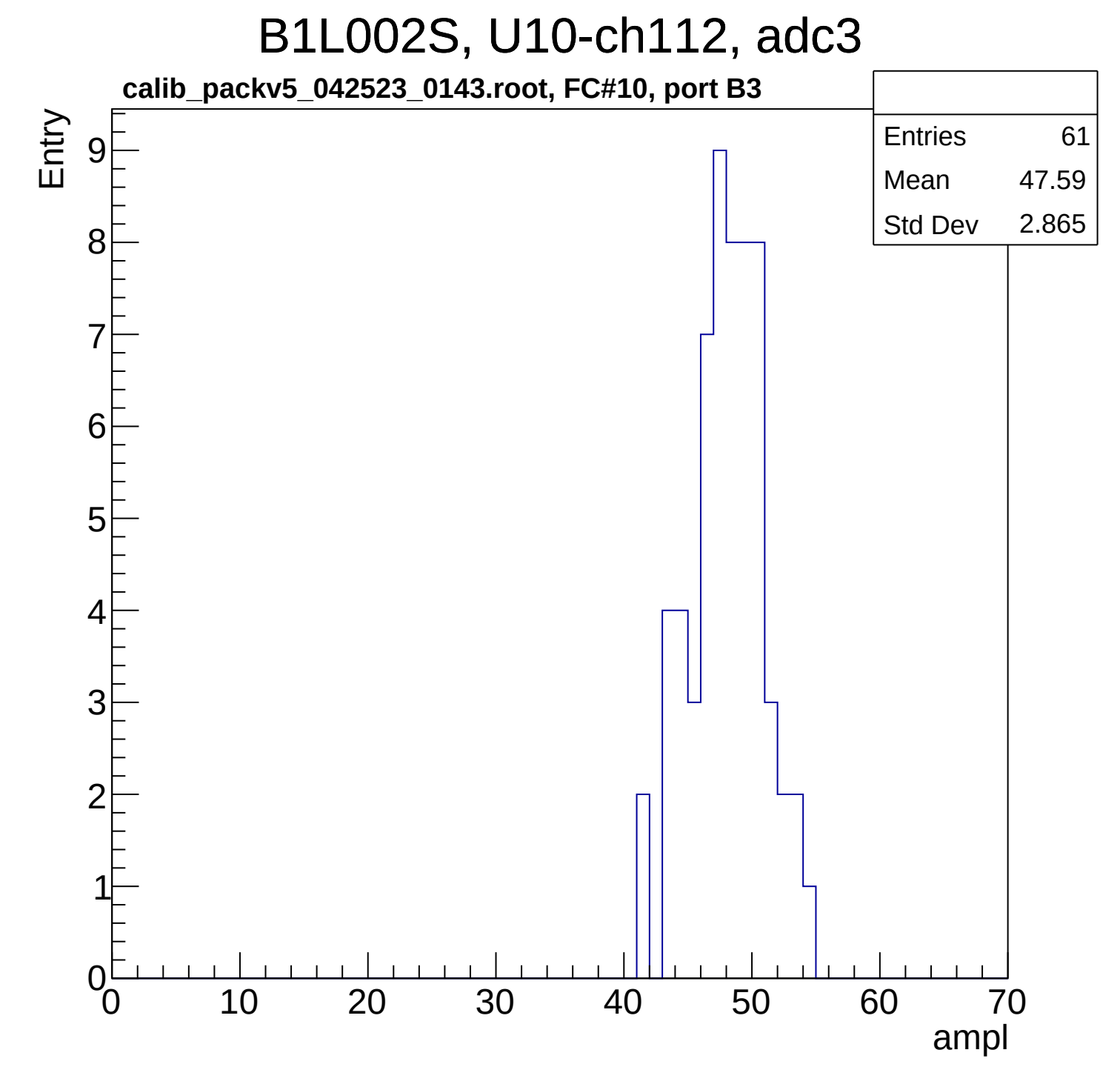
B1L002S, U10-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.59
Std Dev 2.865
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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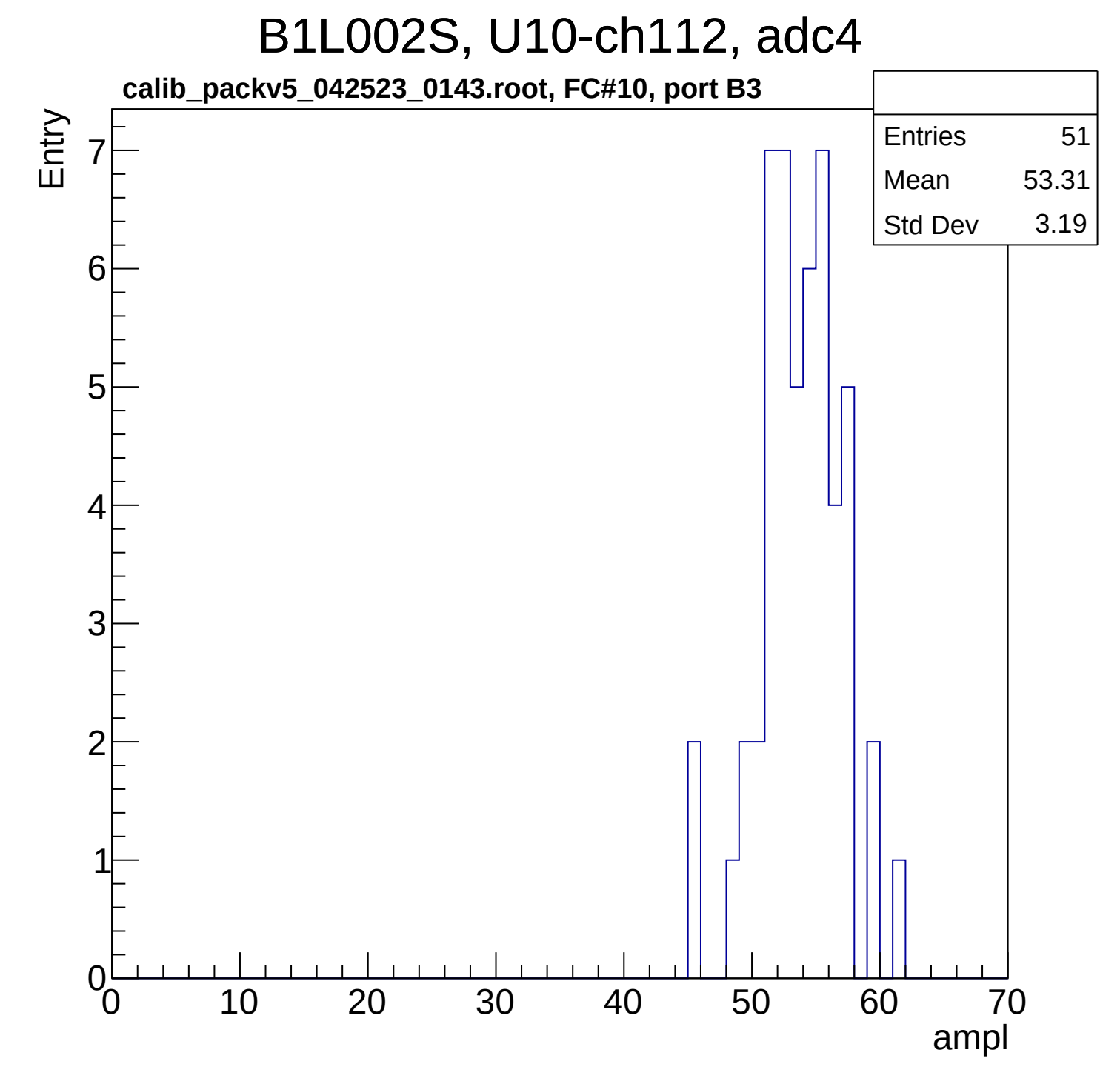
B1L002S, U10-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.31
Std Dev 3.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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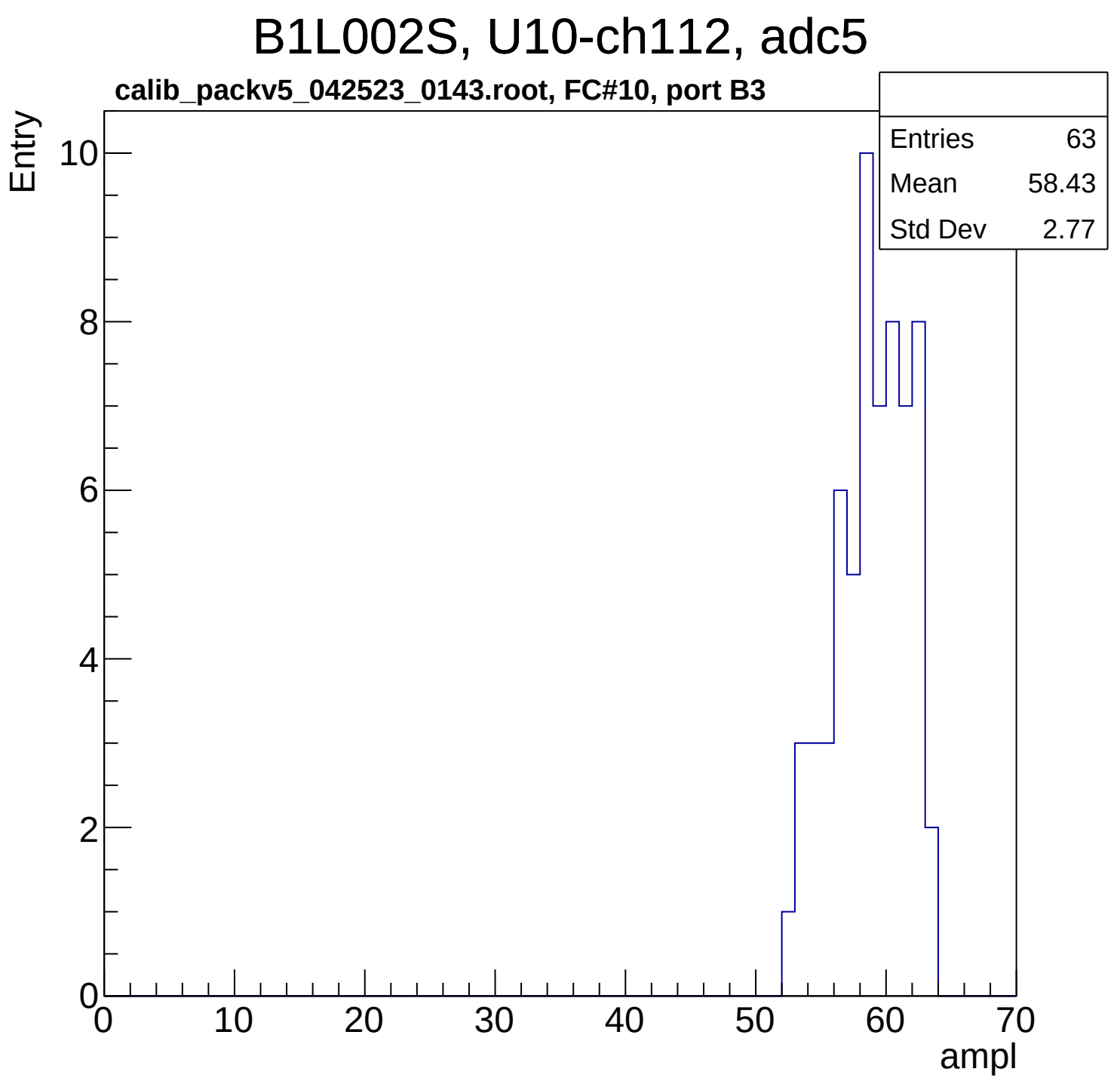
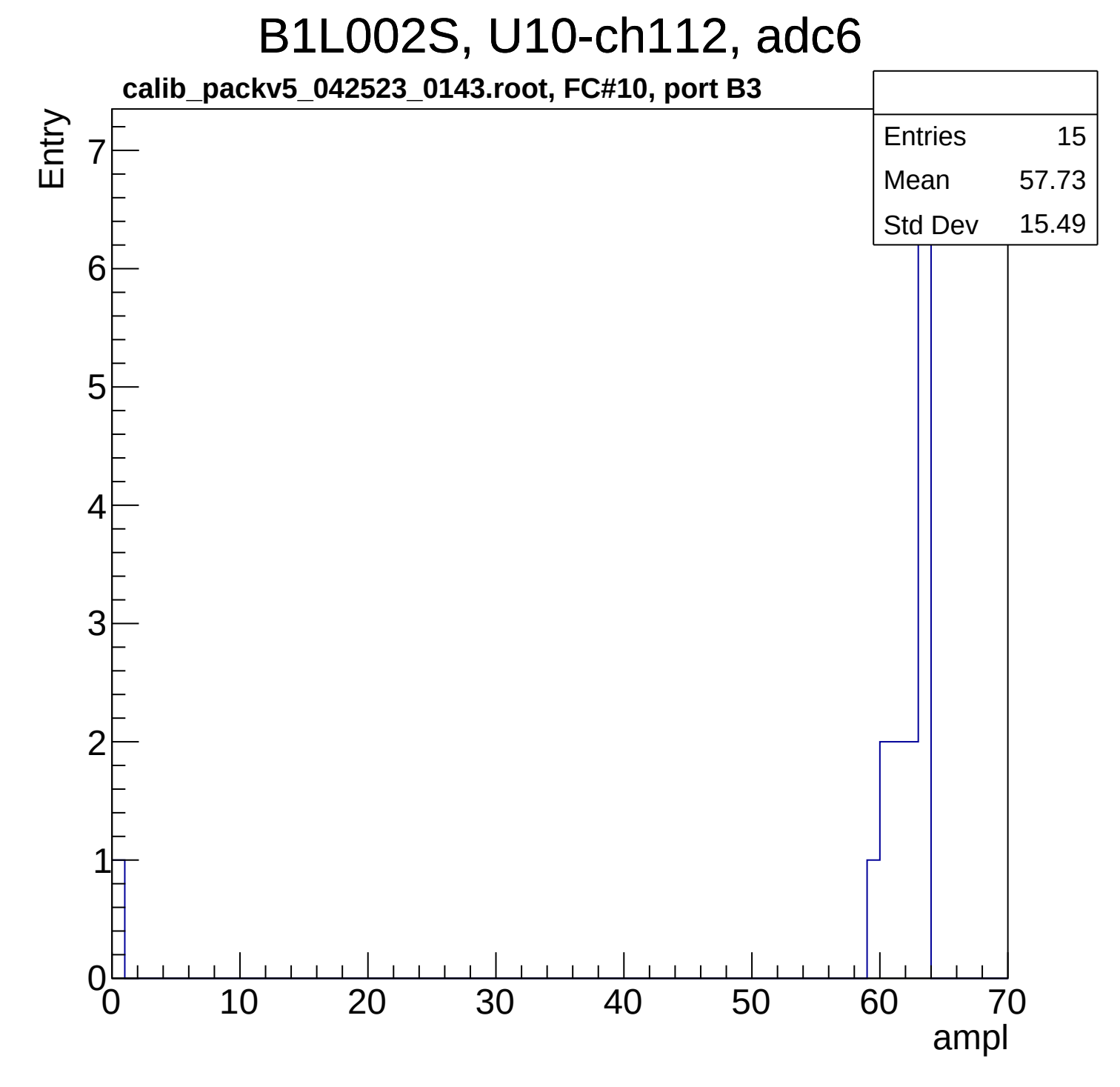

B1L002S, U10-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.43
Std Dev 2.77
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.73
Std Dev 15.49
Entry
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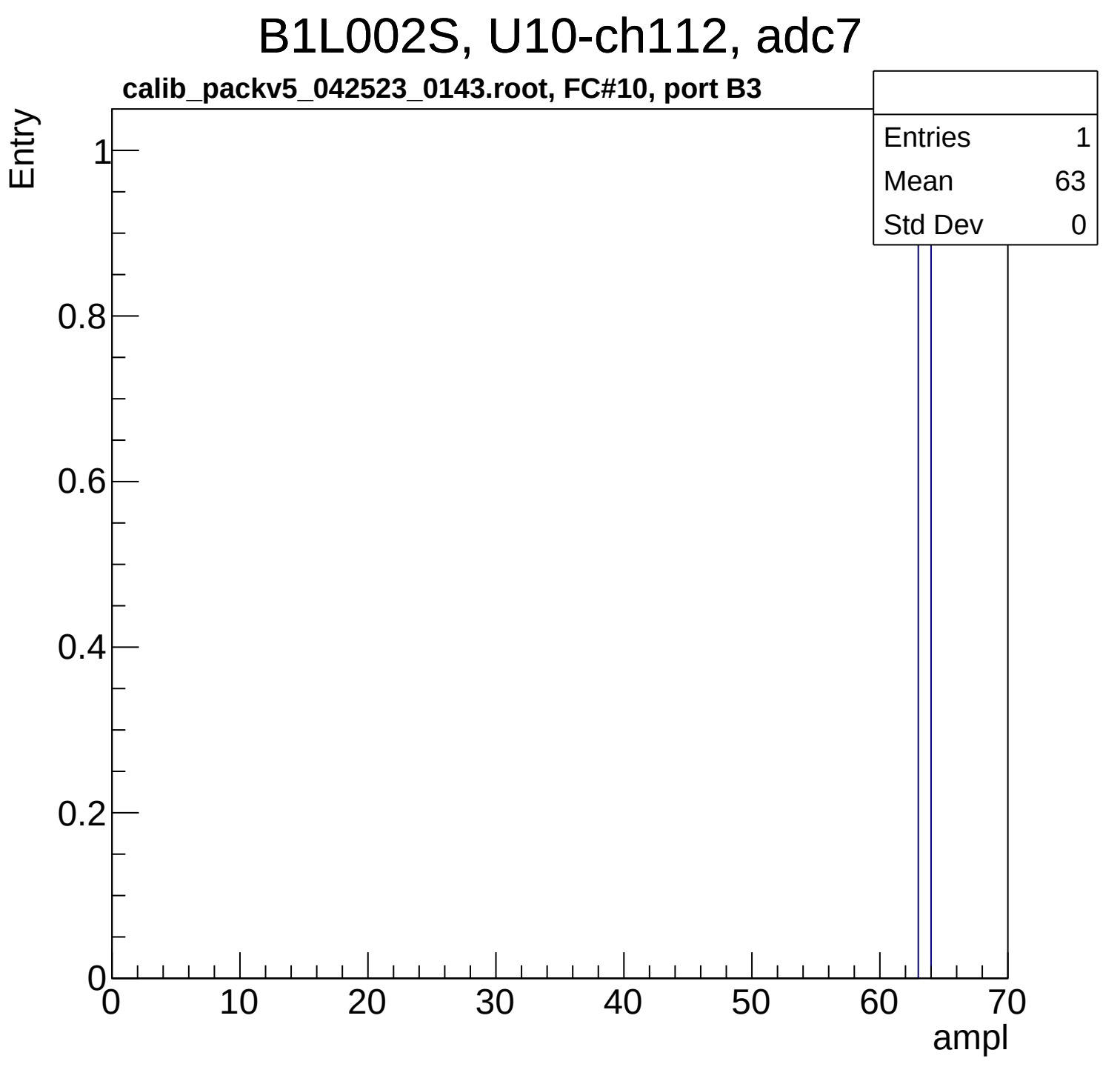
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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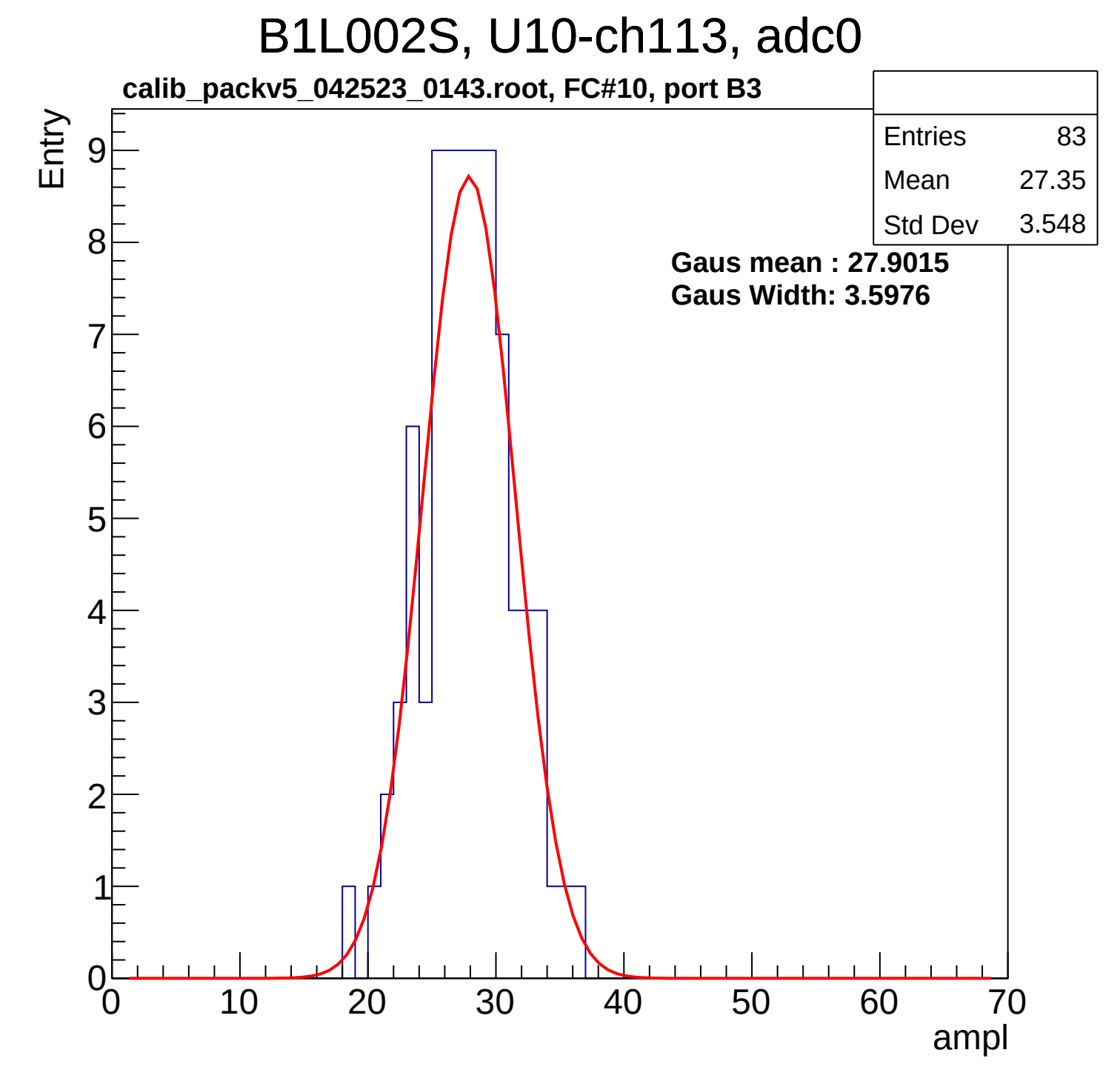
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 27.35
Std Dev 3.548
Gaus mean : 27.9015
Gaus Width: 3.5976
Entry
ampl
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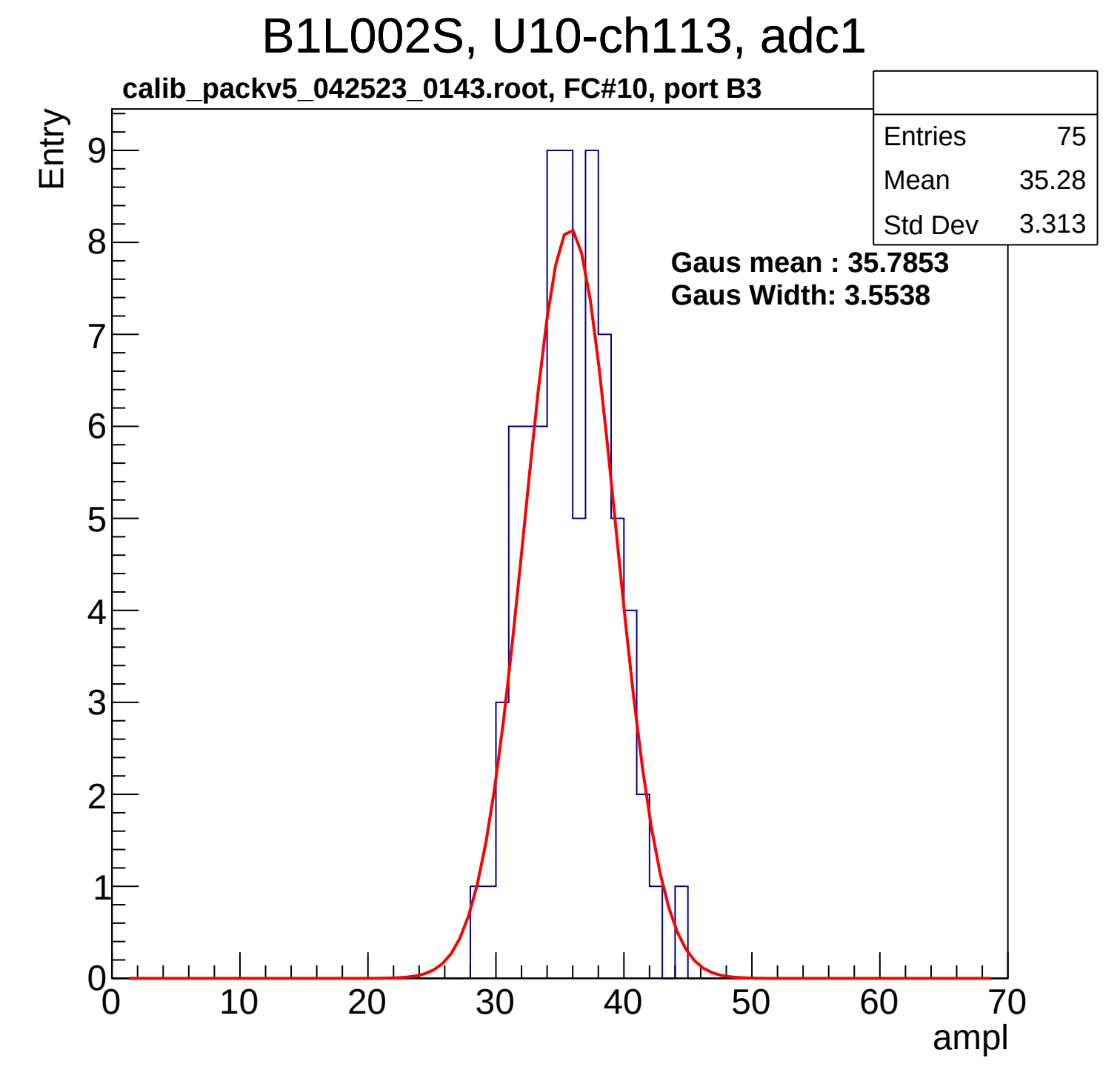
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.28
Std Dev 3.313
Gaus mean : 35.7853
Gaus Width: 3.5538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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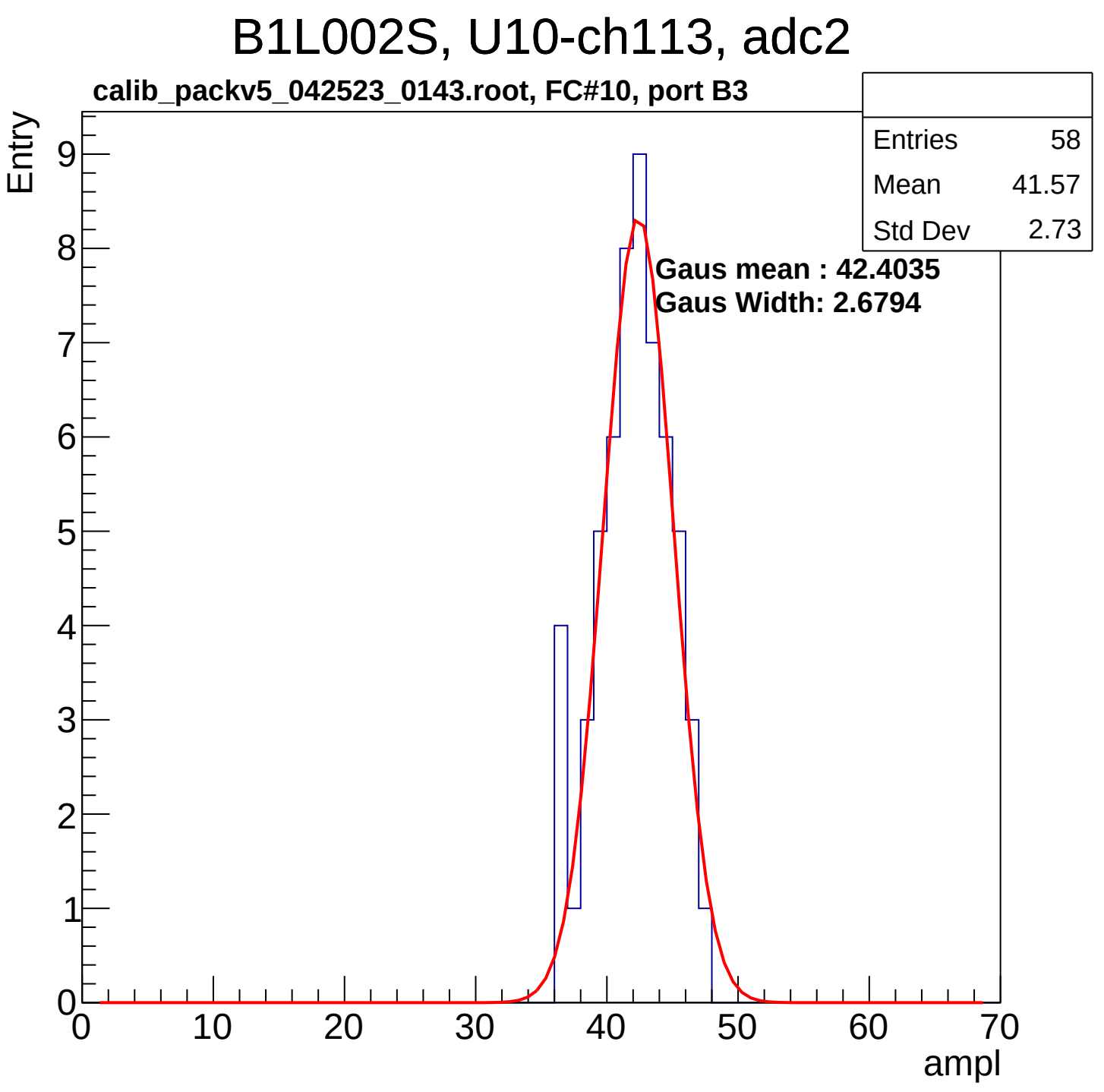
B1L002S, U10-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.57
Std Dev 2.73
Gaus mean : 42.4035
Gaus Width: 2.6794
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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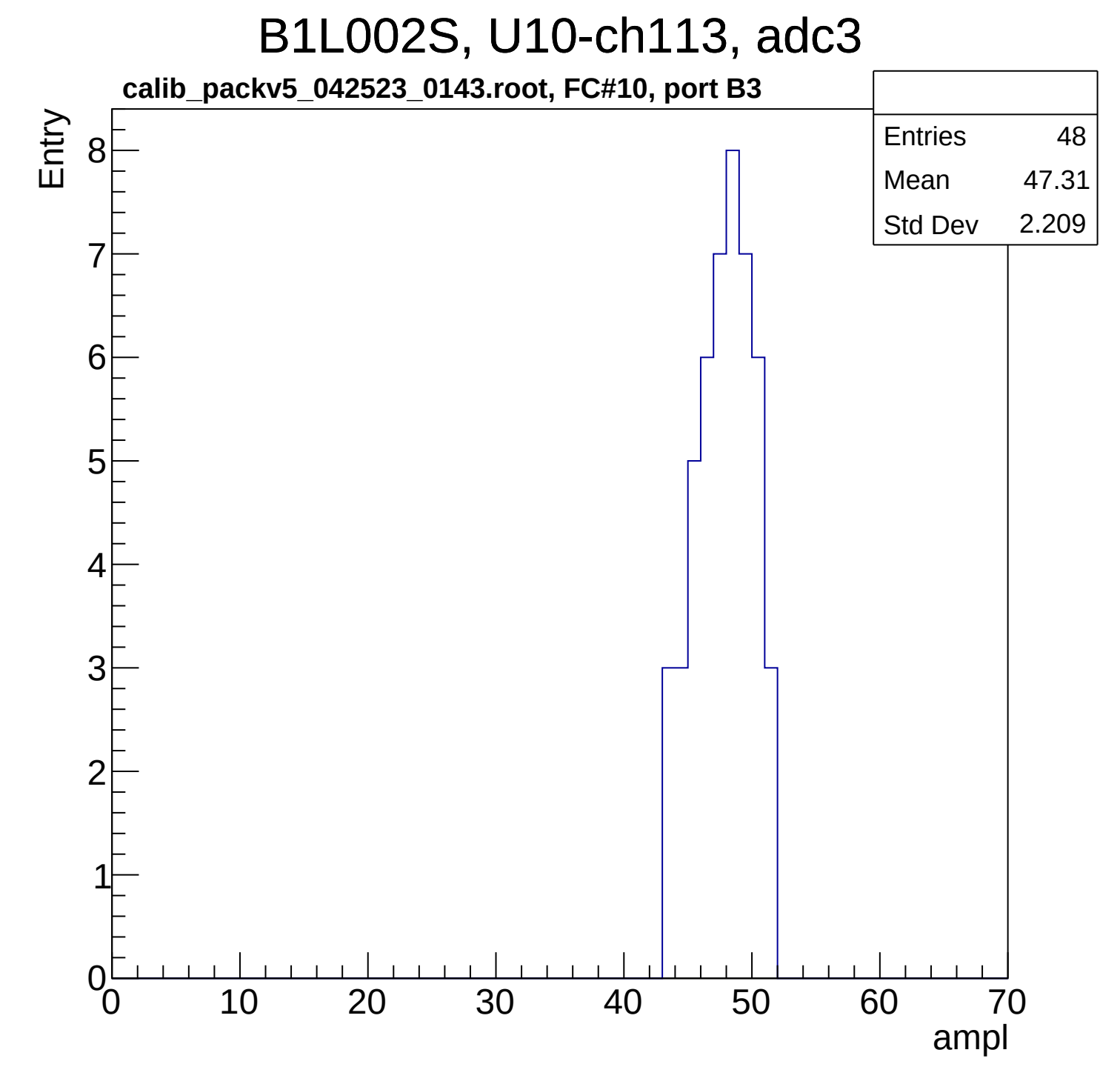
B1L002S, U10-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 47.31
Std Dev 2.209
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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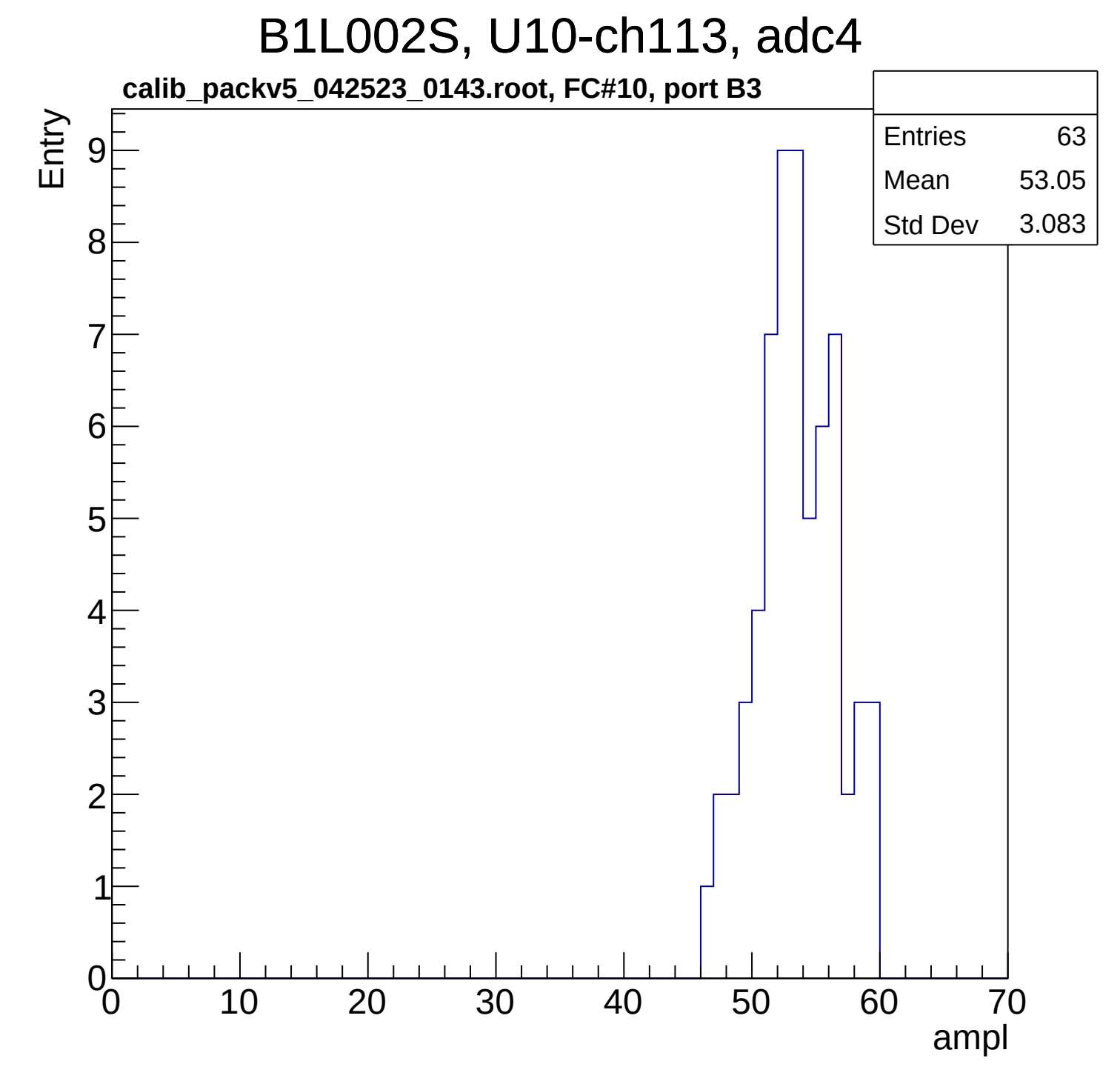
B1L002S, U10-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.05
Std Dev 3.083
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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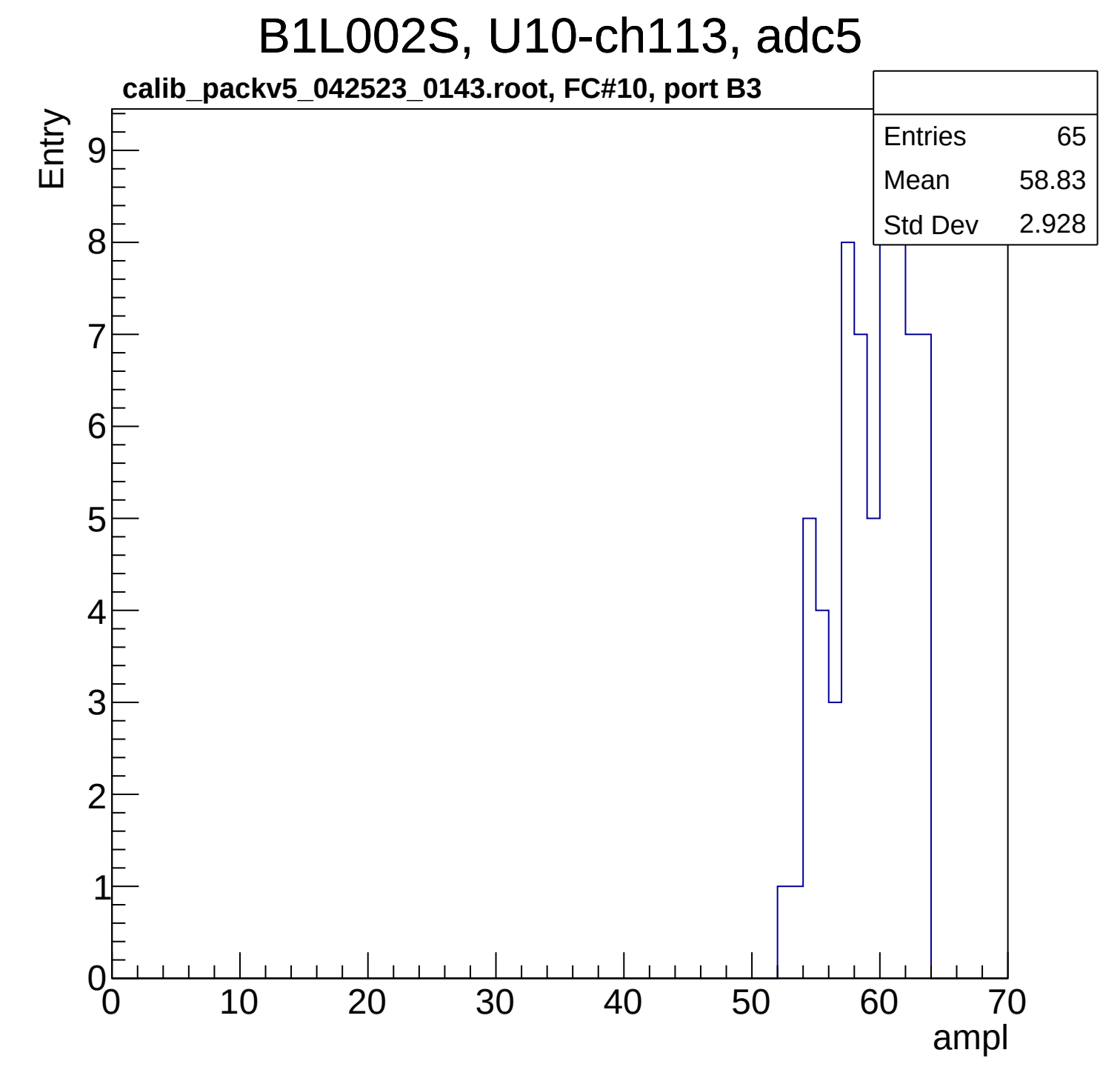
B1L002S, U10-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.83
Std Dev 2.928
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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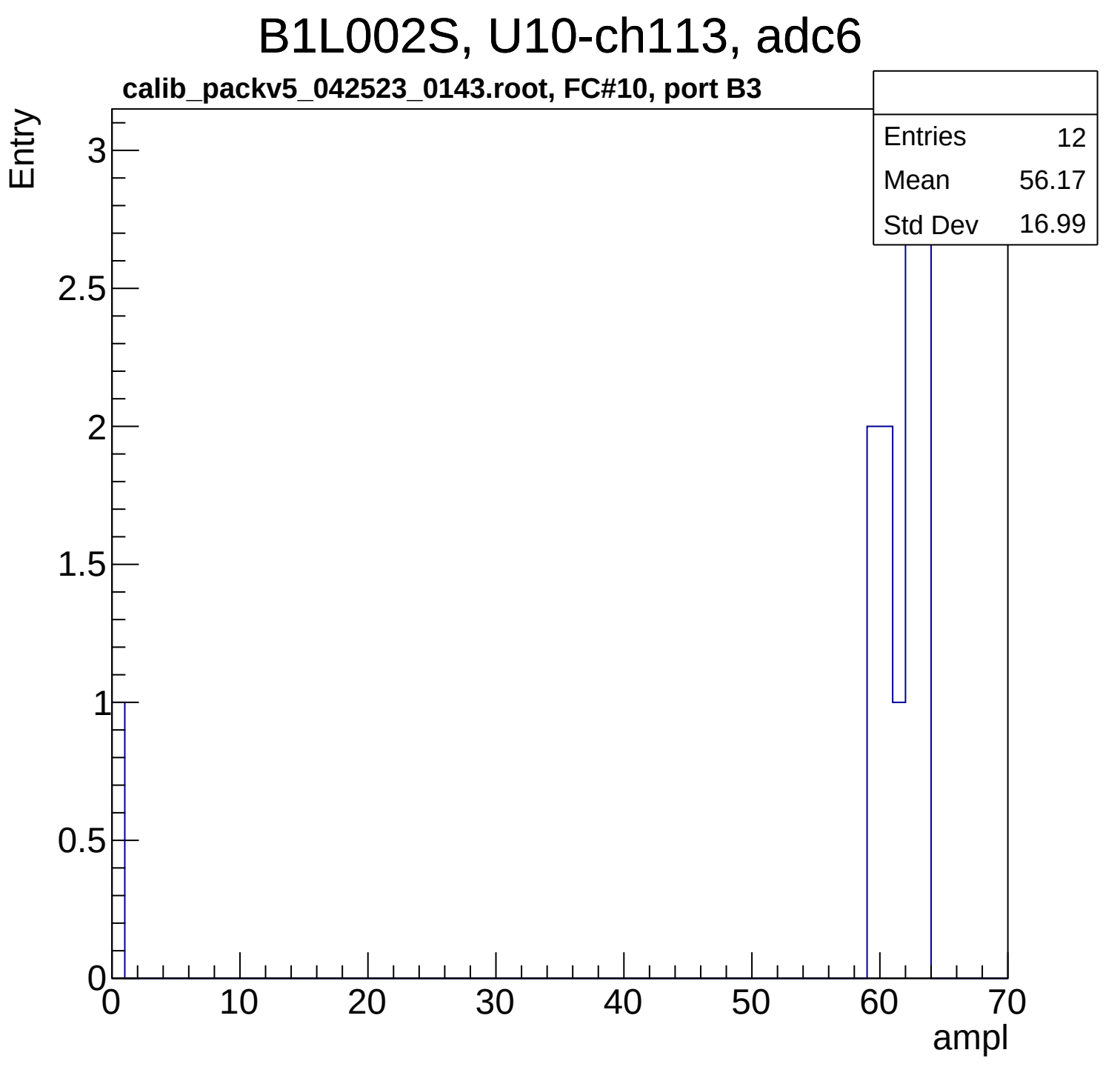
B1L002S, U10-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.17
Std Dev 16.99
Entry
ampl
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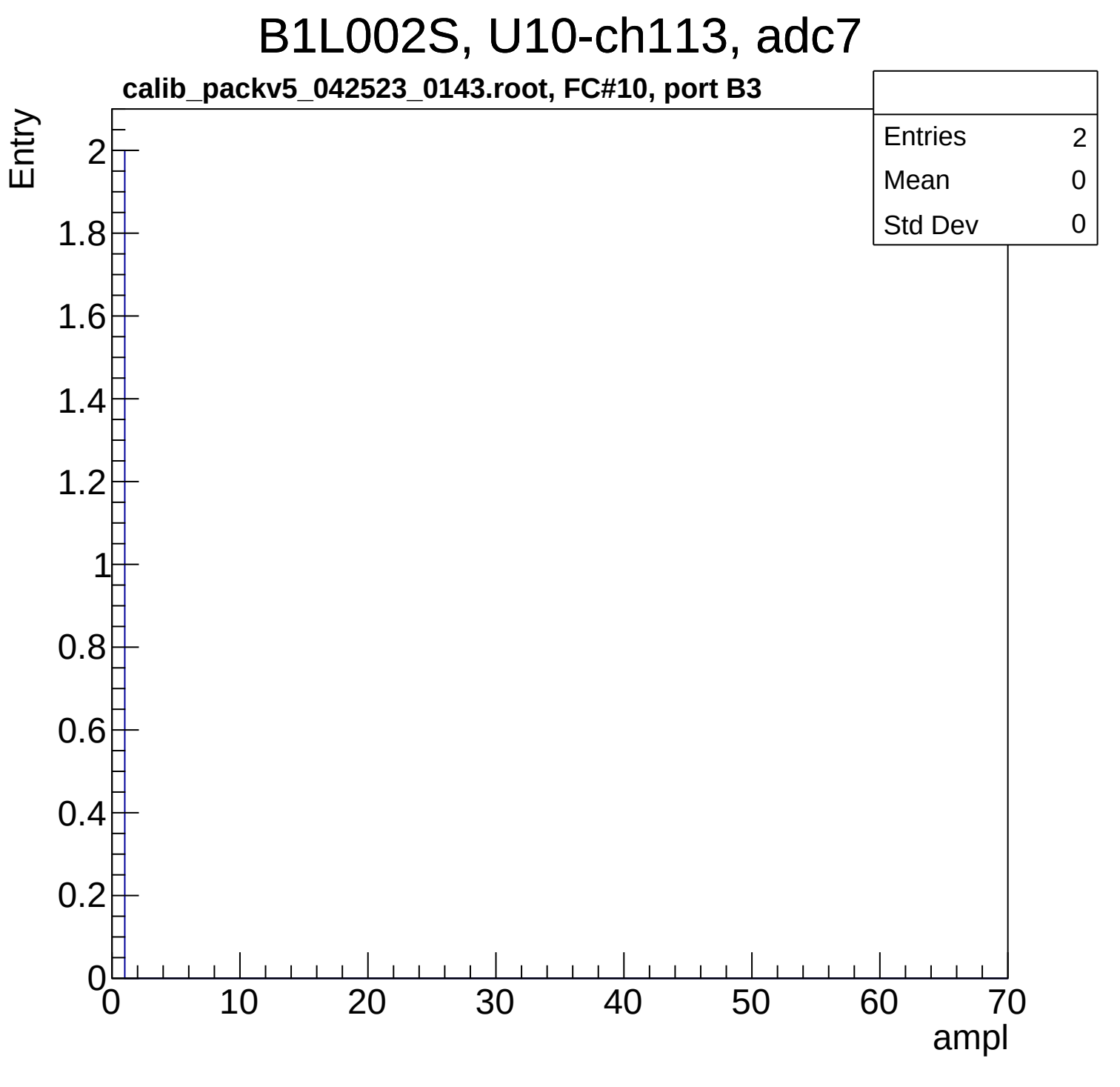
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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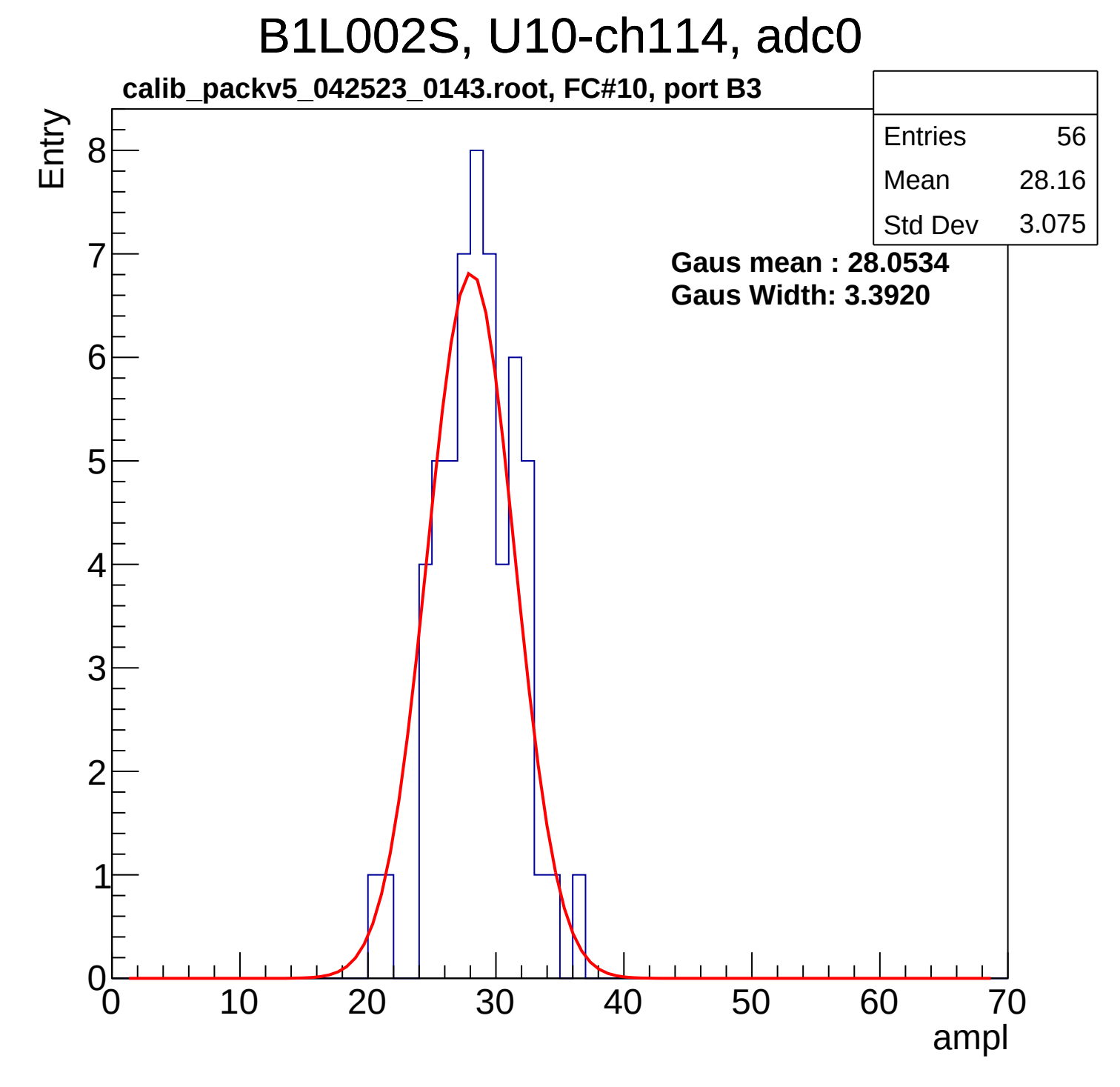
B1L002S, U10-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.16
Std Dev 3.075
Gaus mean : 28.0534
Gaus Width: 3.3920
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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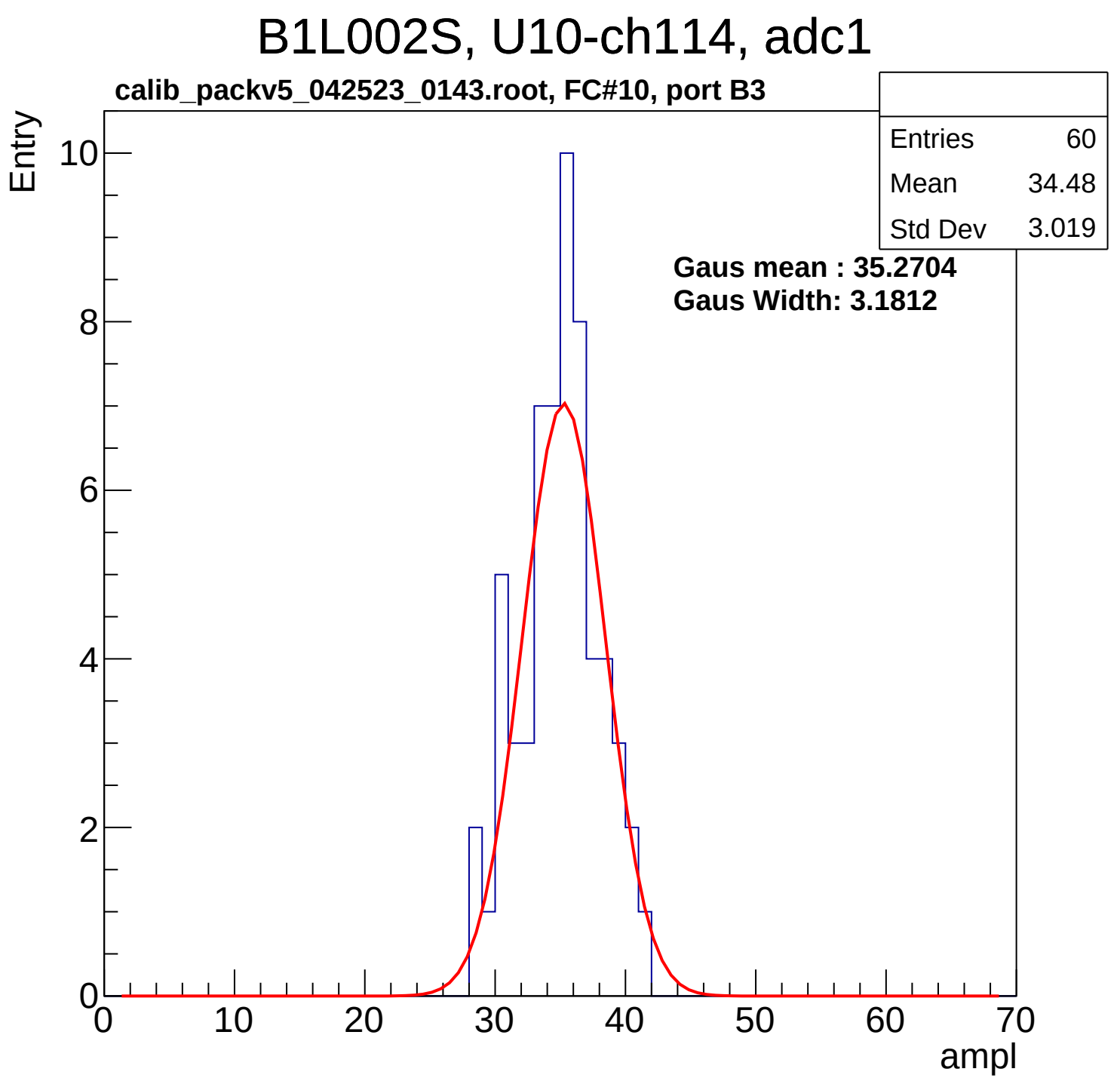
B1L002S, U10-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.48
Std Dev 3.019
Gaus mean : 35.2704
Gaus Width: 3.1812
Entry
ampl
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2
4
6
8
10
0
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20
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40
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60
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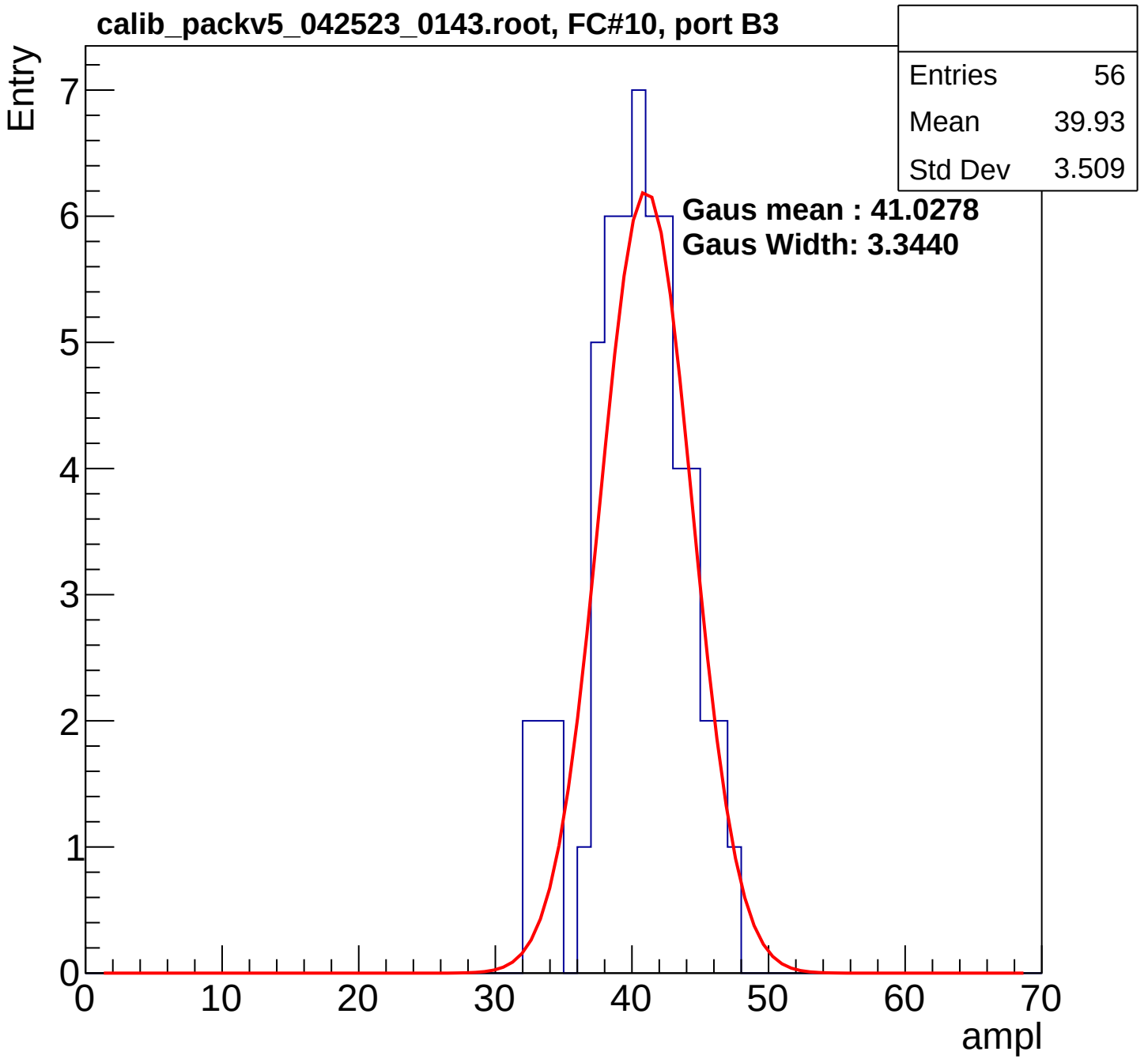
B1L002S, U10-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.93
Std Dev 3.509
Gaus mean : 41.0278
Gaus Width: 3.3440
Entry
ampl
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1
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3
4
5
6
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0
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60
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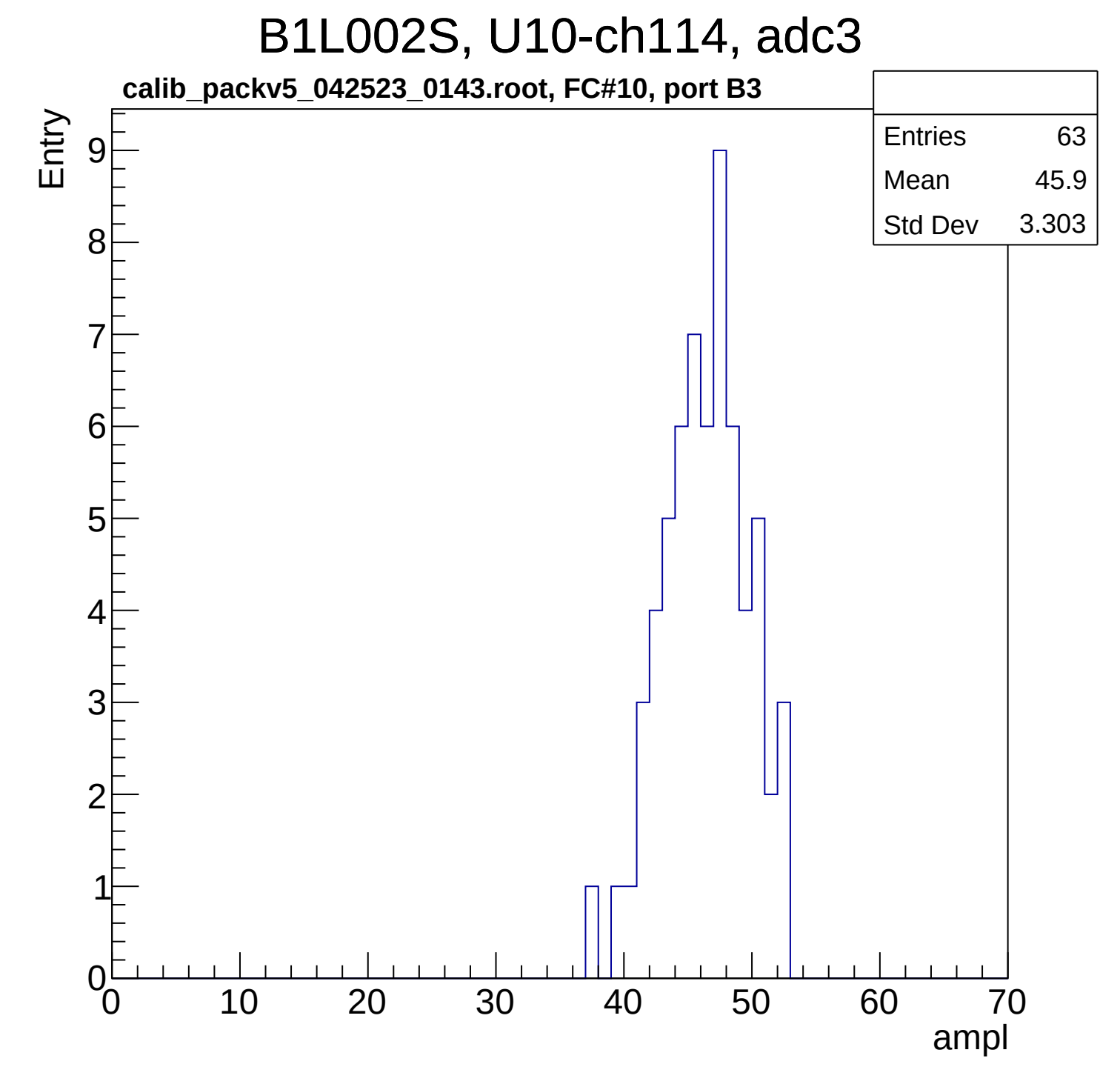
B1L002S, U10-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 45.9
Std Dev 3.303
Entry
ampl
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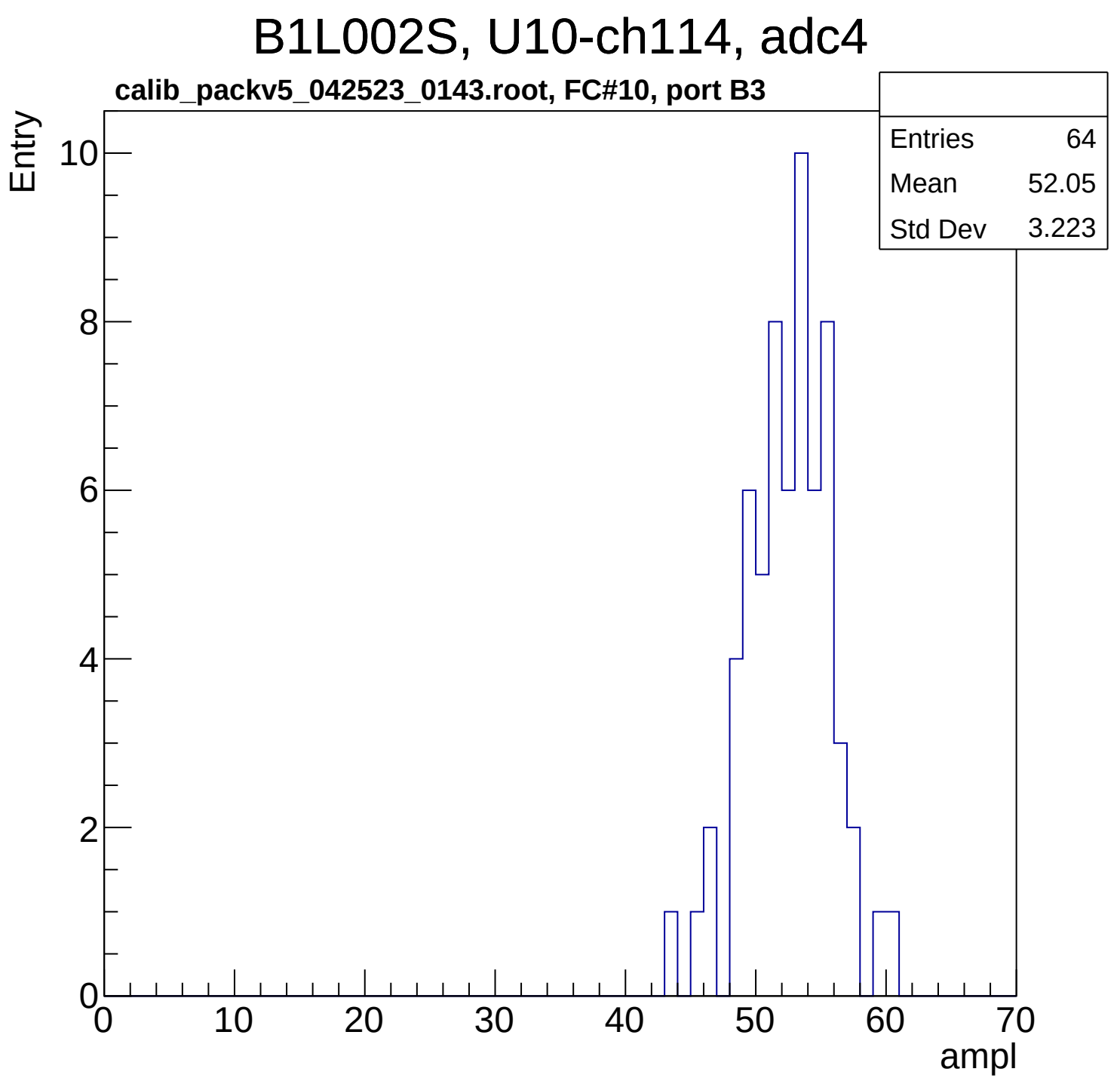
B1L002S, U10-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.05
Std Dev 3.223
Entry
ampl
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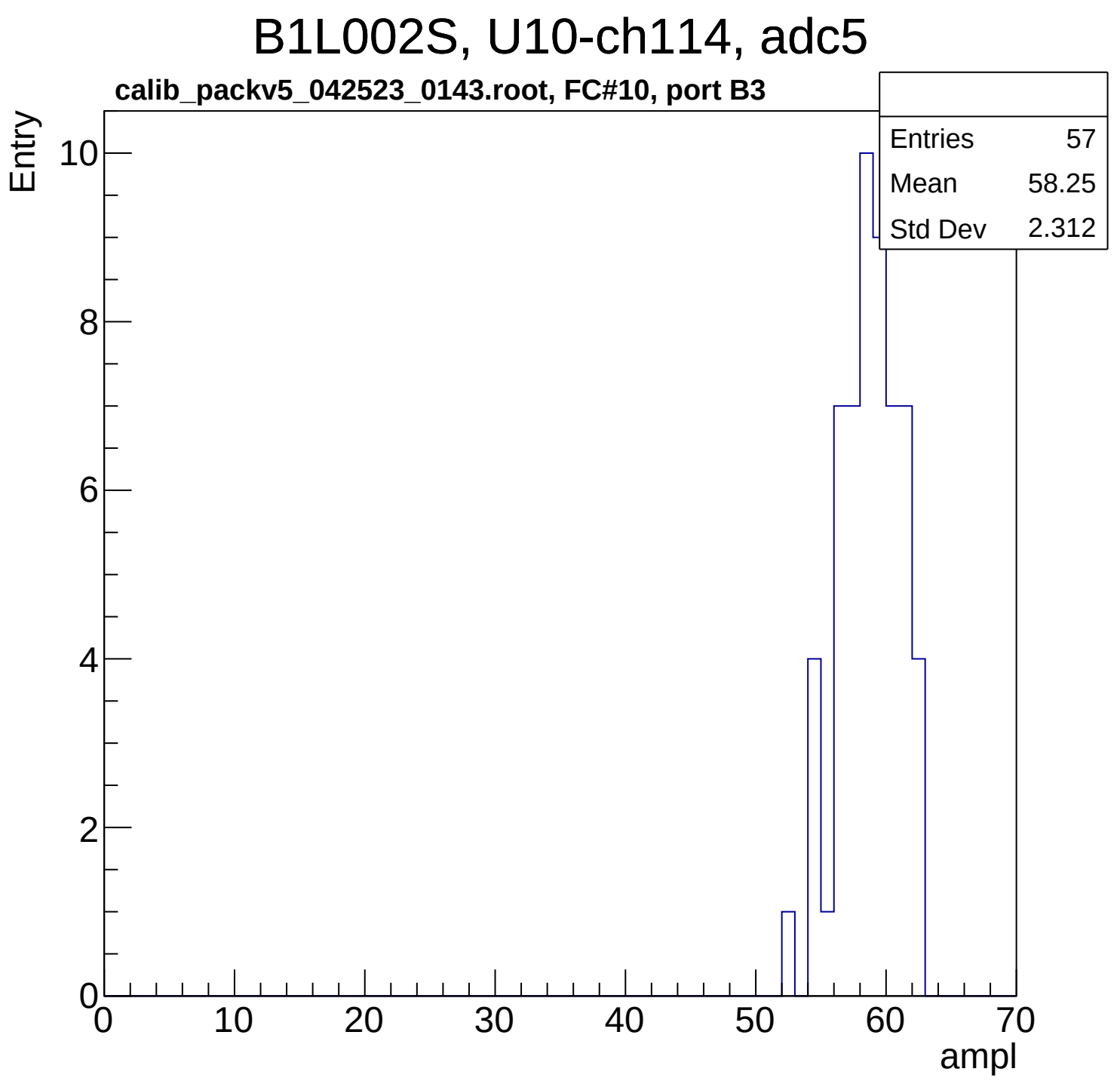
B1L002S, U10-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.25
Std Dev 2.312
Entry
ampl
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2
4
6
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0
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60
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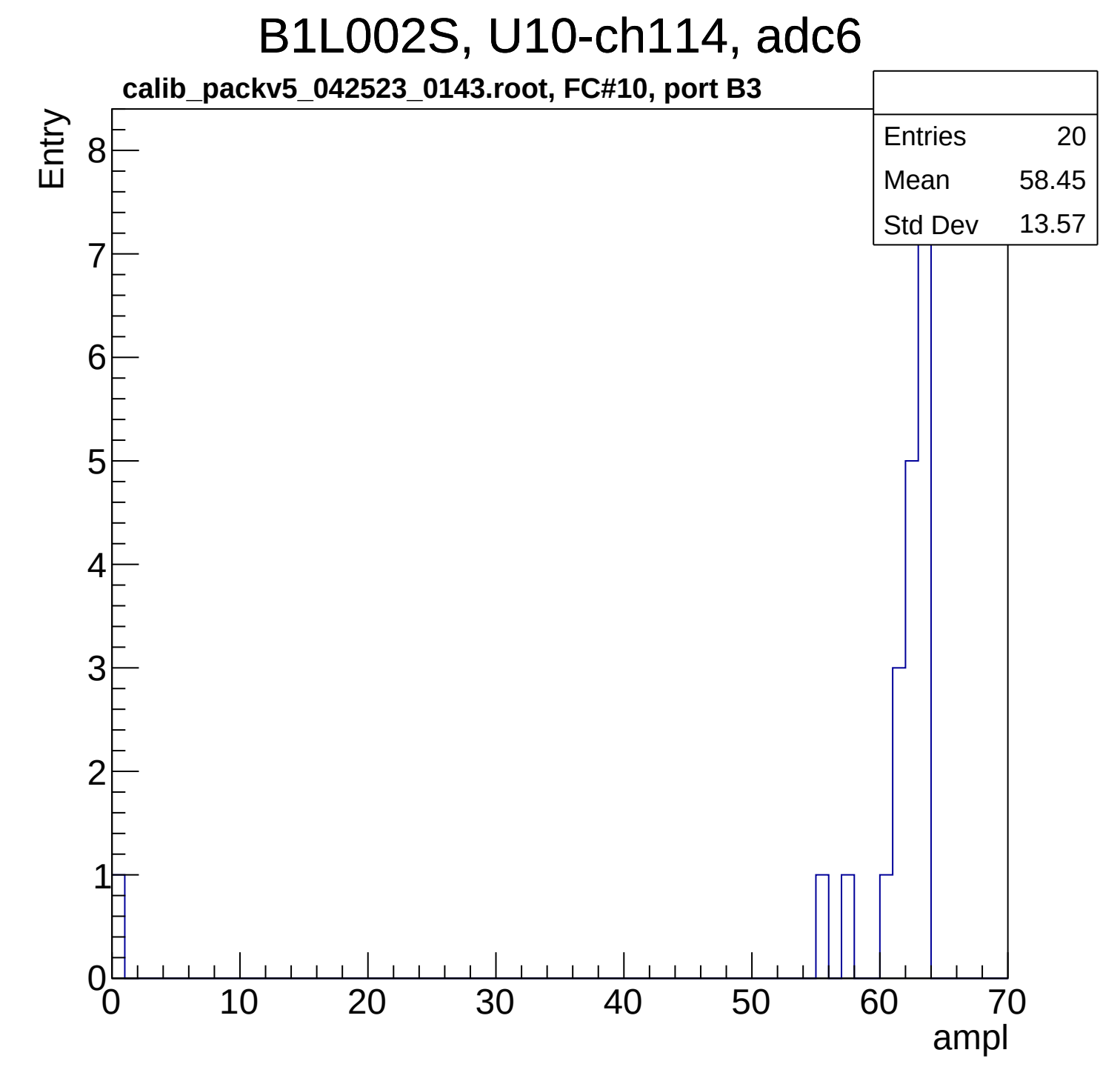
B1L002S, U10-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.45
Std Dev 13.57
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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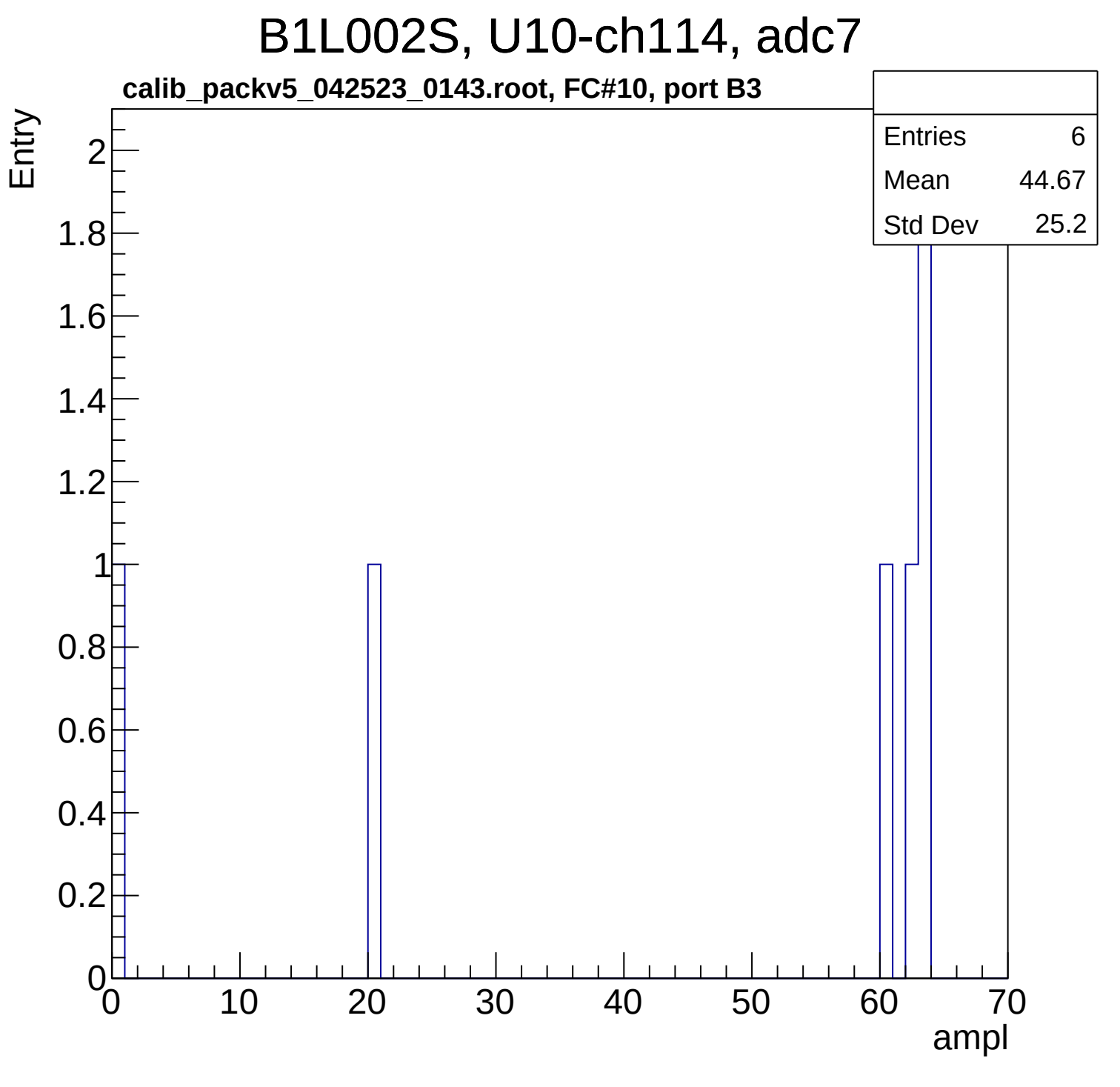
B1L002S, U10-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.67
Std Dev 25.2
Entry
ampl
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0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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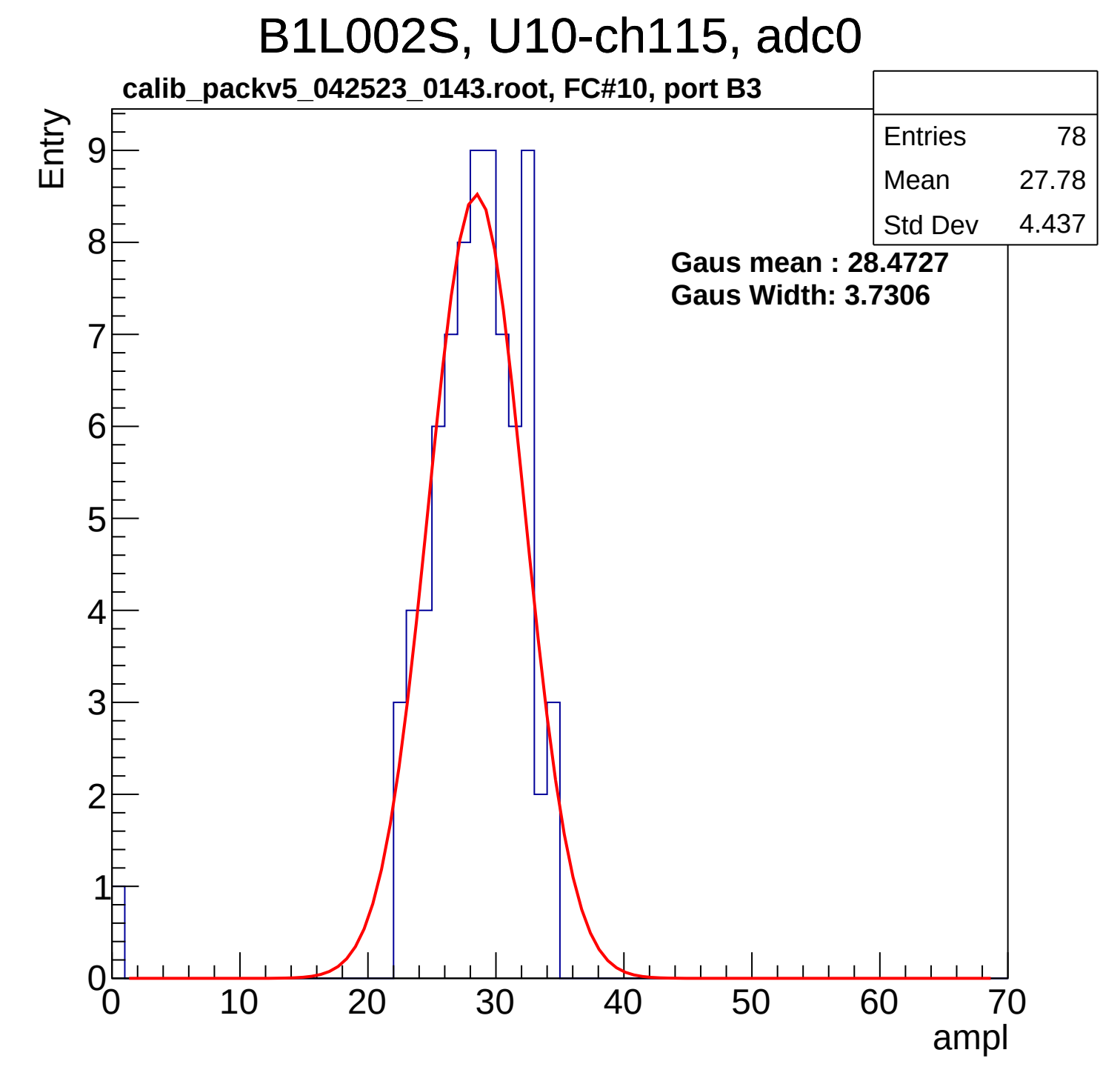
B1L002S, U10-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.78
Std Dev 4.437
Gaus mean : 28.4727
Gaus Width: 3.7306
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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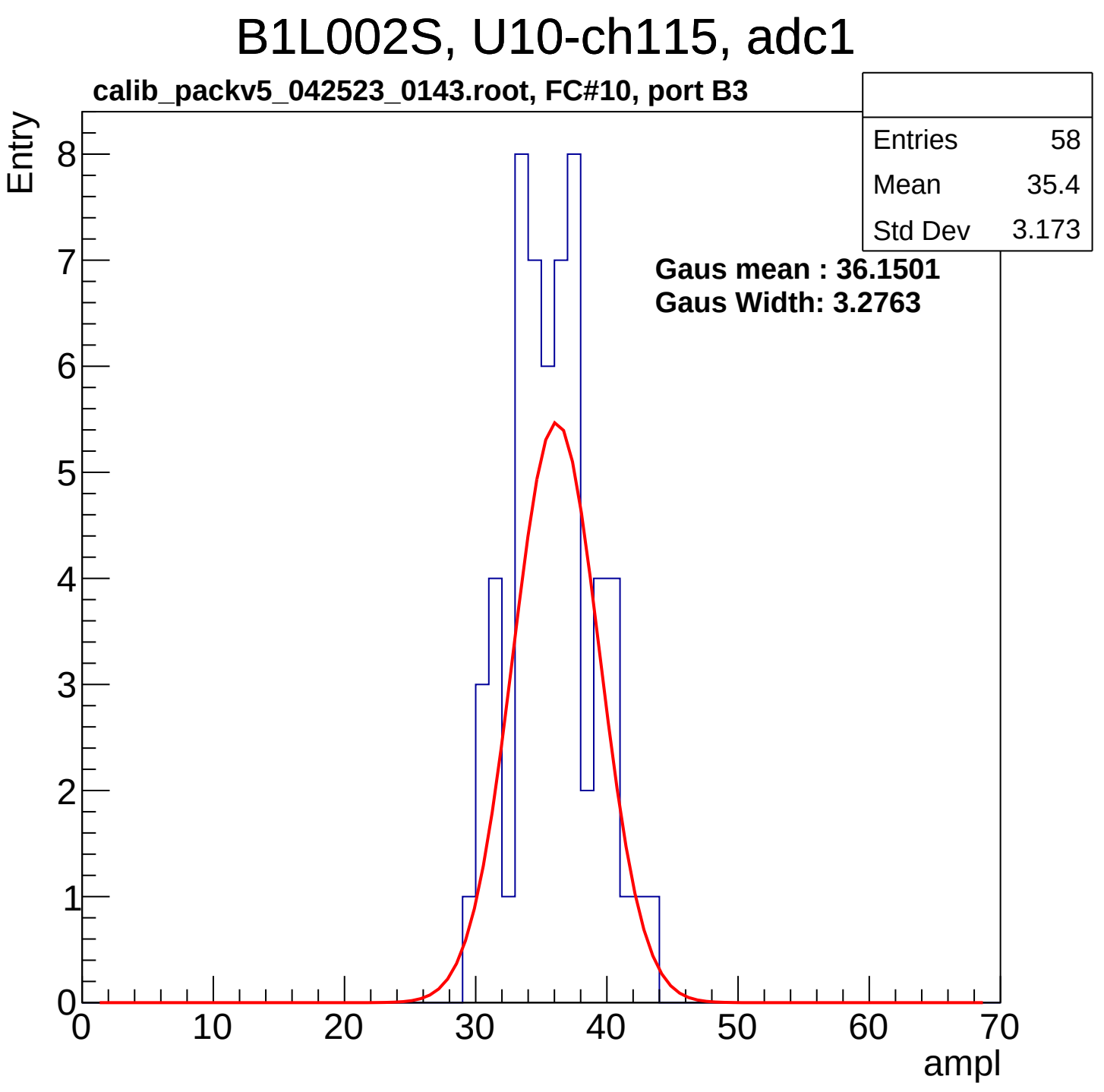
B1L002S, U10-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.4
Std Dev 3.173
Gaus mean : 36.1501
Gaus Width: 3.2763
Entry
ampl
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2
3
4
5
6
7
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0
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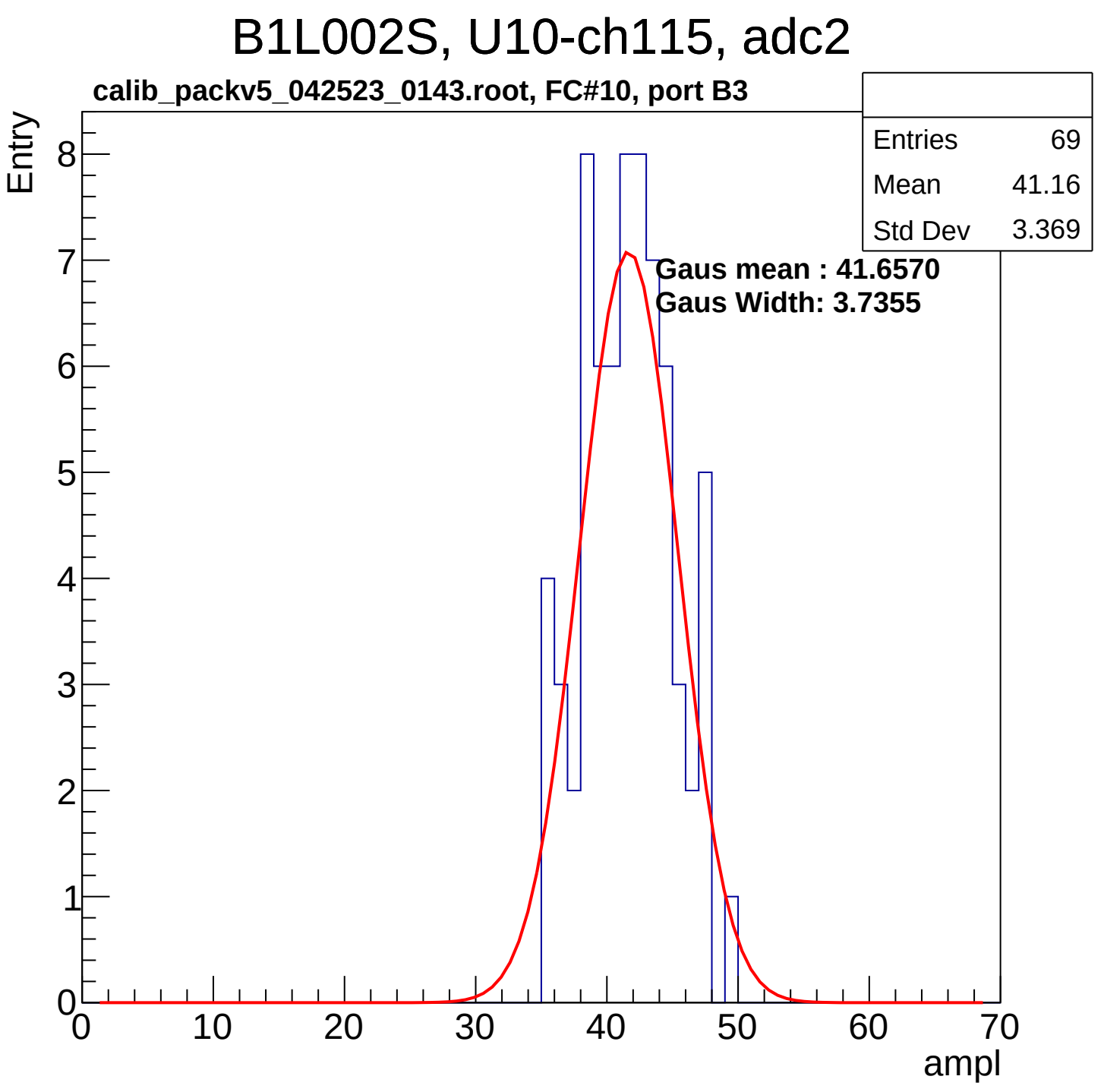
B1L002S, U10-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.16
Std Dev 3.369
Gaus mean : 41.6570
Gaus Width: 3.7355
Entry
ampl
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2
3
4
5
6
7
8
0
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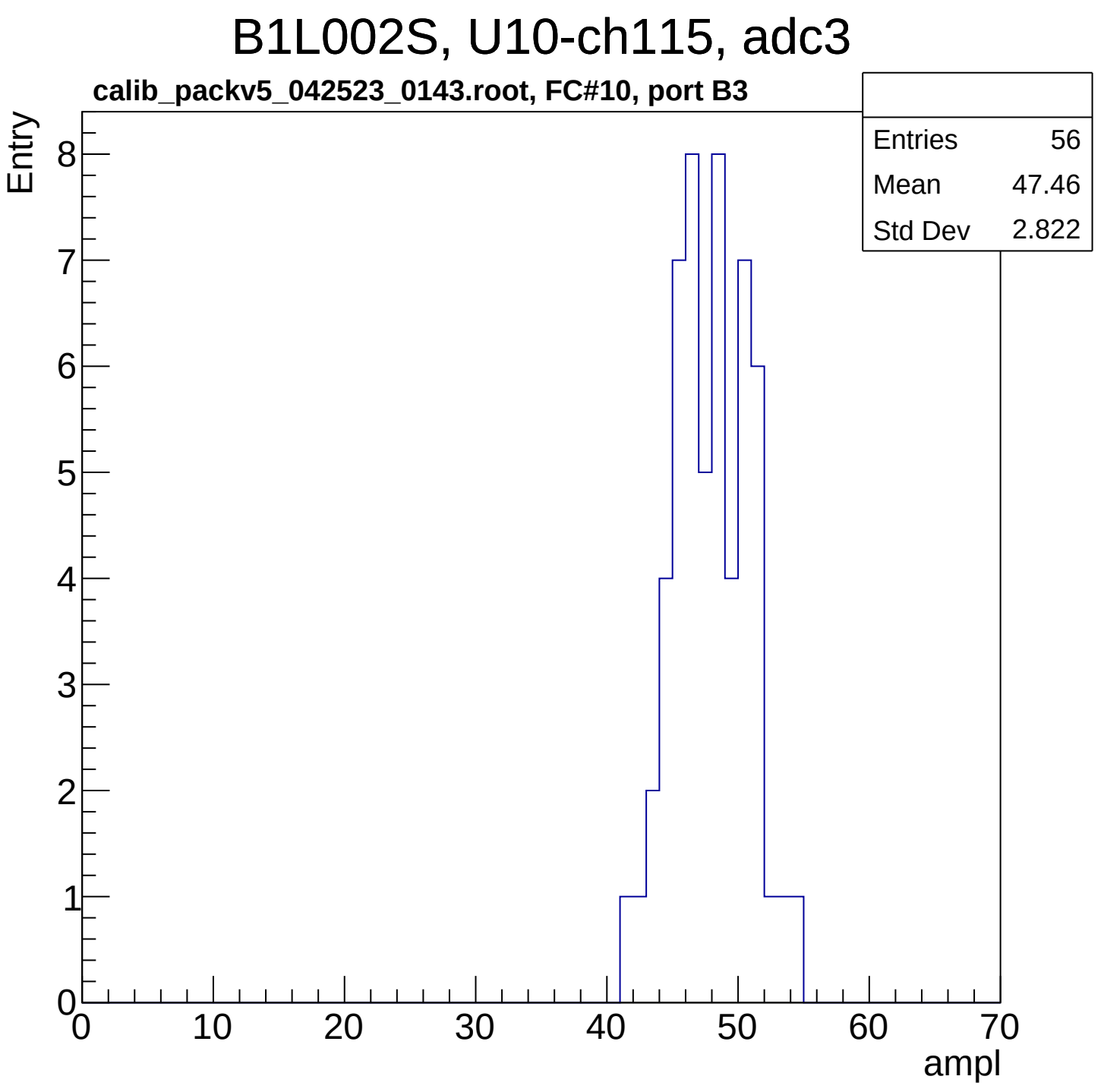
B1L002S, U10-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.46
Std Dev 2.822
Entry
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2
3
4
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10
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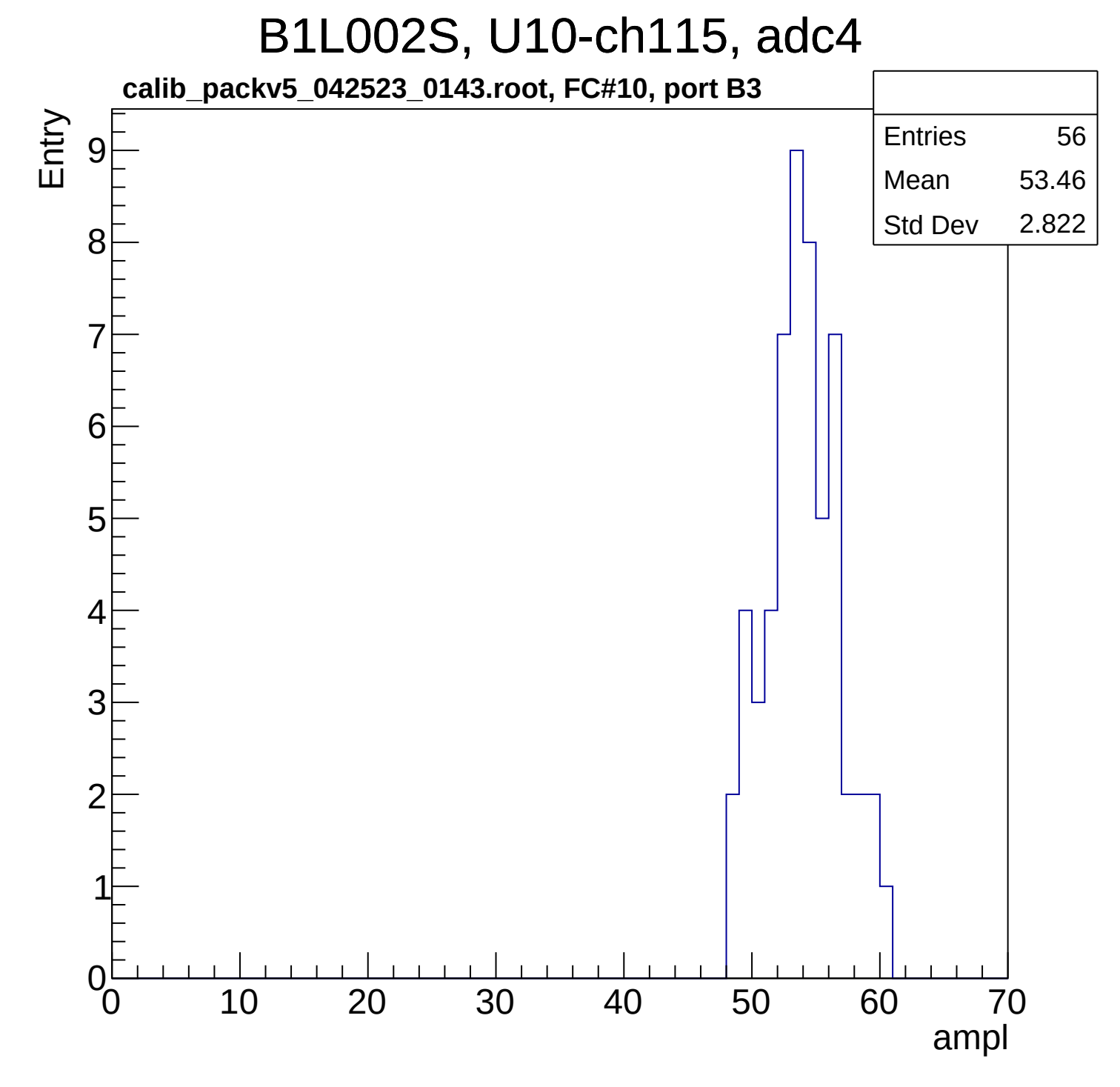
50
60
70
ampl


B1L002S, U10-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.46
Std Dev 2.822
Entry
ampl
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2
3
4
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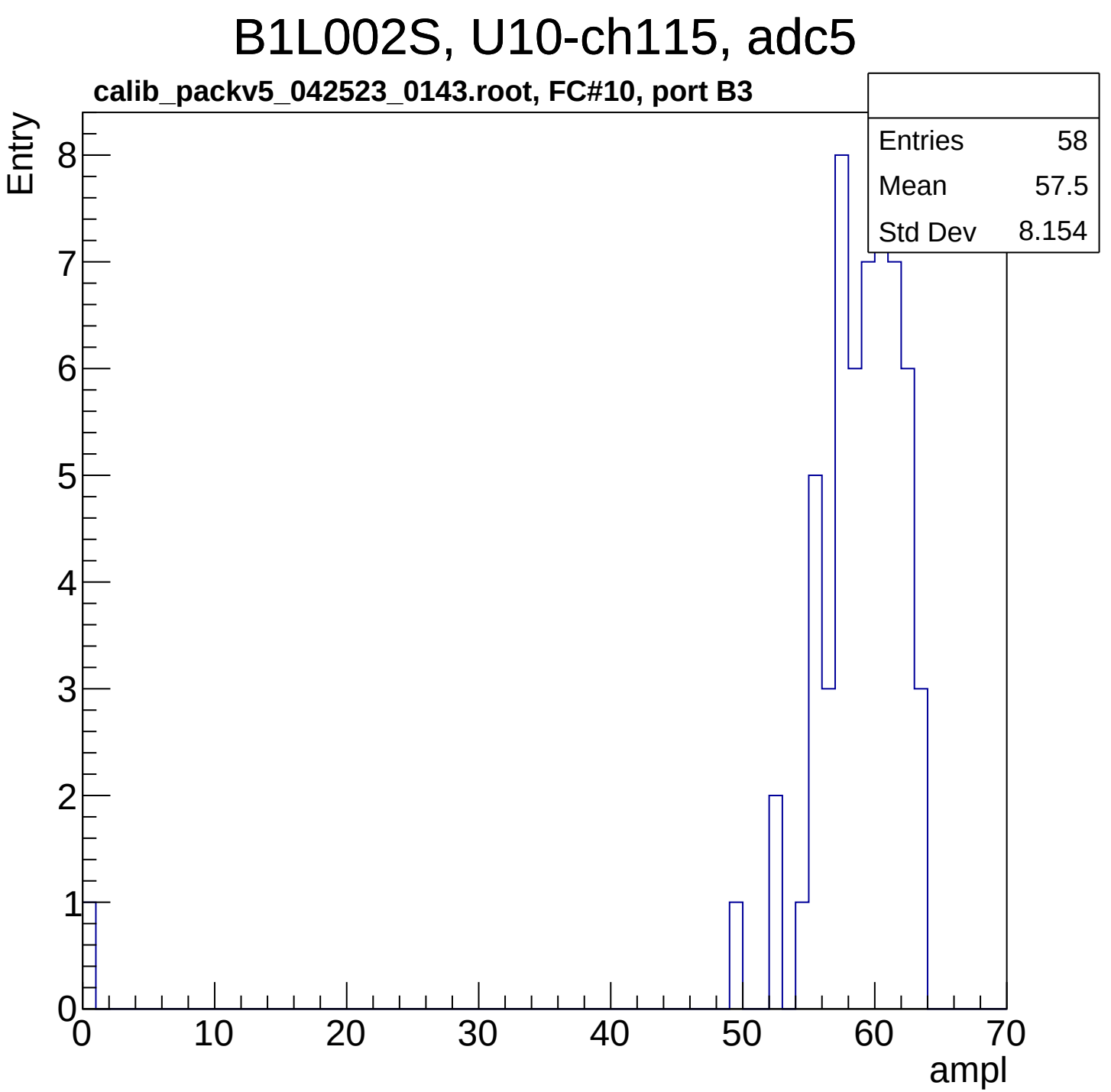
B1L002S, U10-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.5
Std Dev 8.154
Entry
ampl
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4
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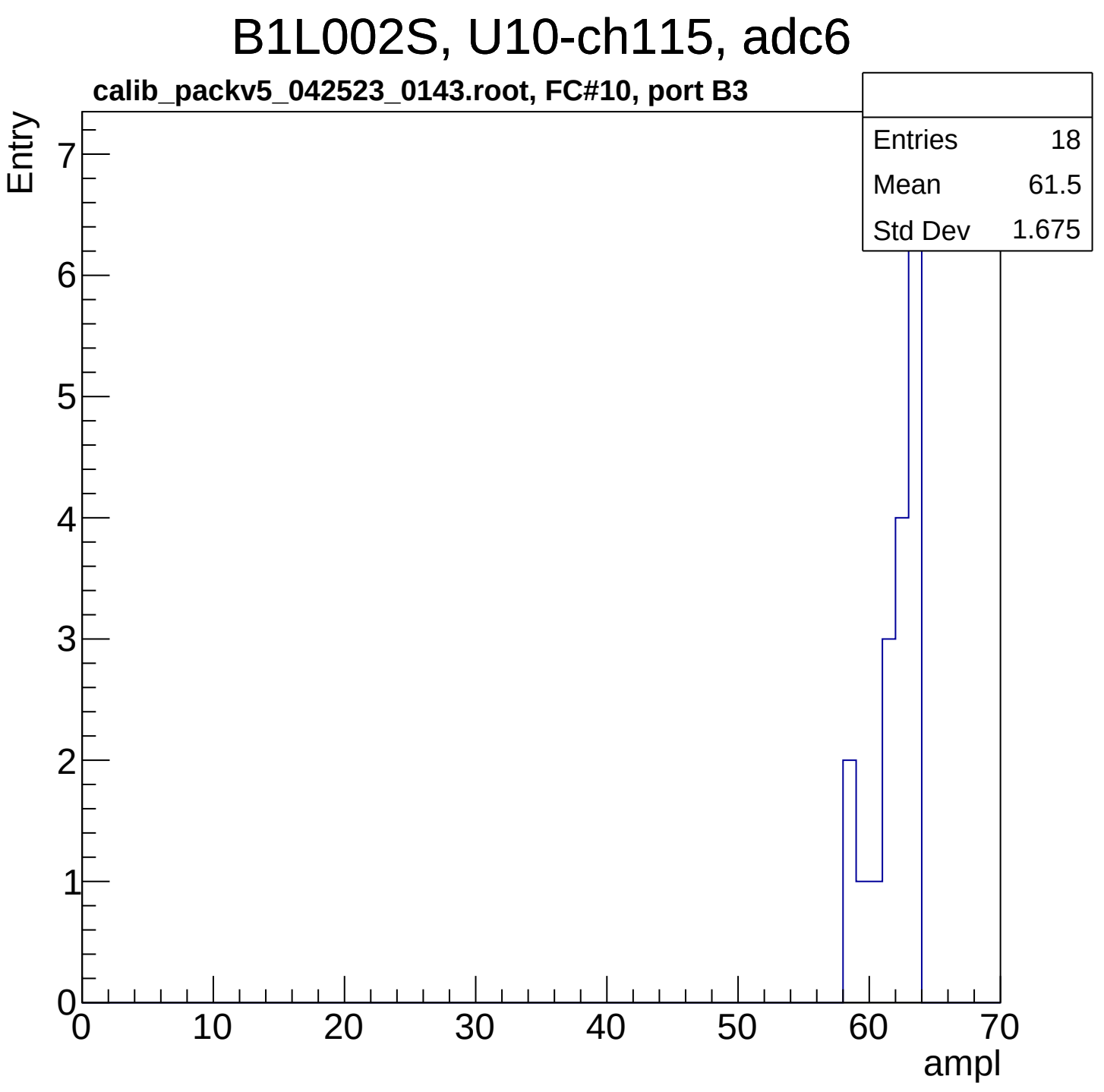
B1L002S, U10-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.5
Std Dev 1.675
Entry
ampl
0
1
2
3
4
5
6
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0
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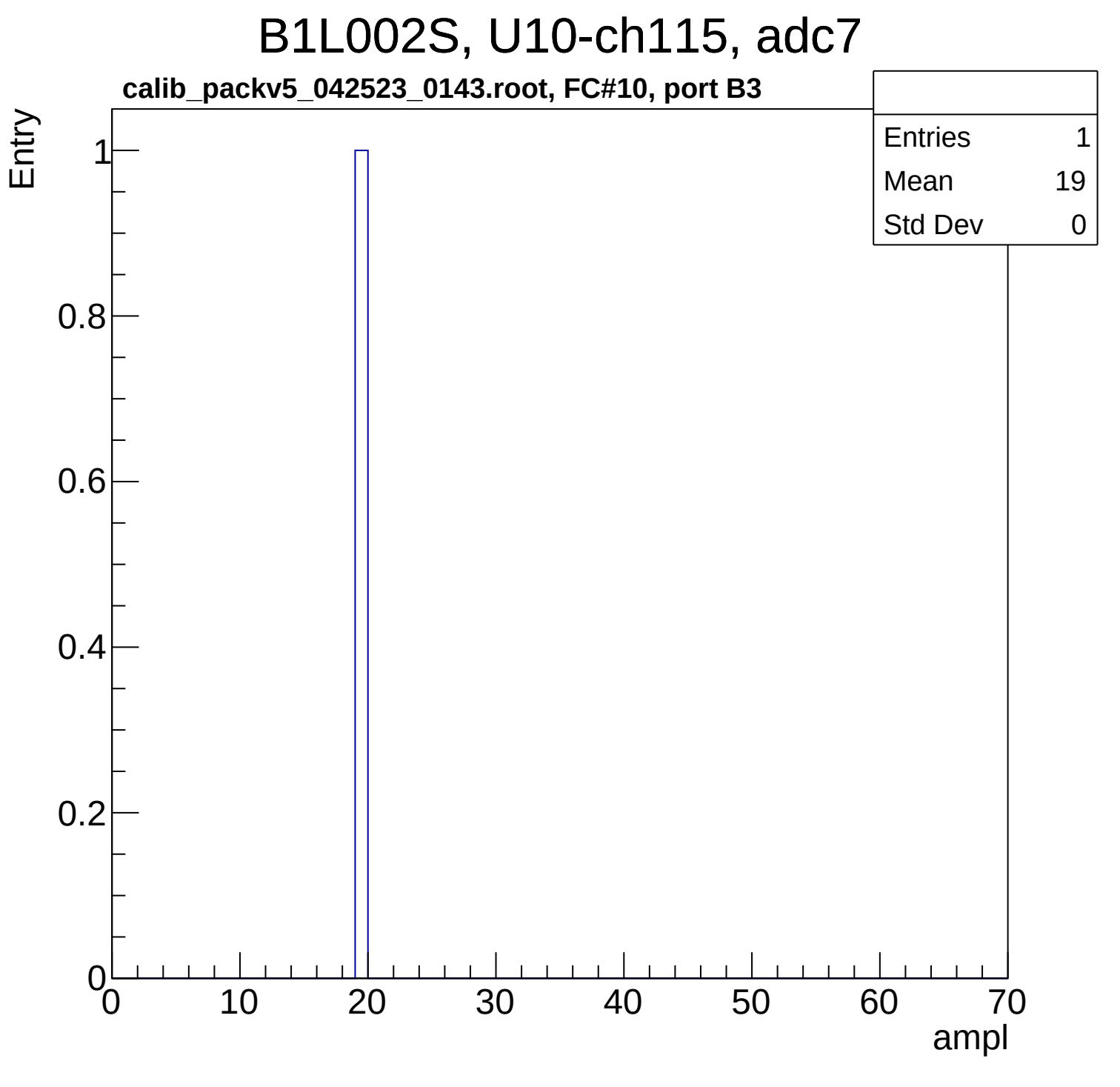
B1L002S, U10-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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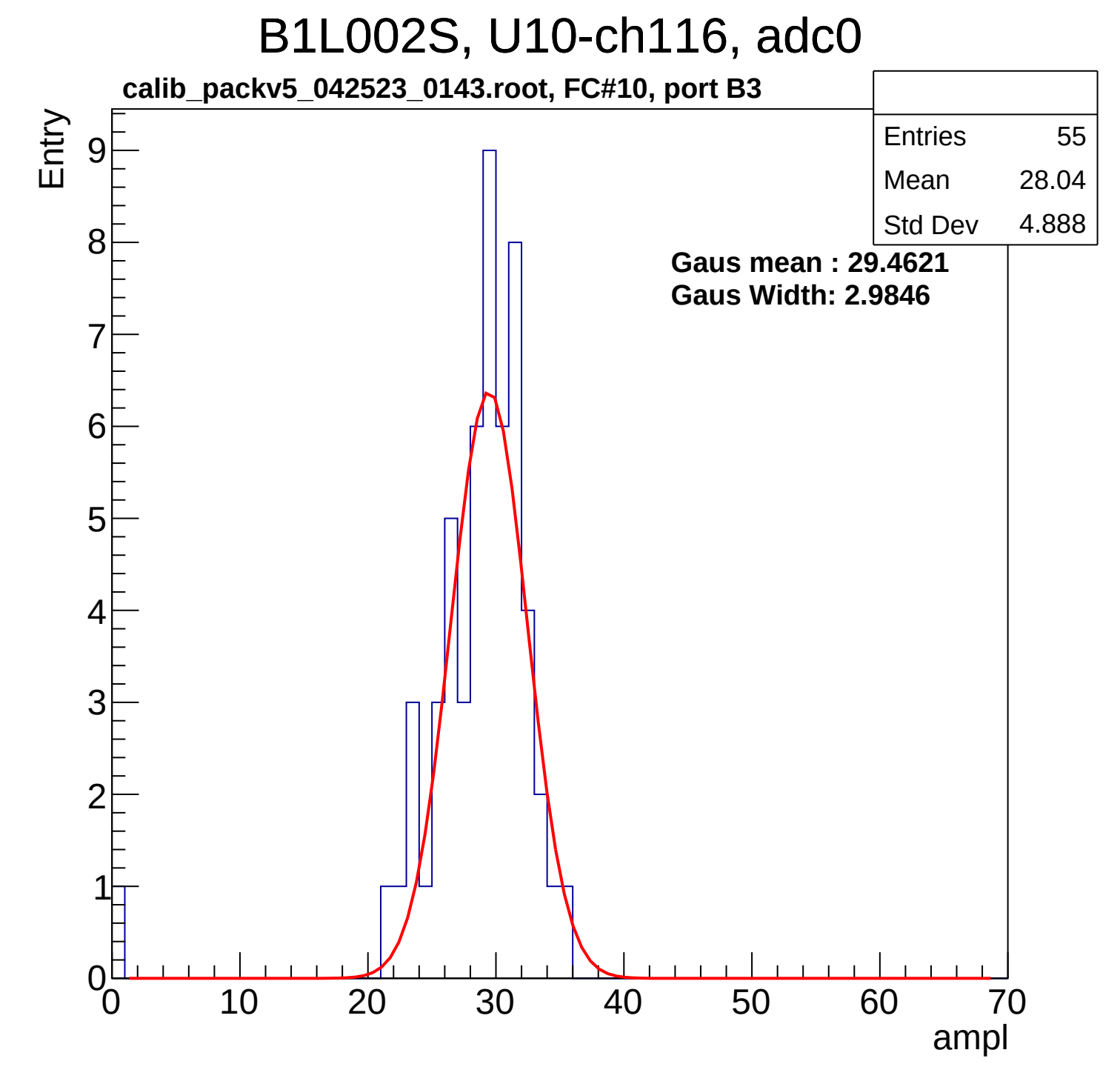
50
60
70
ampl


B1L002S, U10-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.04
Std Dev 4.888
Gaus mean : 29.4621
Gaus Width: 2.9846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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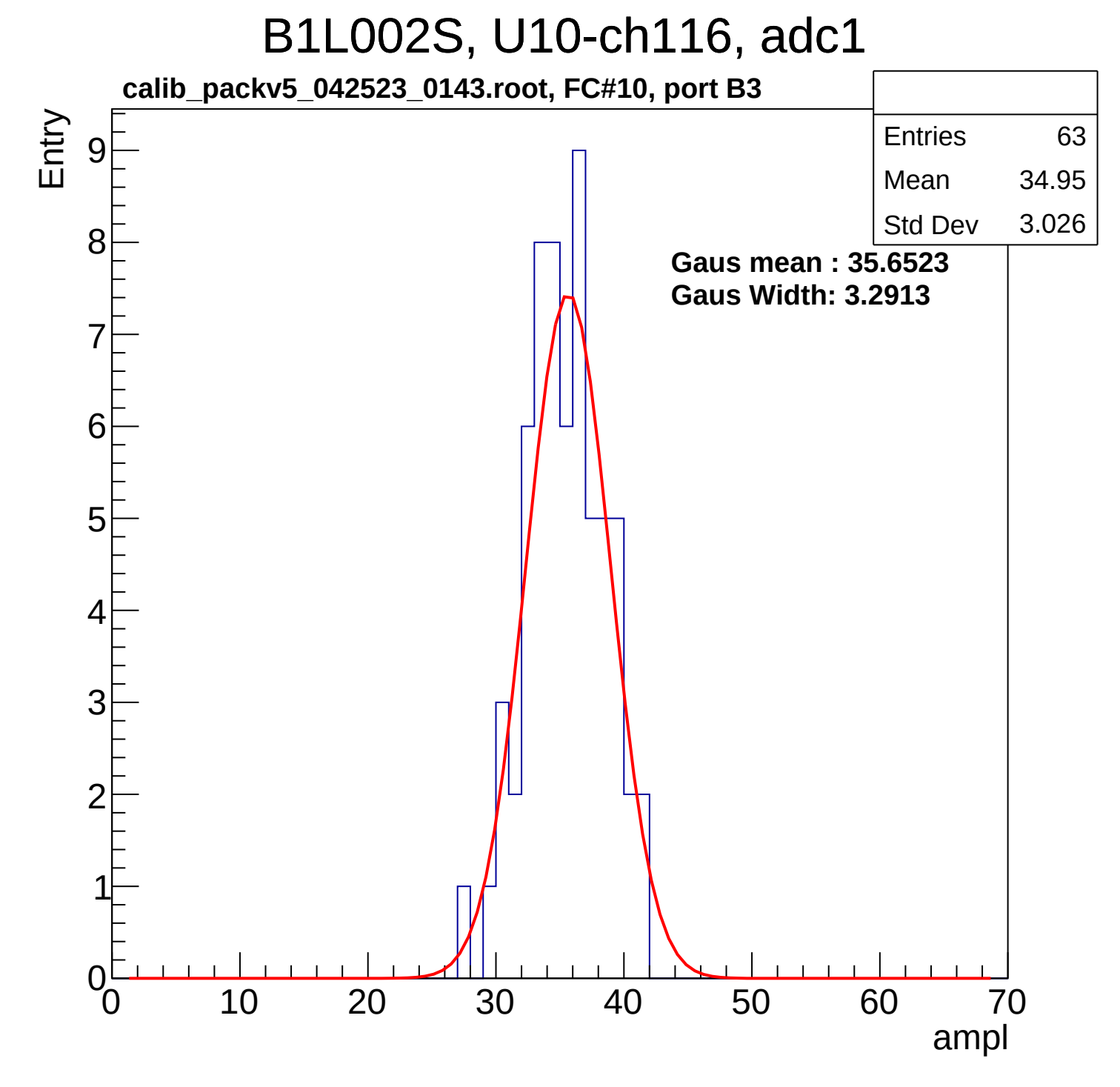
B1L002S, U10-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.95
Std Dev 3.026
Gaus mean : 35.6523
Gaus Width: 3.2913
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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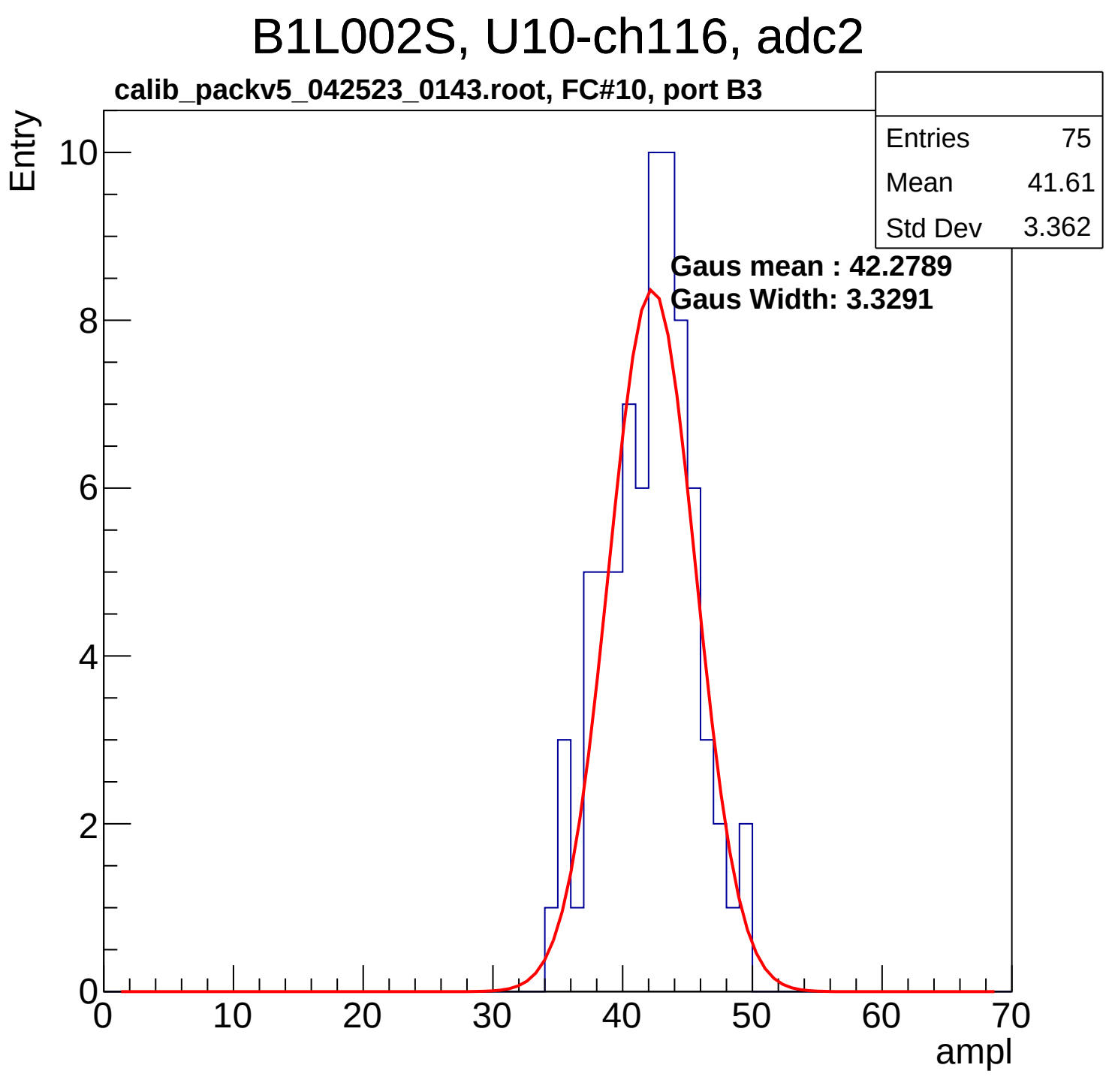
B1L002S, U10-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 41.61
Std Dev 3.362
Gaus mean : 42.2789
Gaus Width: 3.3291
Entry
ampl
0
2
4
6
8
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0
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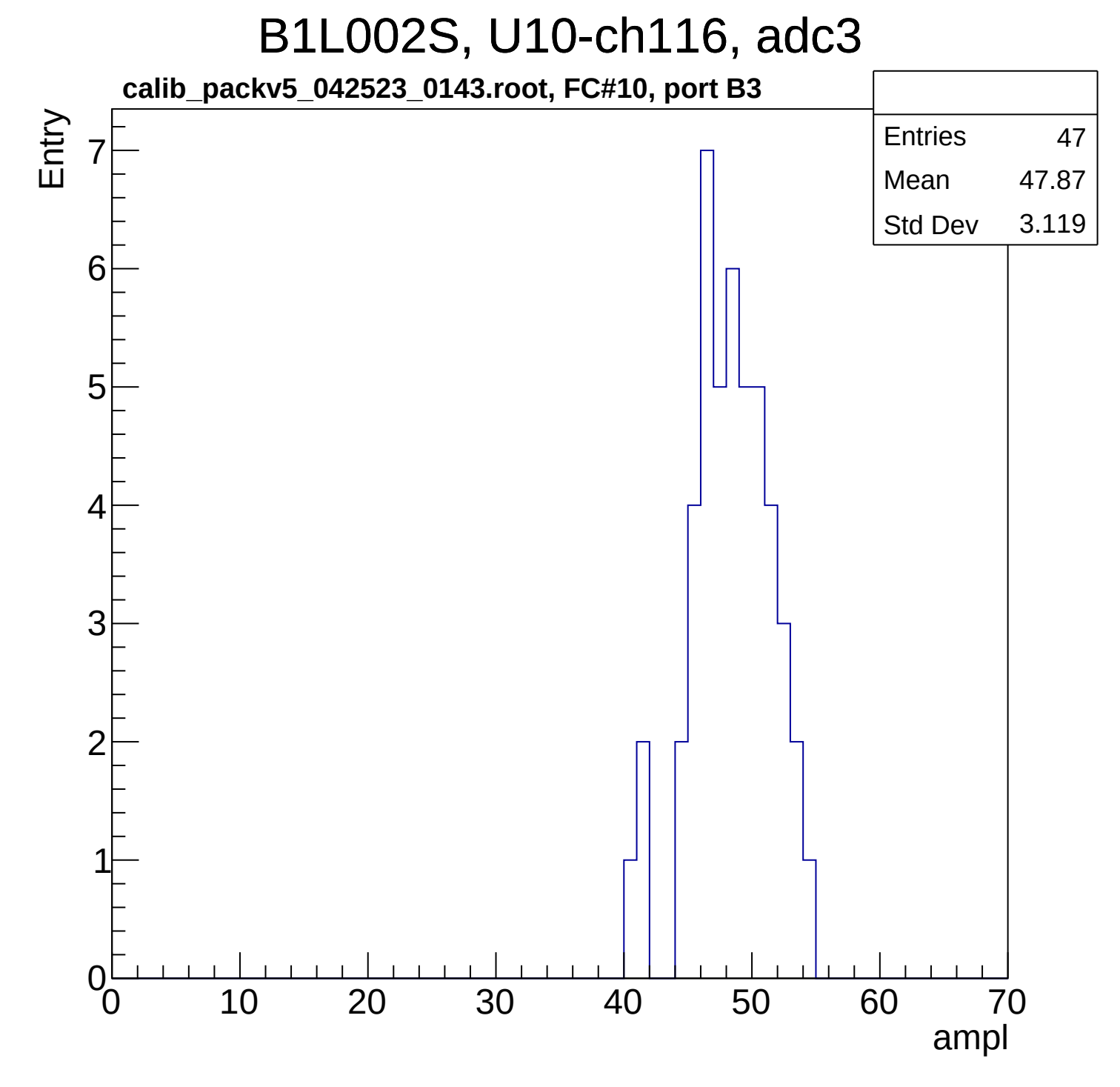
B1L002S, U10-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.87
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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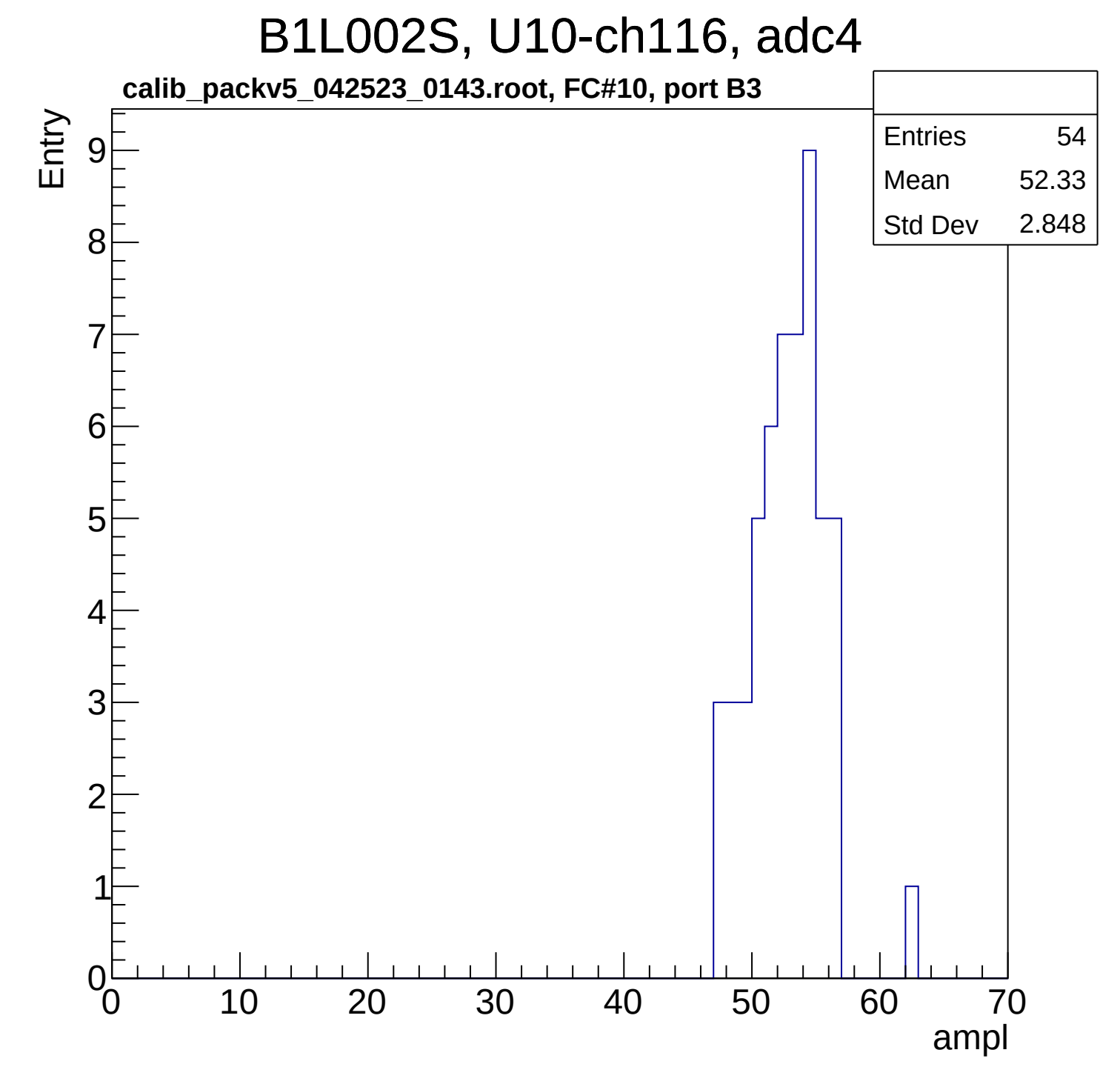
B1L002S, U10-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.33
Std Dev 2.848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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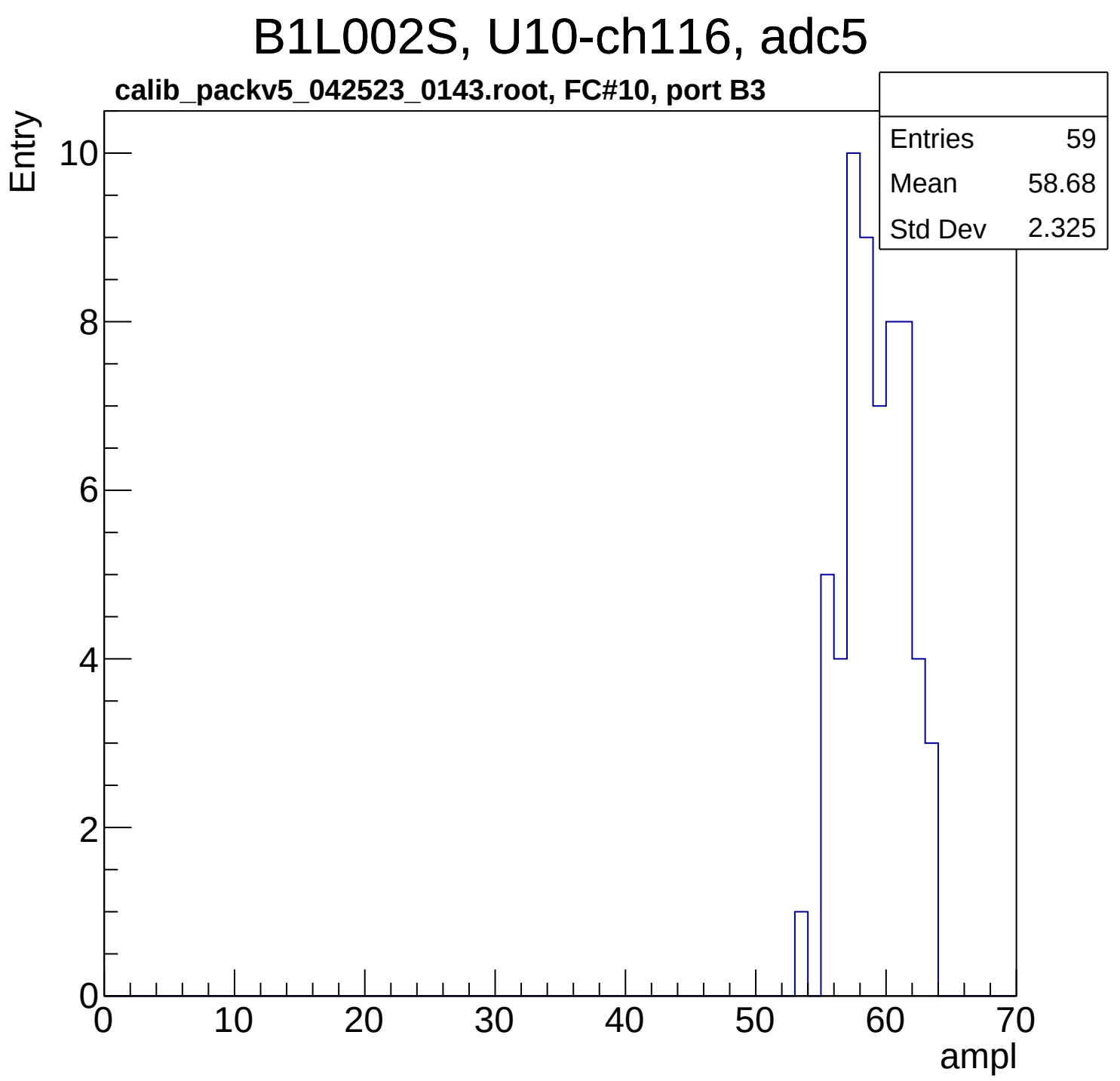
B1L002S, U10-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.68
Std Dev 2.325
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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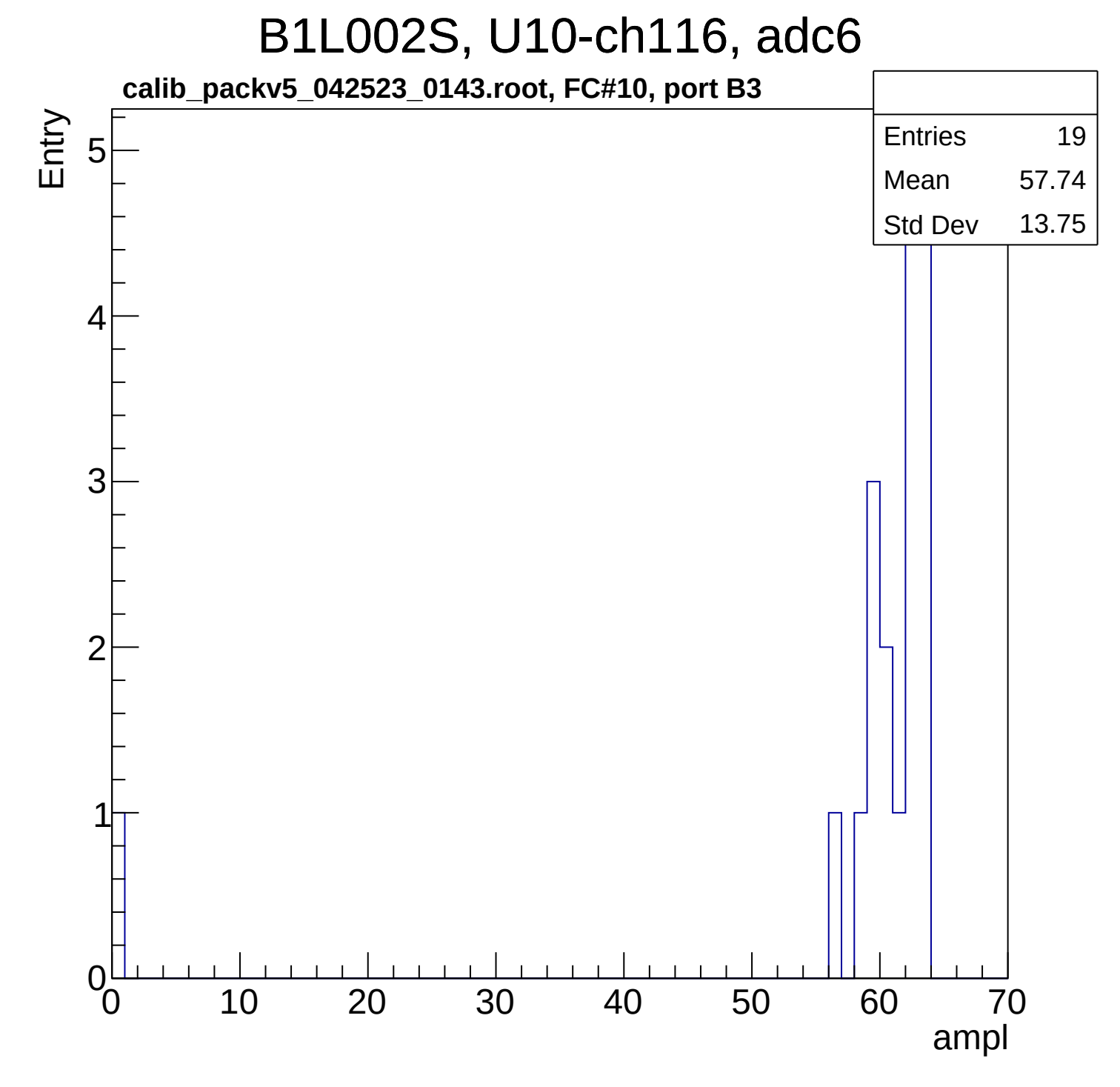
B1L002S, U10-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.74
Std Dev 13.75
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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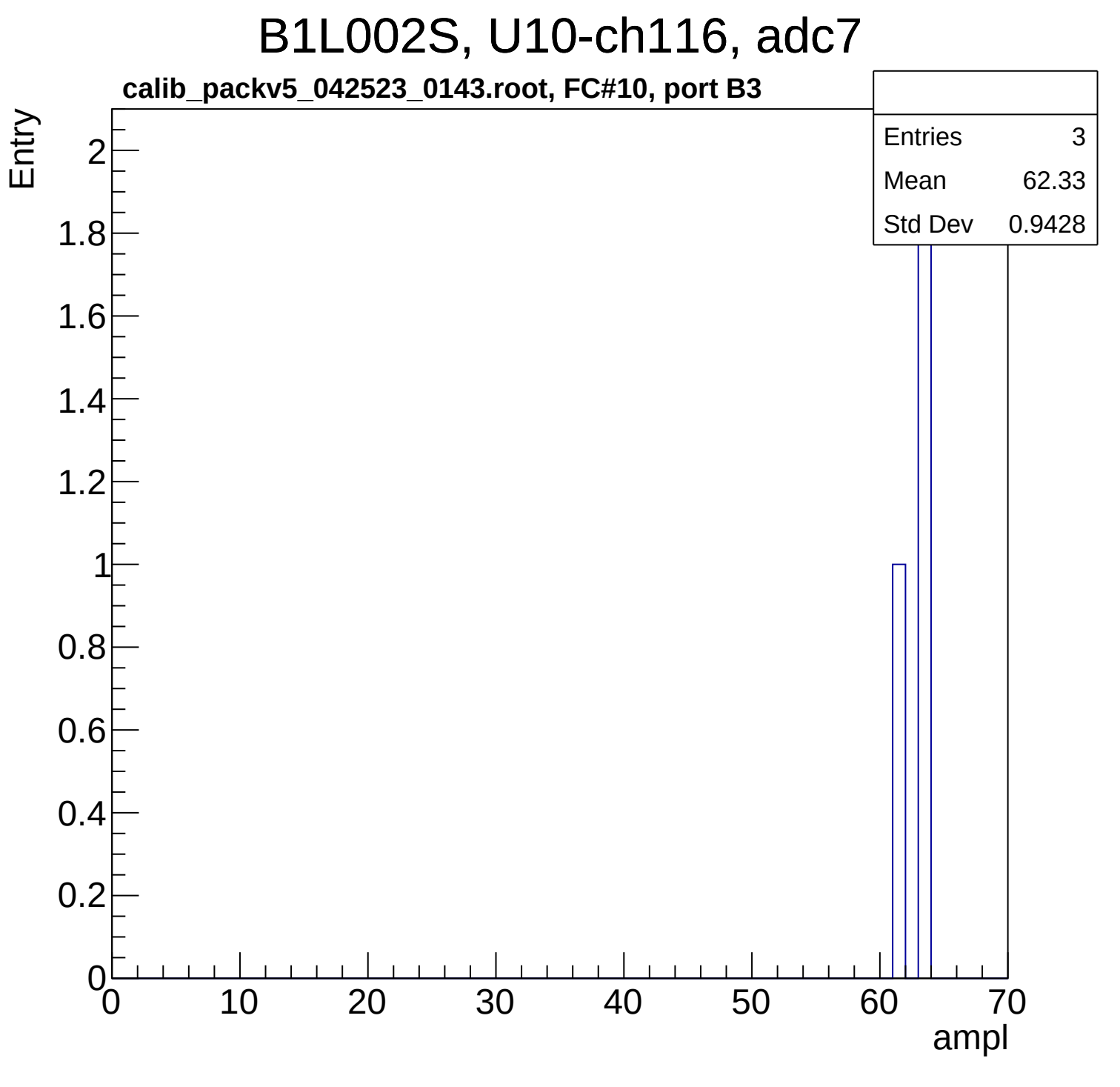
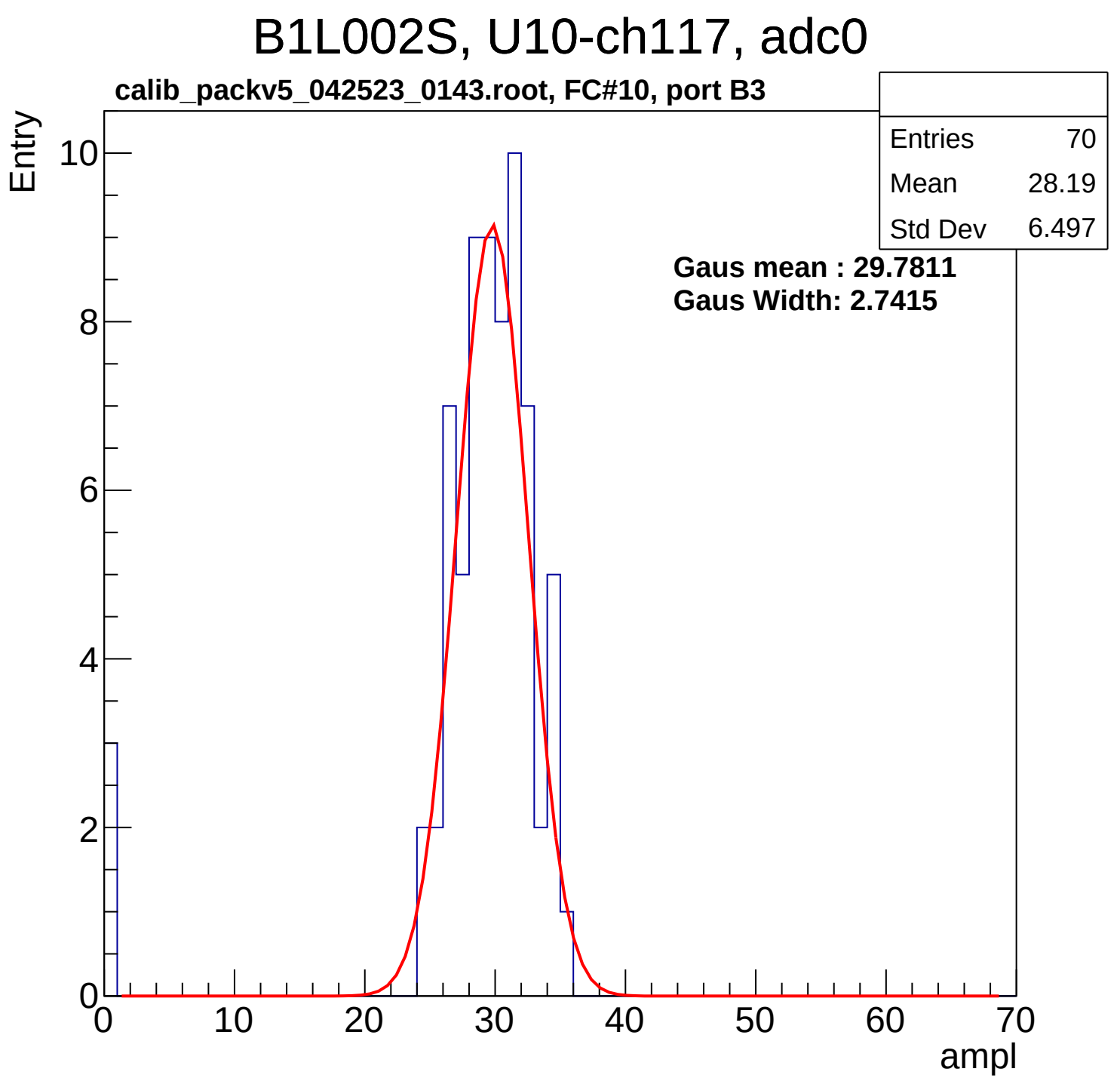

B1L002S, U10-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U10-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.19
Std Dev 6.497
Gaus mean : 29.7811
Gaus Width: 2.7415
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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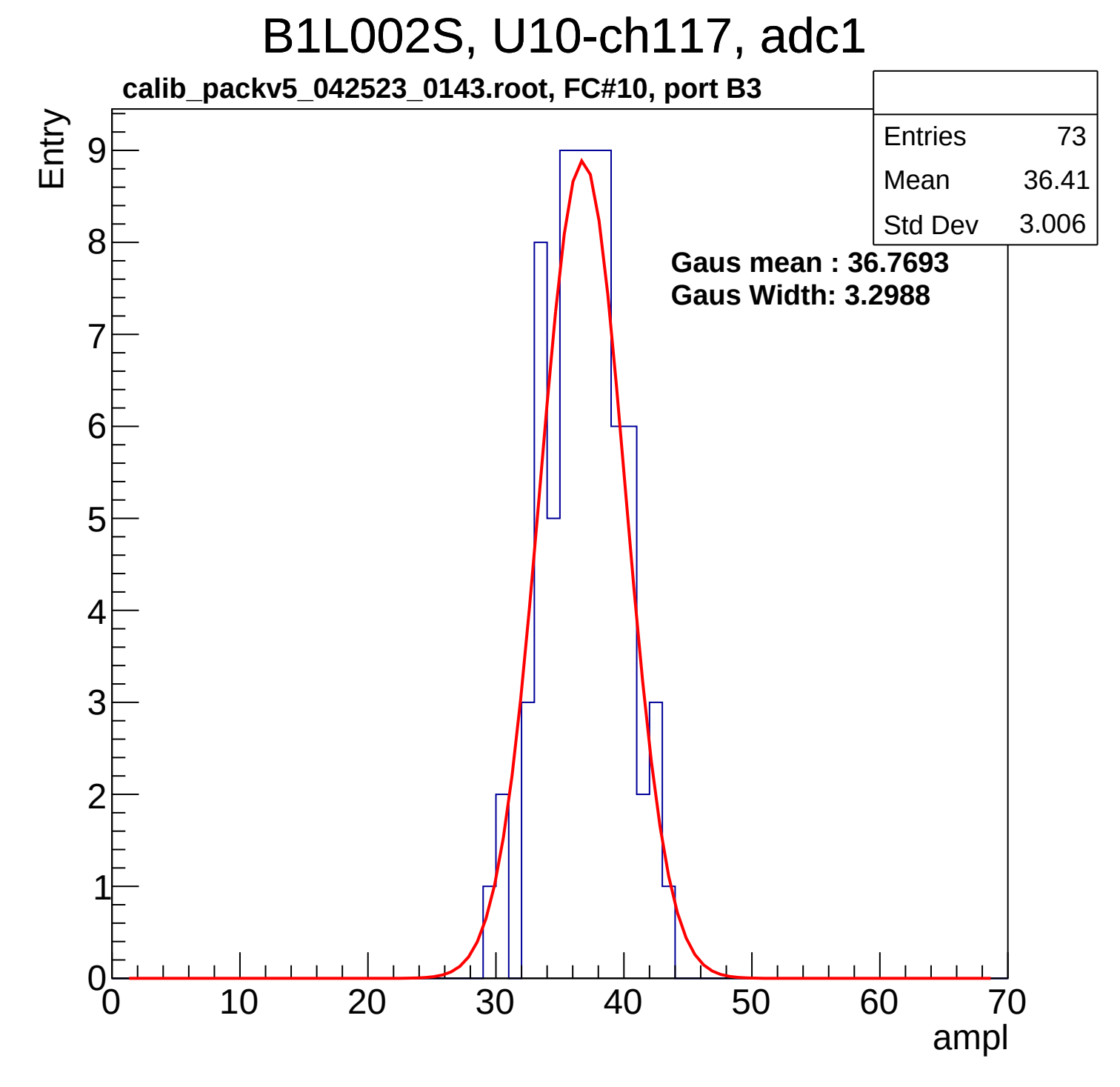
B1L002S, U10-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.41
Std Dev 3.006
Gaus mean : 36.7693
Gaus Width: 3.2988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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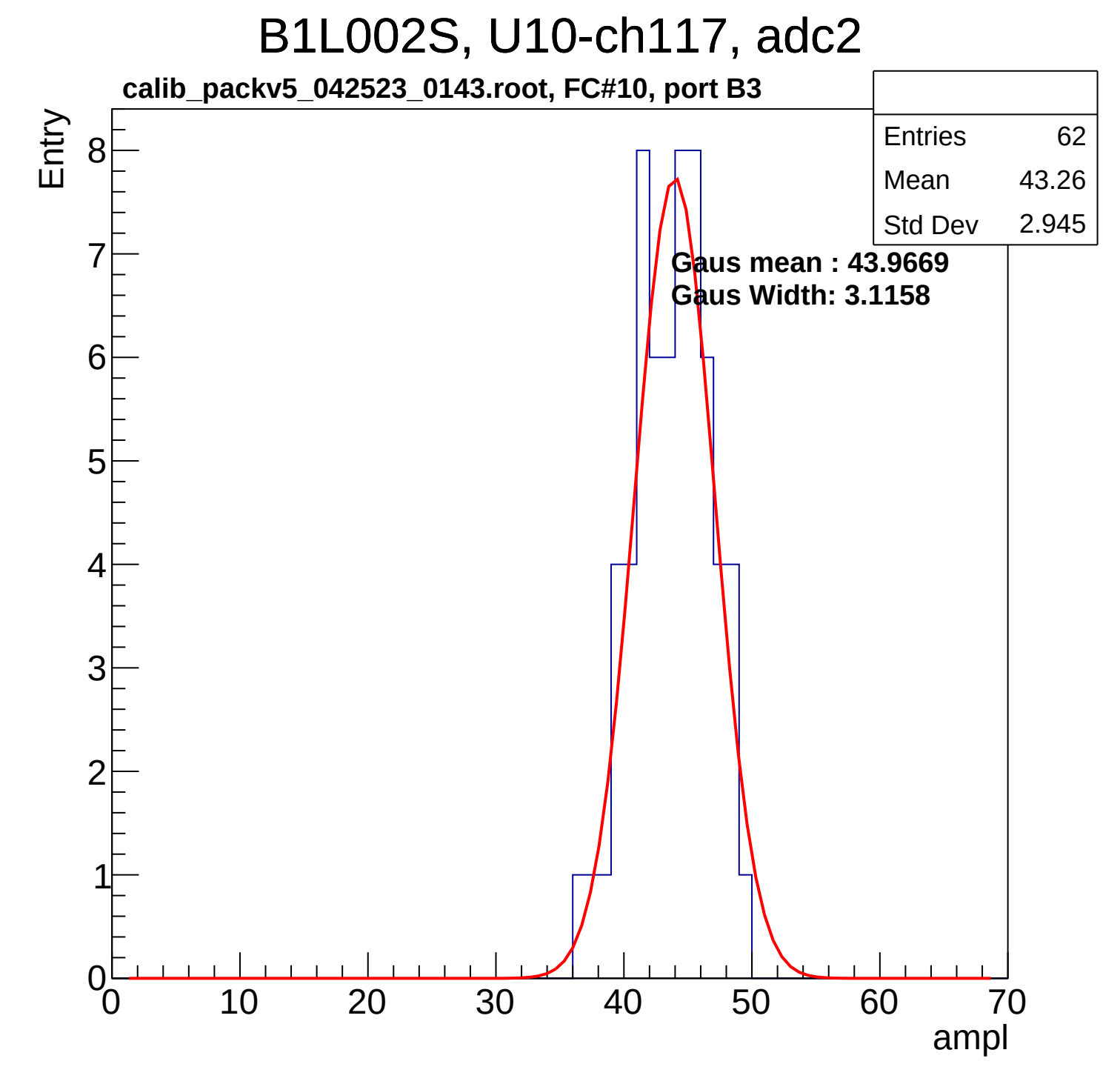
B1L002S, U10-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 43.26
Std Dev 2.945
Gaus mean : 43.9669
Gaus Width: 3.1158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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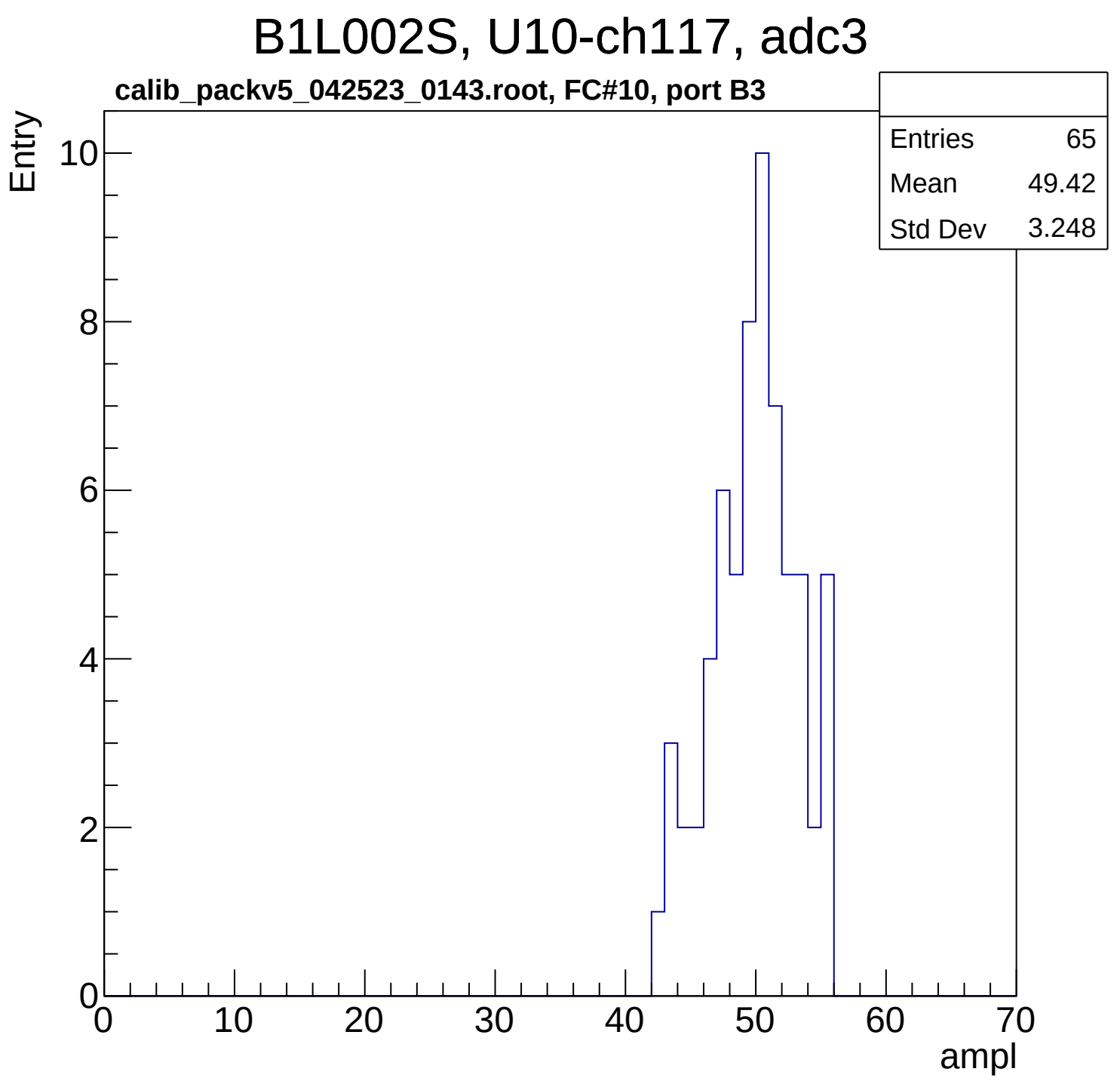
B1L002S, U10-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 49.42
Std Dev 3.248
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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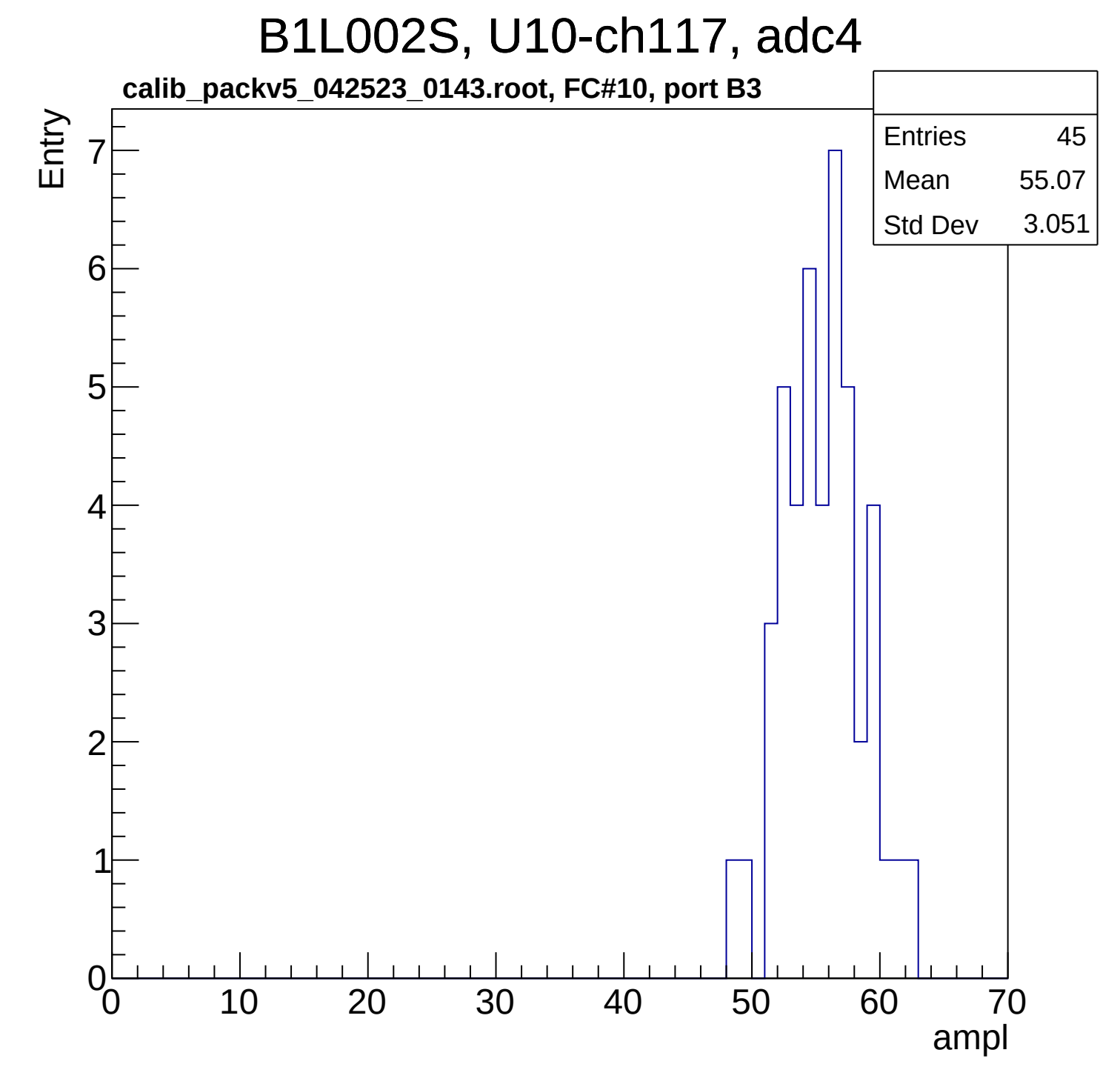
B1L002S, U10-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 55.07
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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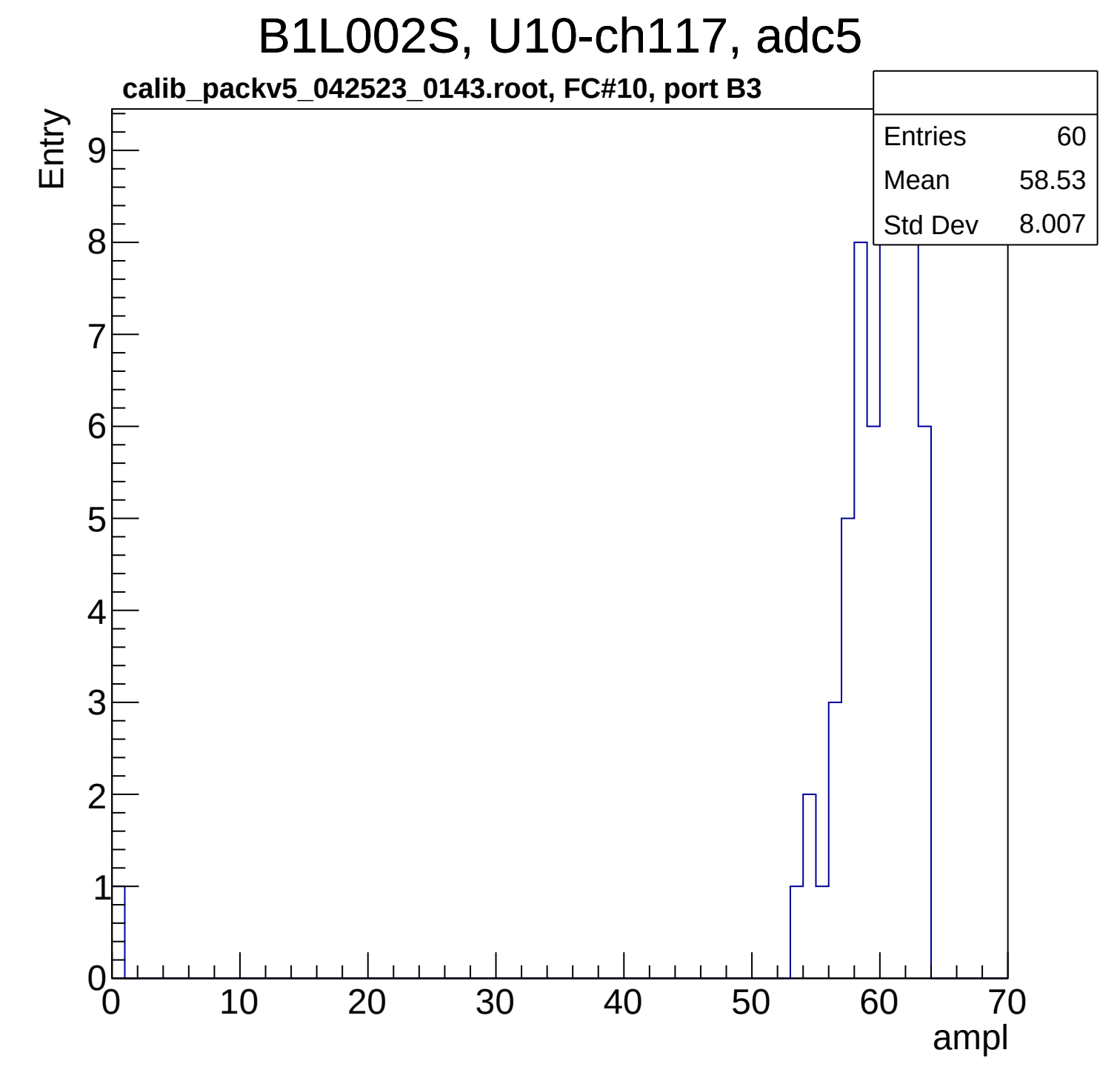
B1L002S, U10-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.53
Std Dev 8.007
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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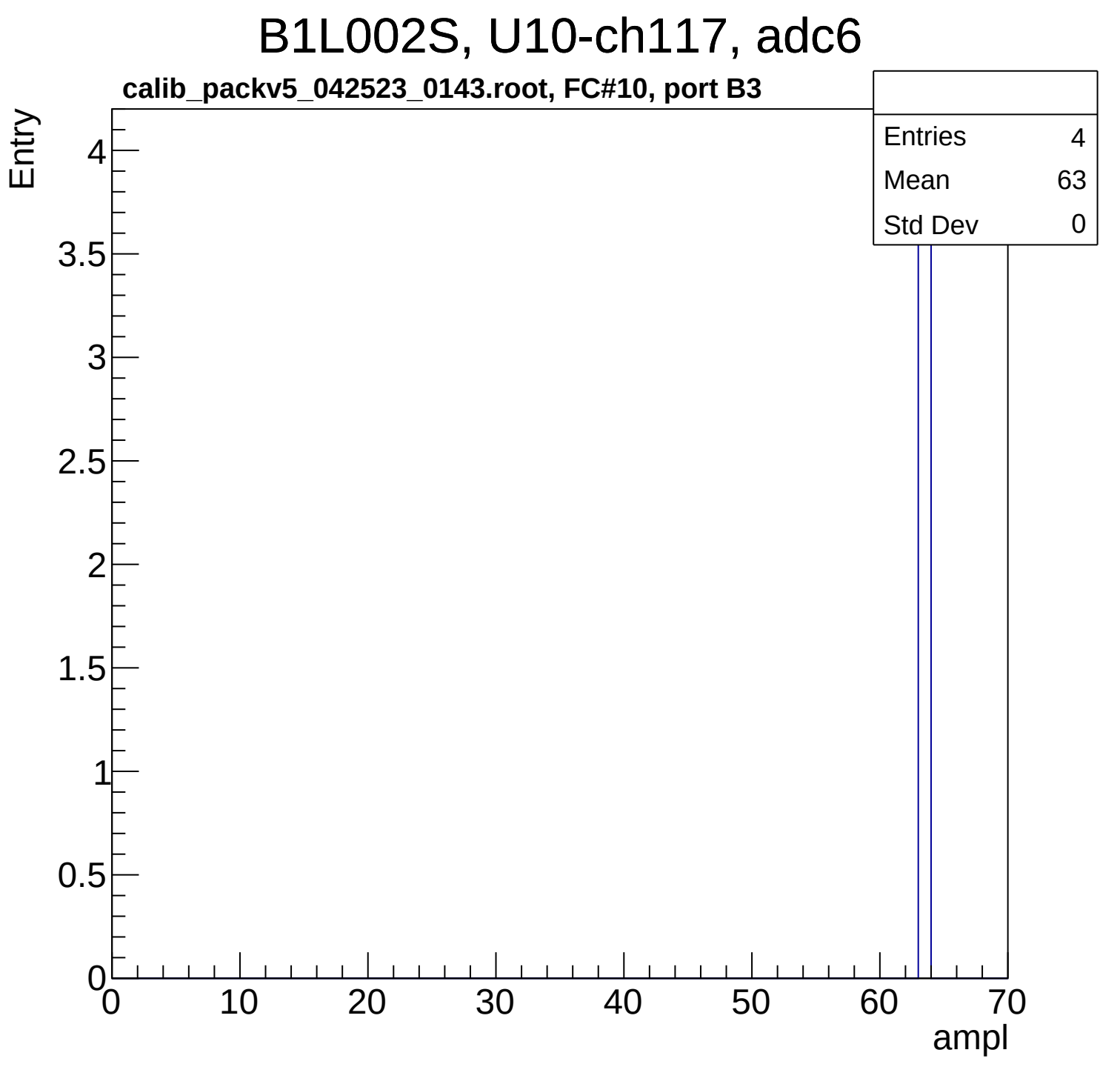
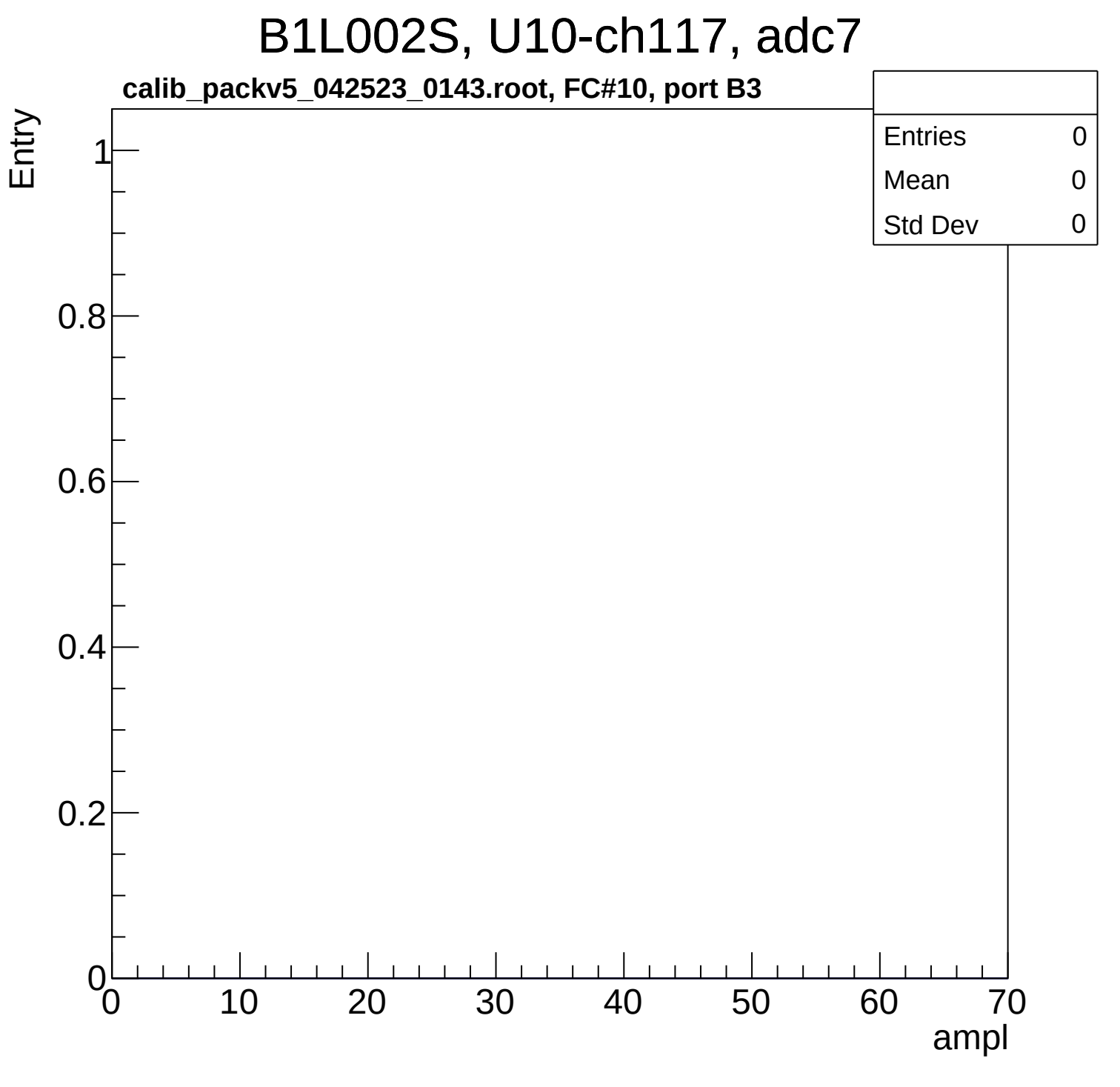

B1L002S, U10-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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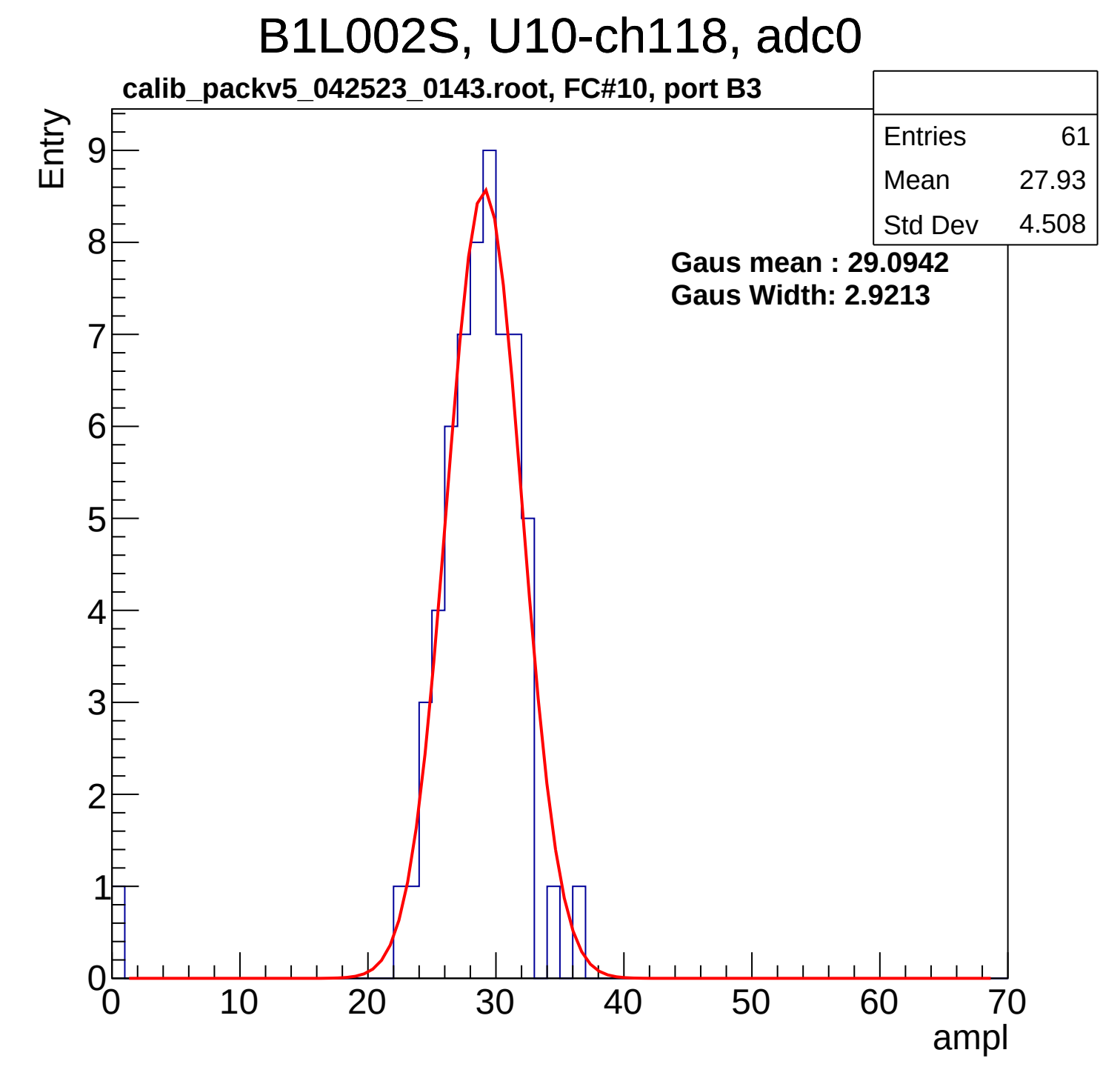
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.93
Std Dev 4.508
Gaus mean : 29.0942
Gaus Width: 2.9213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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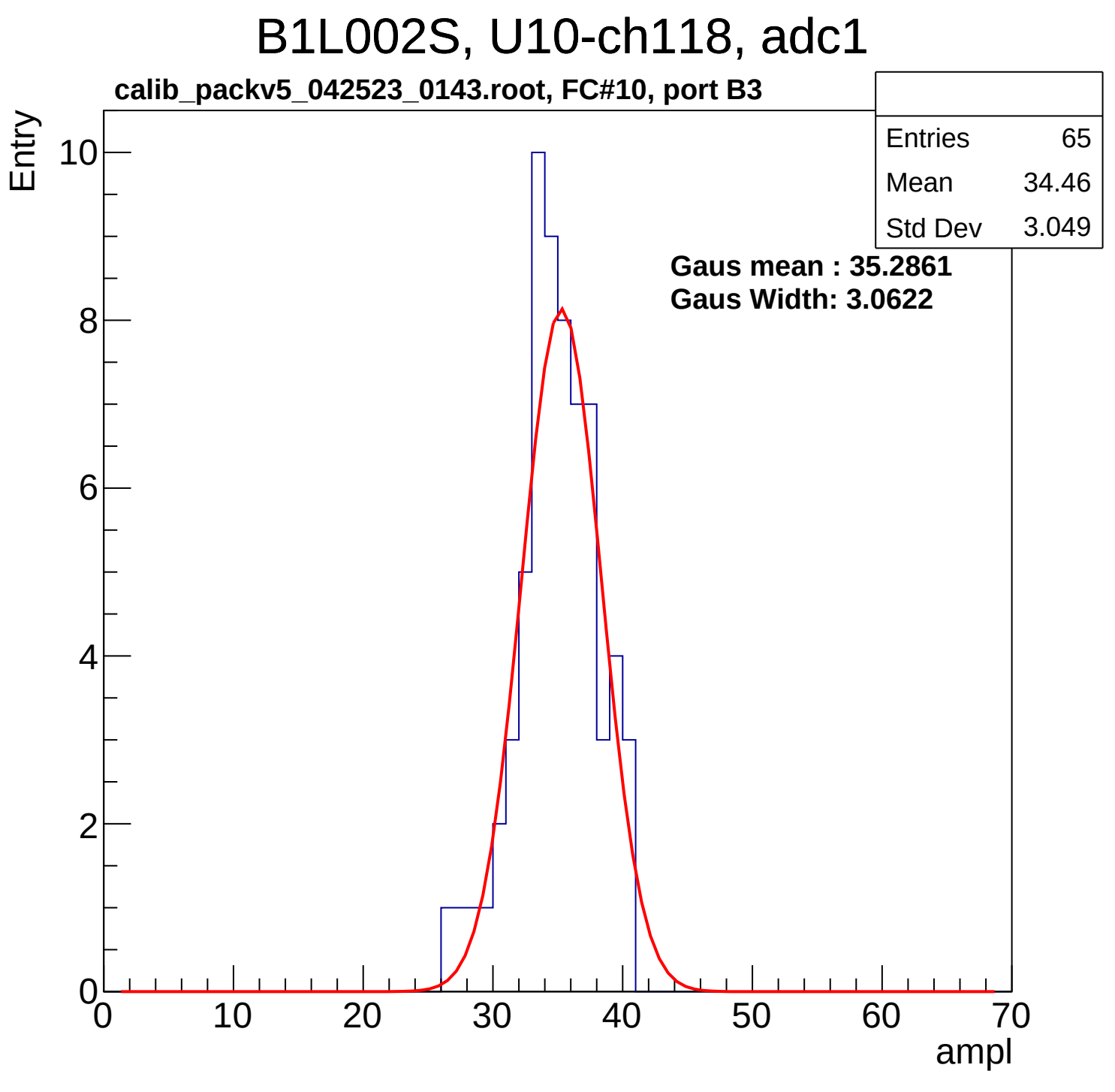
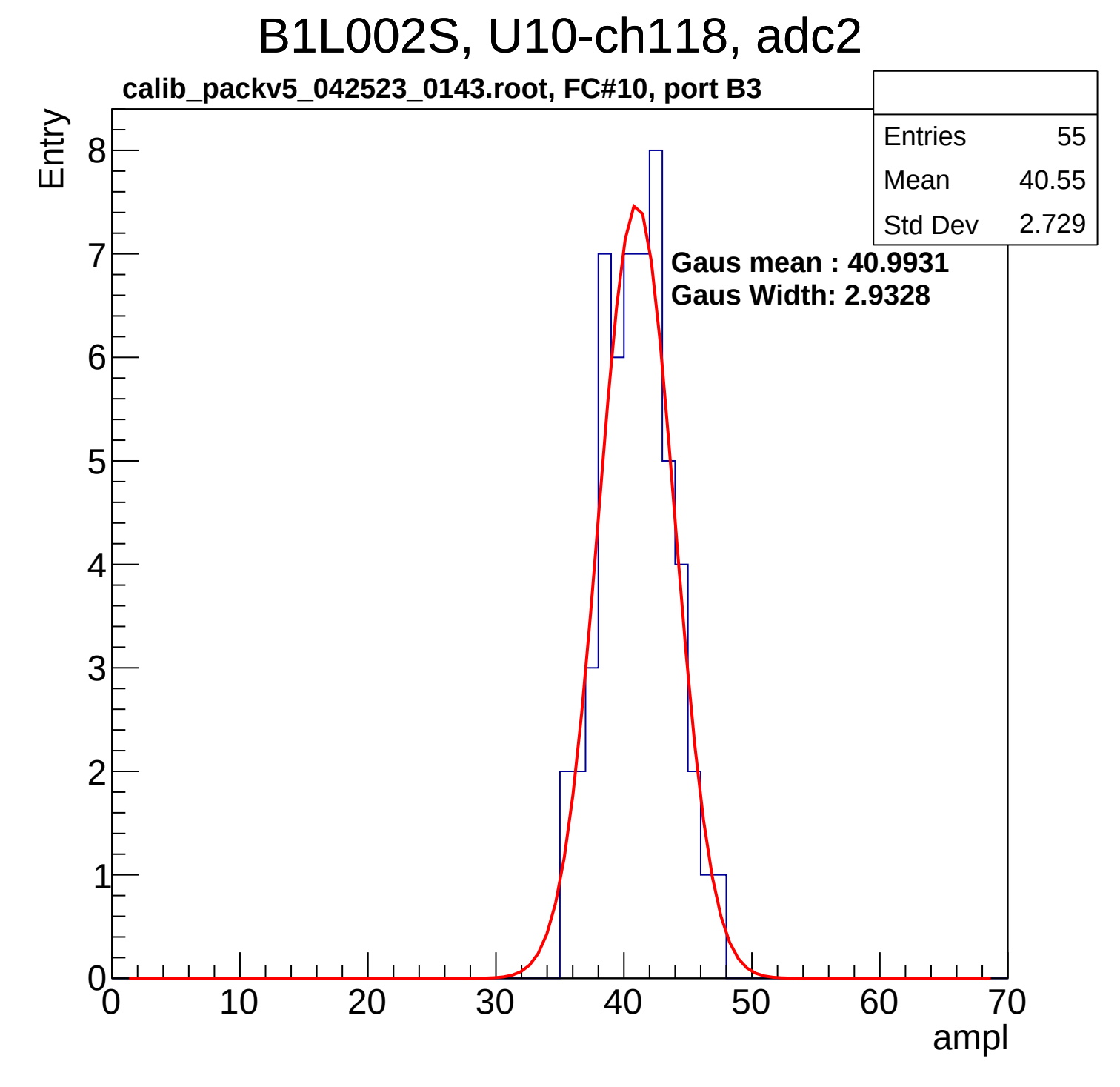

B1L002S, U10-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.46
Std Dev 3.049
Gaus mean : 35.2861
Gaus Width: 3.0622
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.55
Std Dev 2.729
Gaus mean : 40.9931
Gaus Width: 2.9328
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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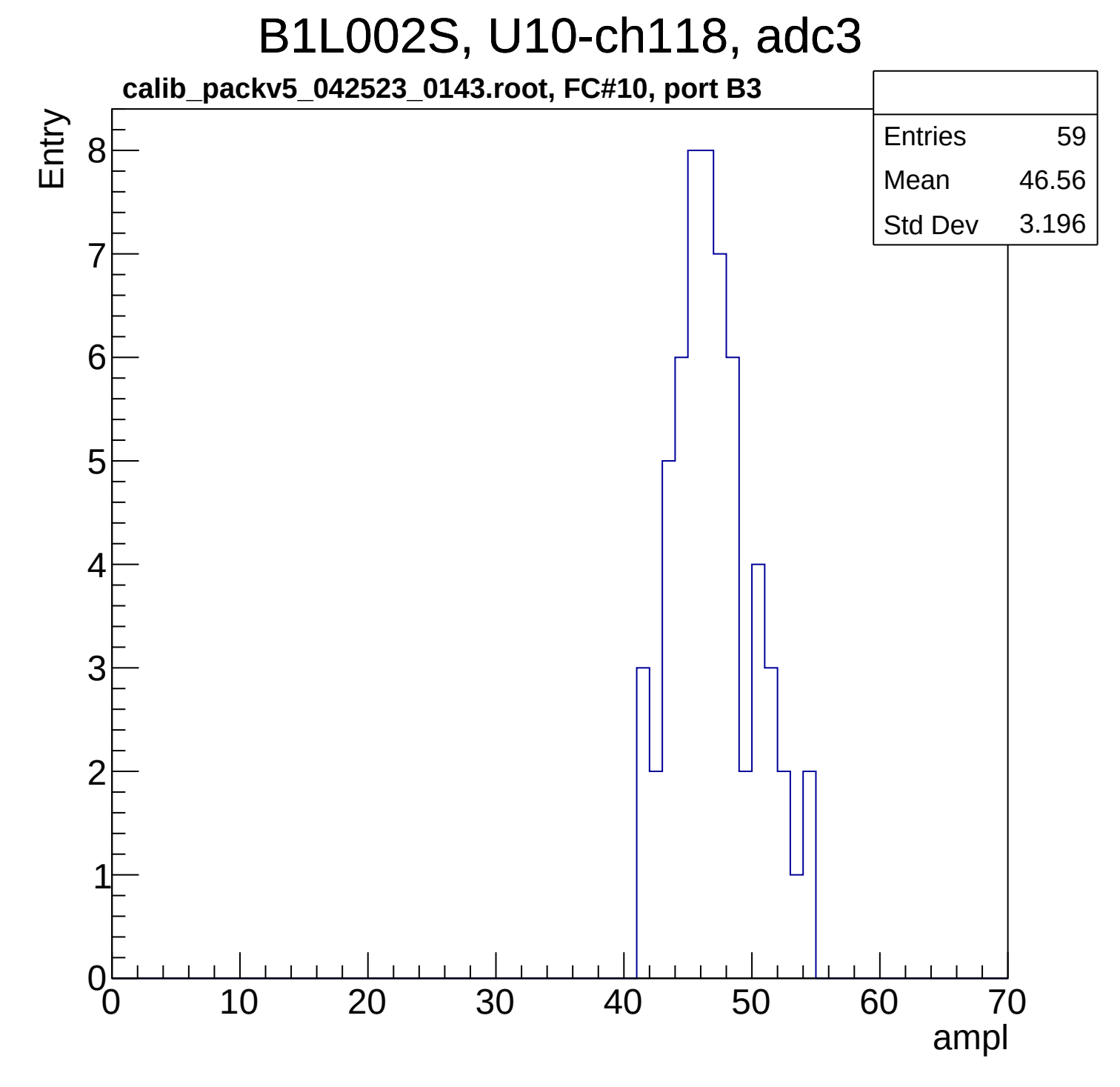
B1L002S, U10-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.56
Std Dev 3.196
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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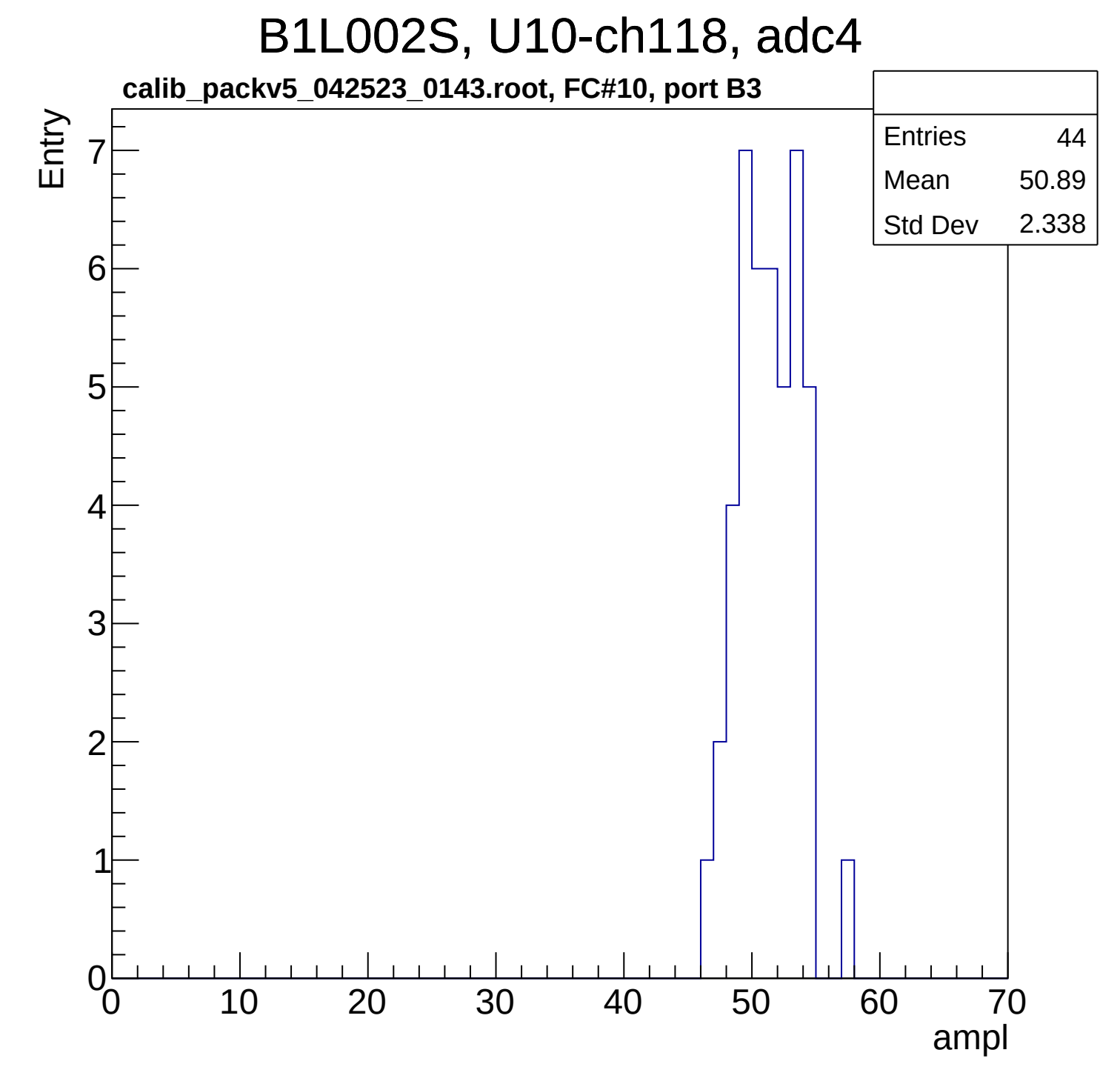
B1L002S, U10-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 50.89
Std Dev 2.338
Entry
ampl
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6
7
0
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60
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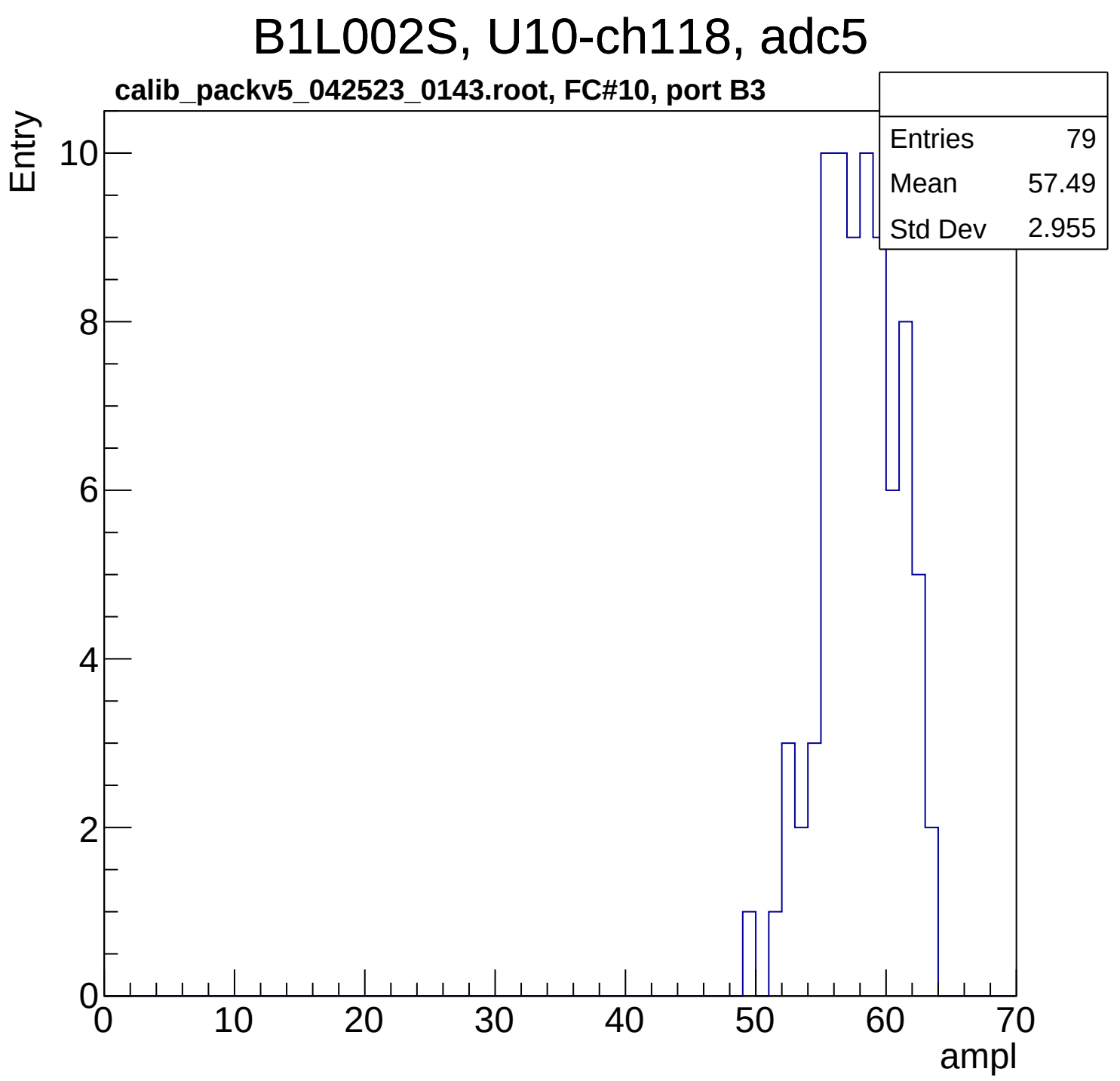
B1L002S, U10-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 57.49
Std Dev 2.955
Entry
ampl
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0
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30
40
50
60
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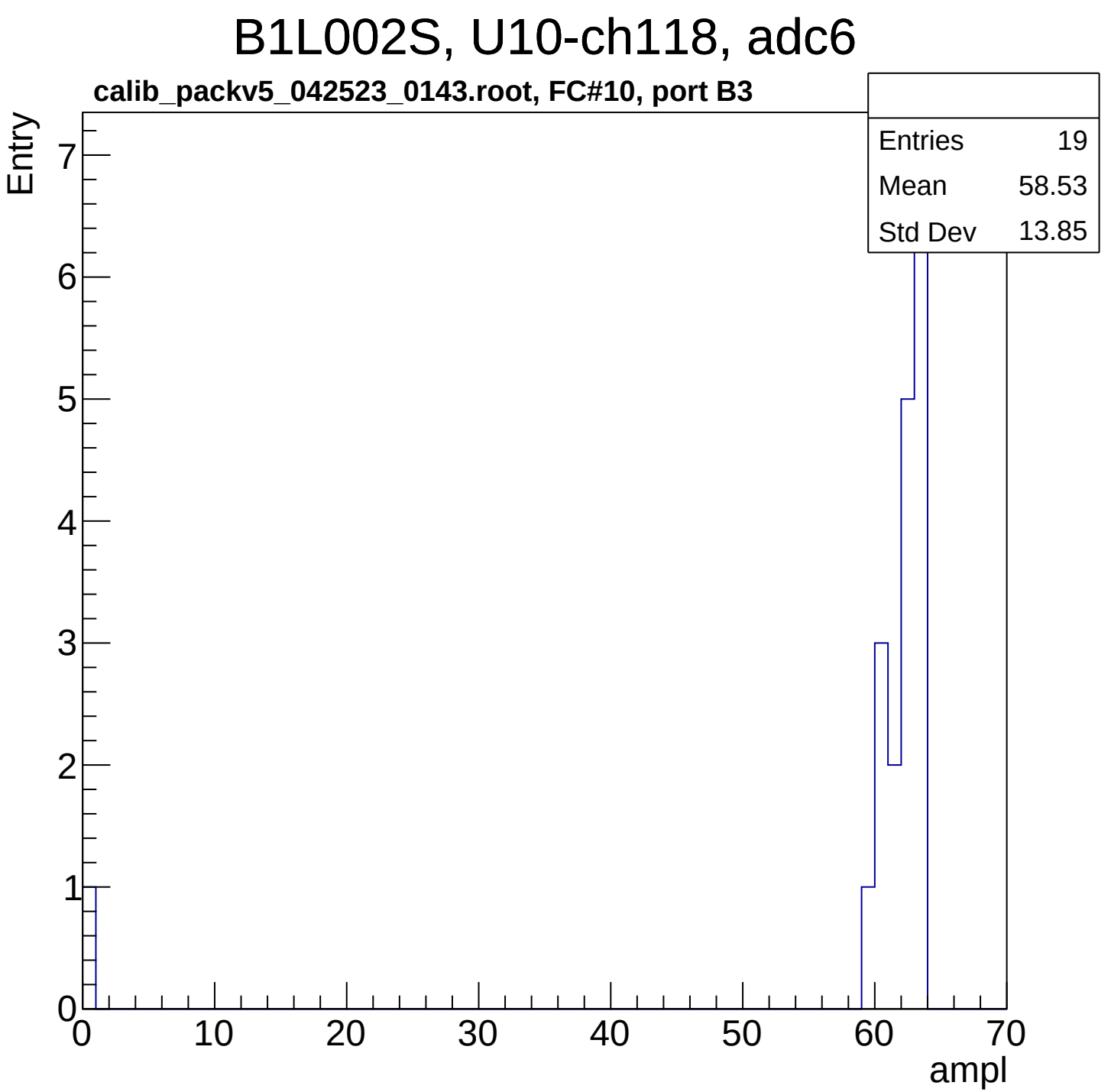
B1L002S, U10-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.53
Std Dev 13.85
Entry
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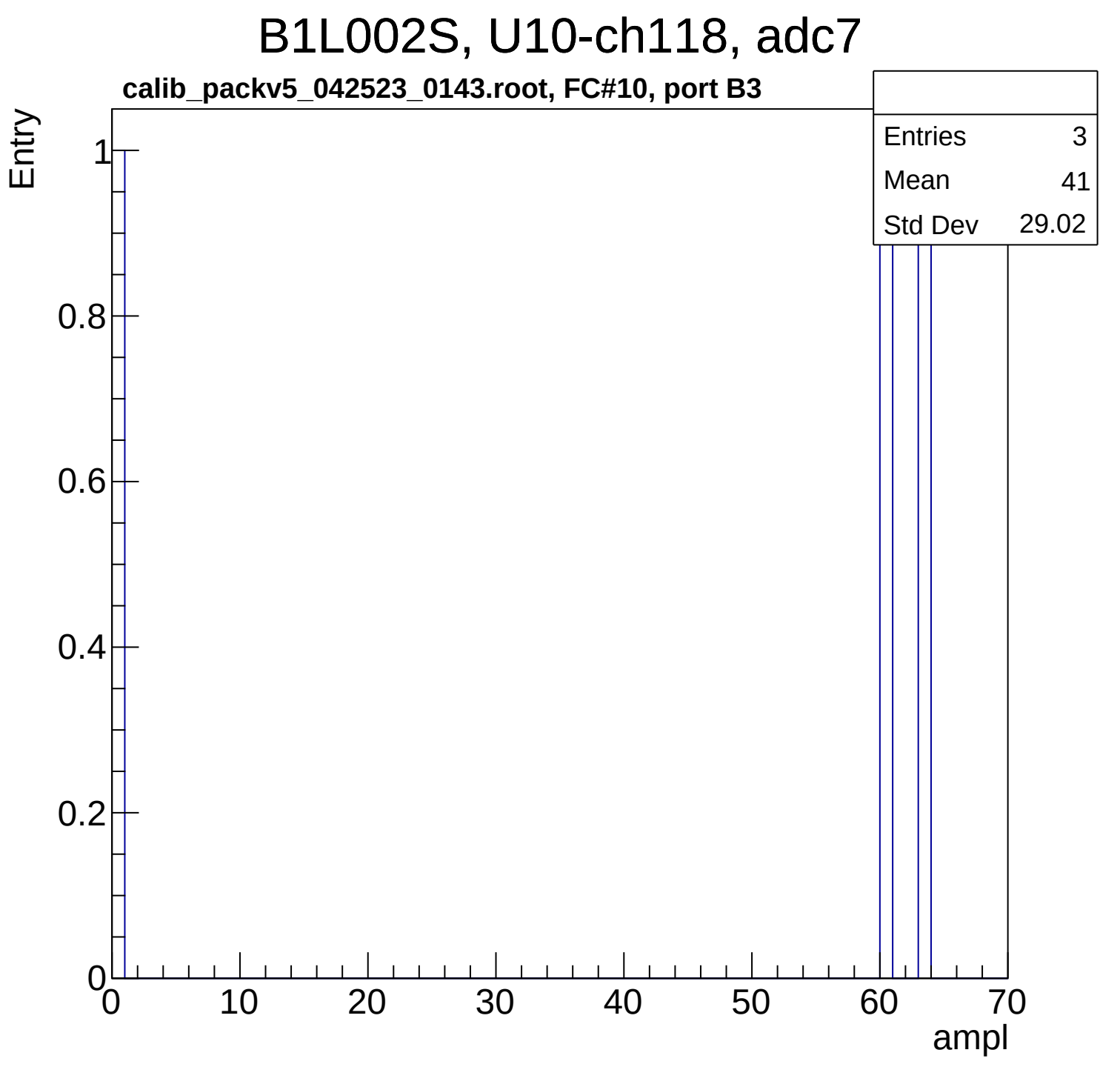
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
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0.6
0.4
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10
20
30
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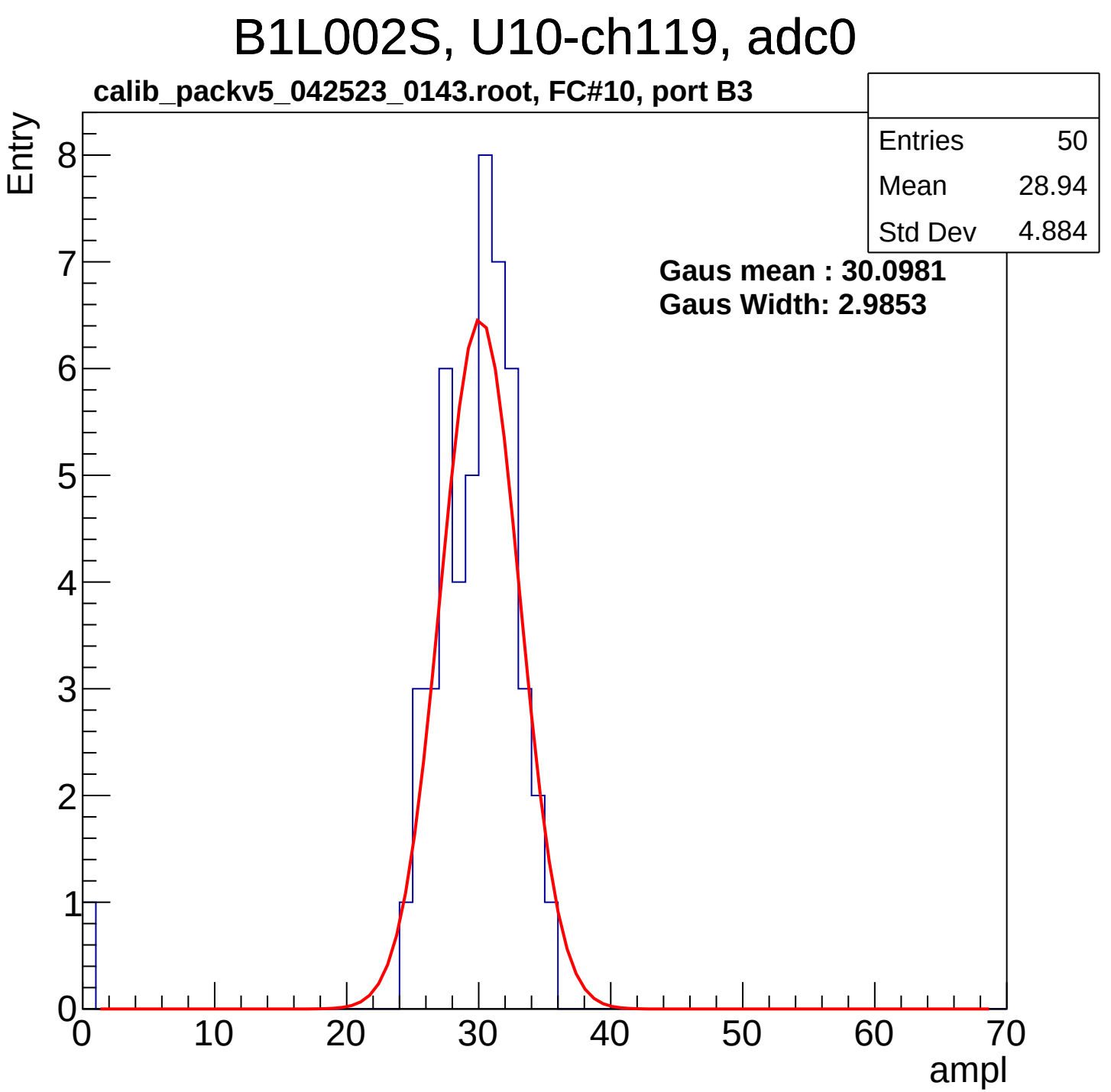
50
60
70
ampl


B1L002S, U10-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 28.94
Std Dev 4.884
Gaus mean : 30.0981
Gaus Width: 2.9853
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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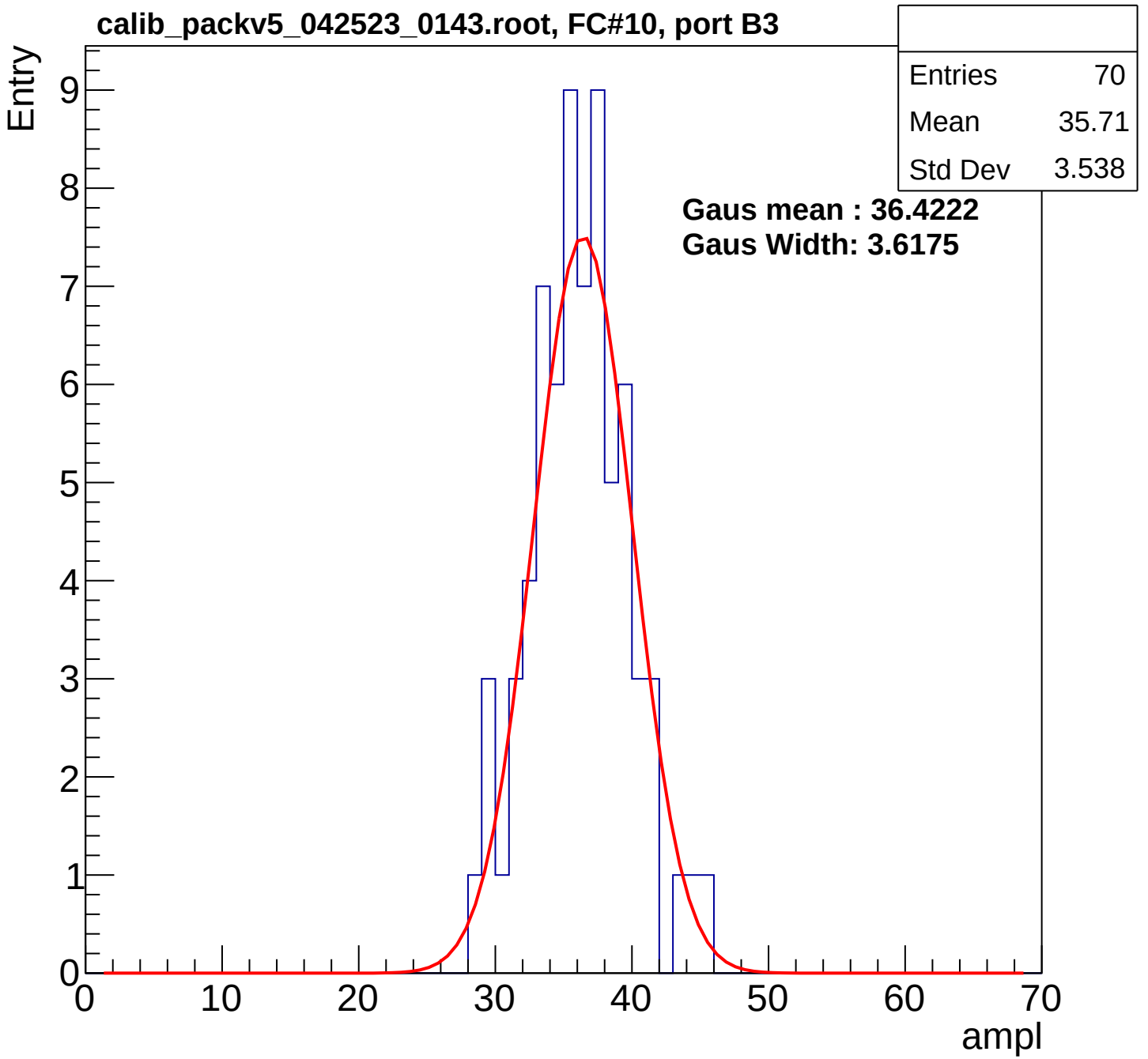
B1L002S, U10-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.71
Std Dev 3.538
Gaus mean : 36.4222
Gaus Width: 3.6175
Entry
ampl
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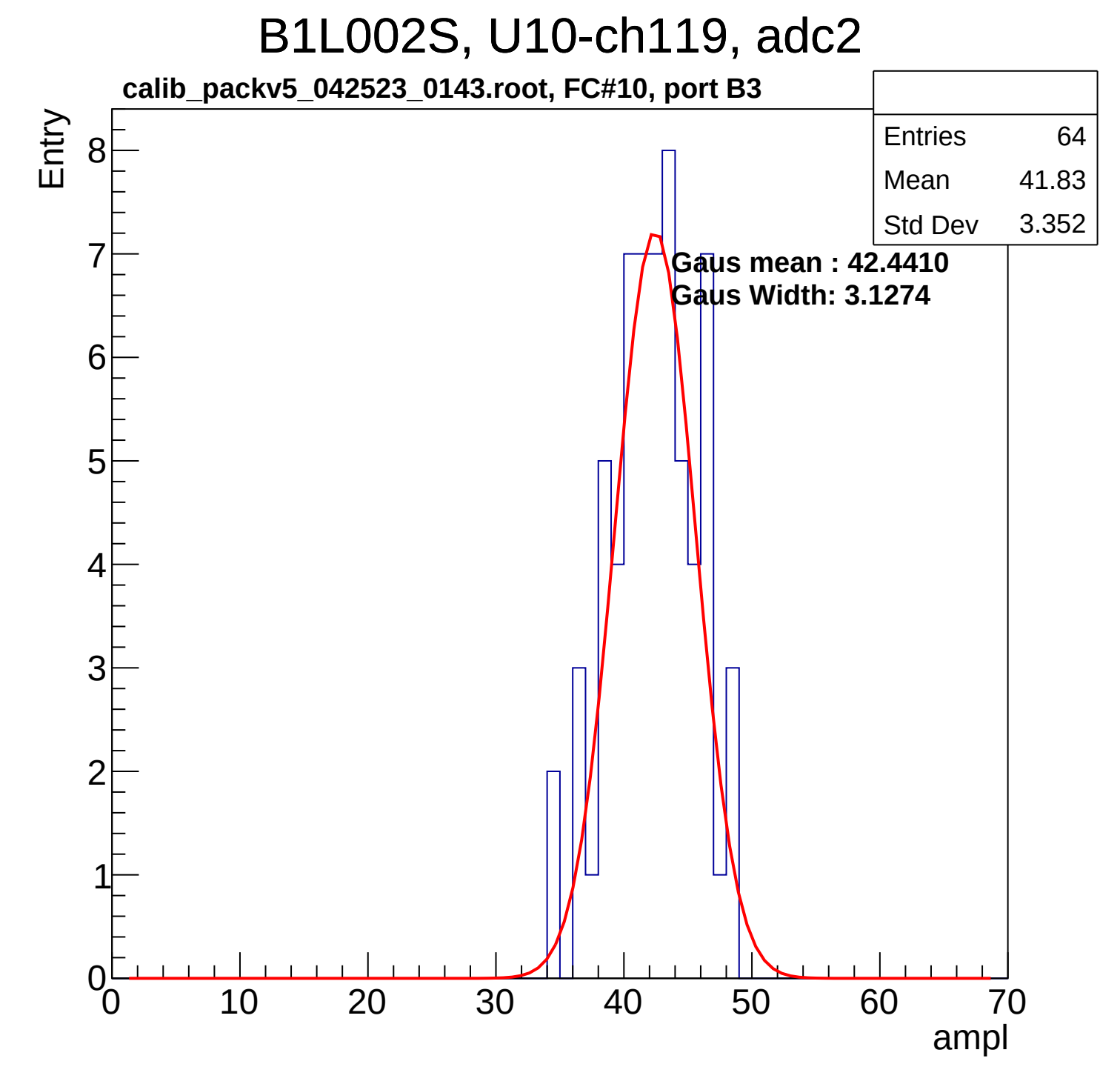
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.83
Std Dev 3.352
Gaus mean : 42.4410
Gaus Width: 3.1274
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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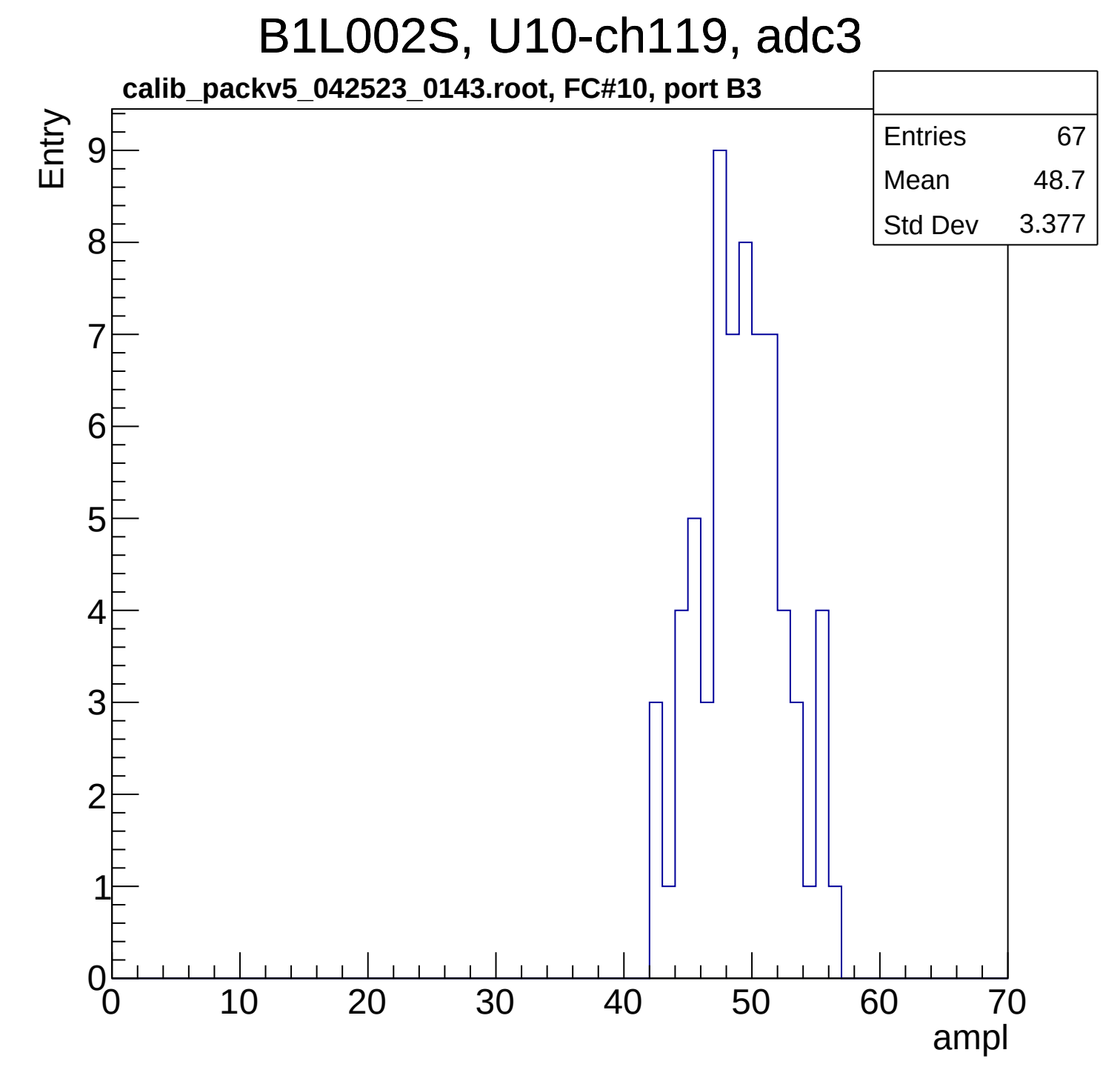
B1L002S, U10-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.7
Std Dev 3.377
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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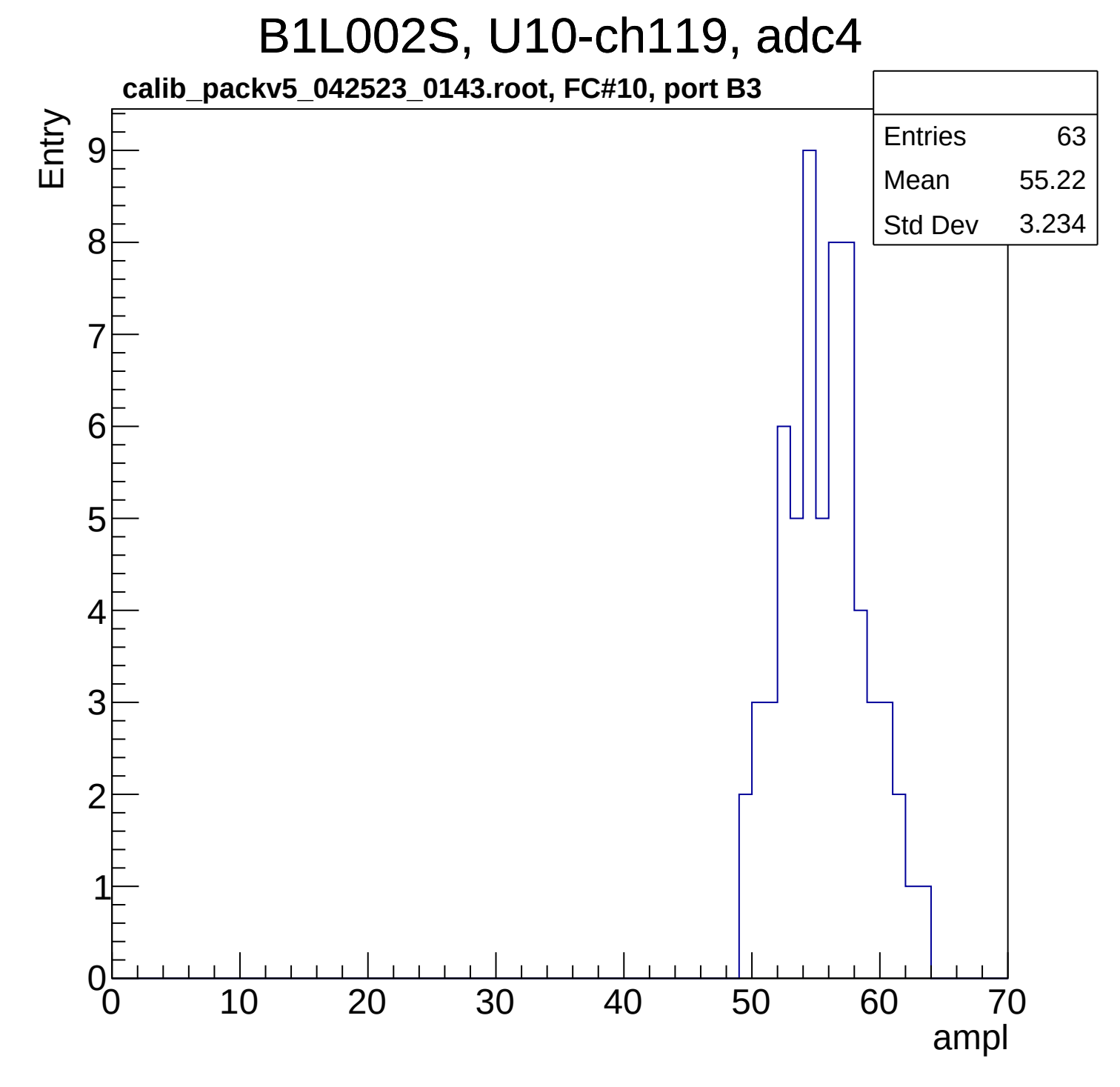
B1L002S, U10-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 55.22
Std Dev 3.234
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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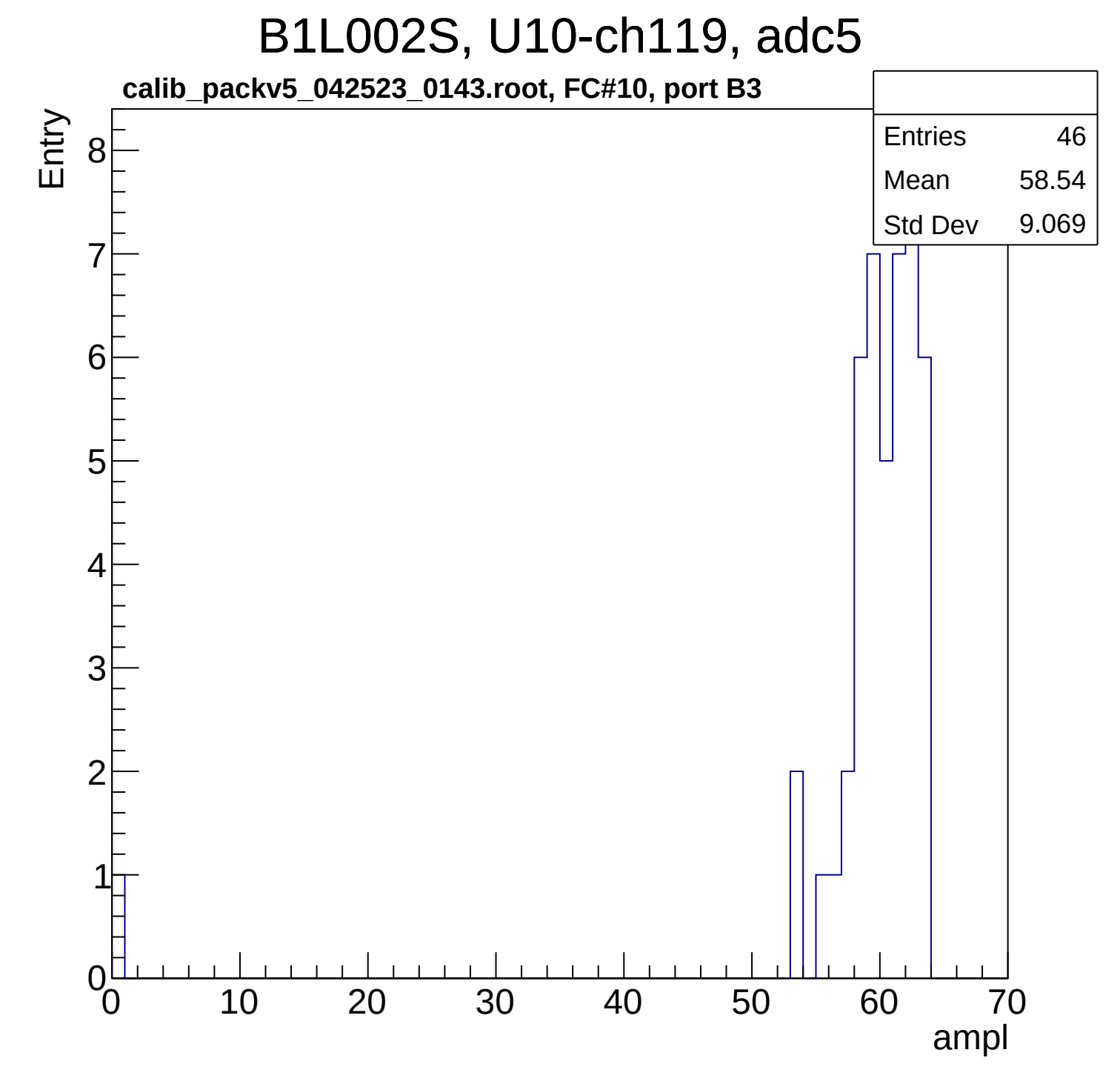
B1L002S, U10-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.54
Std Dev 9.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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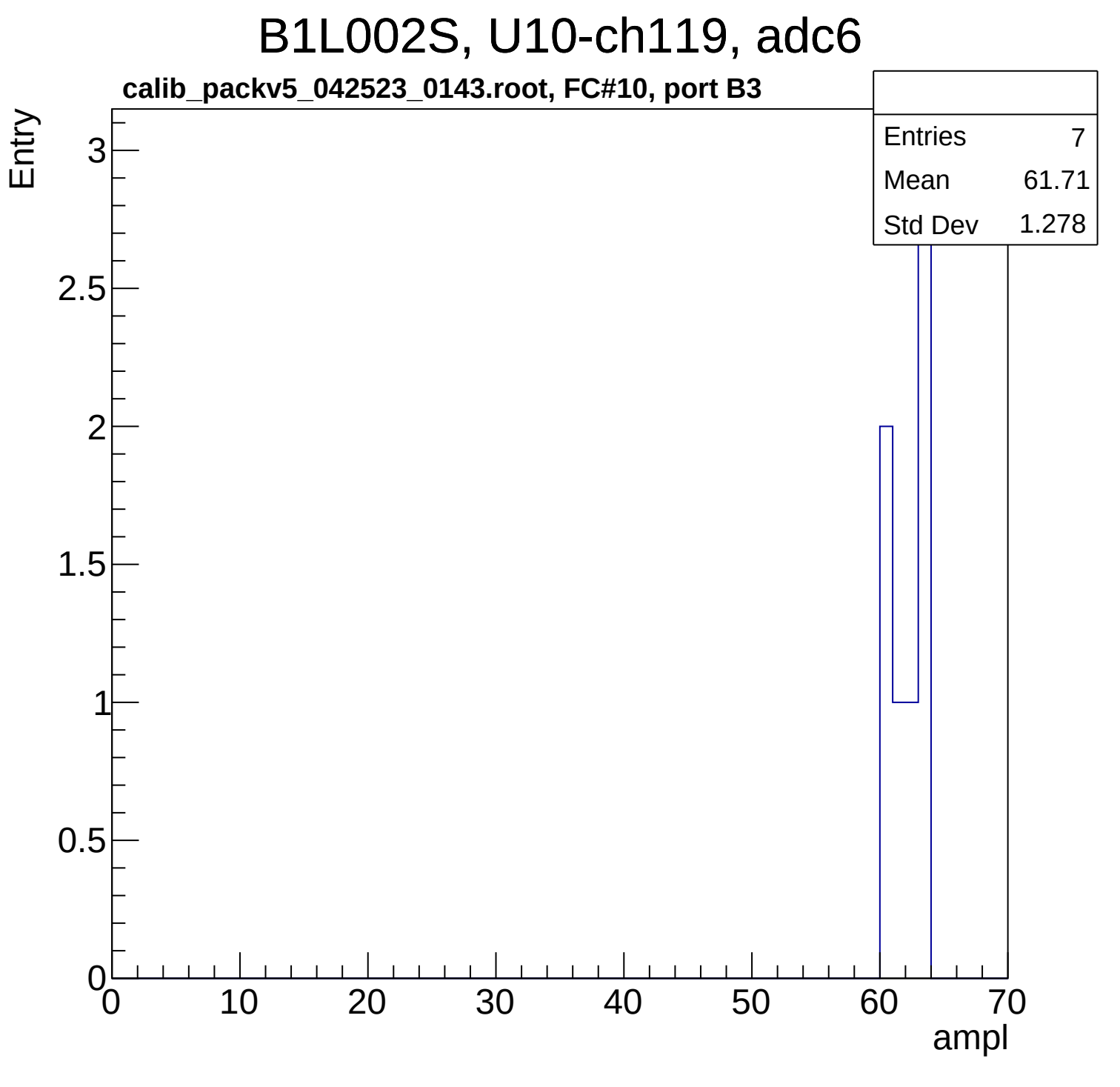
B1L002S, U10-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.71
Std Dev 1.278
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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40
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60
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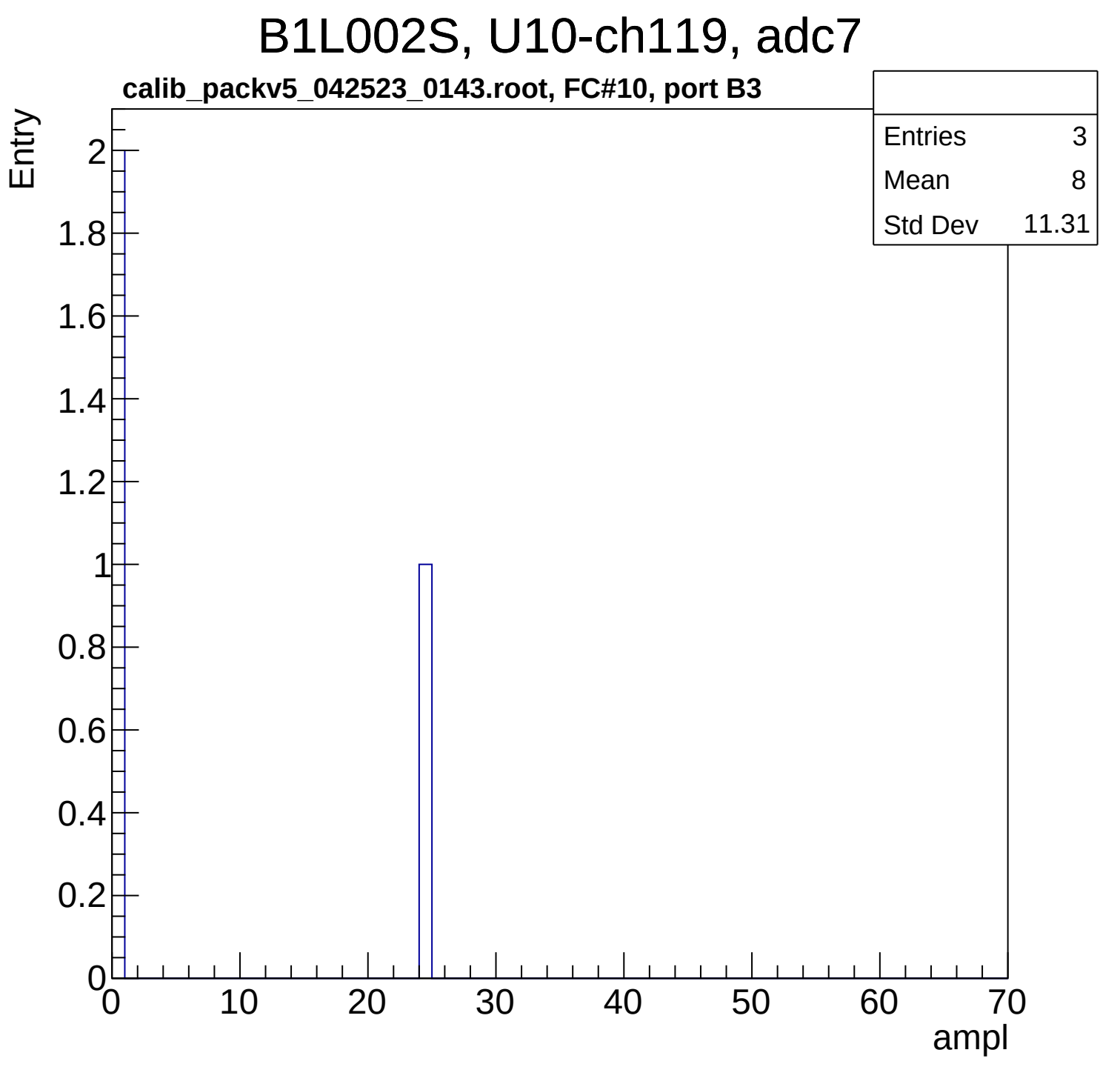
B1L002S, U10-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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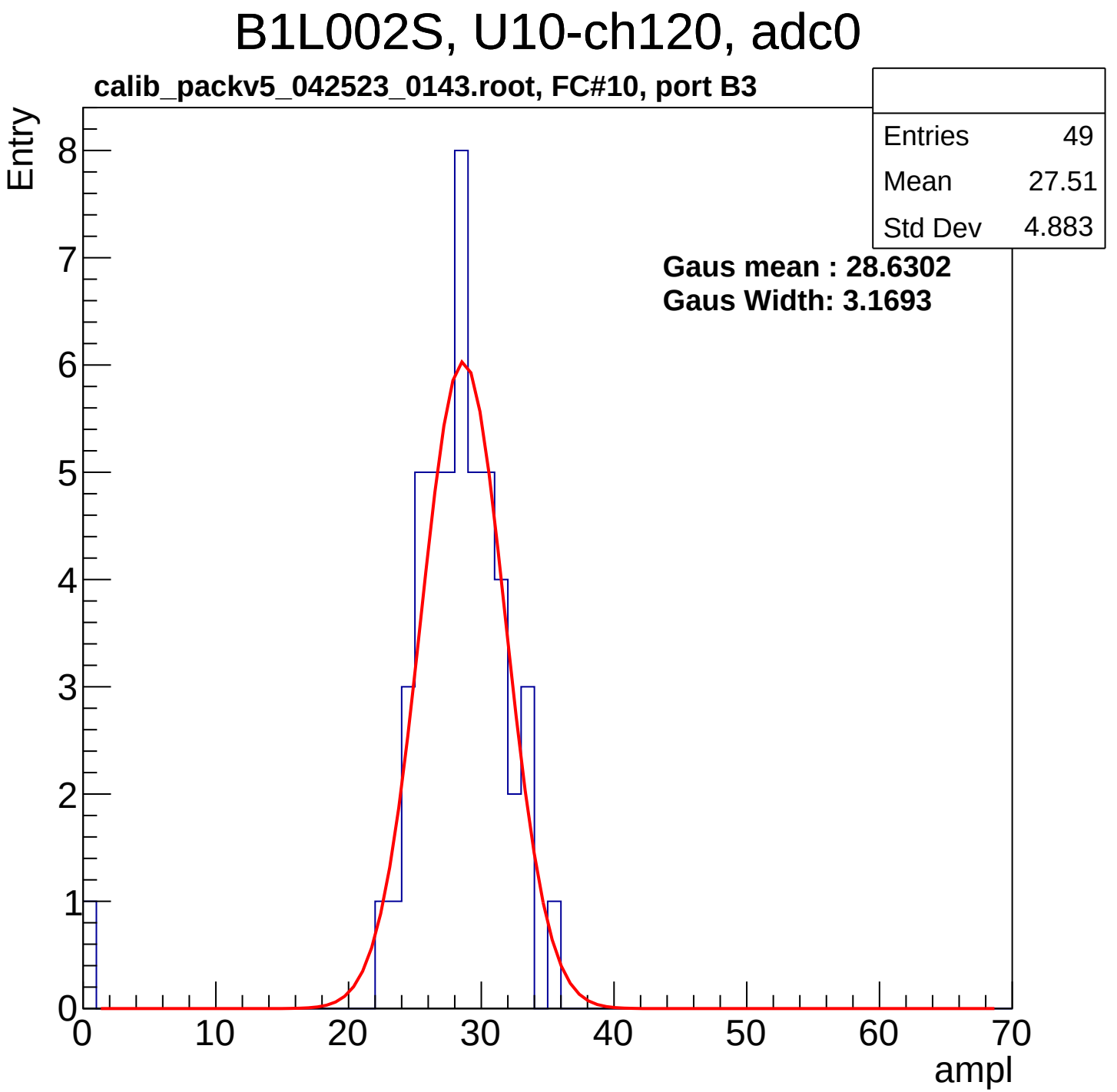
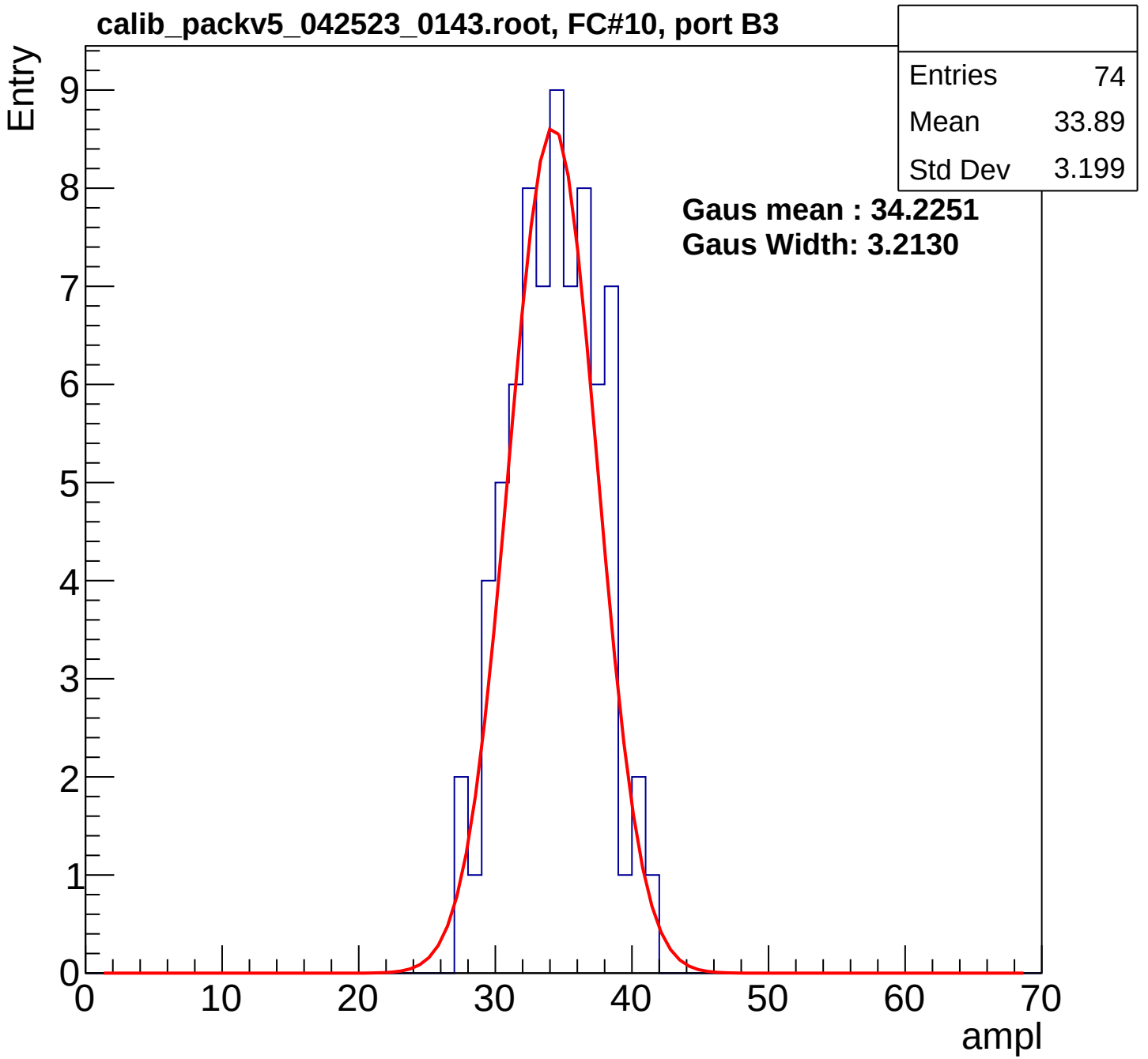

B1L002S, U10-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 27.51
Std Dev 4.883
Gaus mean : 28.6302
Gaus Width: 3.1693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.89
Std Dev 3.199
Gaus mean : 34.2251
Gaus Width: 3.2130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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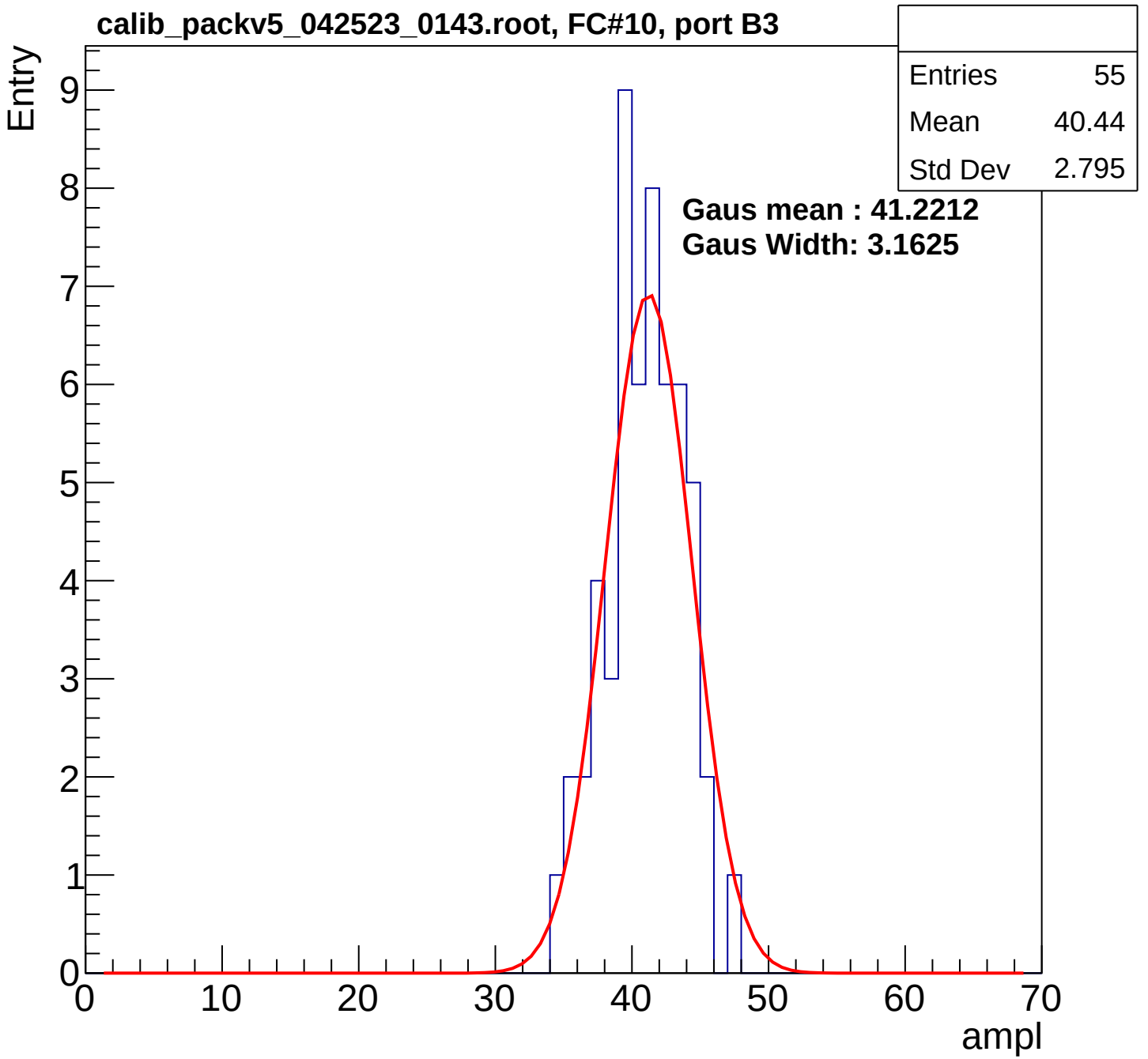
B1L002S, U10-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.44
Std Dev 2.795
Gaus mean : 41.2212
Gaus Width: 3.1625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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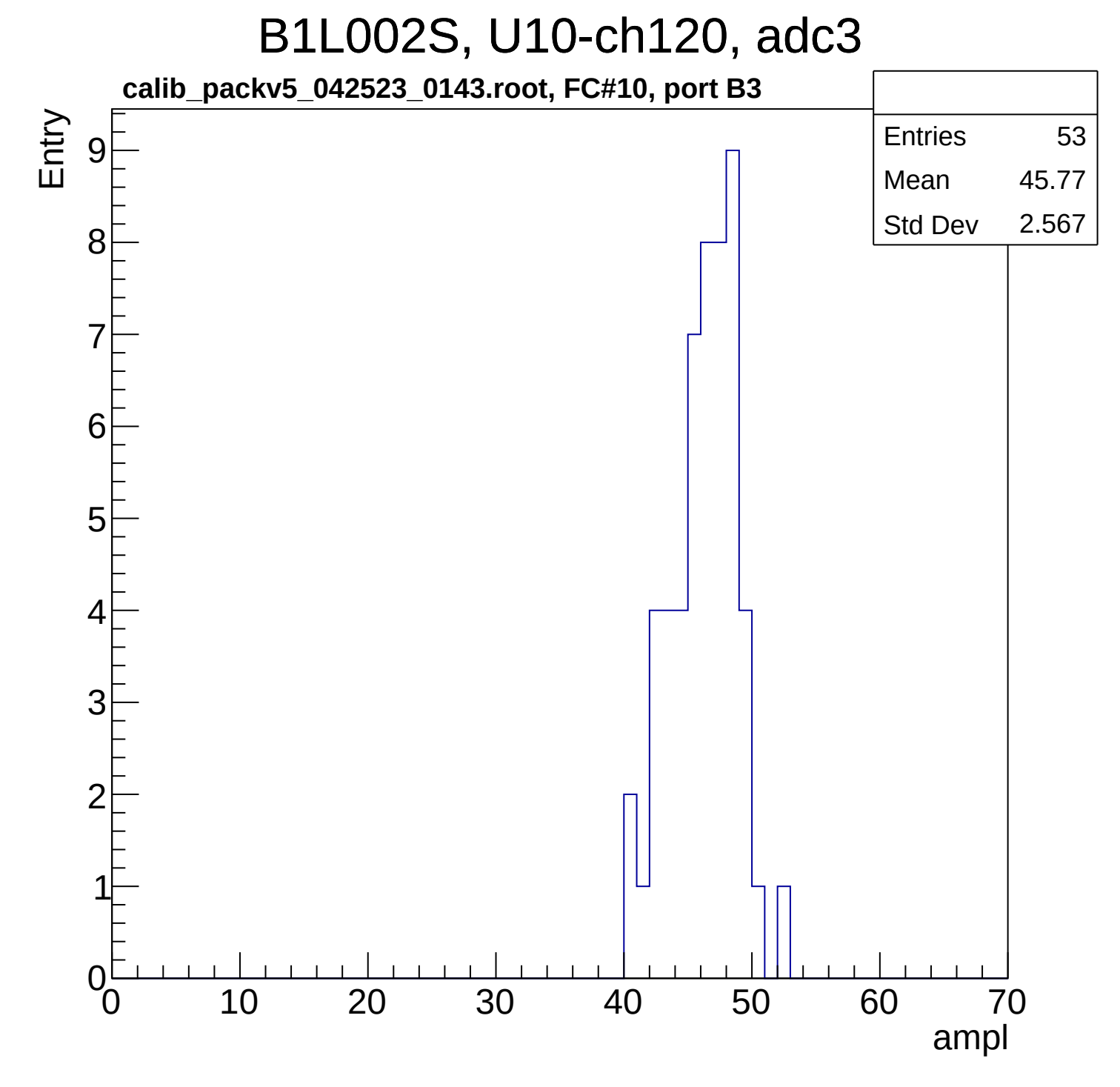
B1L002S, U10-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.77
Std Dev 2.567
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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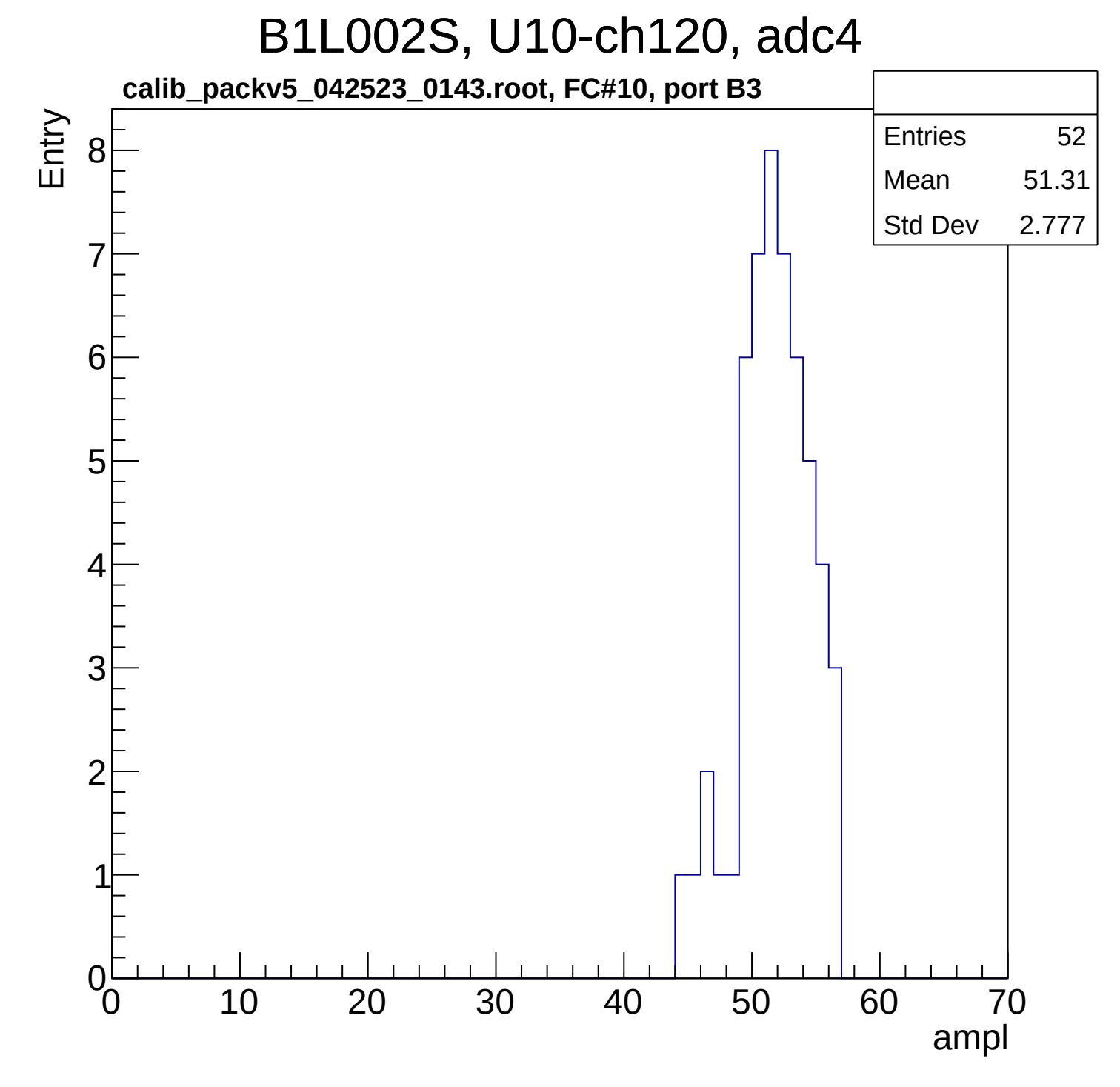
B1L002S, U10-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.31
Std Dev 2.777
Entry
ampl
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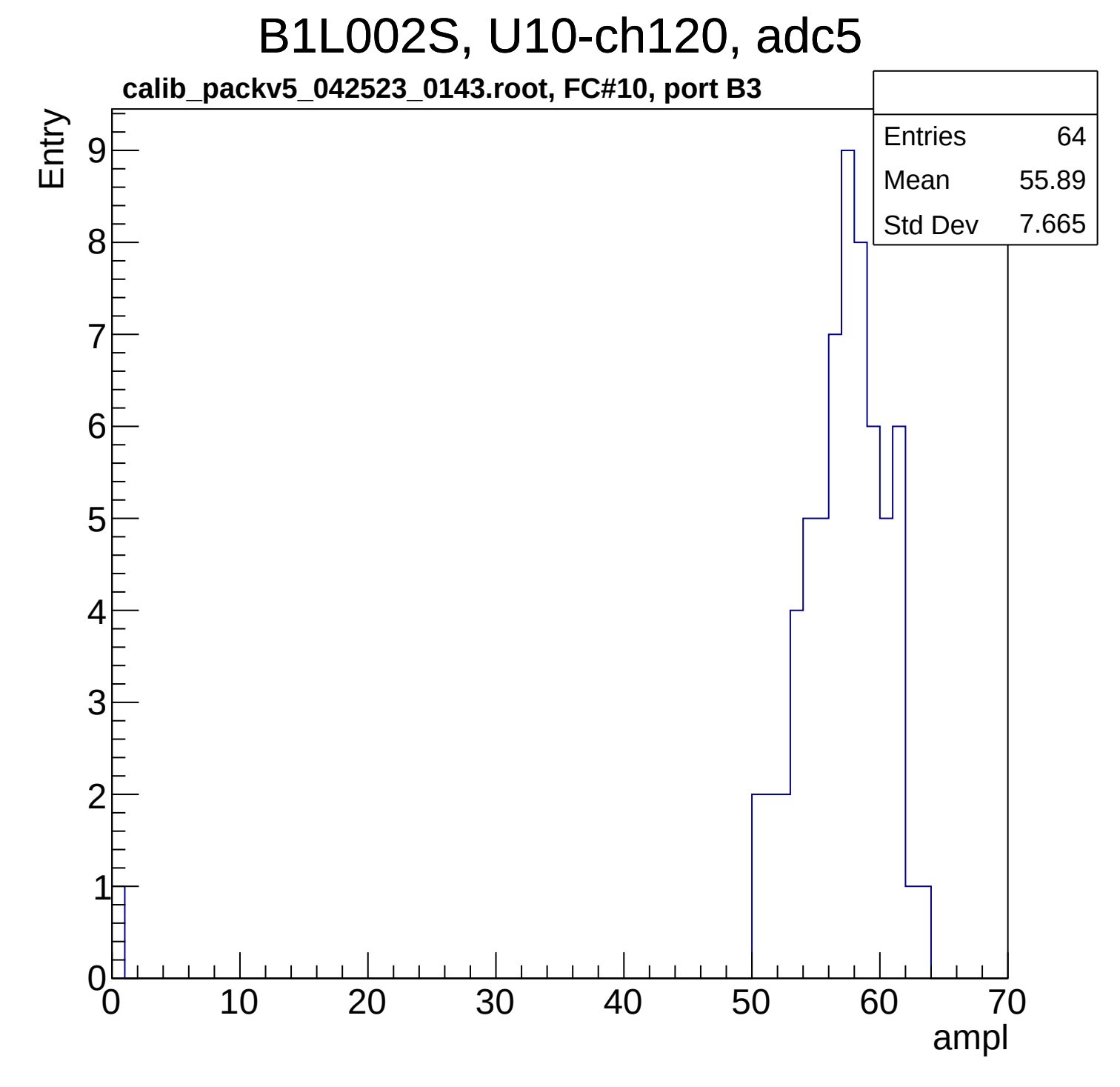
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 55.89
Std Dev 7.665
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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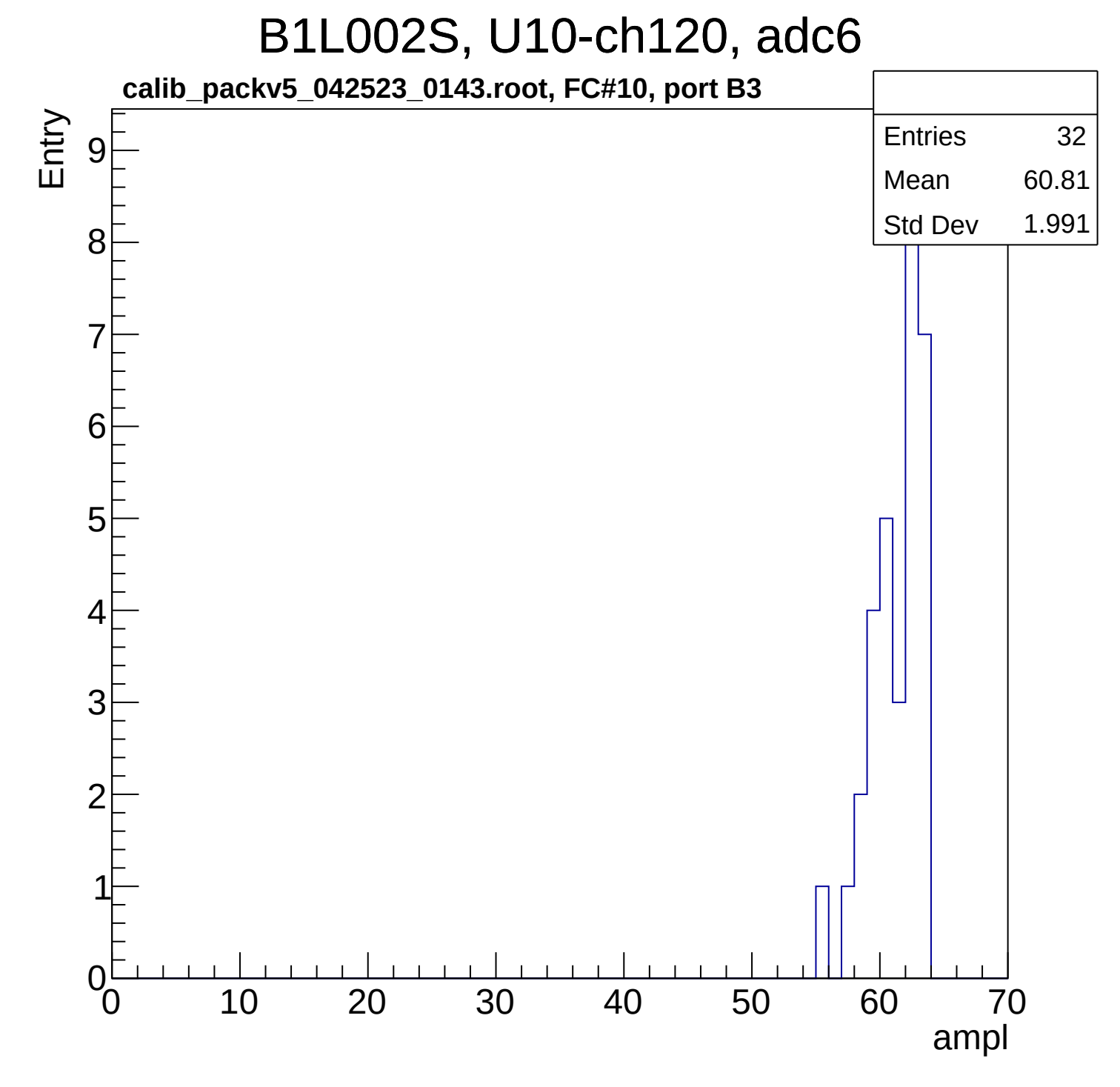
B1L002S, U10-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 60.81
Std Dev 1.991
Entry
ampl
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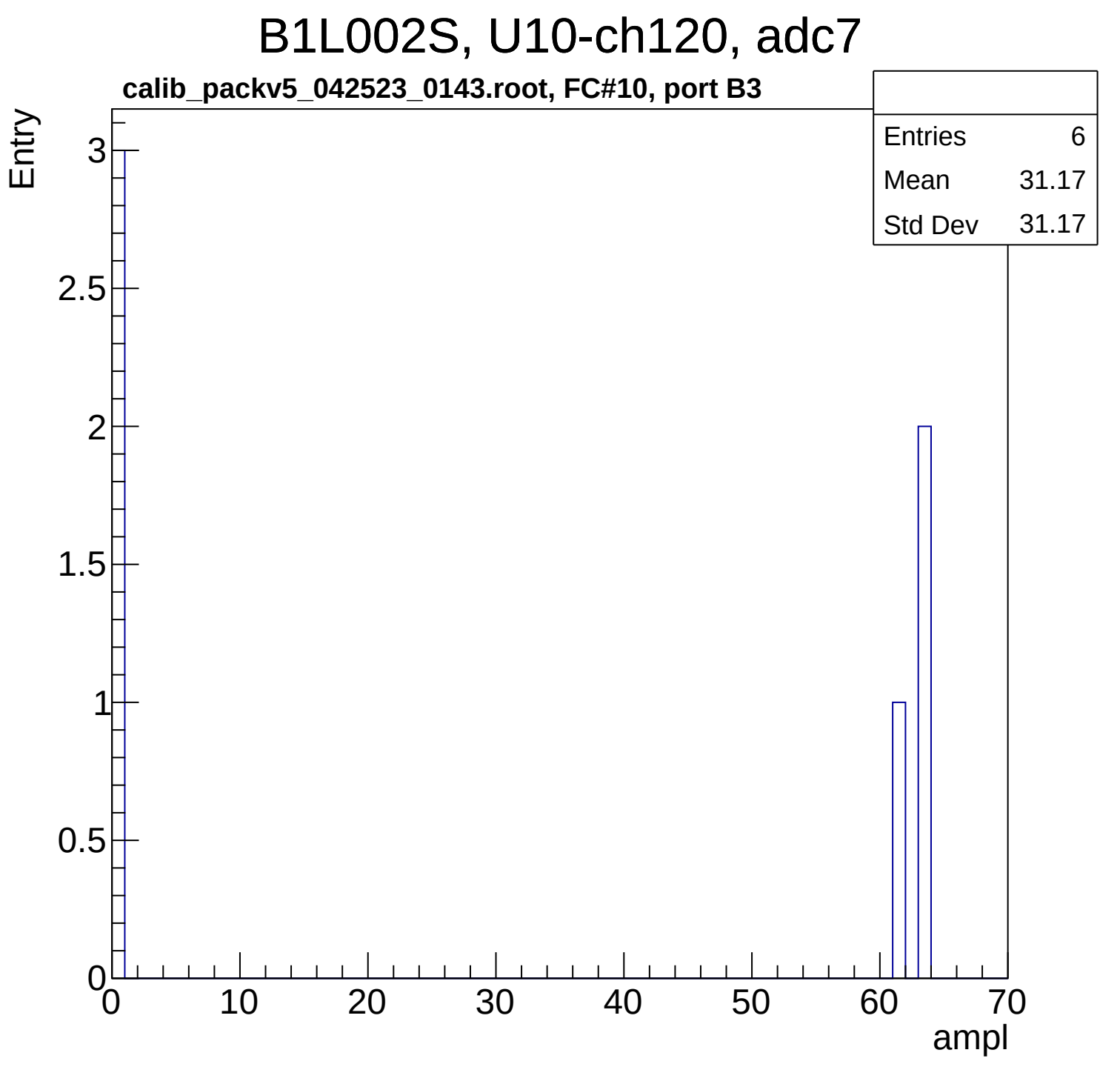
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 31.17
Std Dev 31.17
Entry
ampl
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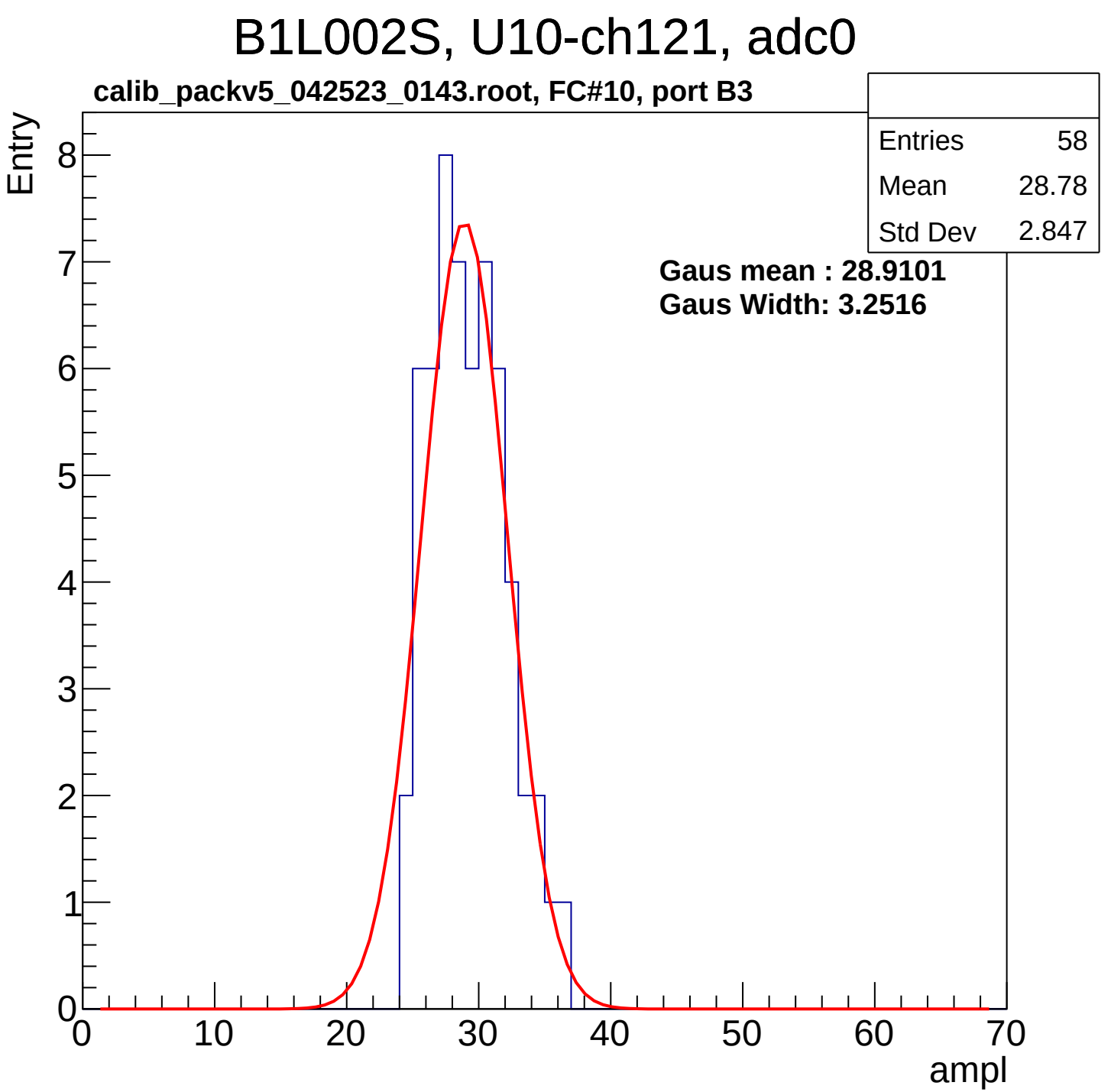
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U10-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.78
Std Dev 2.847
Gaus mean : 28.9101
Gaus Width: 3.2516
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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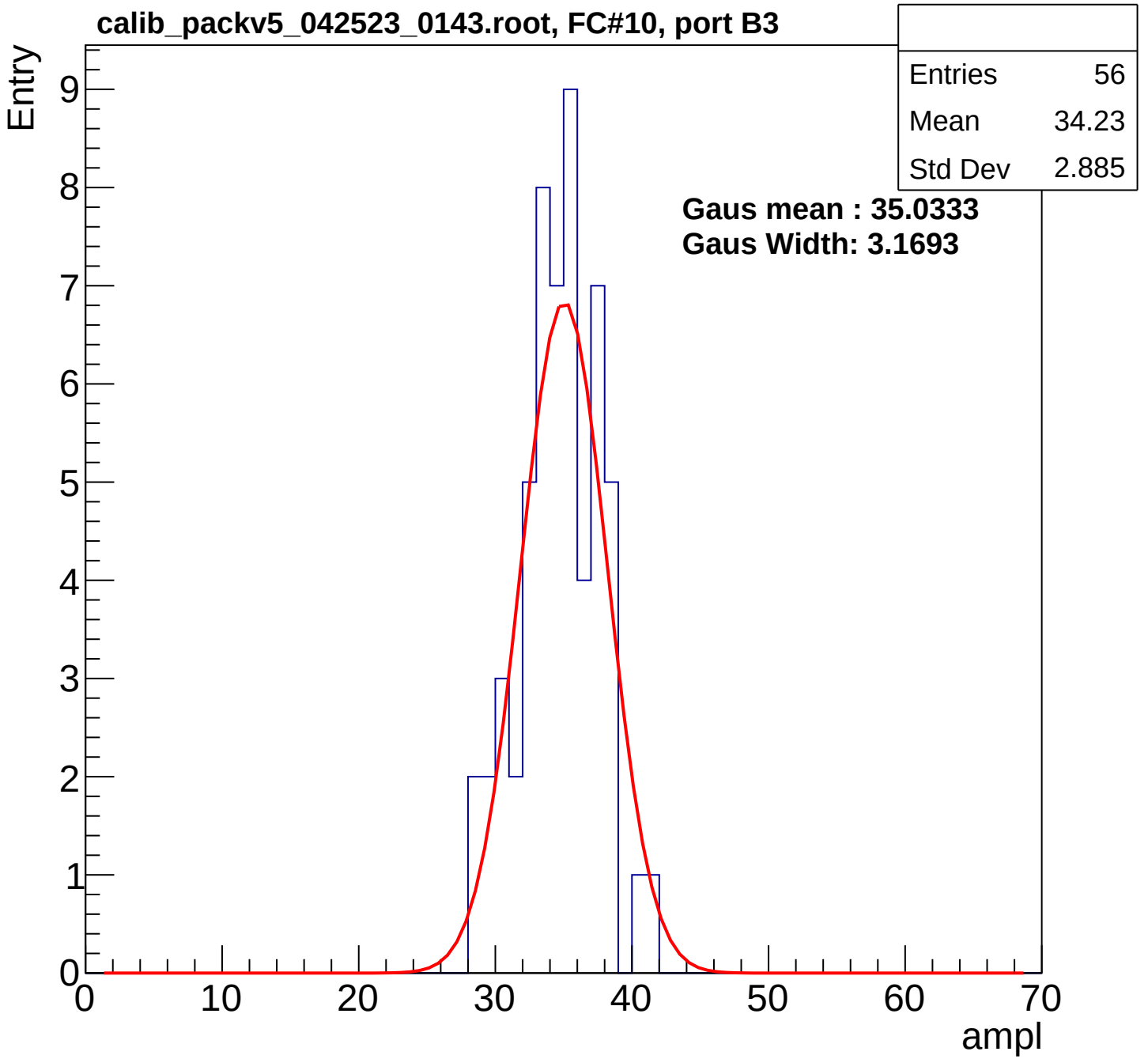
B1L002S, U10-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.23
Std Dev 2.885
Gaus mean : 35.0333
Gaus Width: 3.1693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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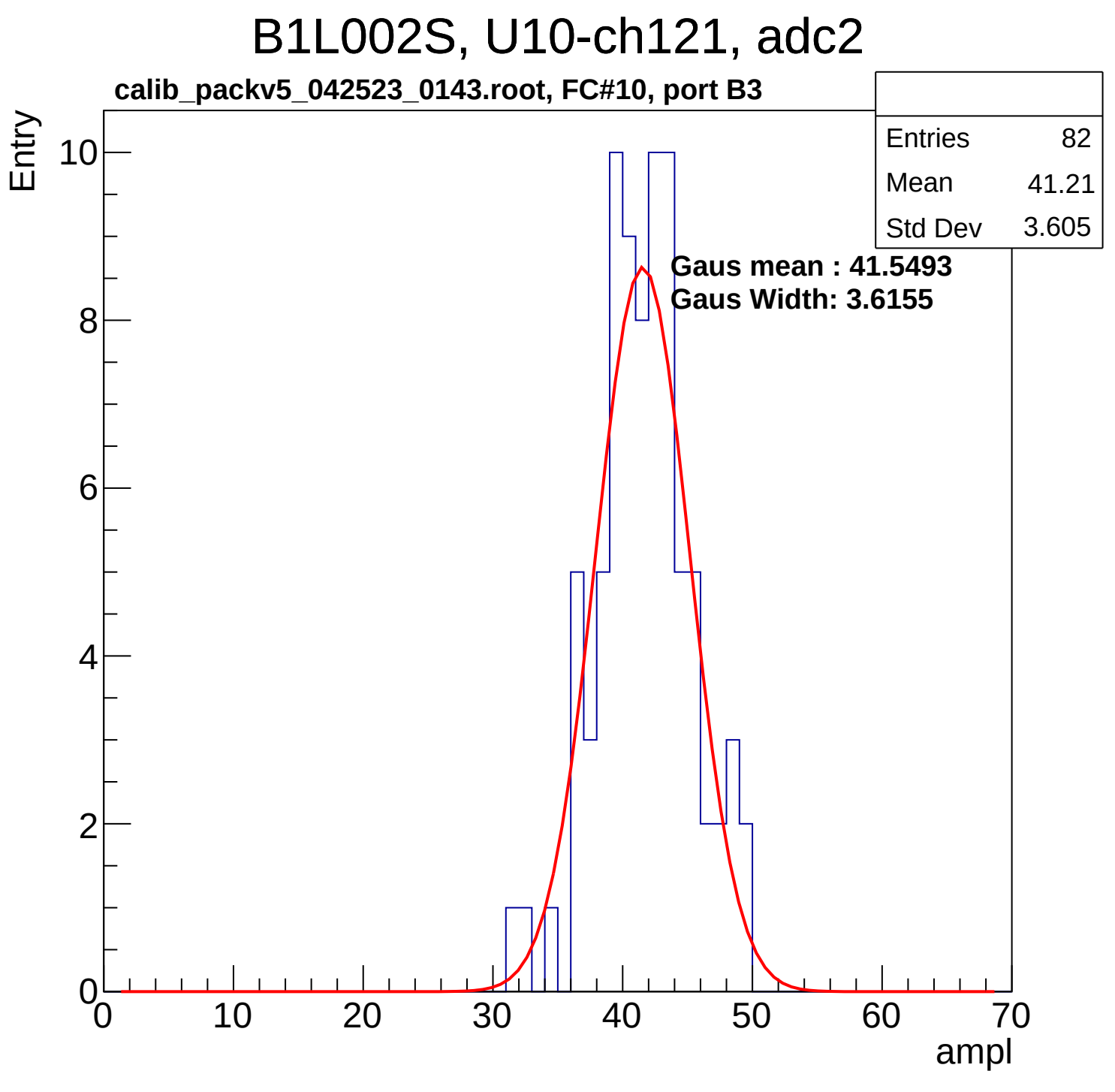
B1L002S, U10-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 41.21
Std Dev 3.605
Gaus mean : 41.5493
Gaus Width: 3.6155
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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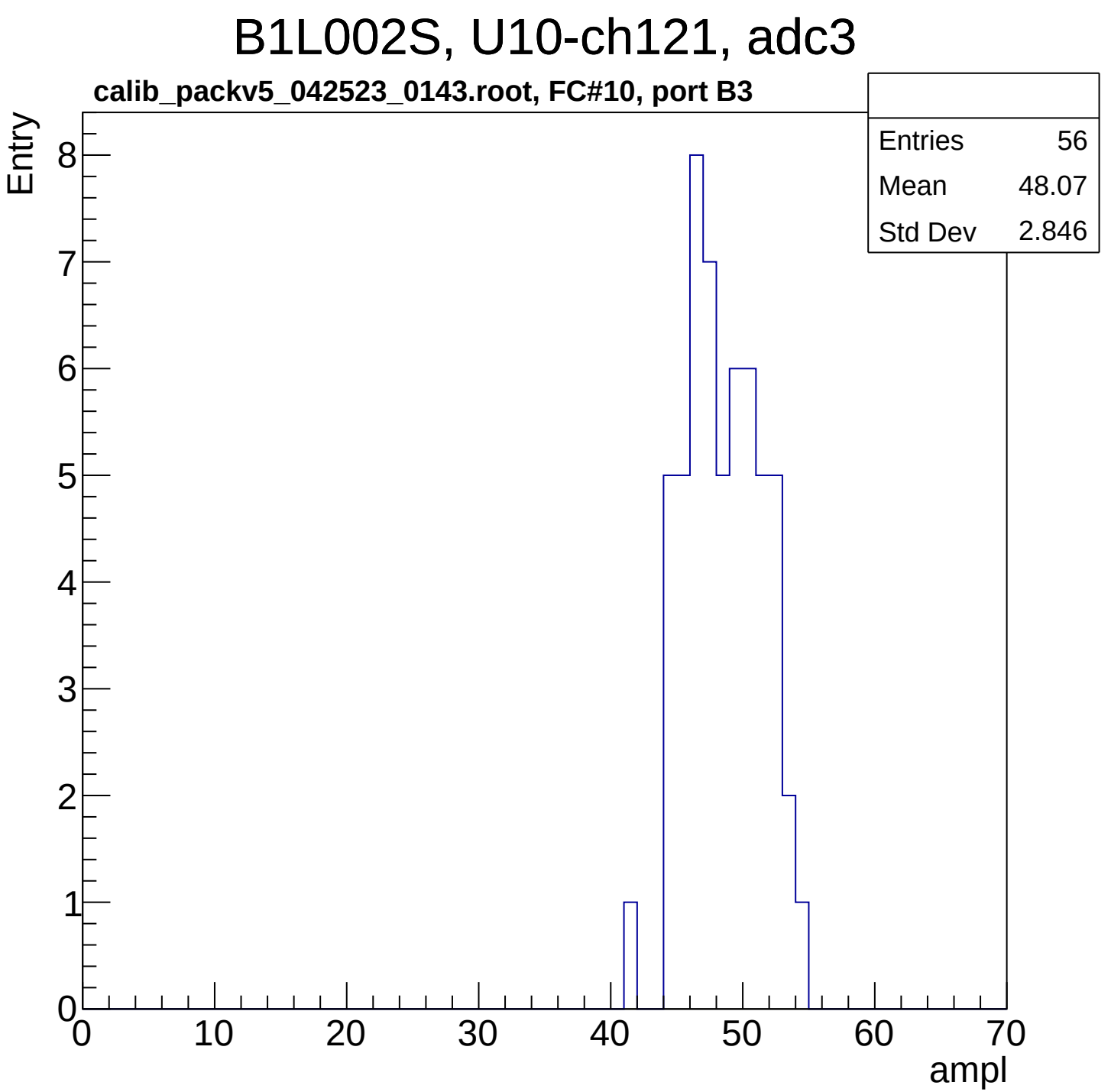
B1L002S, U10-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 48.07
Std Dev 2.846
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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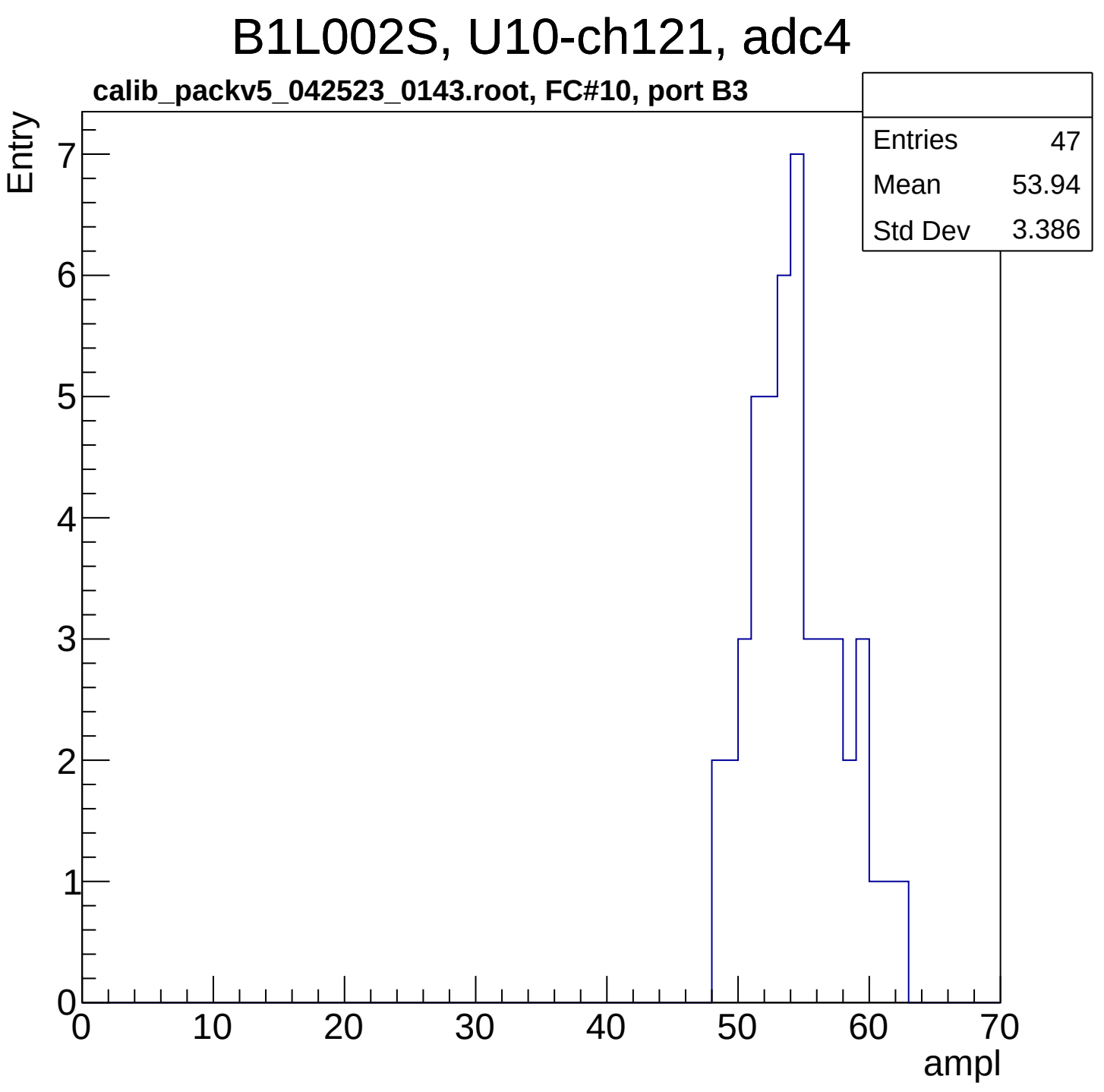
B1L002S, U10-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.94
Std Dev 3.386
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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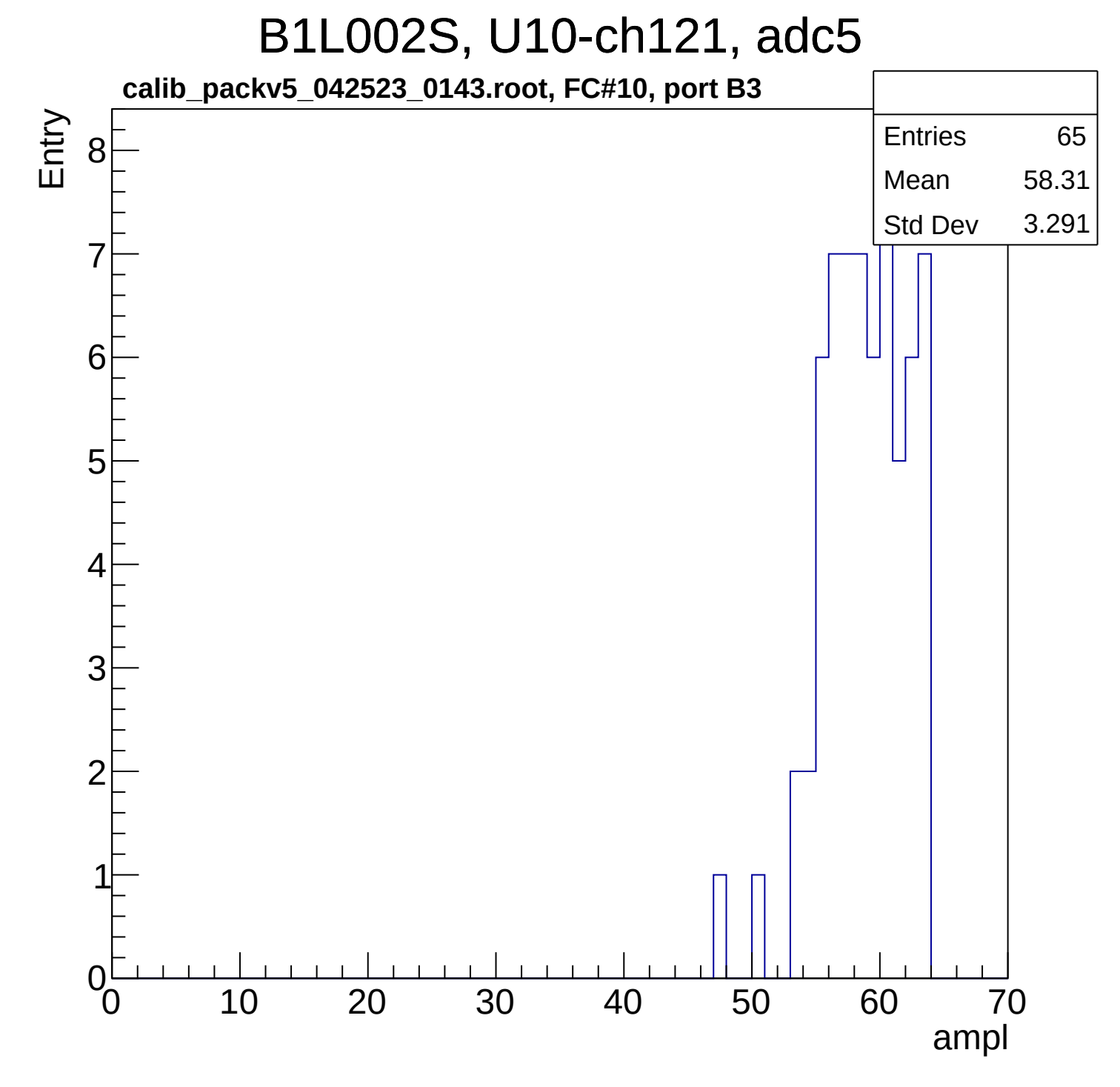
B1L002S, U10-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.31
Std Dev 3.291
Entry
ampl
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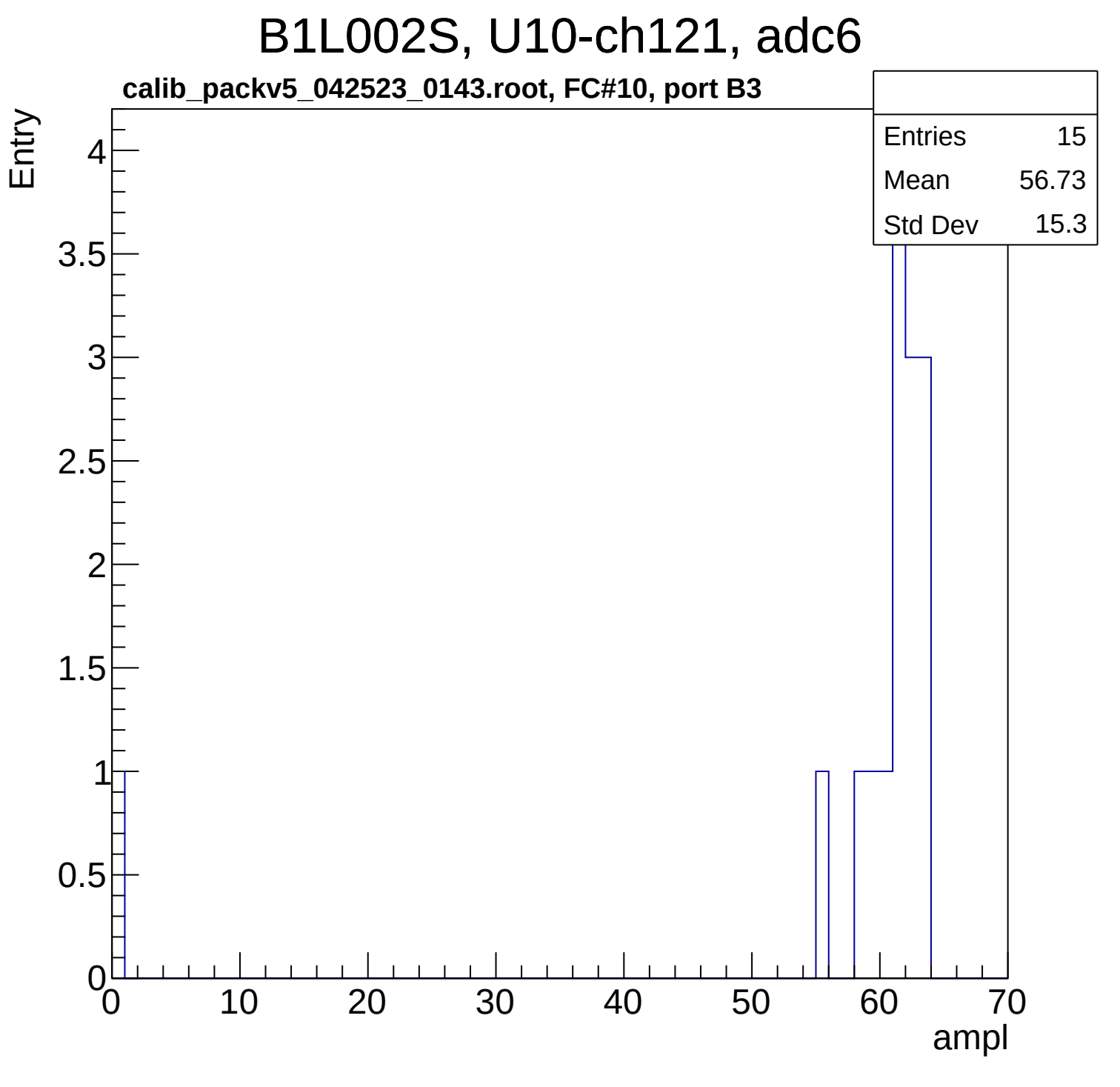
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 56.73
Std Dev 15.3
Entry
ampl
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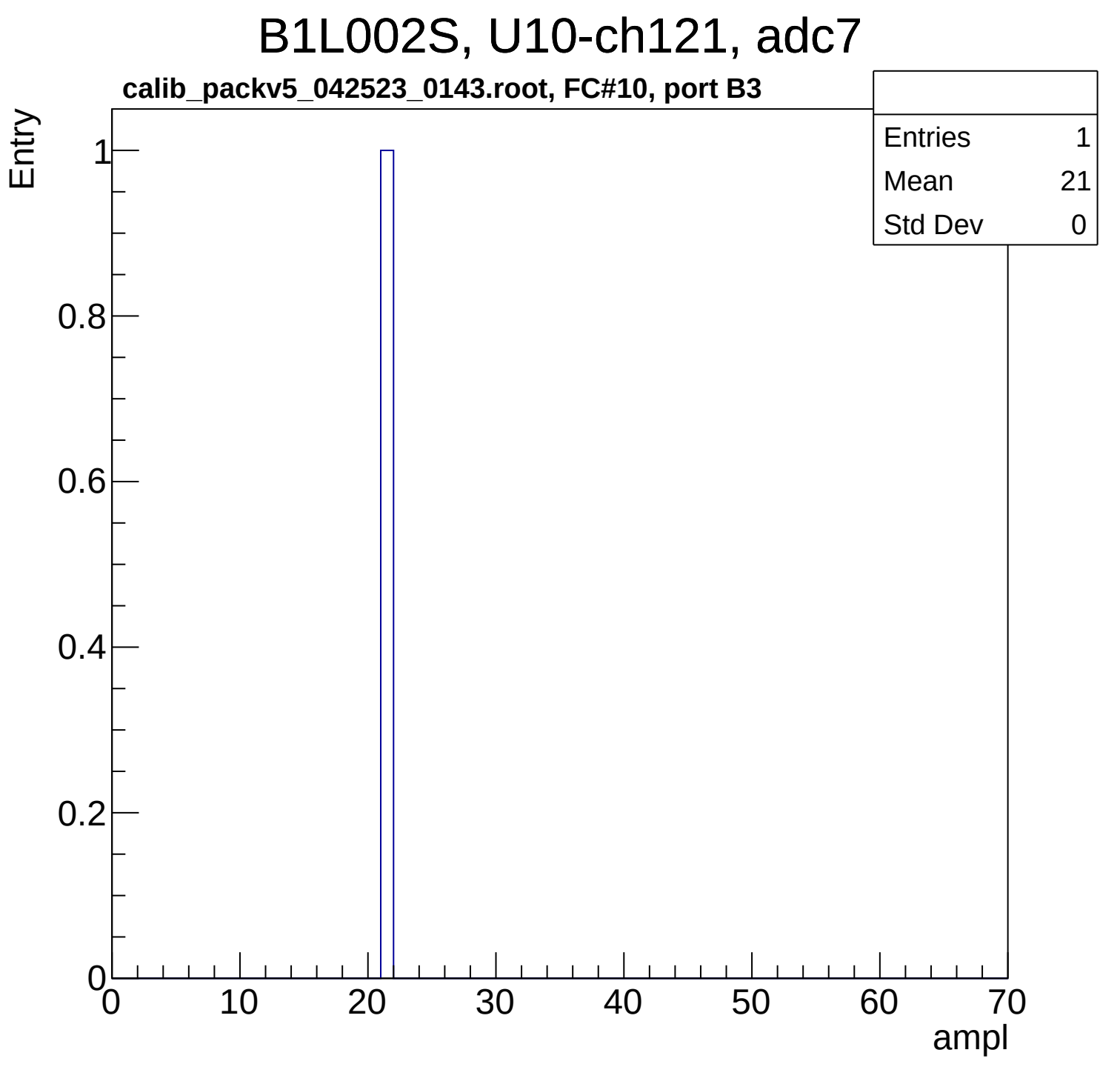
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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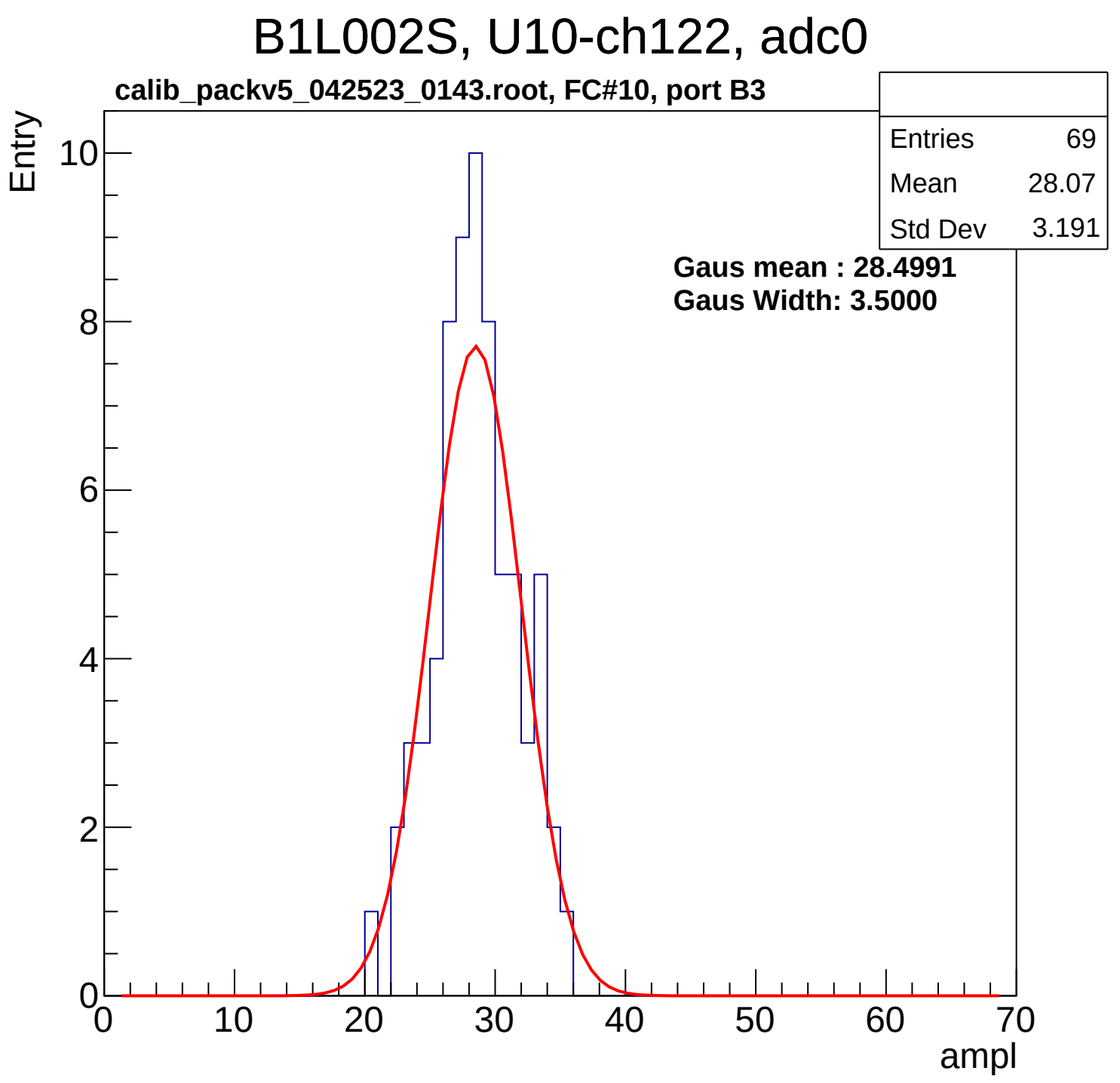
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.07
Std Dev 3.191
Gaus mean : 28.4991
Gaus Width: 3.5000
Entry
ampl
0
2
4
6
8
10
0
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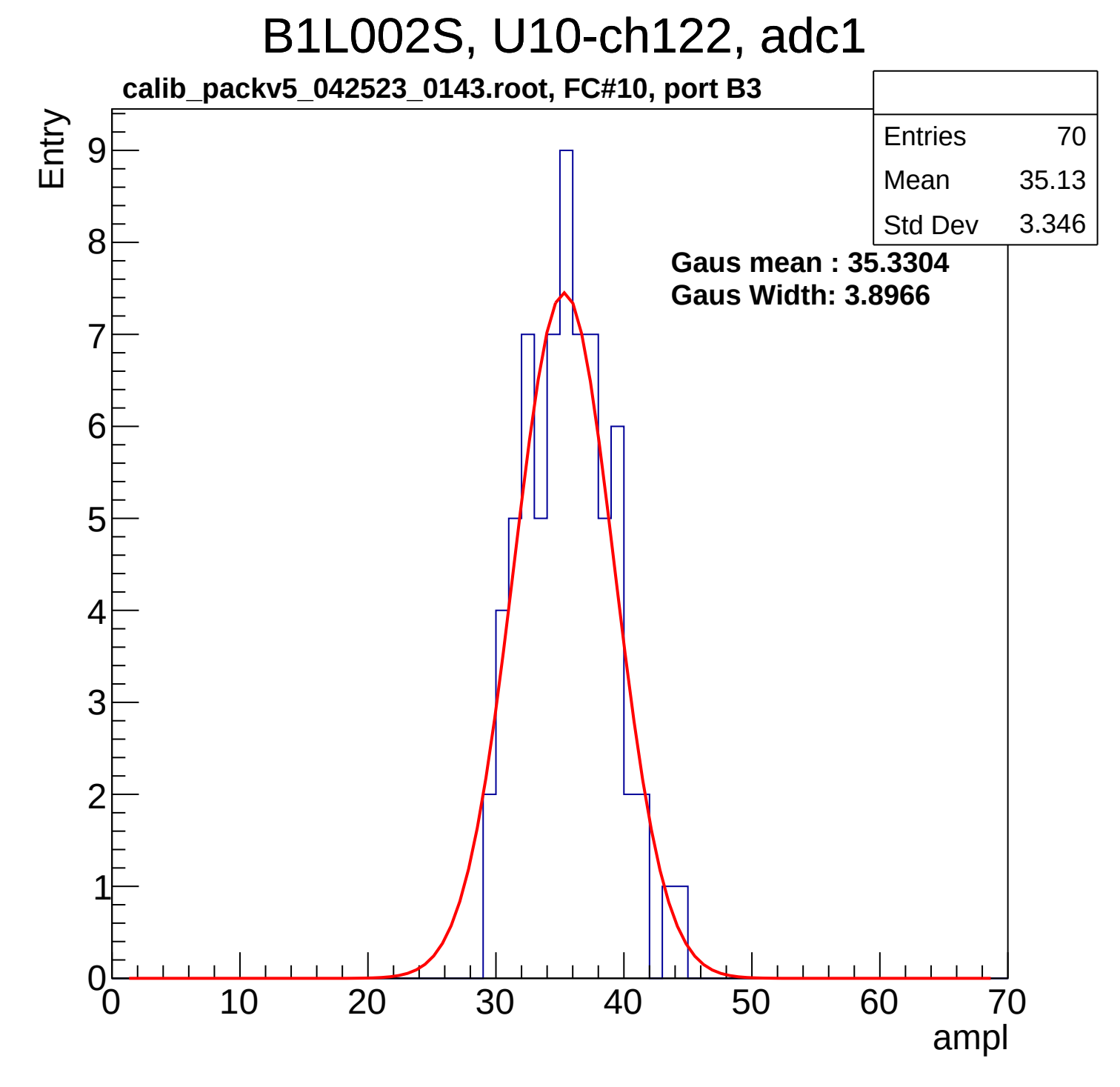
20
30
40
50
60
70


B1L002S, U10-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.13
Std Dev 3.346
Gaus mean : 35.3304
Gaus Width: 3.8966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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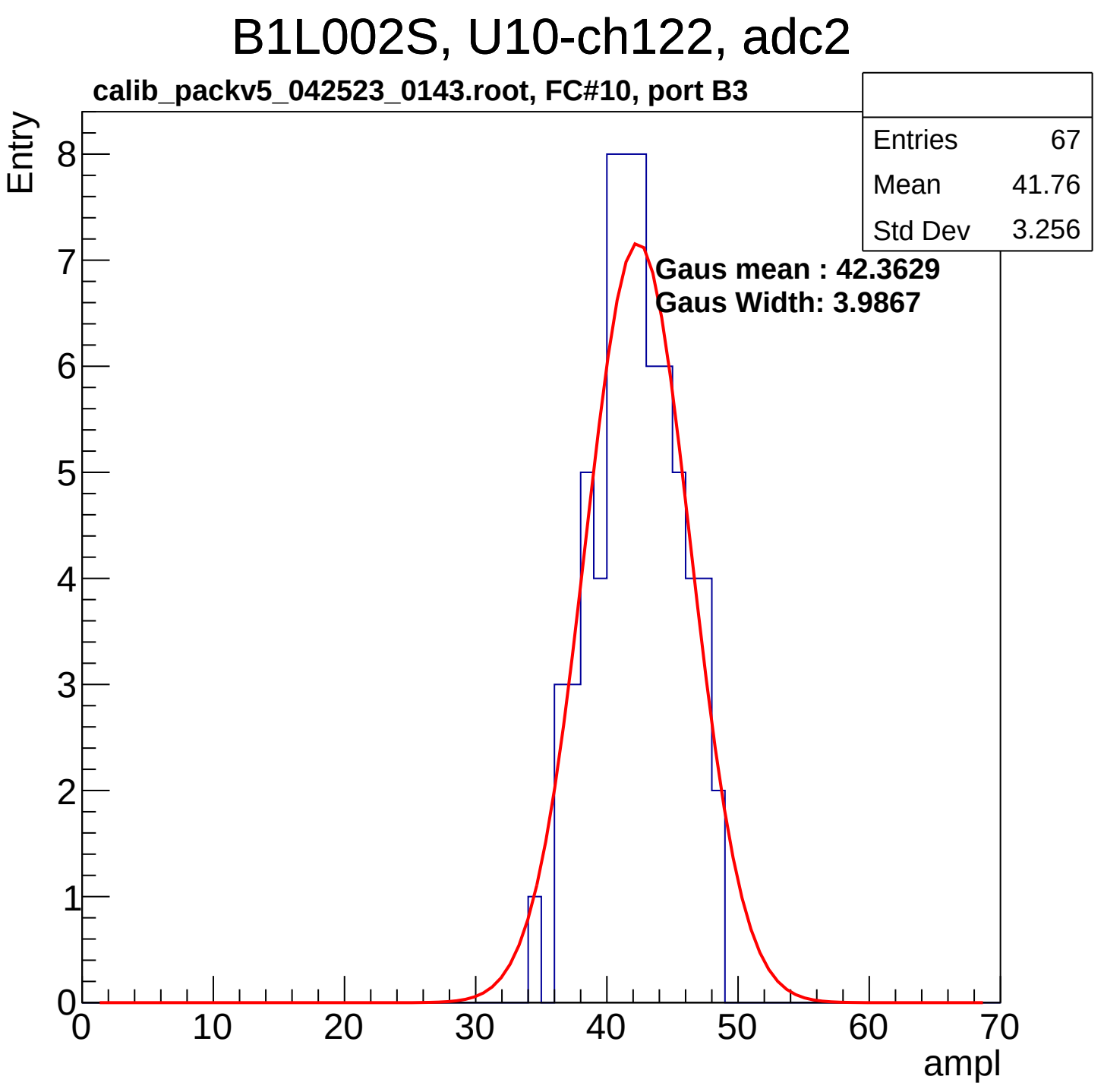
B1L002S, U10-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.76
Std Dev 3.256
Gaus mean : 42.3629
Gaus Width: 3.9867
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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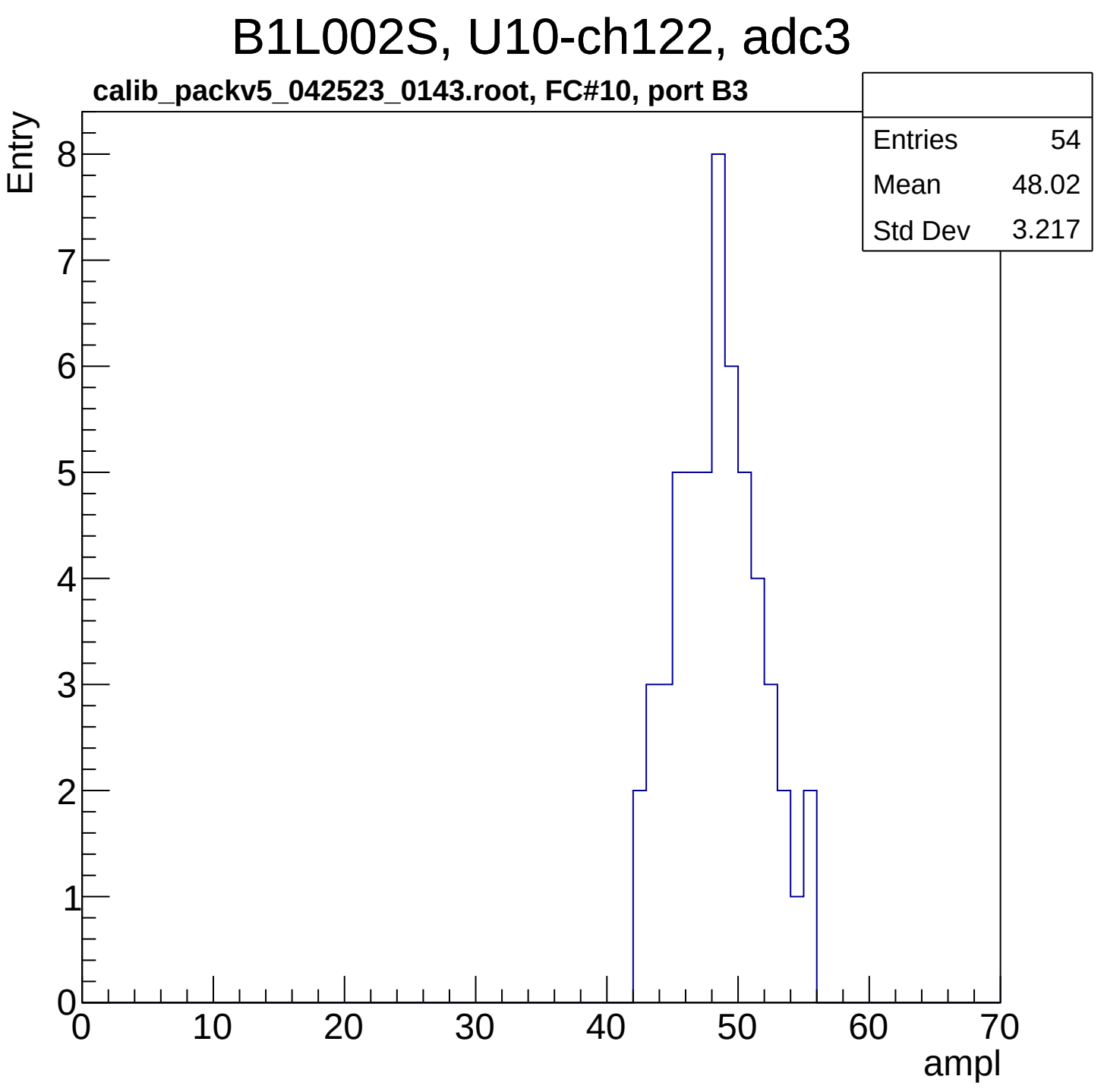
B1L002S, U10-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.02
Std Dev 3.217
Entry
ampl
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4
5
6
7
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0
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20
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40
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60
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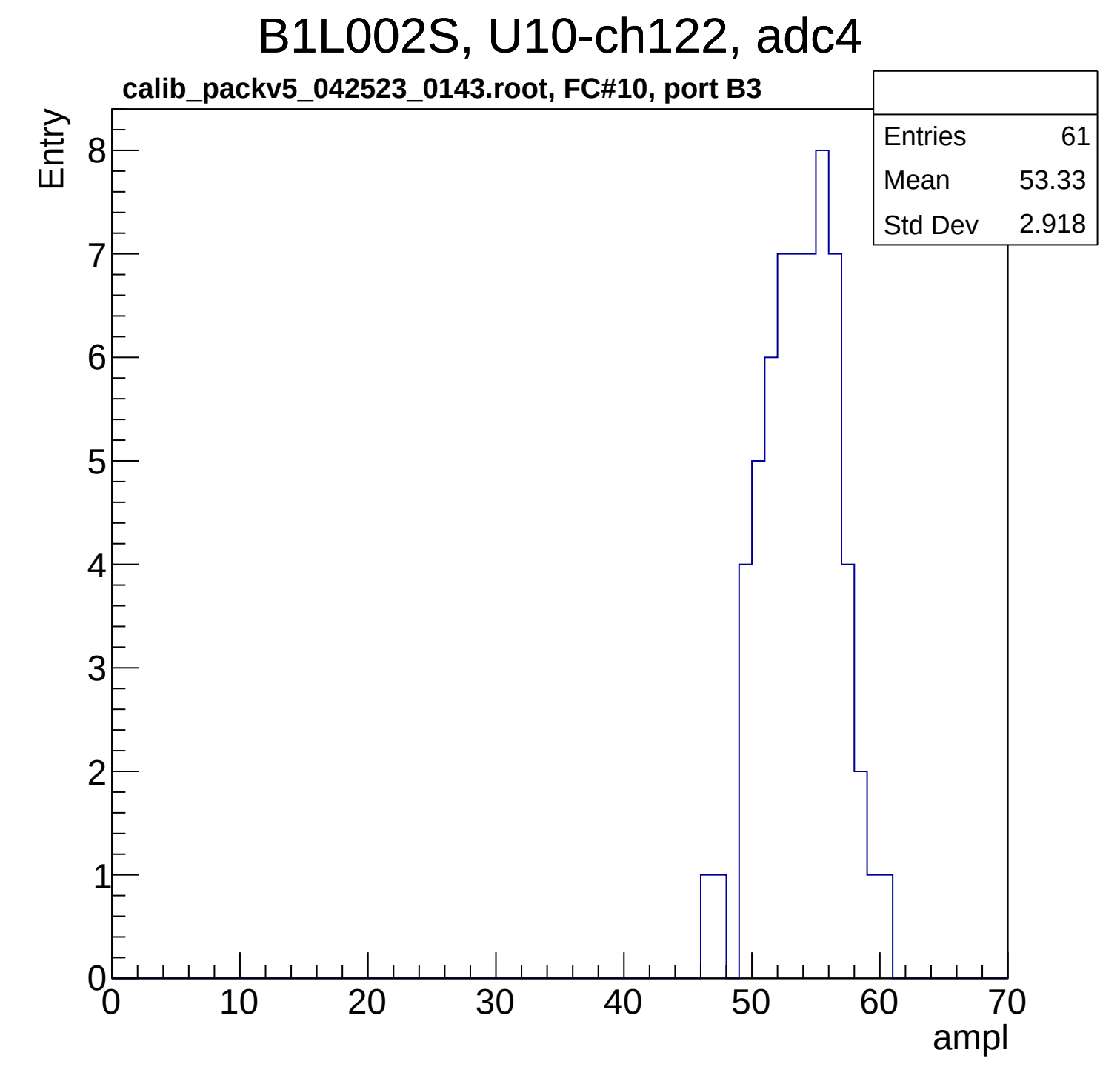
B1L002S, U10-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.33
Std Dev 2.918
Entry
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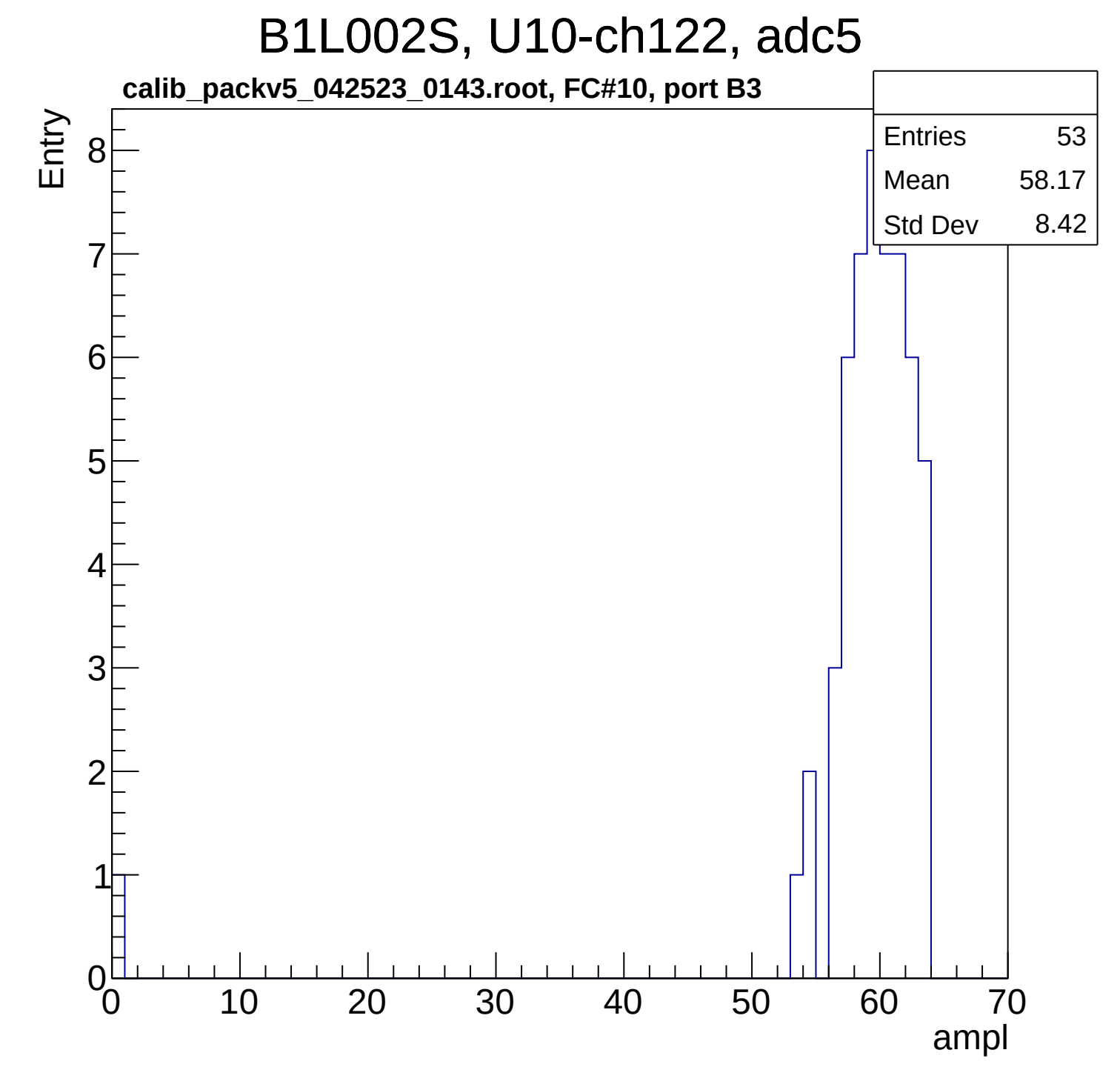
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.17
Std Dev 8.42
Entry
ampl
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5
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0
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40
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60
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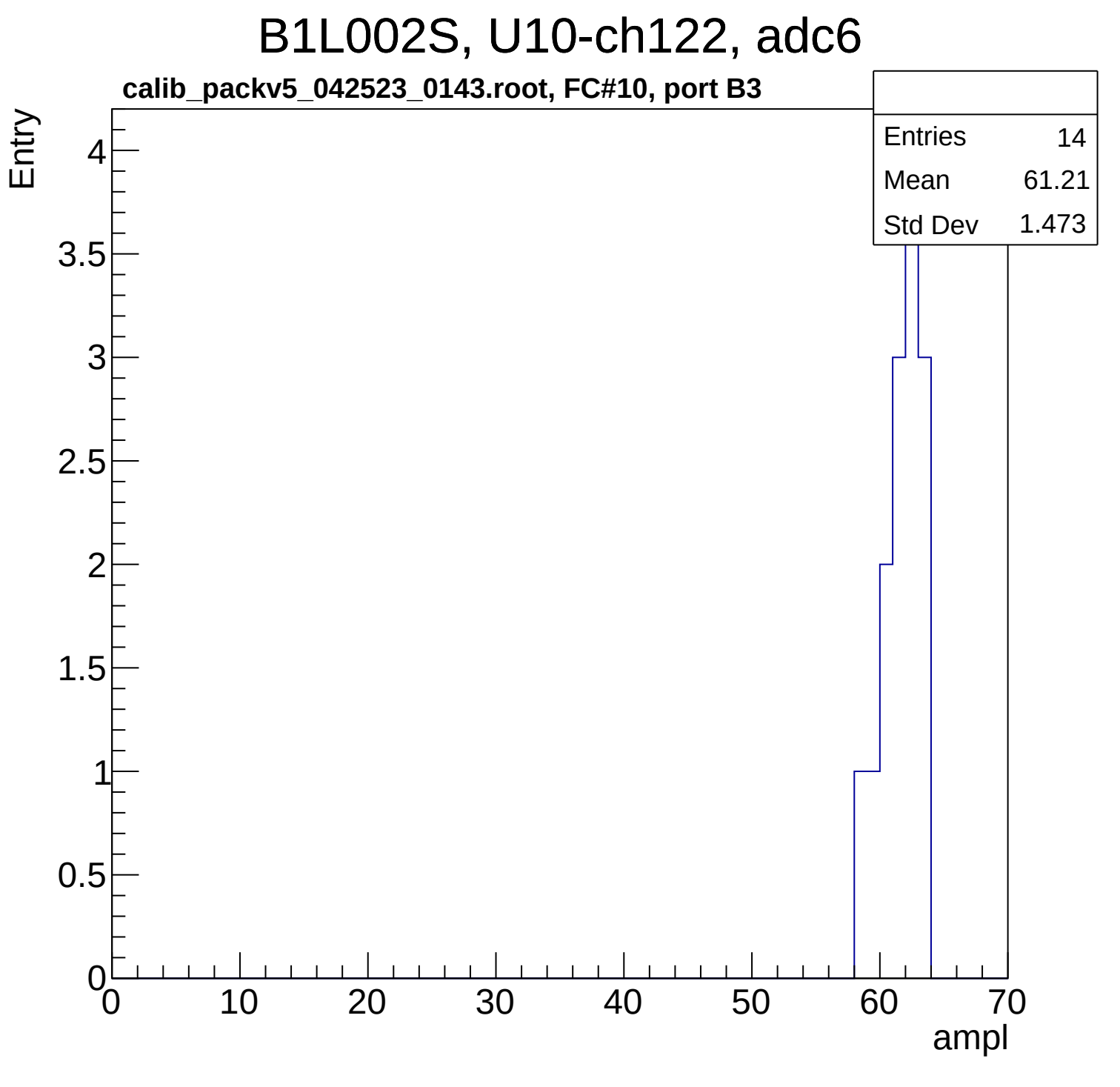
B1L002S, U10-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.21
Std Dev 1.473
Entry
ampl
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1.5
2
2.5
3
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60
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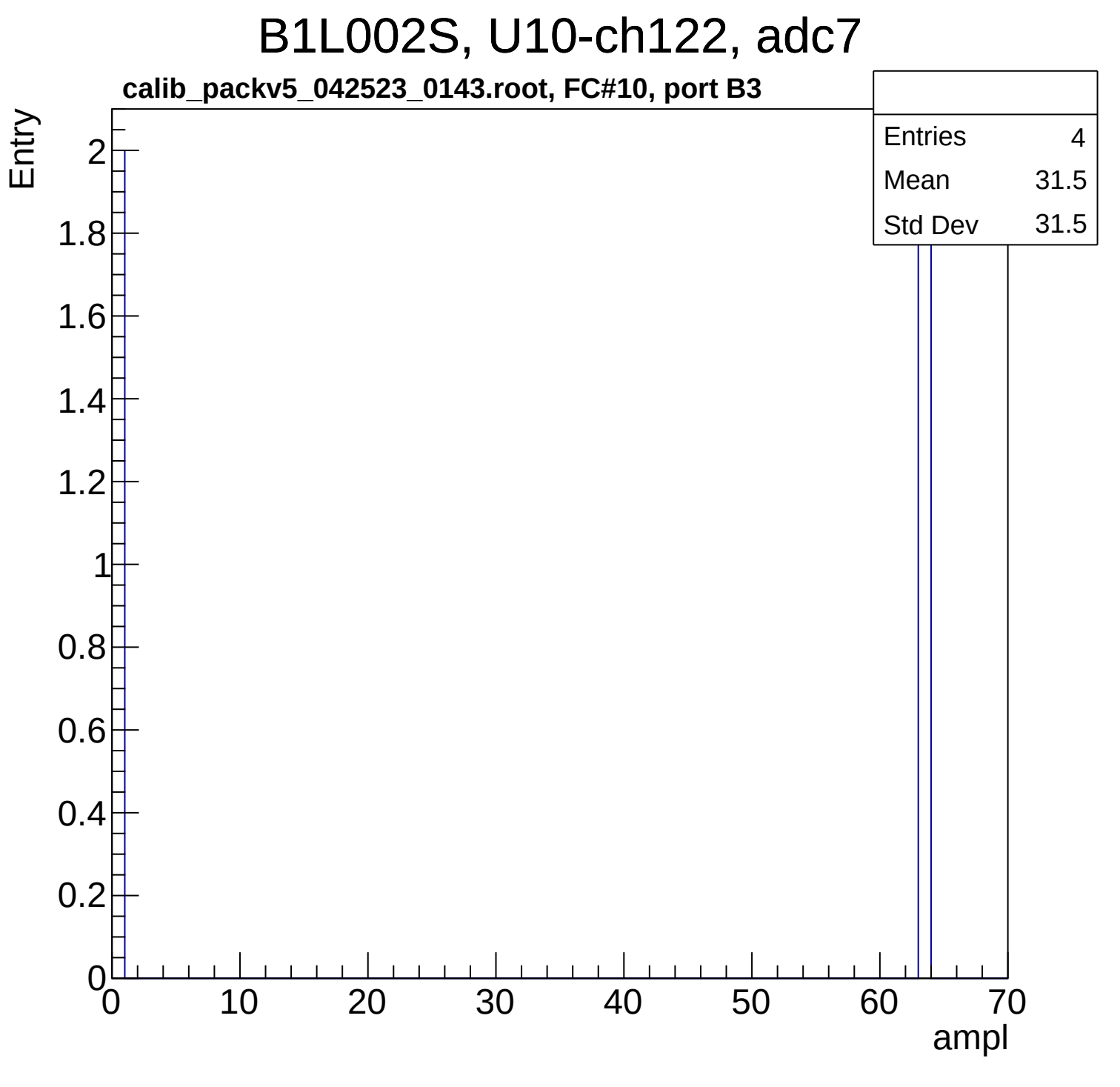
B1L002S, U10-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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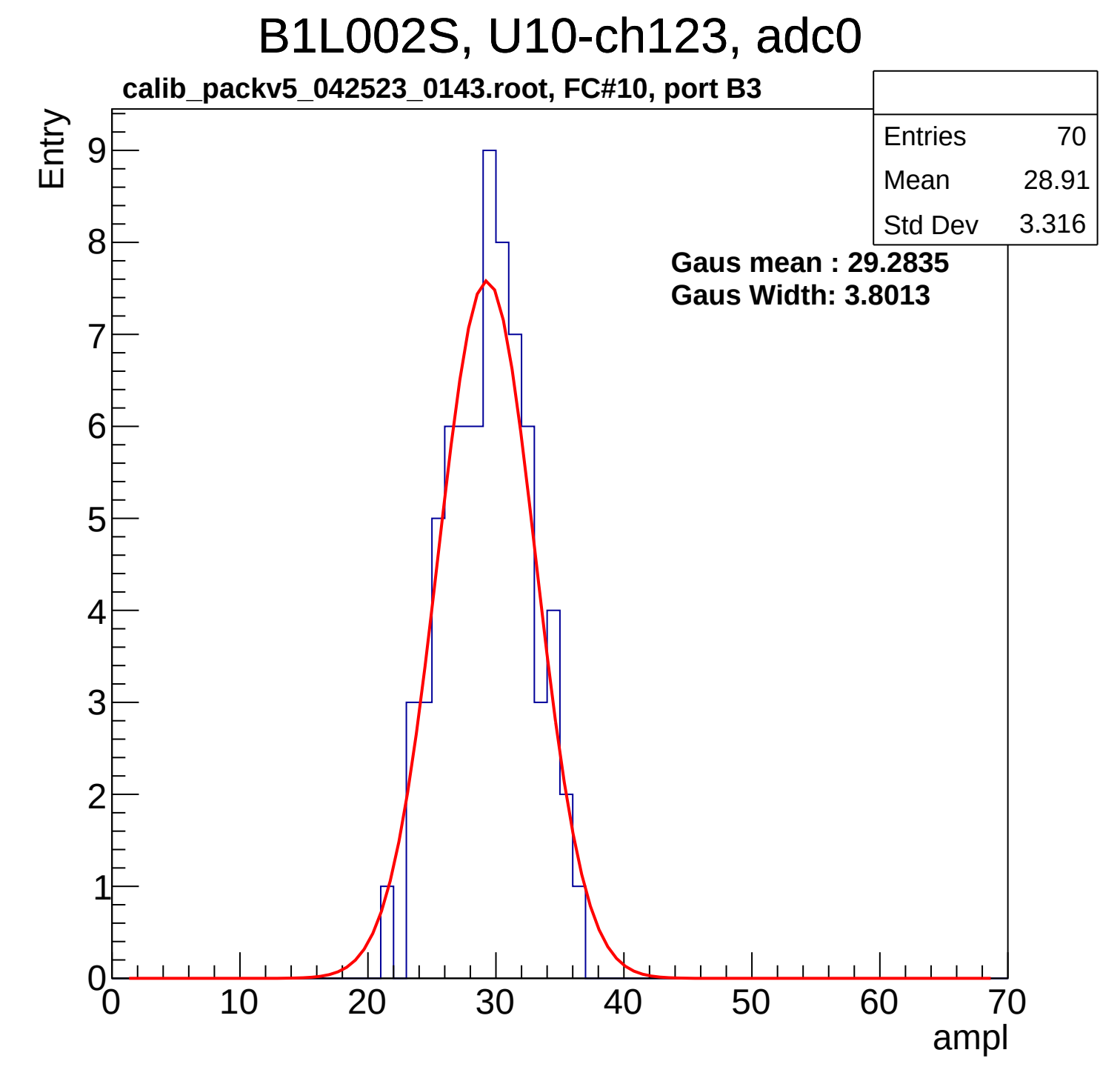
B1L002S, U10-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.91
Std Dev 3.316
Gaus mean : 29.2835
Gaus Width: 3.8013
Entry
ampl
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2
3
4
5
6
7
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0
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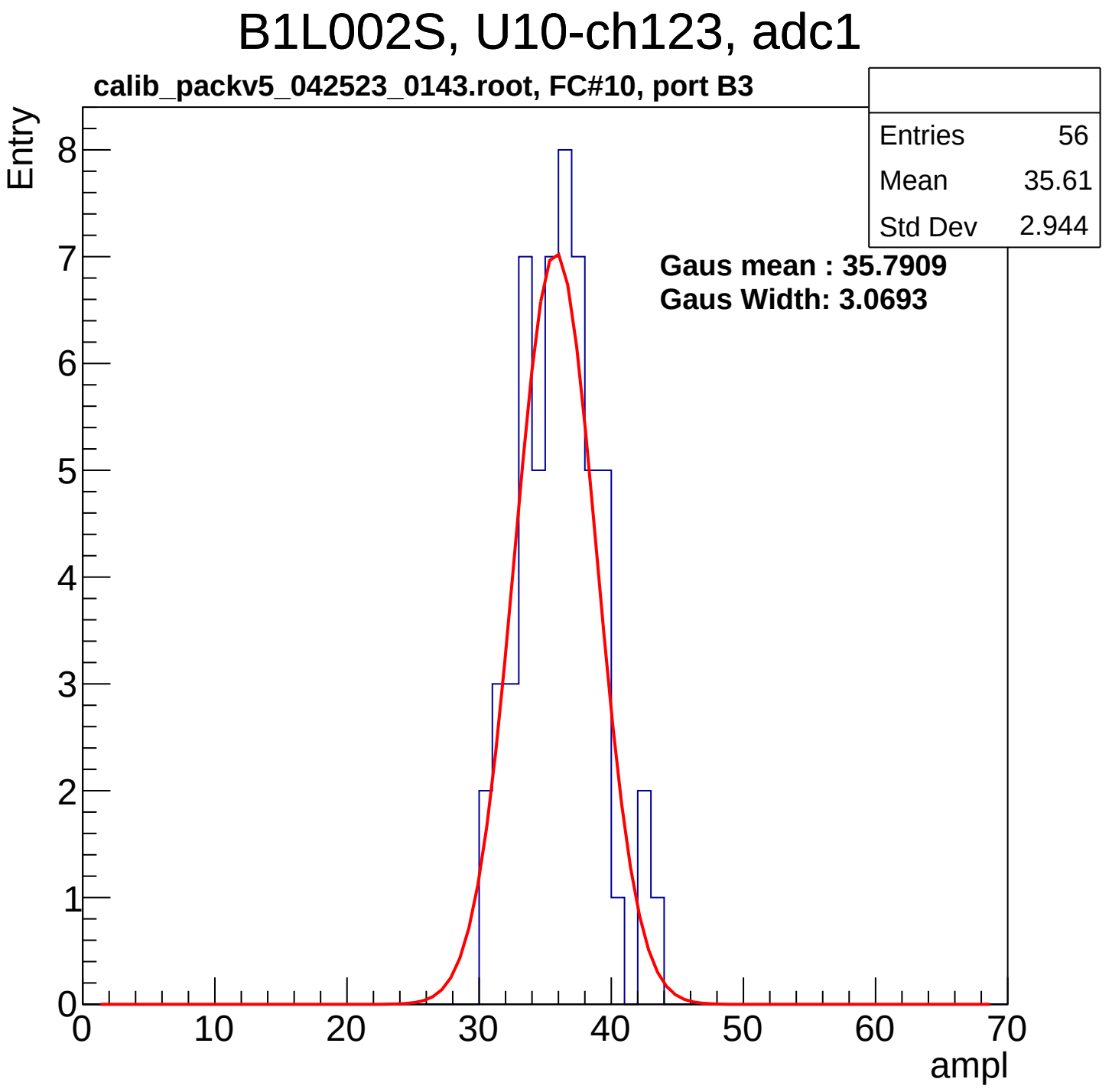
B1L002S, U10-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.61
Std Dev 2.944
Gaus mean : 35.7909
Gaus Width: 3.0693
Entry
ampl
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2
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4
5
6
7
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0
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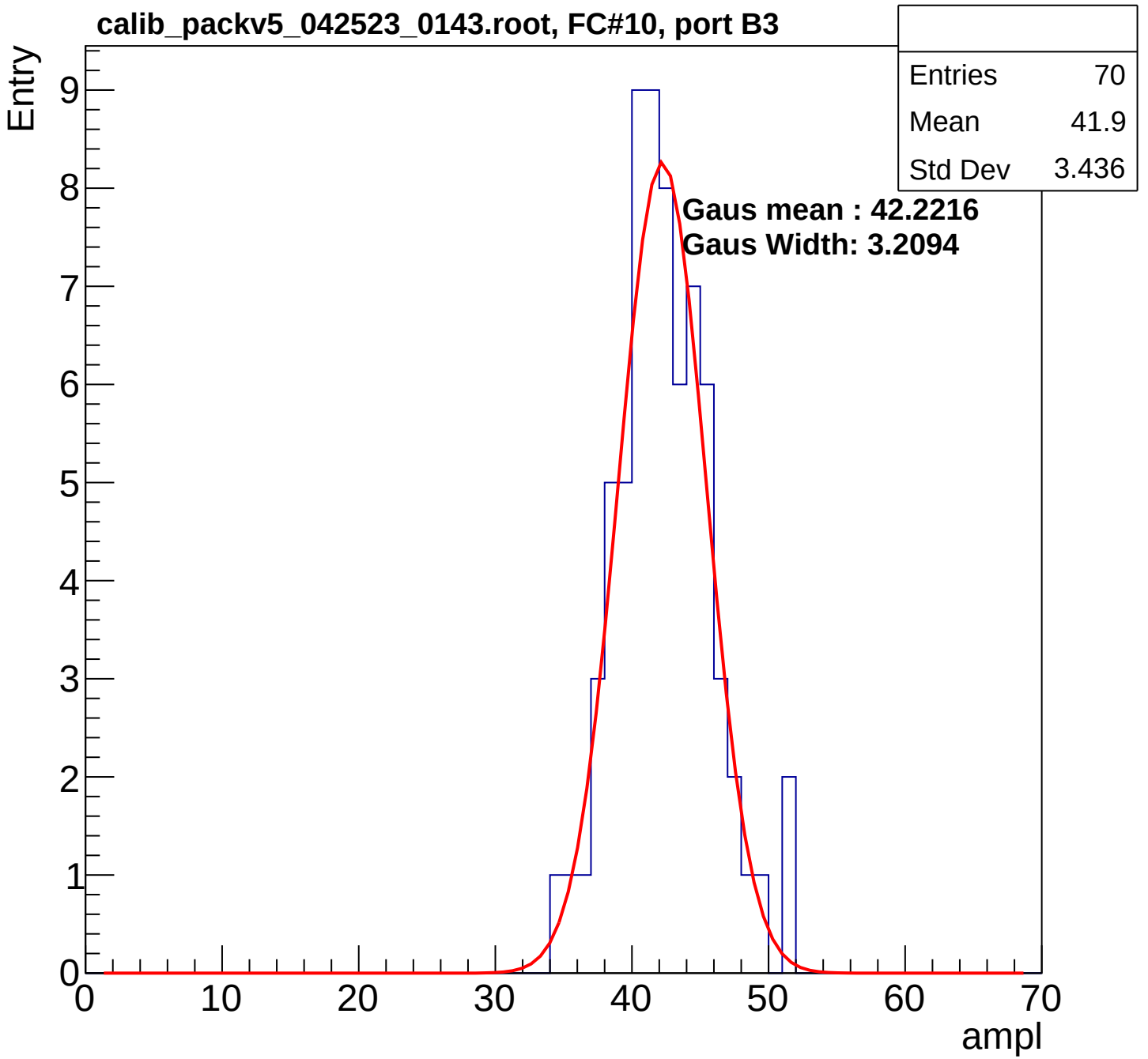
B1L002S, U10-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.9
Std Dev 3.436
Gaus mean : 42.2216
Gaus Width: 3.2094
Entry
ampl
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2
3
4
5
6
7
8
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0
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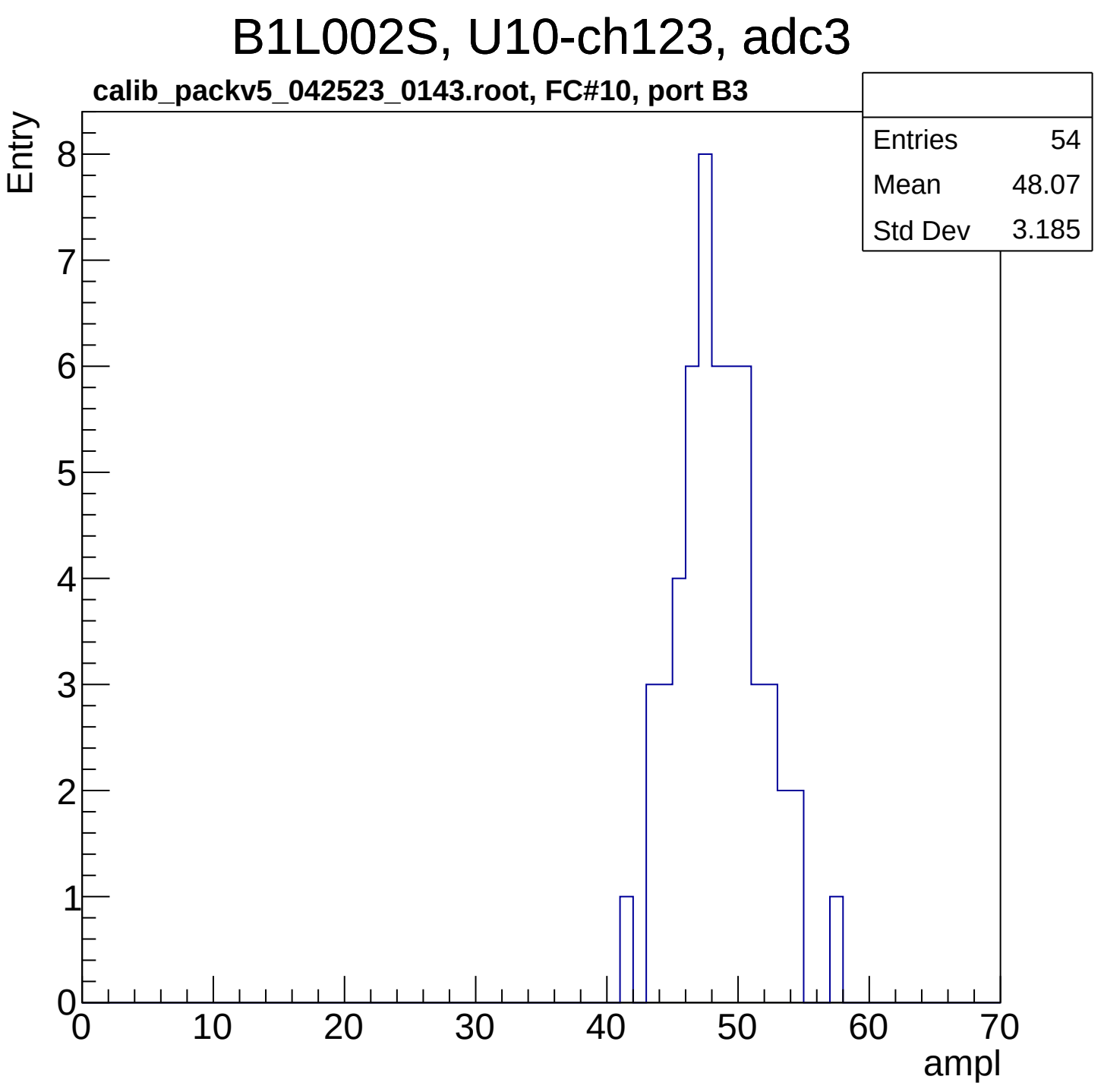
B1L002S, U10-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.07
Std Dev 3.185
Entry
ampl
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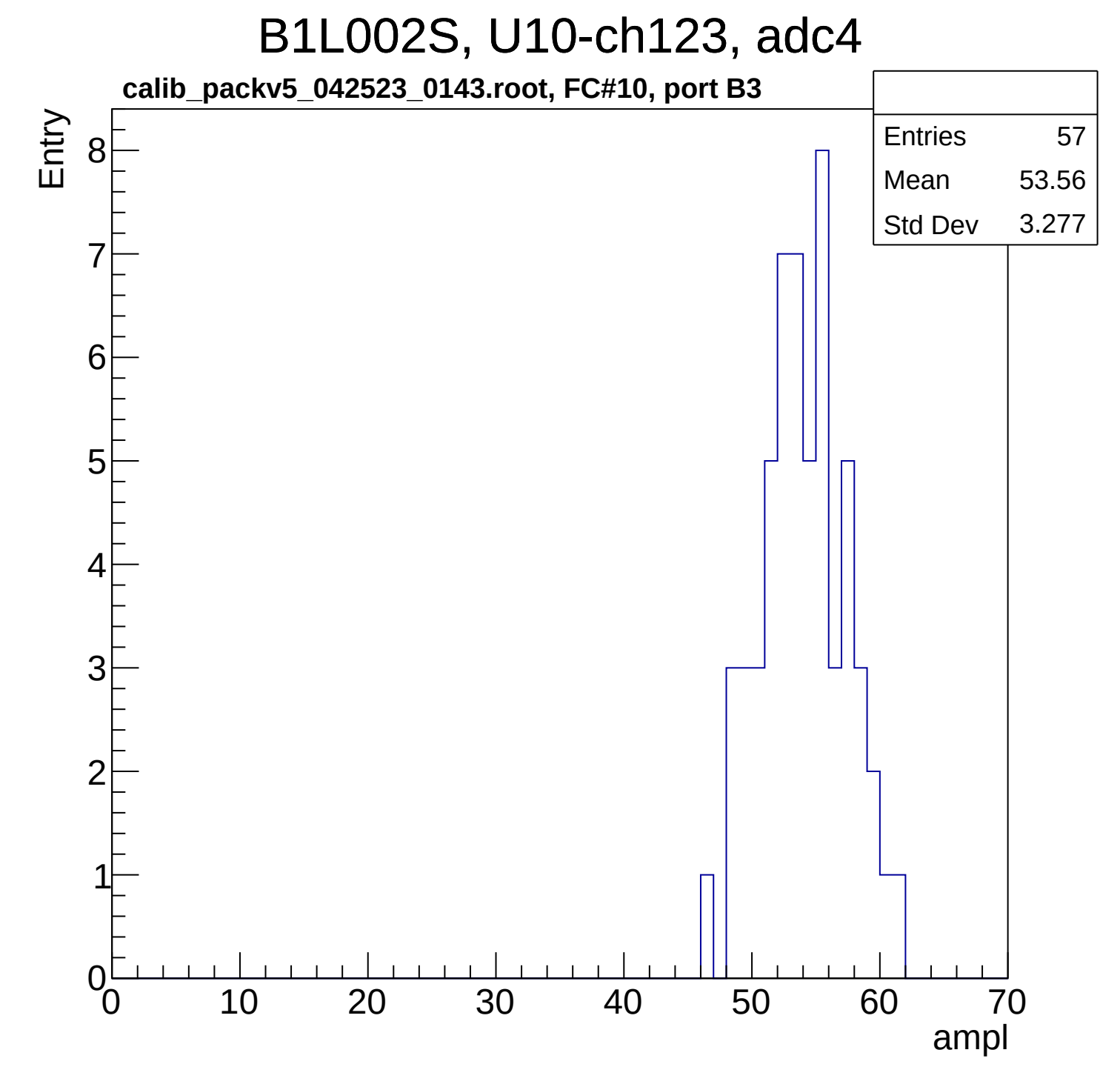
B1L002S, U10-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.56
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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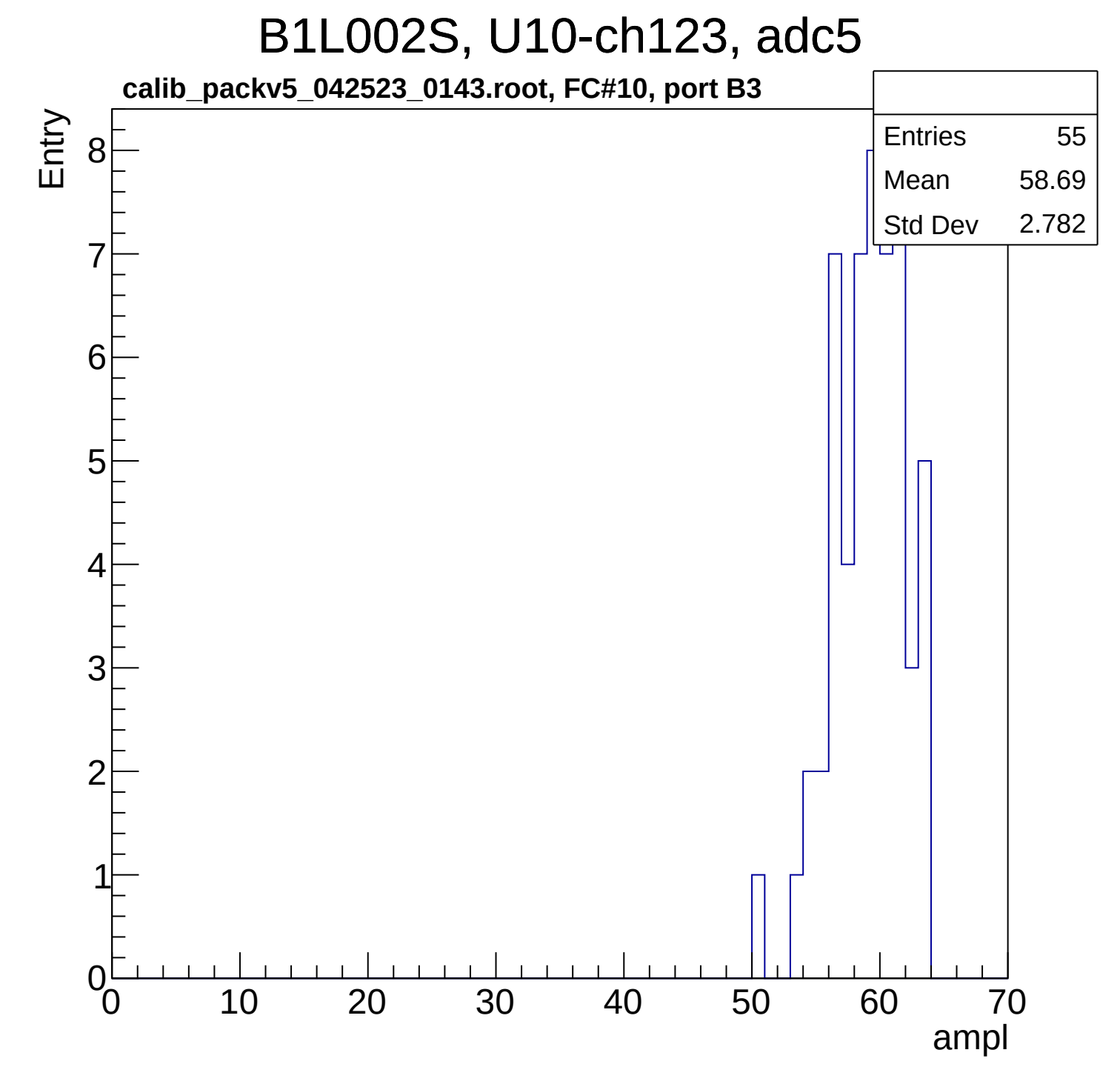
B1L002S, U10-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.69
Std Dev 2.782
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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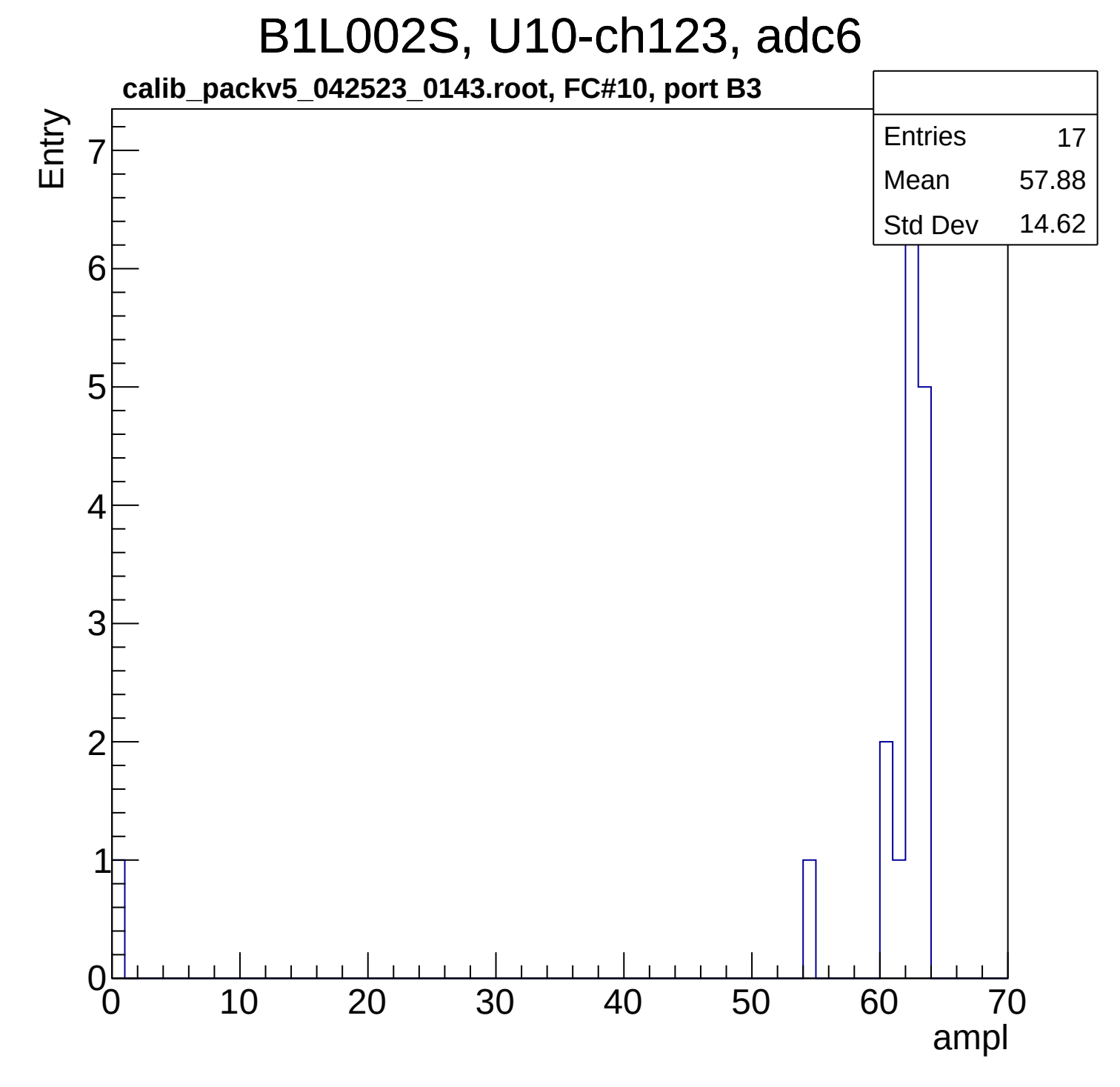
B1L002S, U10-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.88
Std Dev 14.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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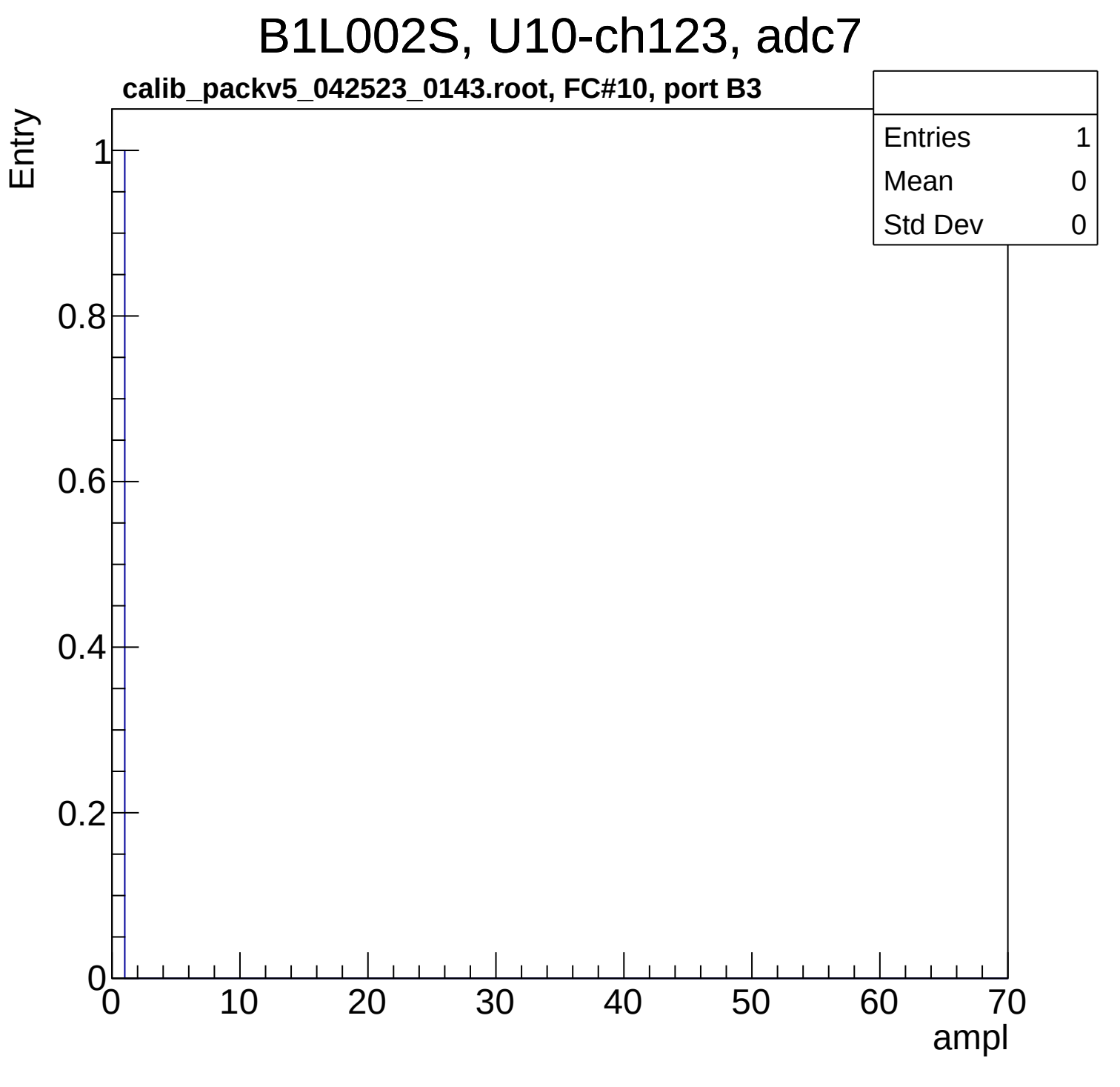
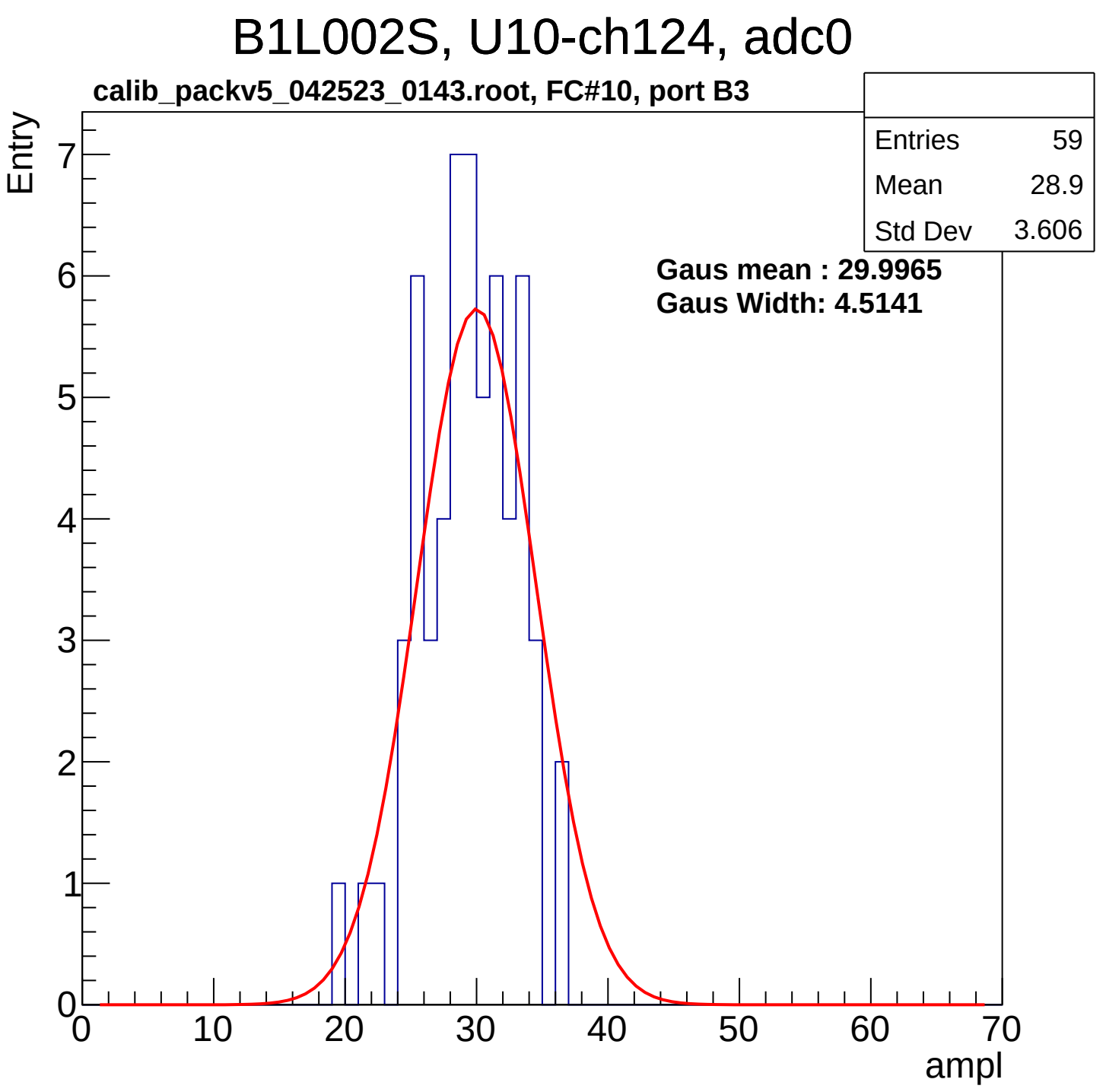

B1L002S, U10-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.9
Std Dev 3.606
Gaus mean : 29.9965
Gaus Width: 4.5141
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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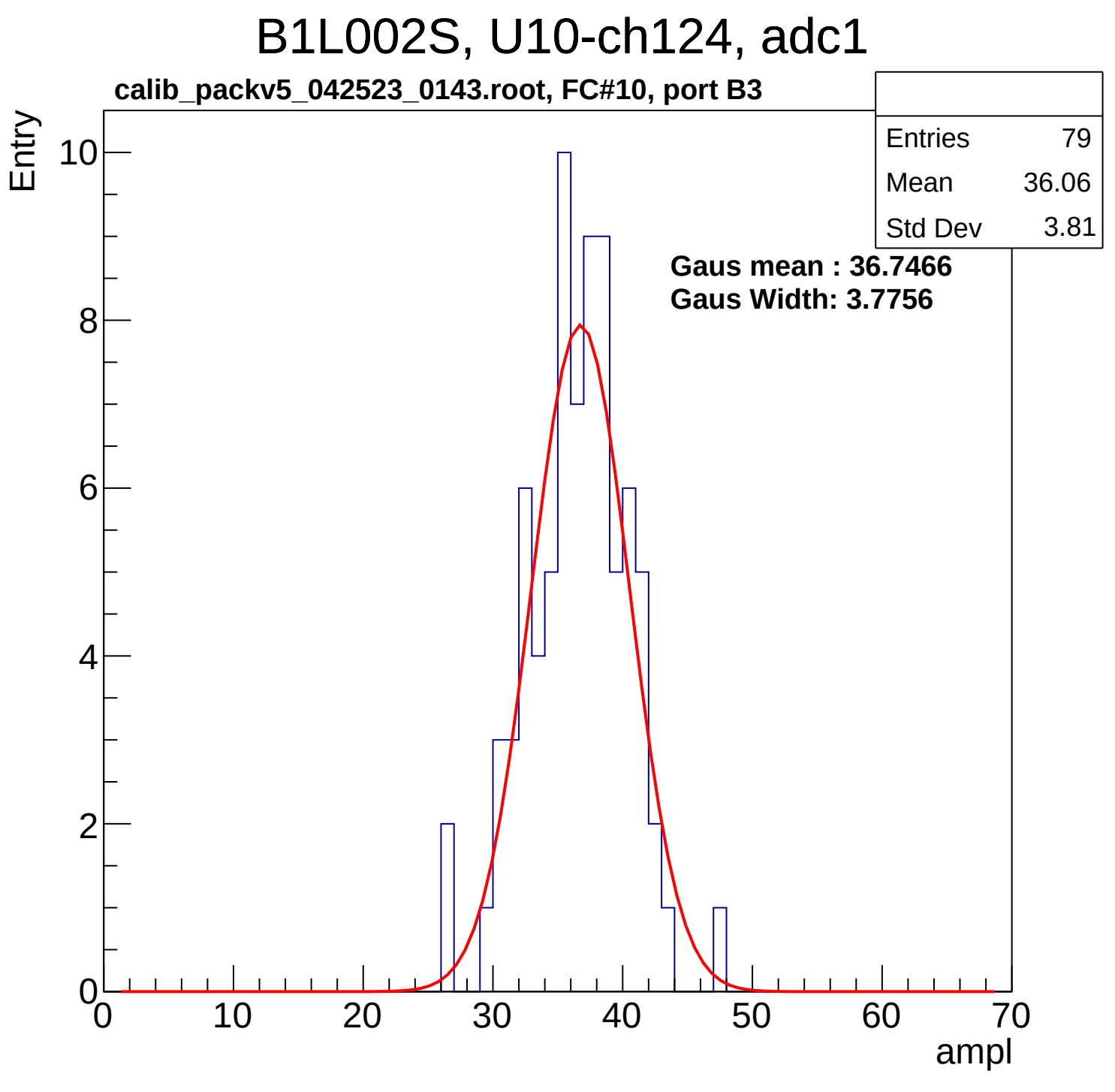
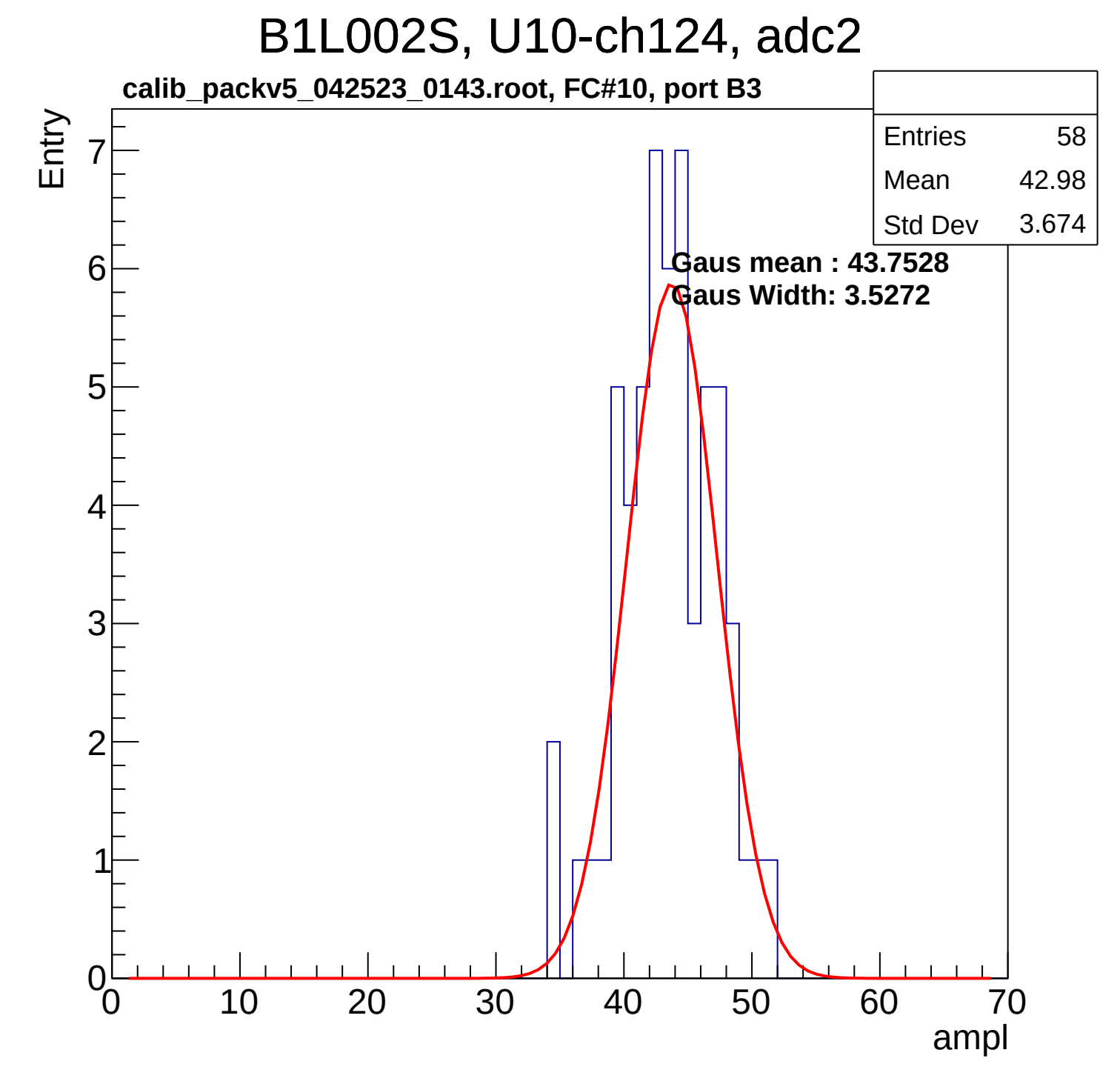

B1L002S, U10-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 36.06
Std Dev 3.81
Gaus mean : 36.7466
Gaus Width: 3.7756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.98
Std Dev 3.674
Gaus mean : 43.7528
Gaus Width: 3.5272
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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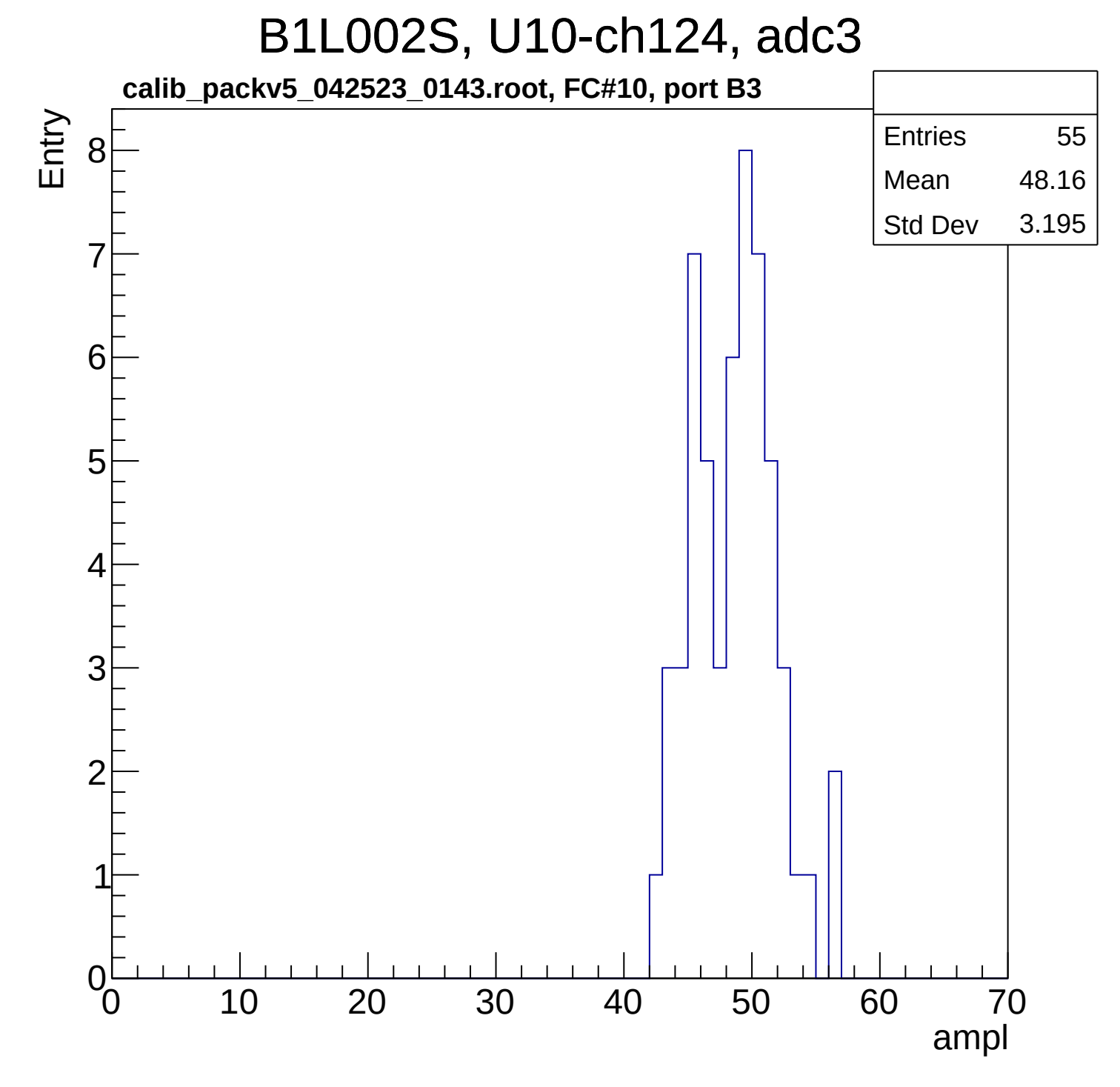
B1L002S, U10-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.16
Std Dev 3.195
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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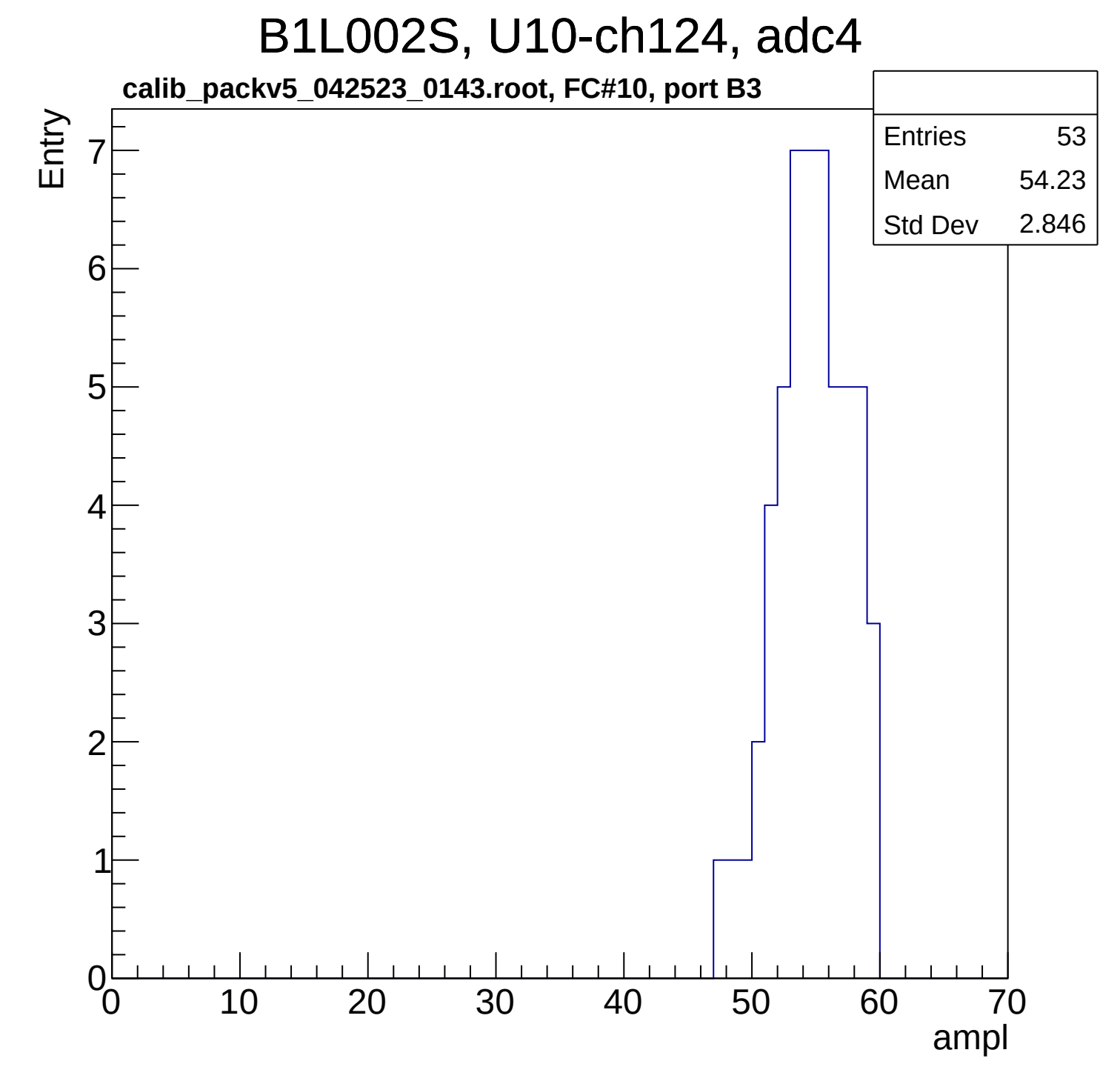
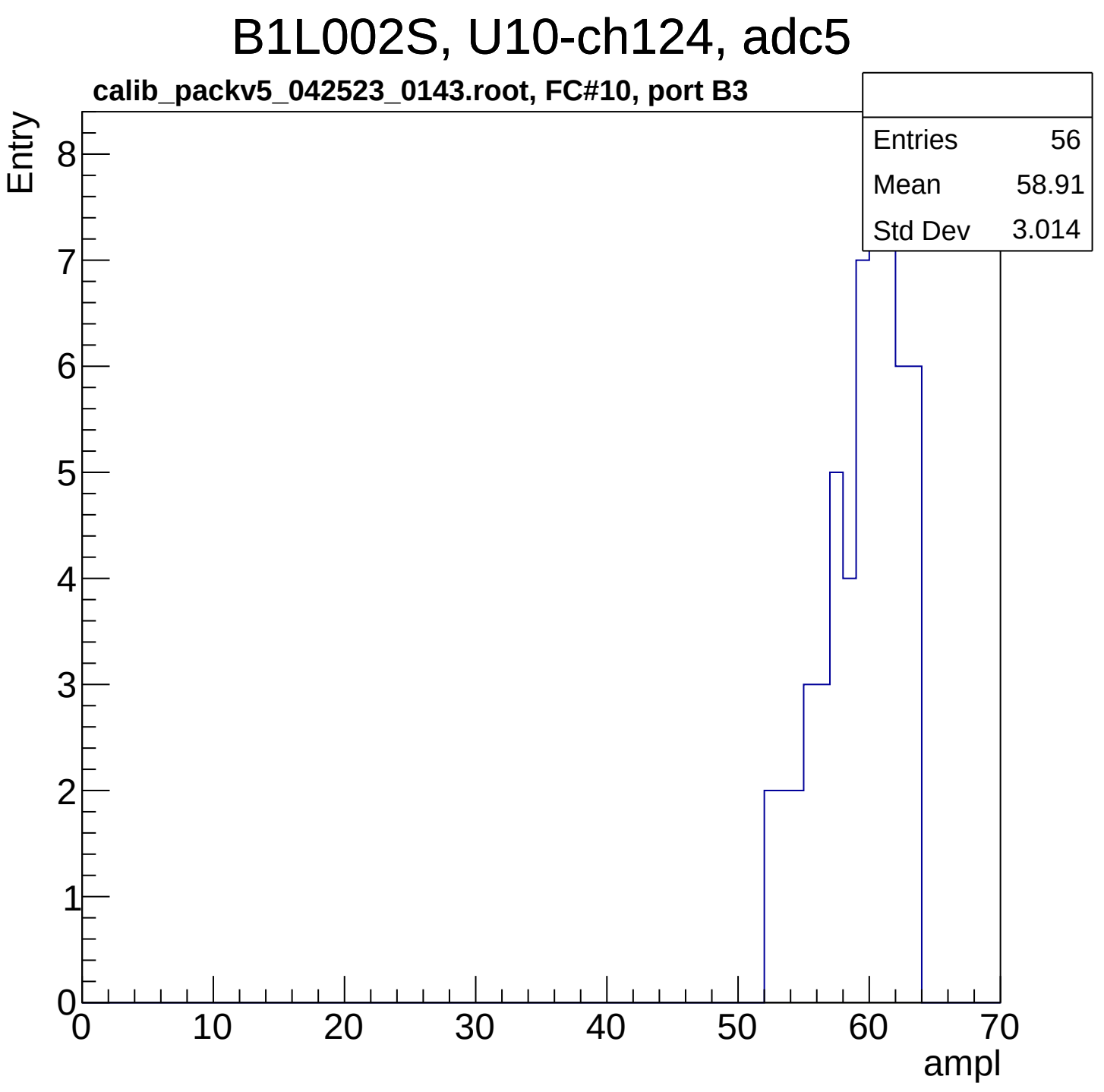
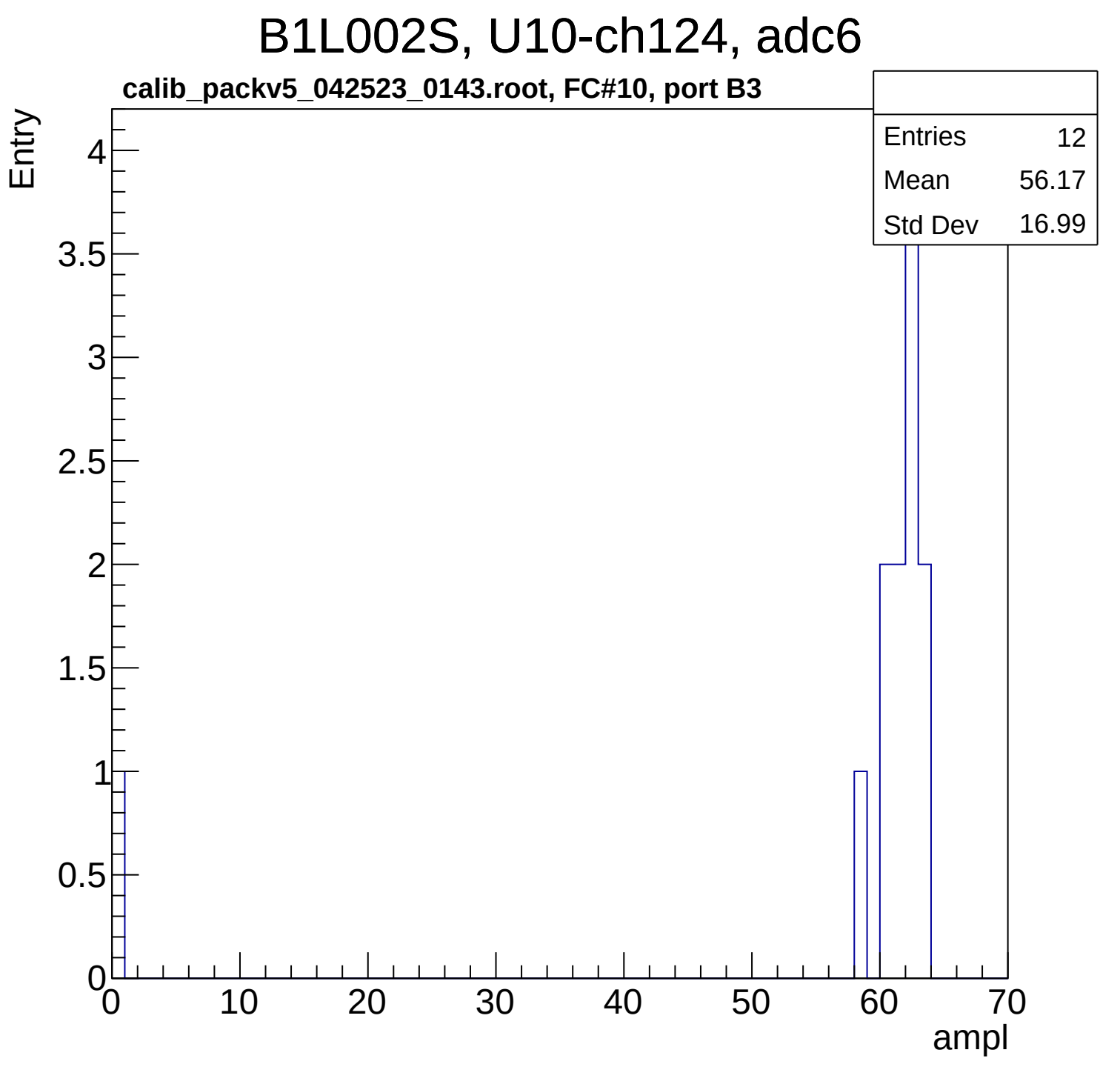

B1L002S, U10-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.23
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.91
Std Dev 3.014
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.17
Std Dev 16.99
Entry
ampl
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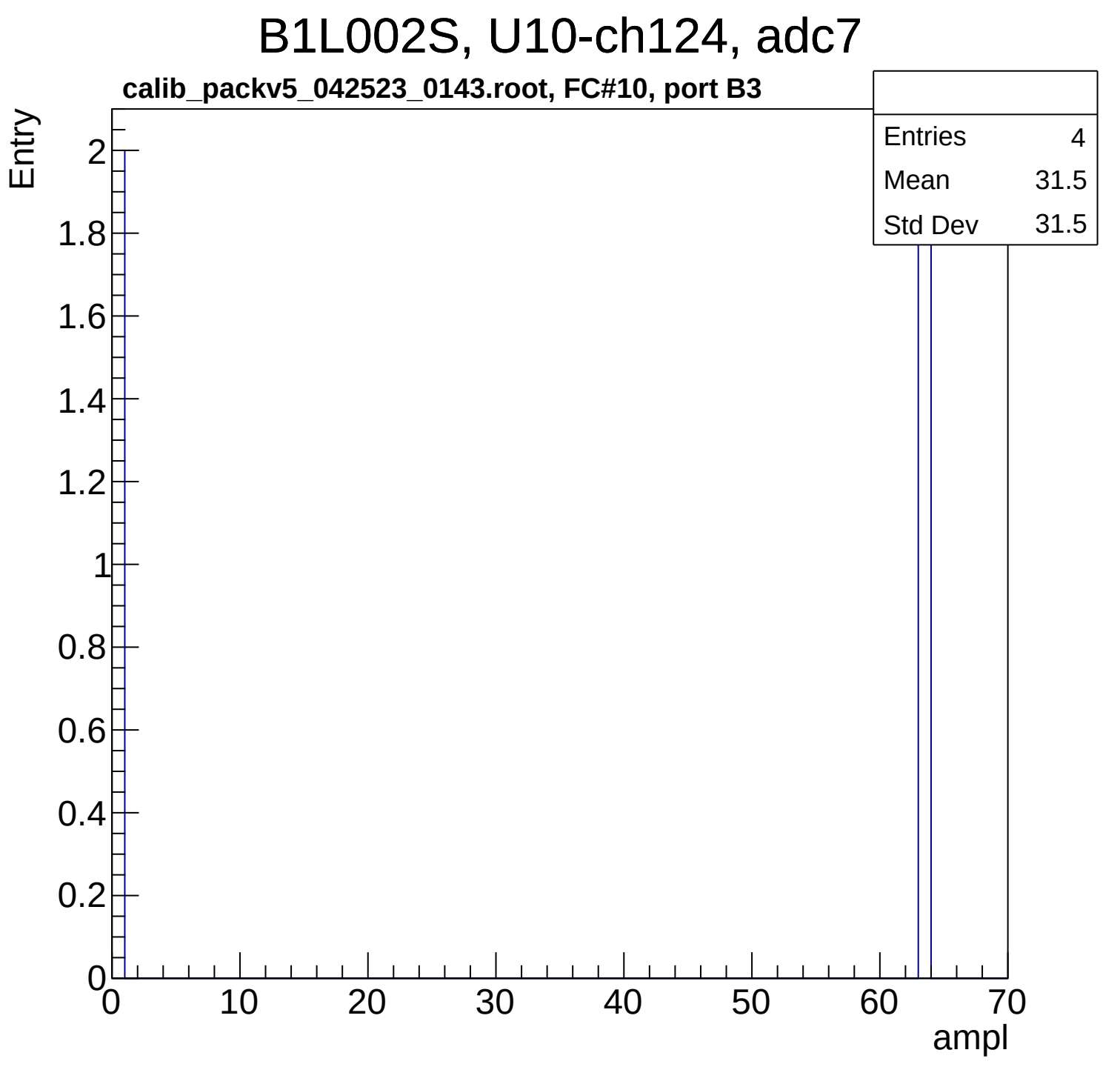
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U10-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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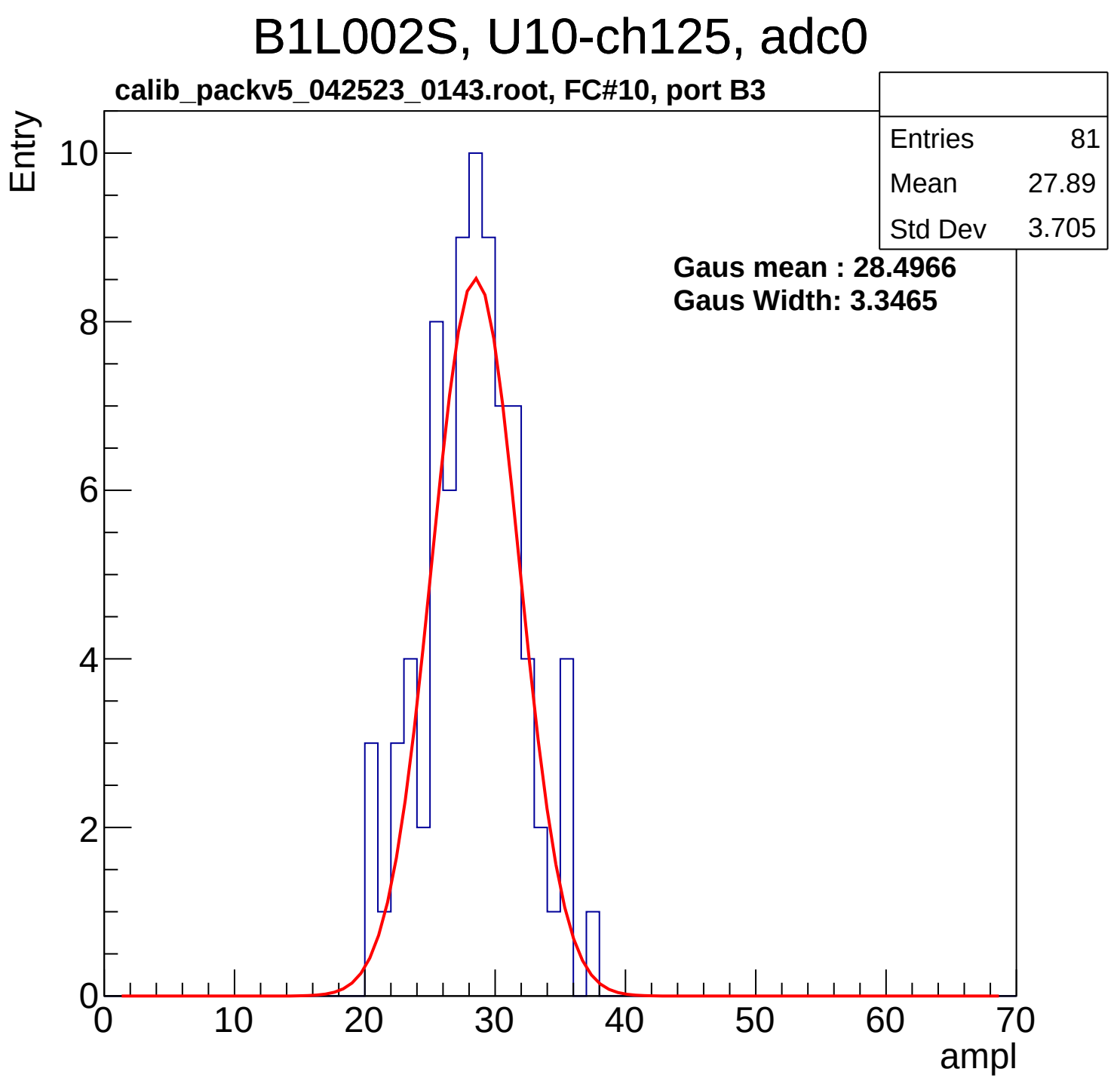
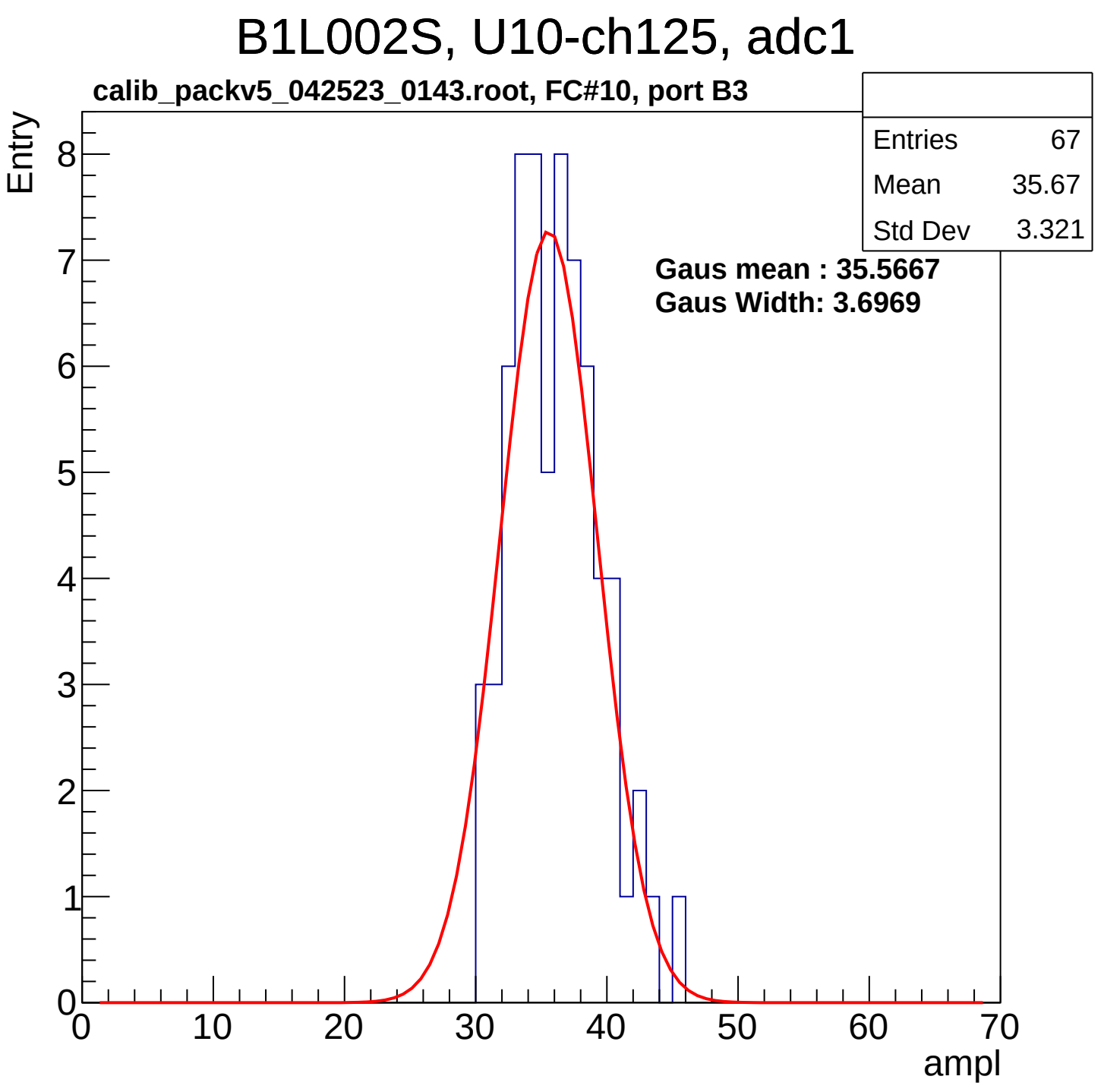
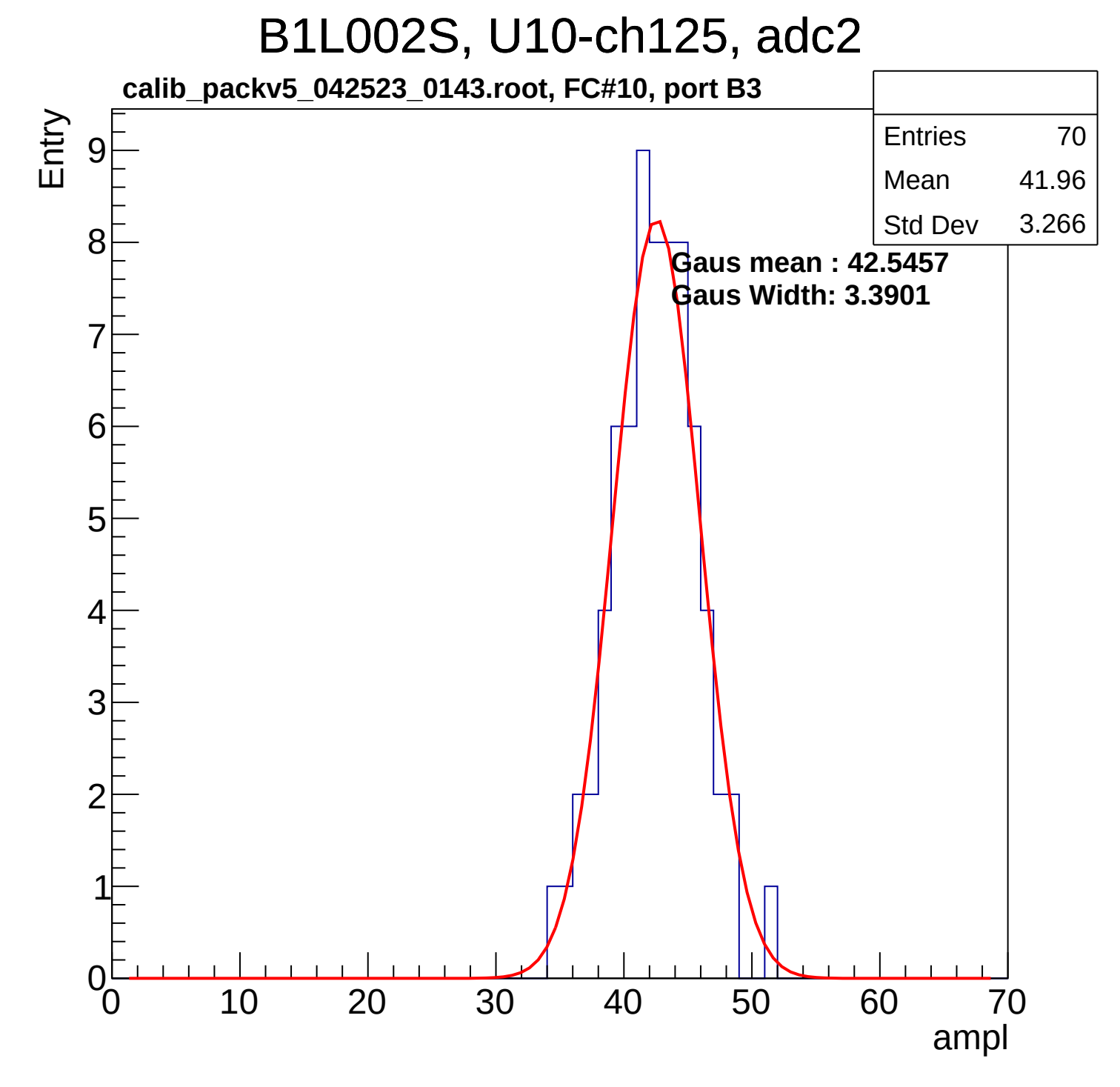

B1L002S, U10-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 27.89
Std Dev 3.705
Gaus mean : 28.4966
Gaus Width: 3.3465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.67
Std Dev 3.321
Gaus mean : 35.5667
Gaus Width: 3.6969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.96
Std Dev 3.266
Gaus mean : 42.5457
Gaus Width: 3.3901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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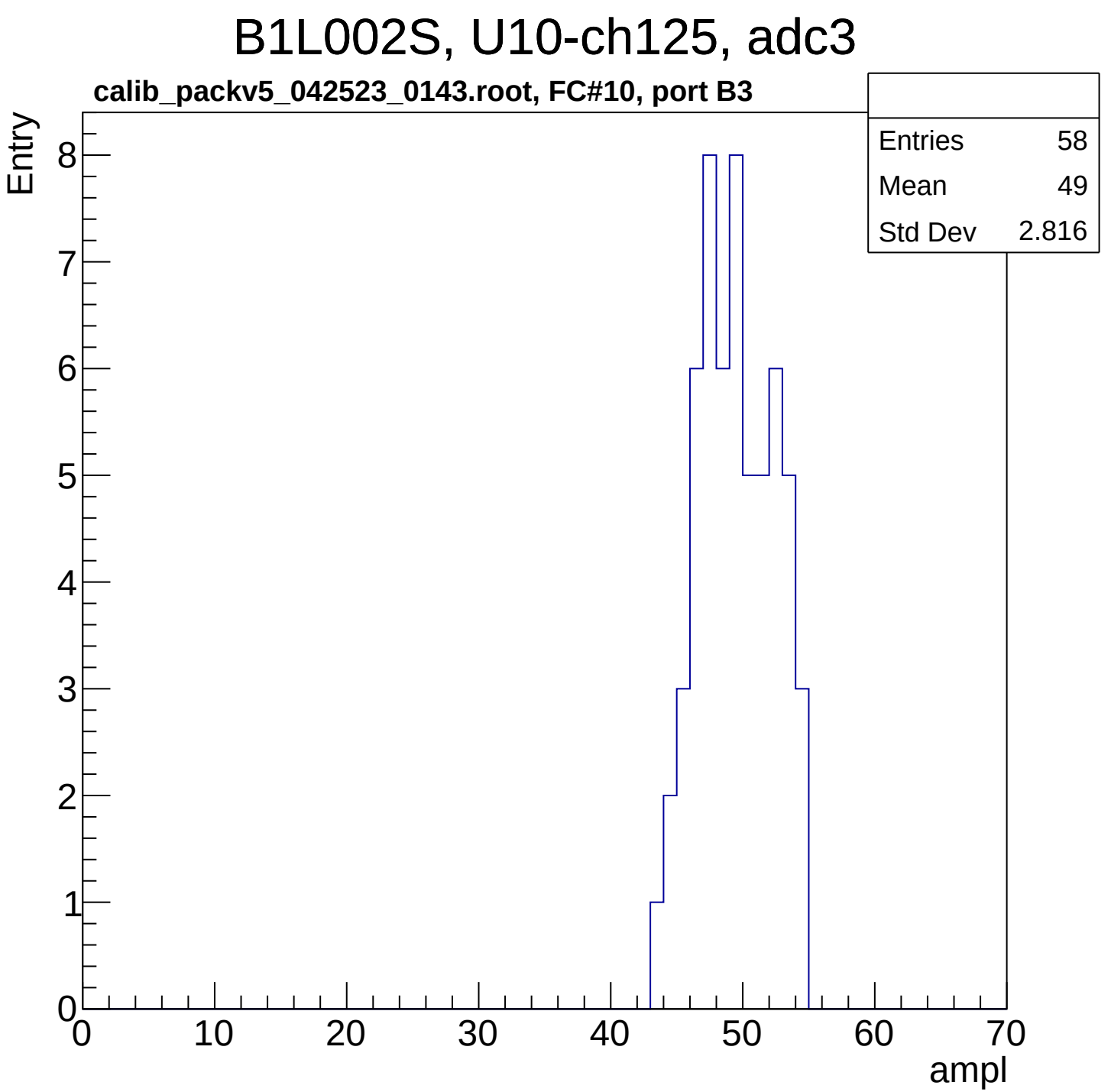
B1L002S, U10-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 49
Std Dev 2.816
Entry
ampl
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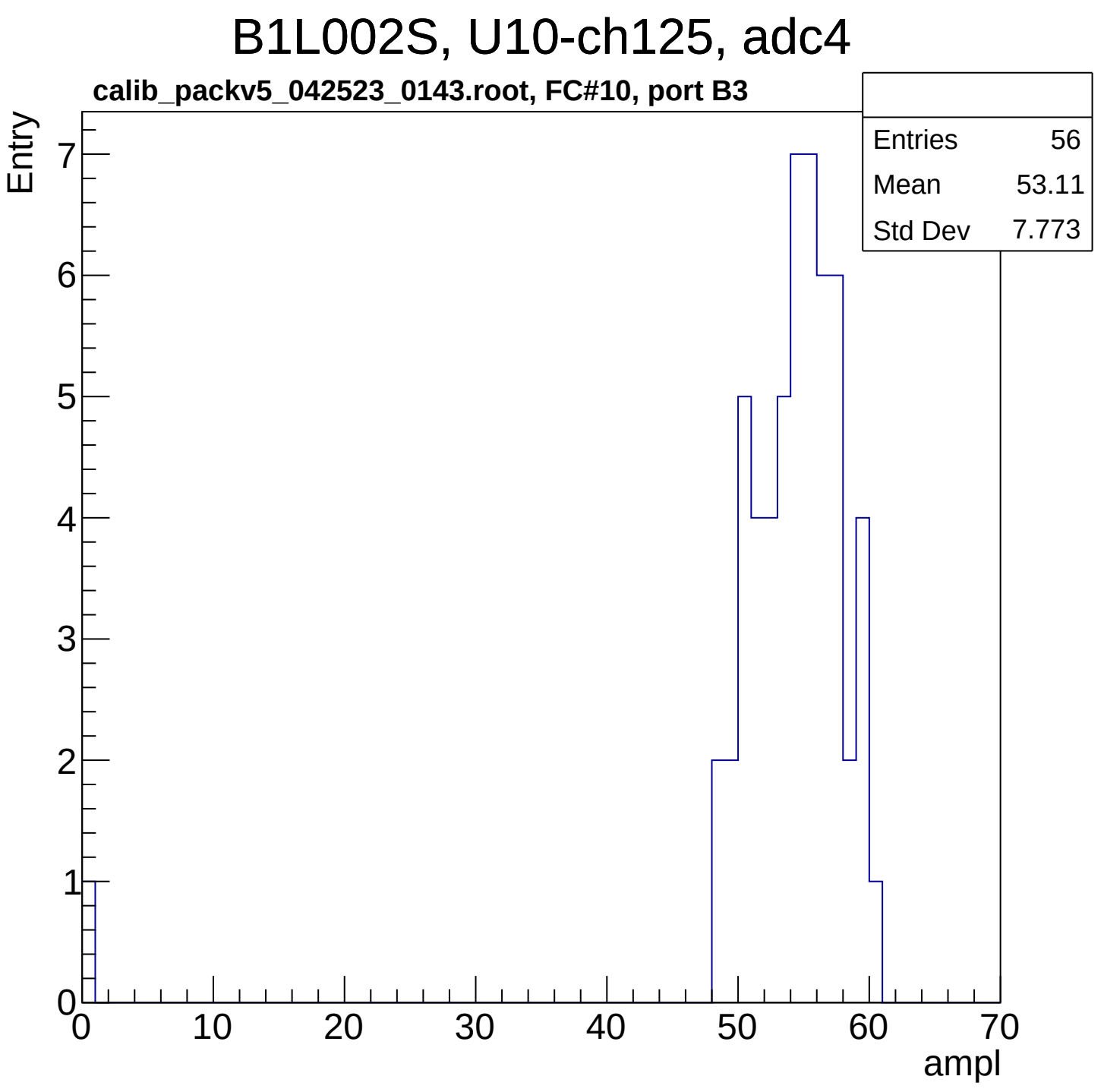
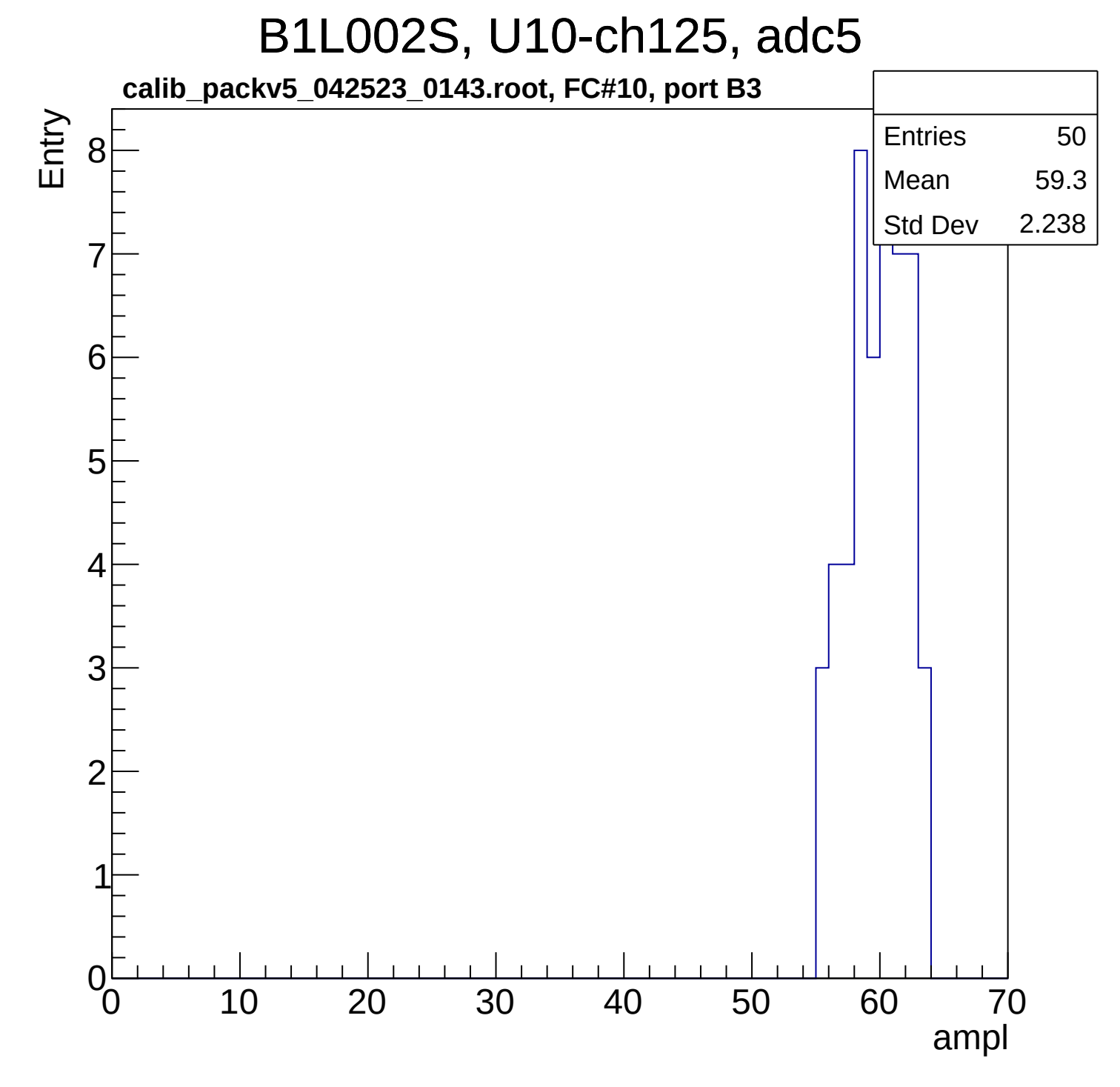
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.11
Std Dev 7.773
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.3
Std Dev 2.238
Entry
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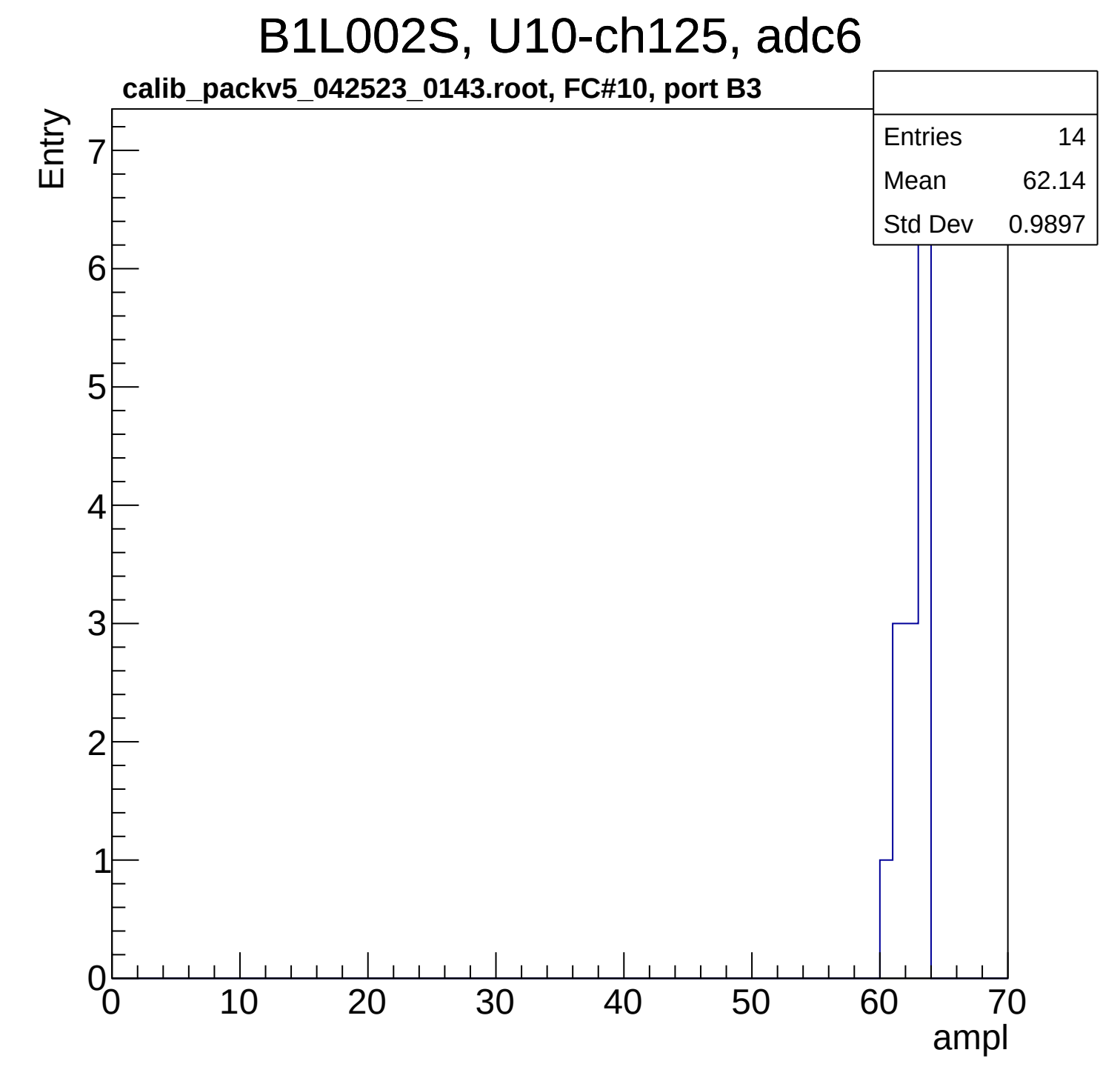
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62.14
Std Dev 0.9897
Entry
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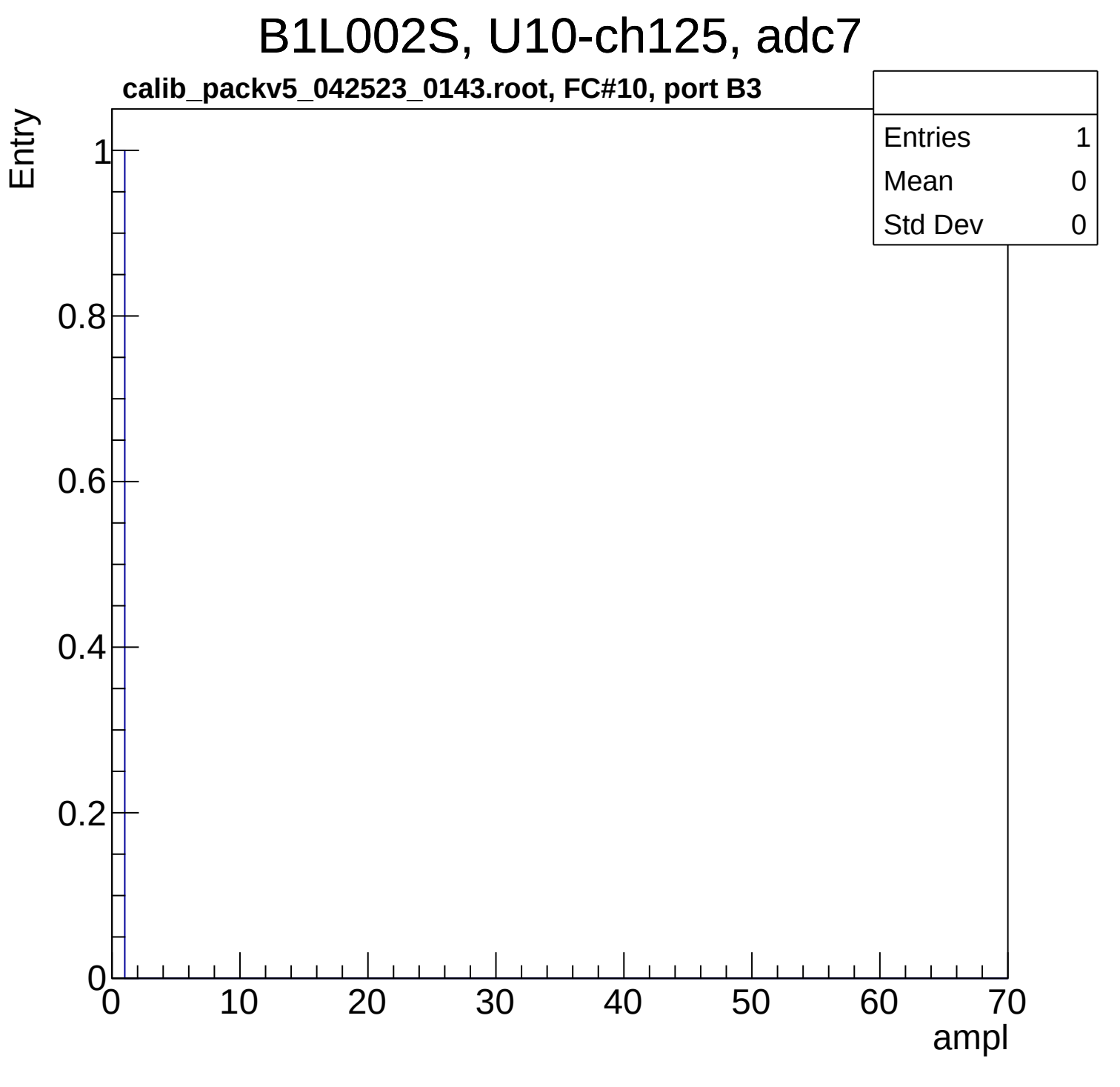
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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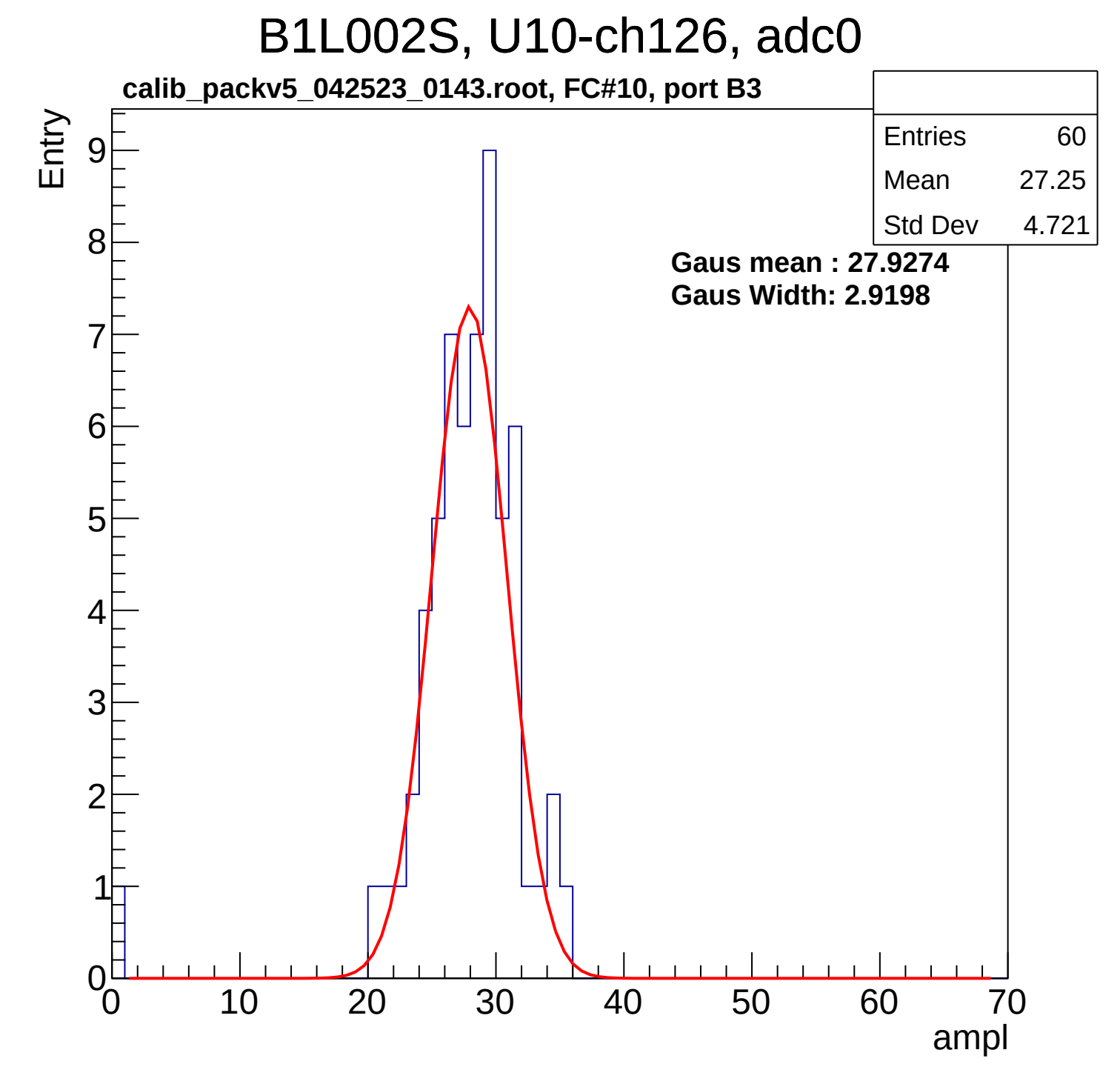
30
40
50
60
70
ampl


B1L002S, U10-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.25
Std Dev 4.721
Gaus mean : 27.9274
Gaus Width: 2.9198
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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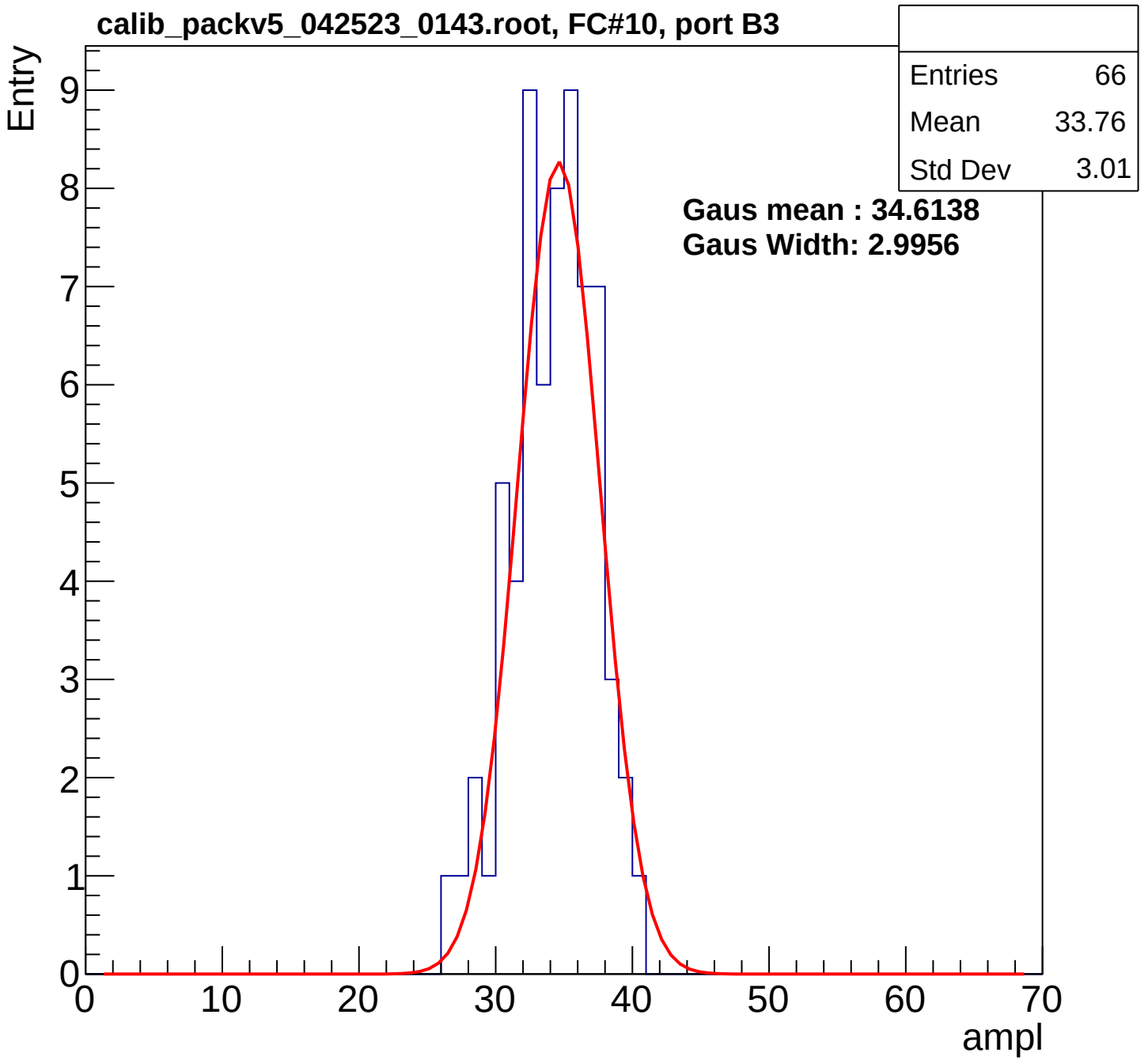
B1L002S, U10-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.76
Std Dev 3.01
Gaus mean : 34.6138
Gaus Width: 2.9956
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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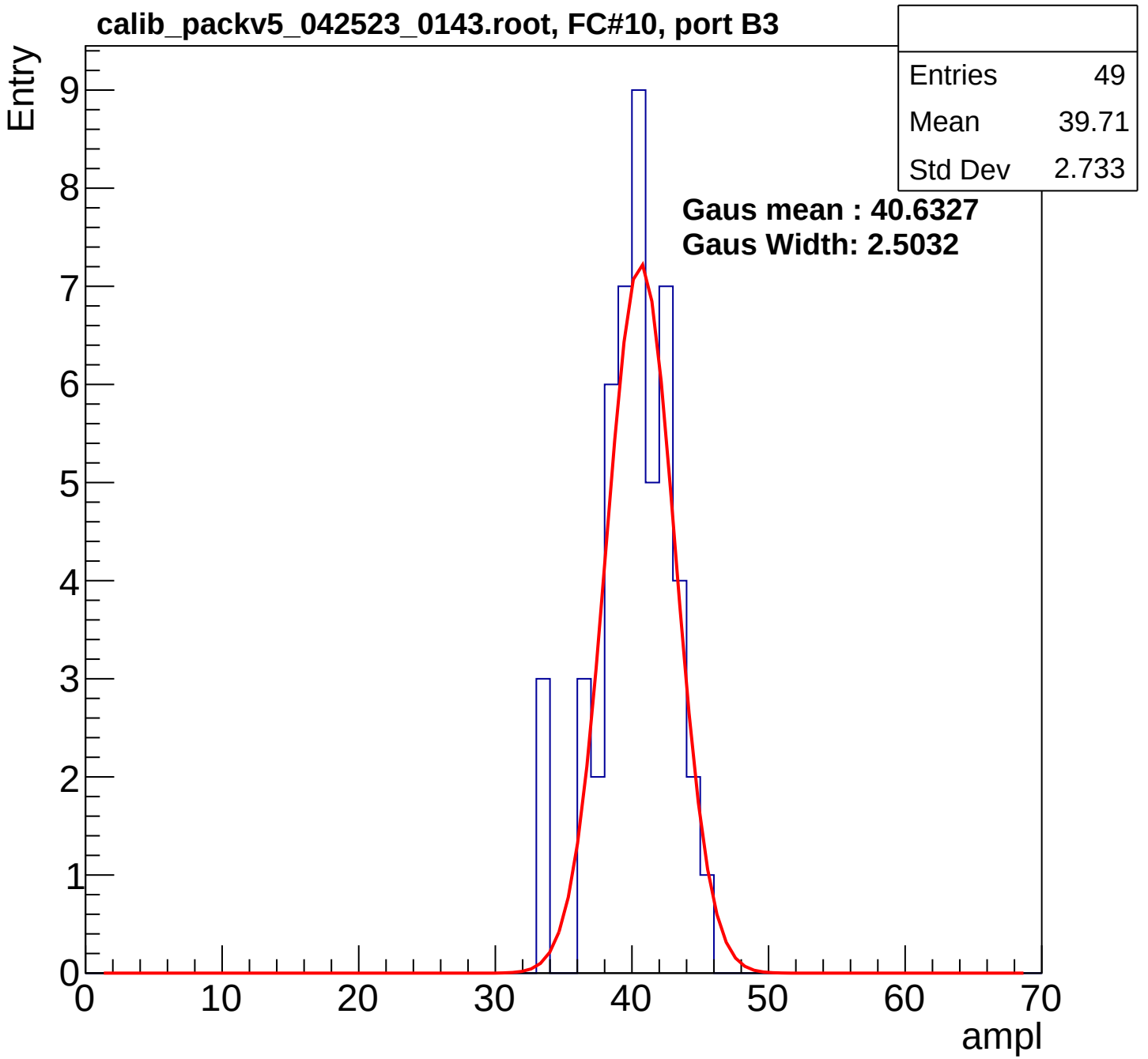
B1L002S, U10-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 39.71
Std Dev 2.733
Gaus mean : 40.6327
Gaus Width: 2.5032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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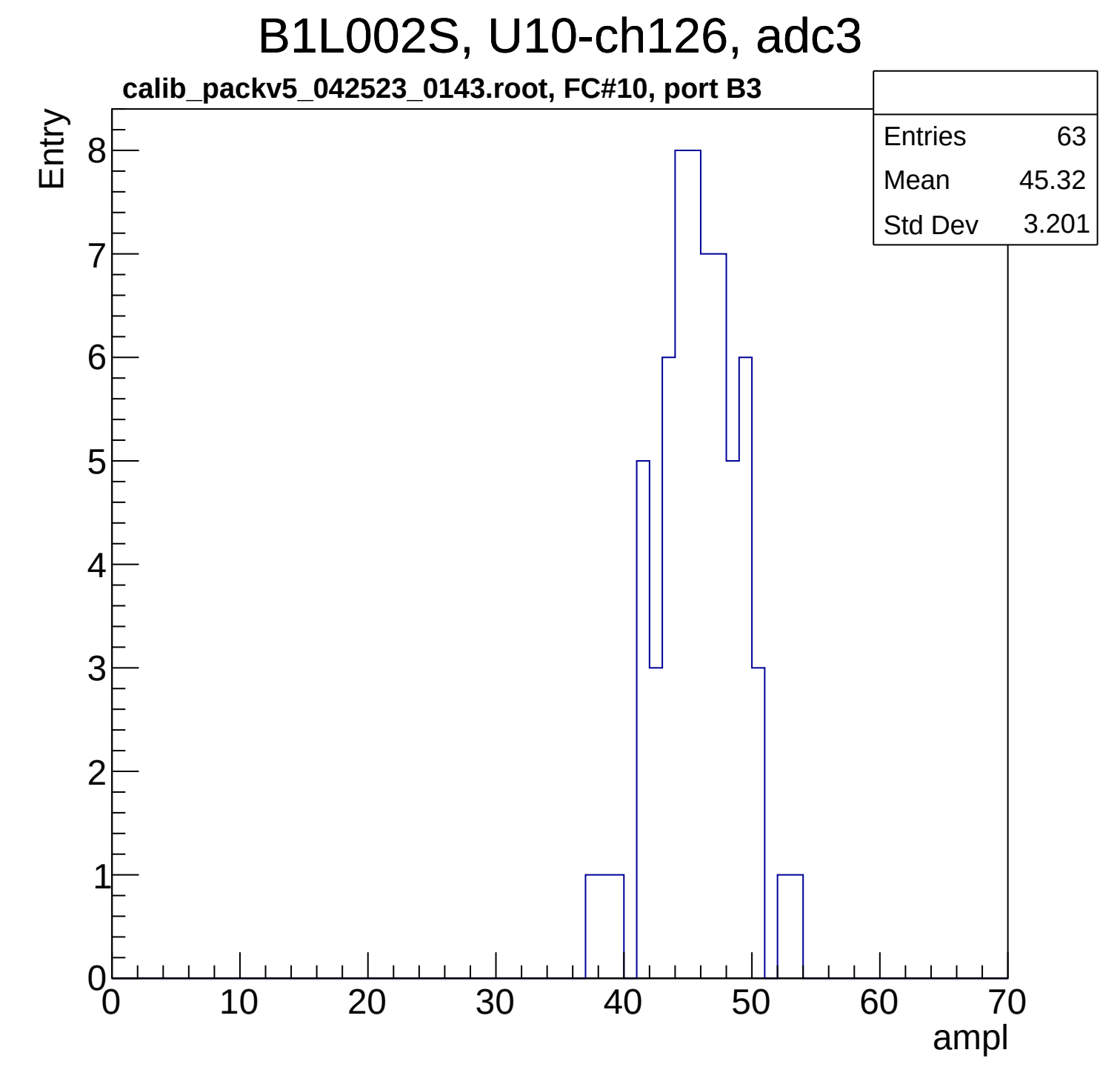
B1L002S, U10-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 45.32
Std Dev 3.201
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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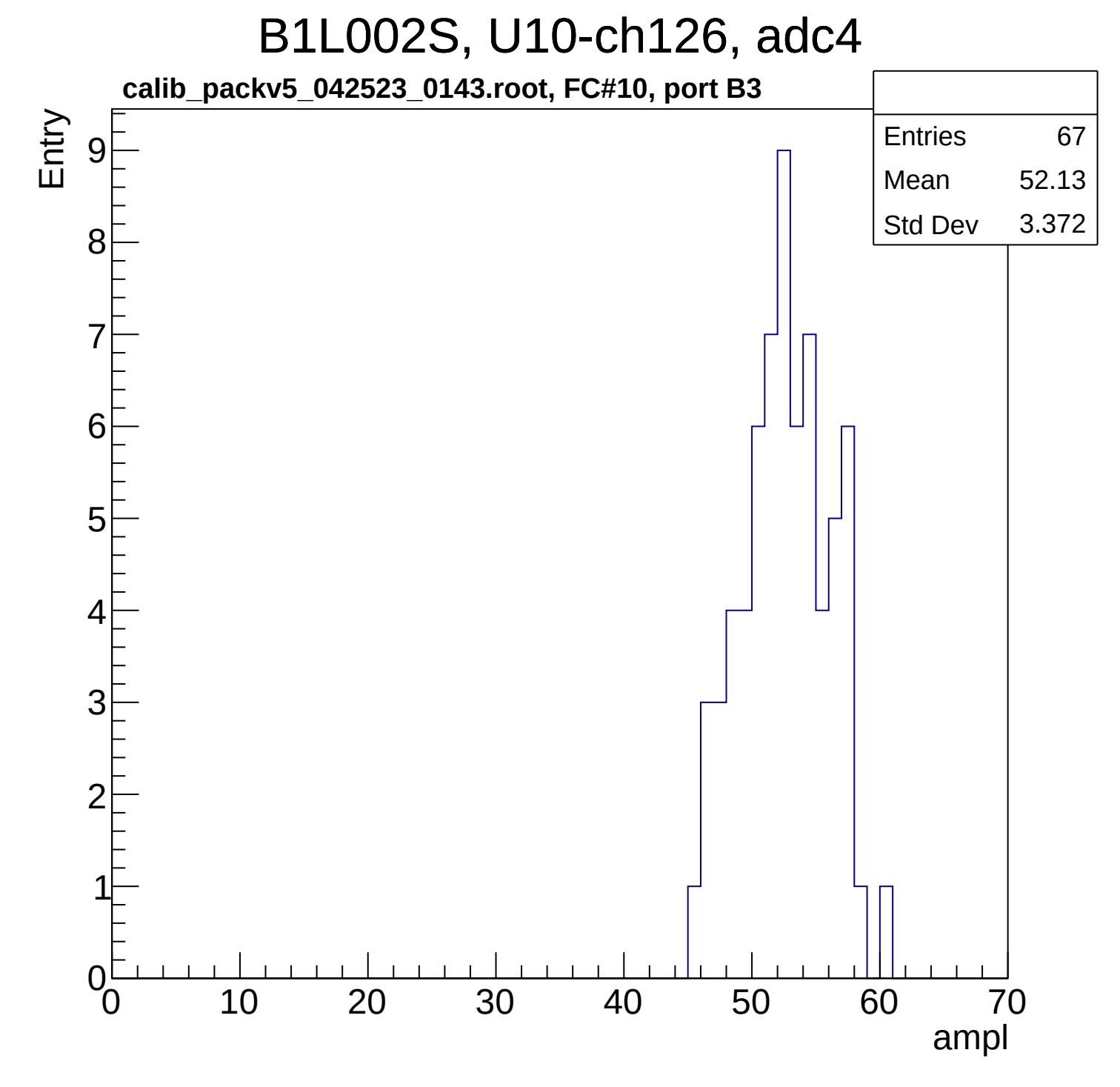
B1L002S, U10-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 52.13
Std Dev 3.372
Entry
ampl
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3
4
5
6
7
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0
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60
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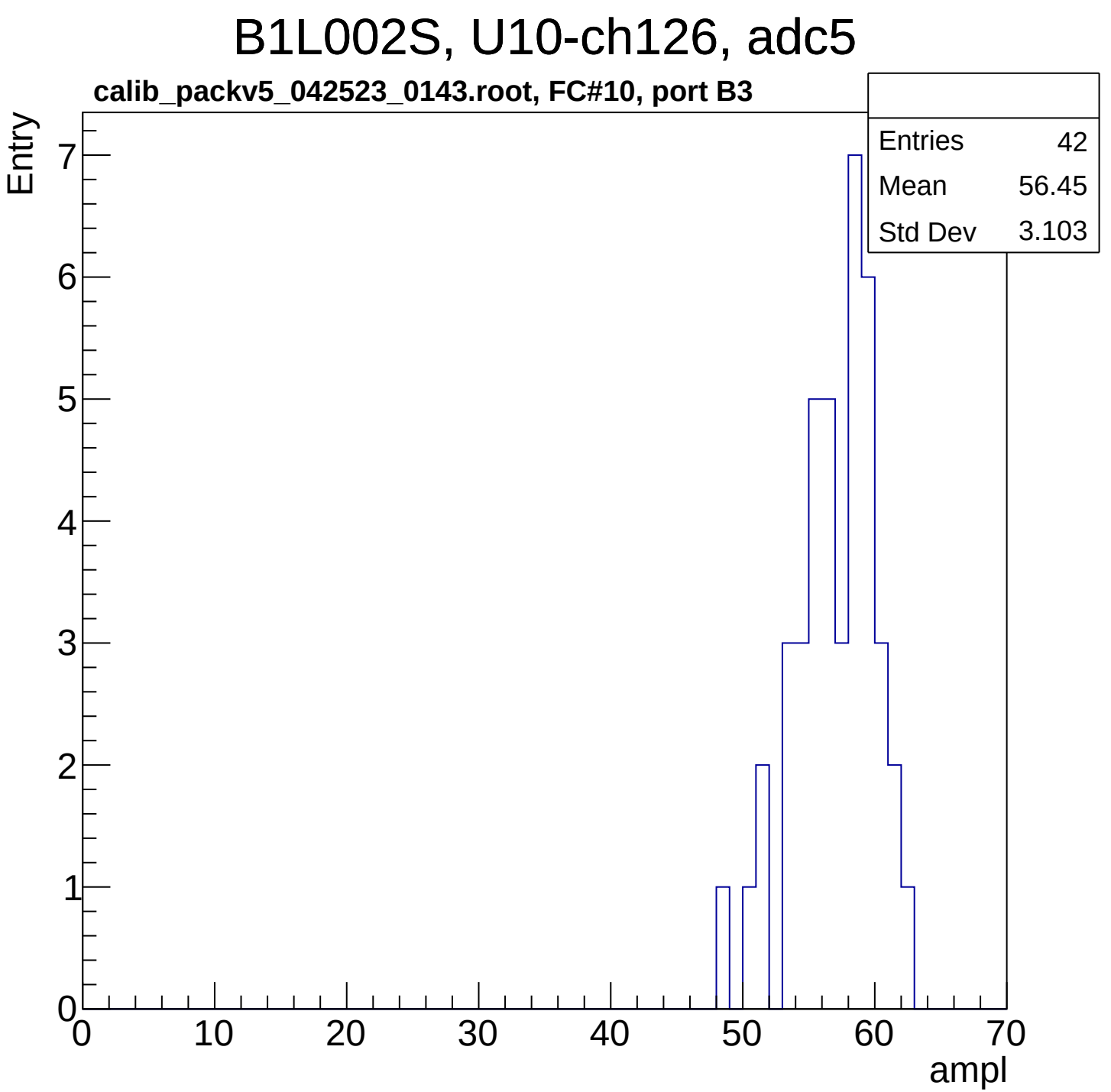
B1L002S, U10-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 56.45
Std Dev 3.103
Entry
ampl
0
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2
3
4
5
6
7
0
10
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40
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60
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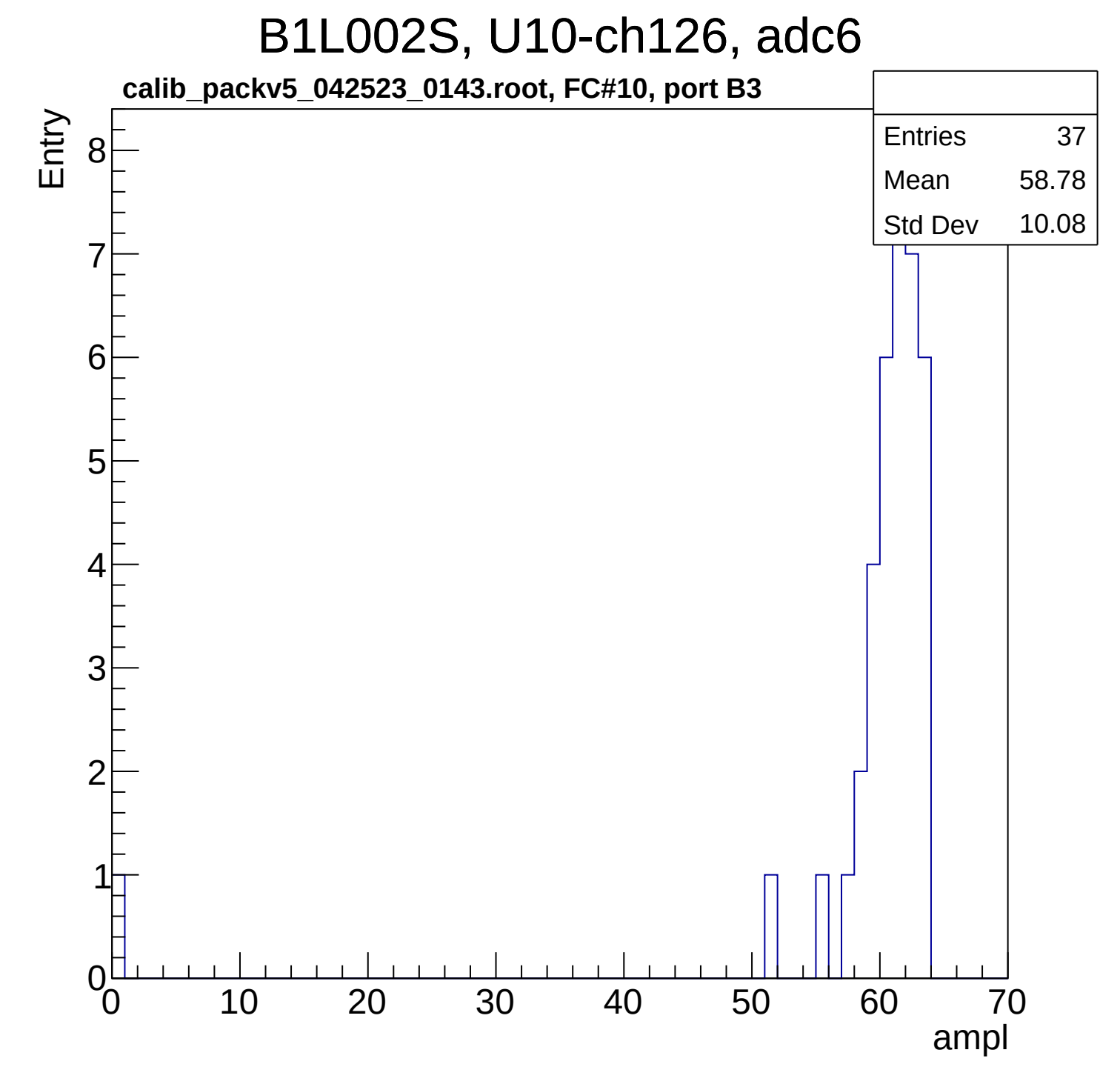
B1L002S, U10-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.78
Std Dev 10.08
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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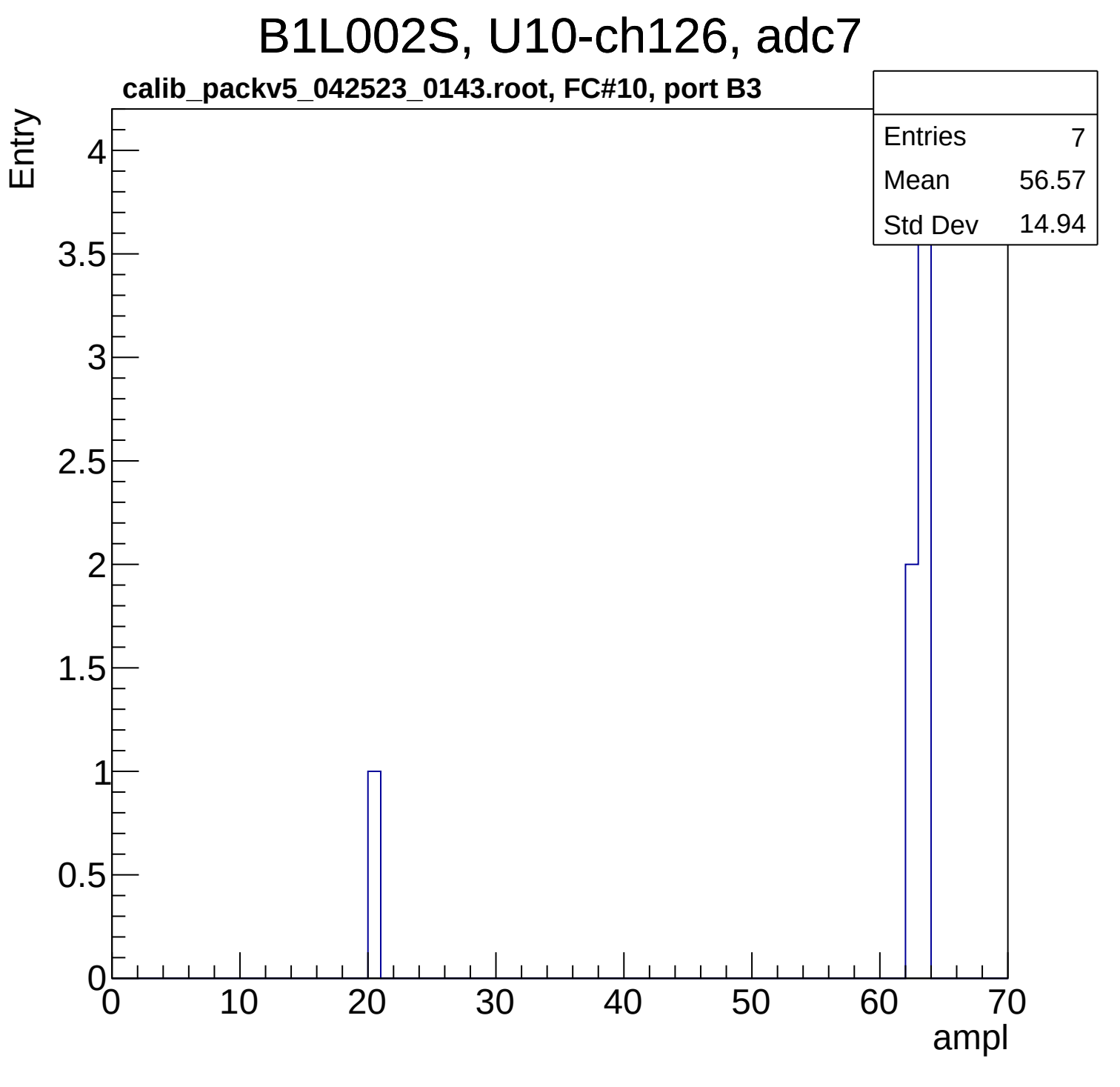
B1L002S, U10-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 56.57
Std Dev 14.94
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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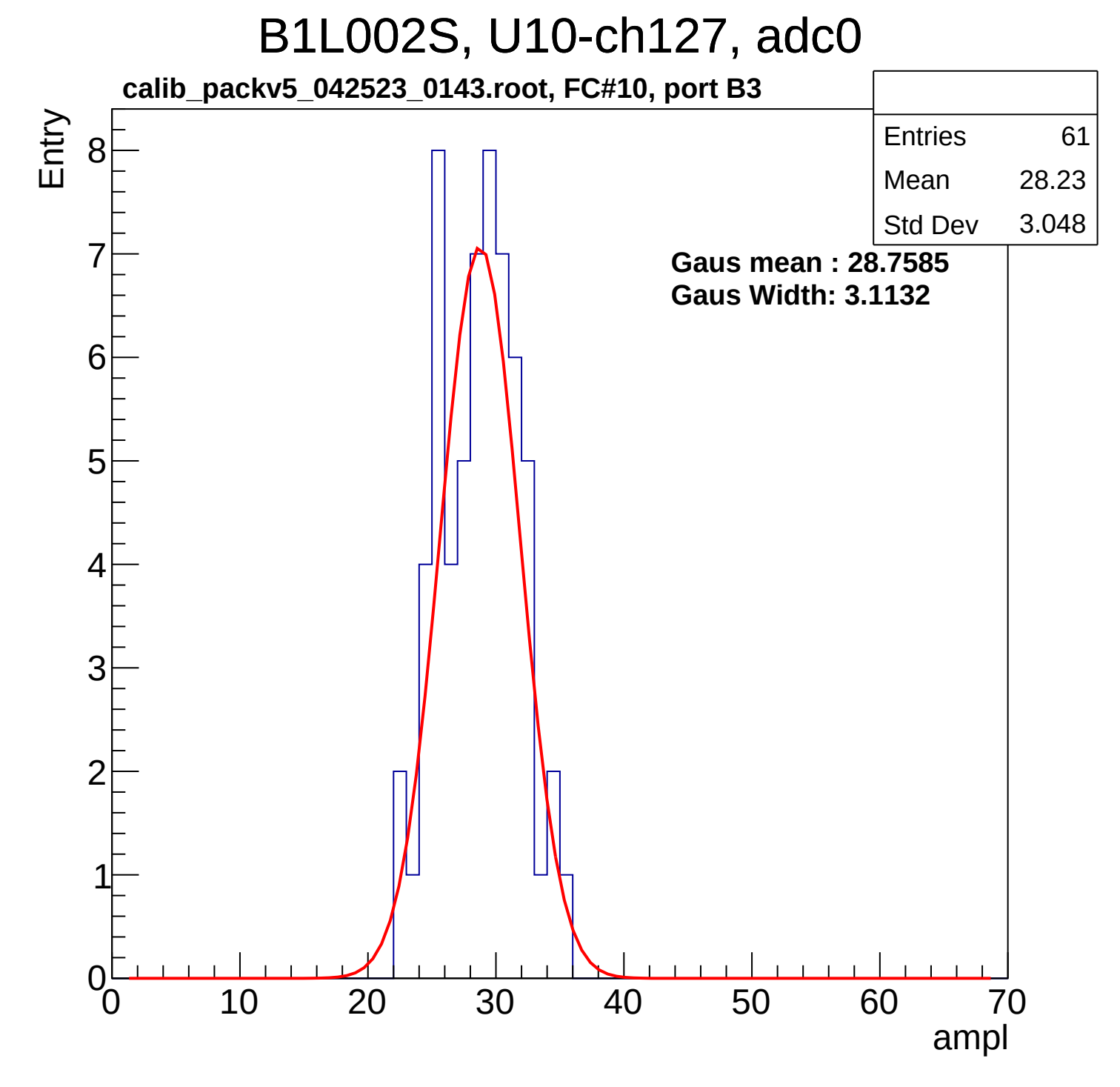
40
50
60
70


B1L002S, U10-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.23
Std Dev 3.048
Gaus mean : 28.7585
Gaus Width: 3.1132
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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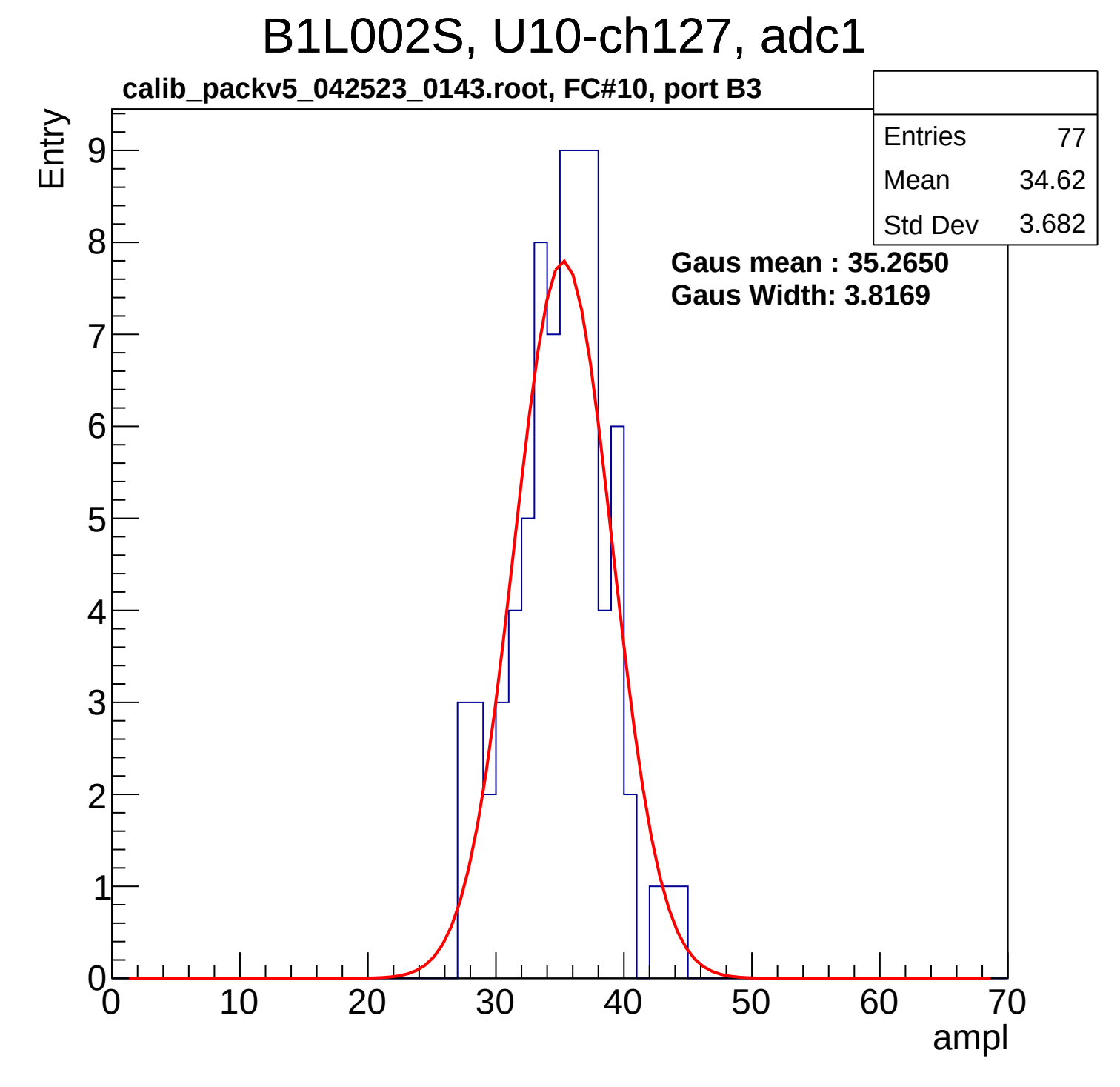
B1L002S, U10-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.62
Std Dev 3.682
Gaus mean : 35.2650
Gaus Width: 3.8169
Entry
ampl
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2
3
4
5
6
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0
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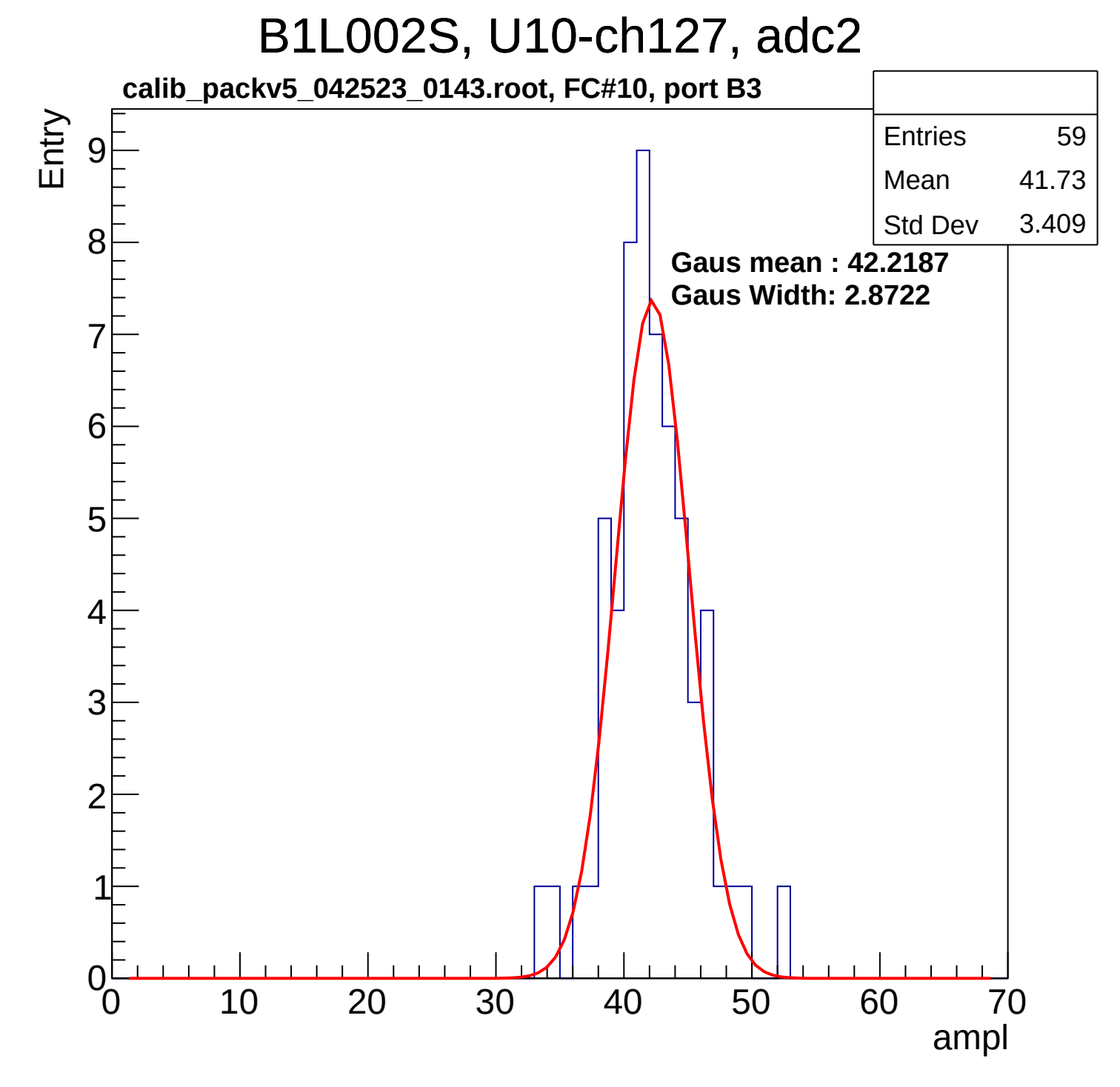
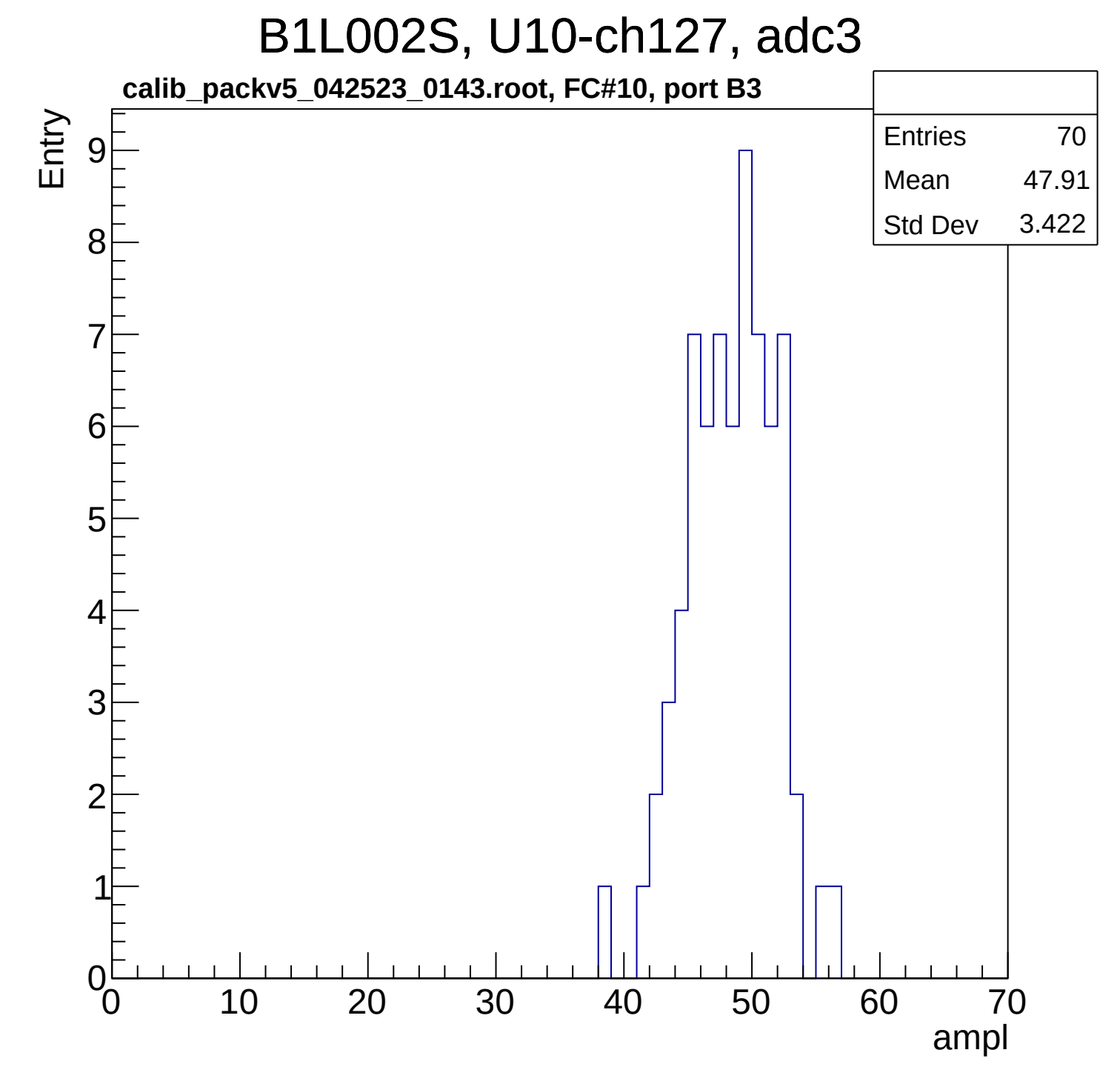
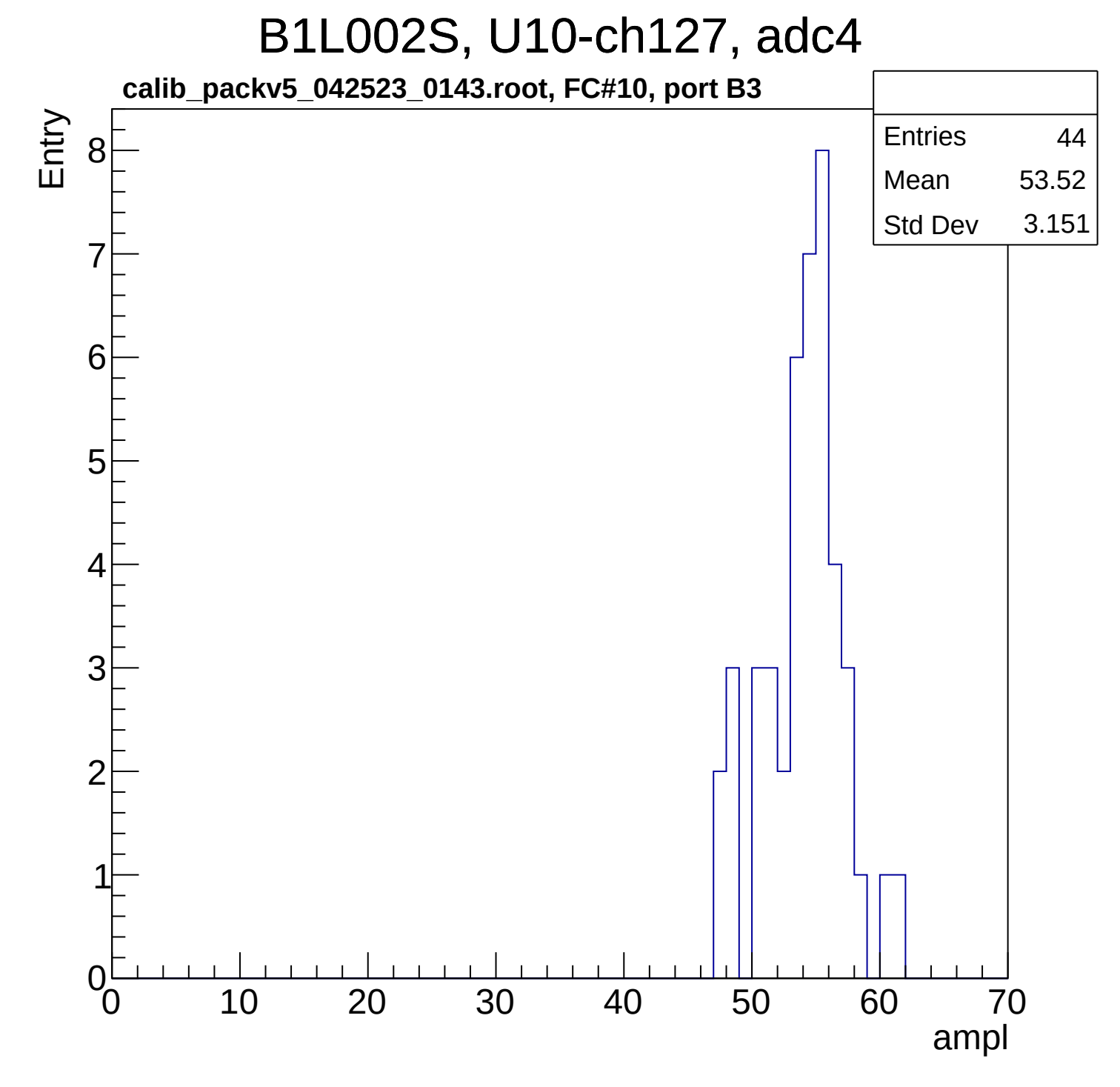
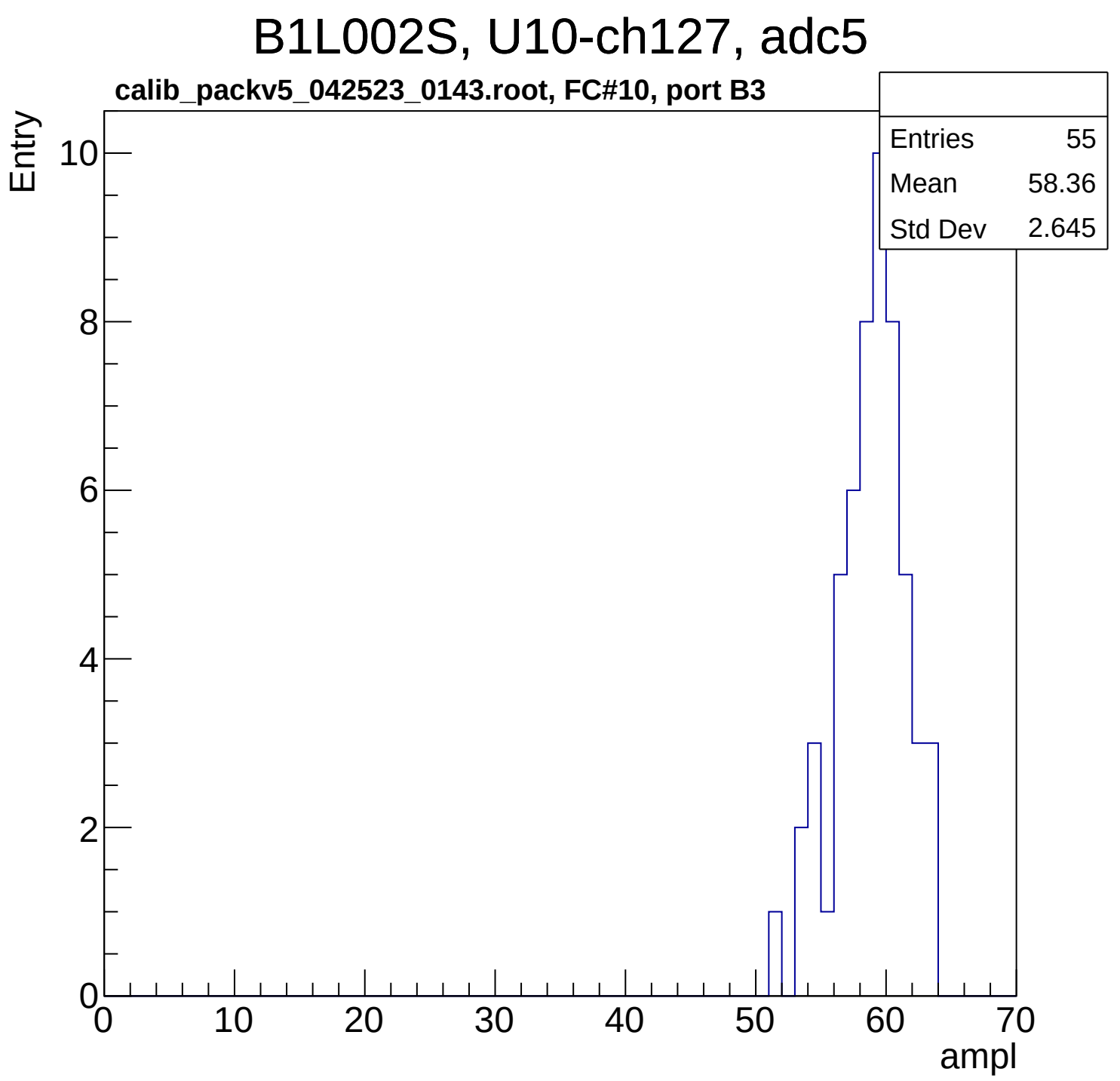
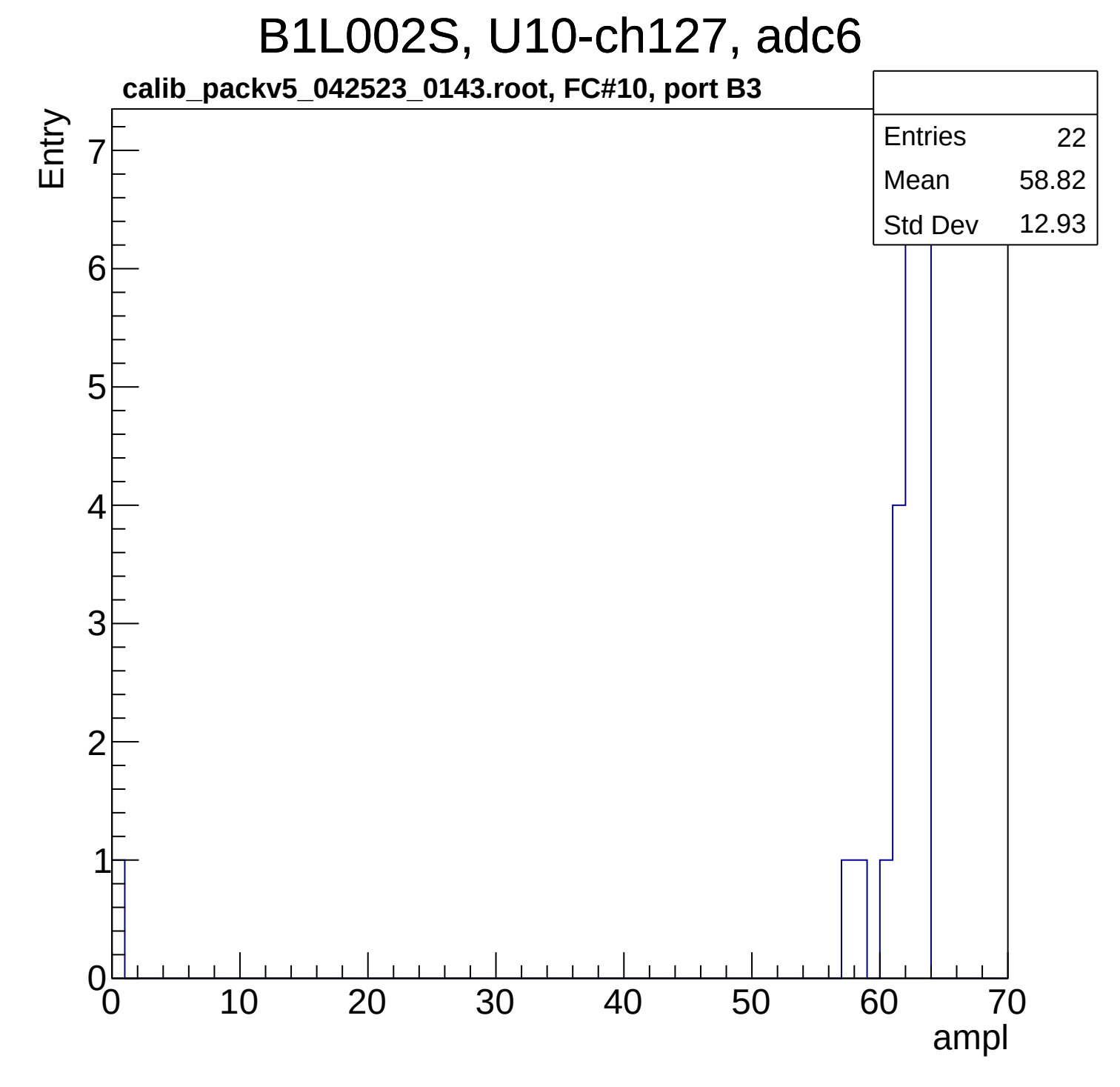
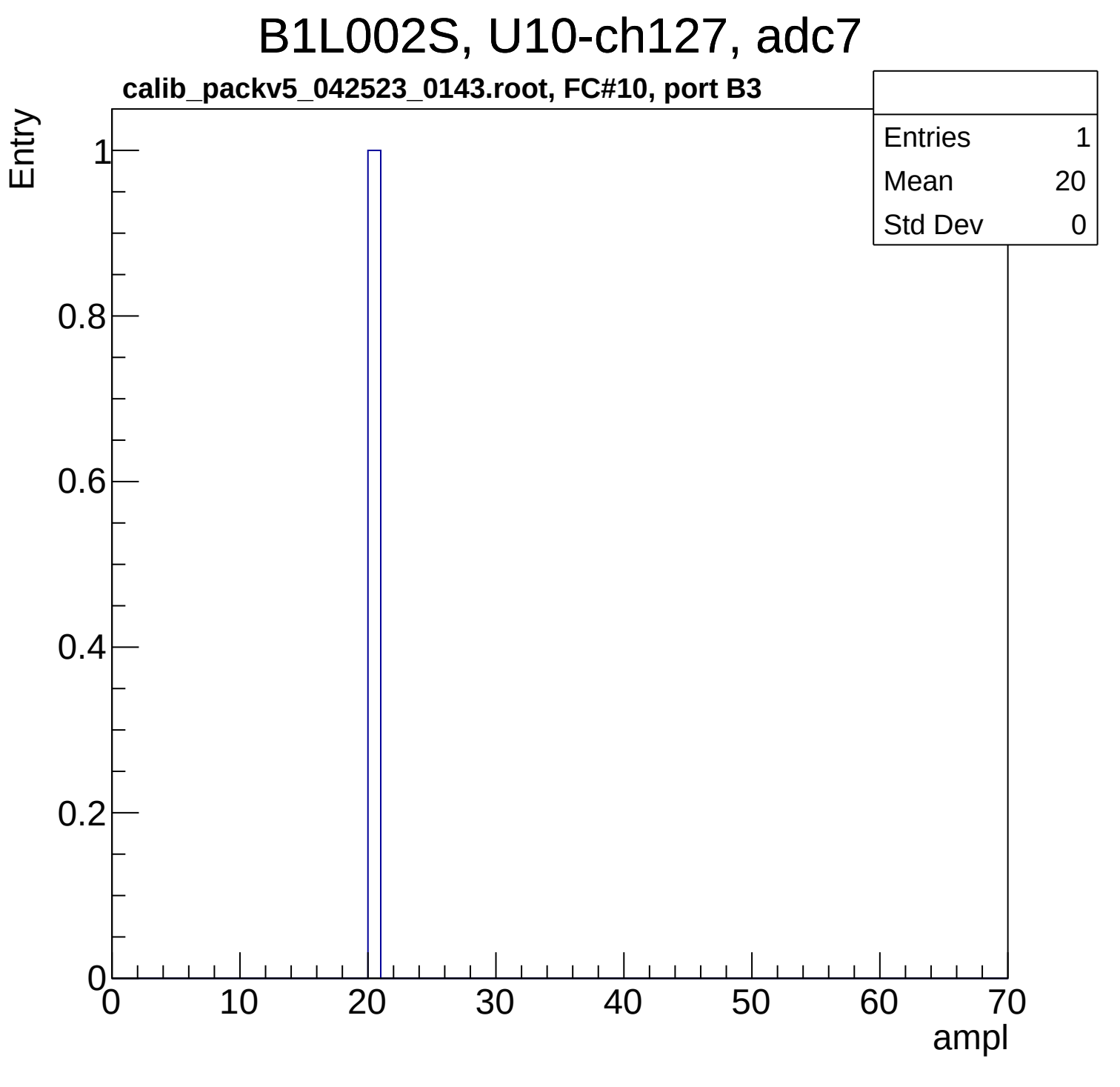

B1L002S, U10-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.73
Std Dev 3.409
Gaus mean : 42.2187
Gaus Width: 2.8722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

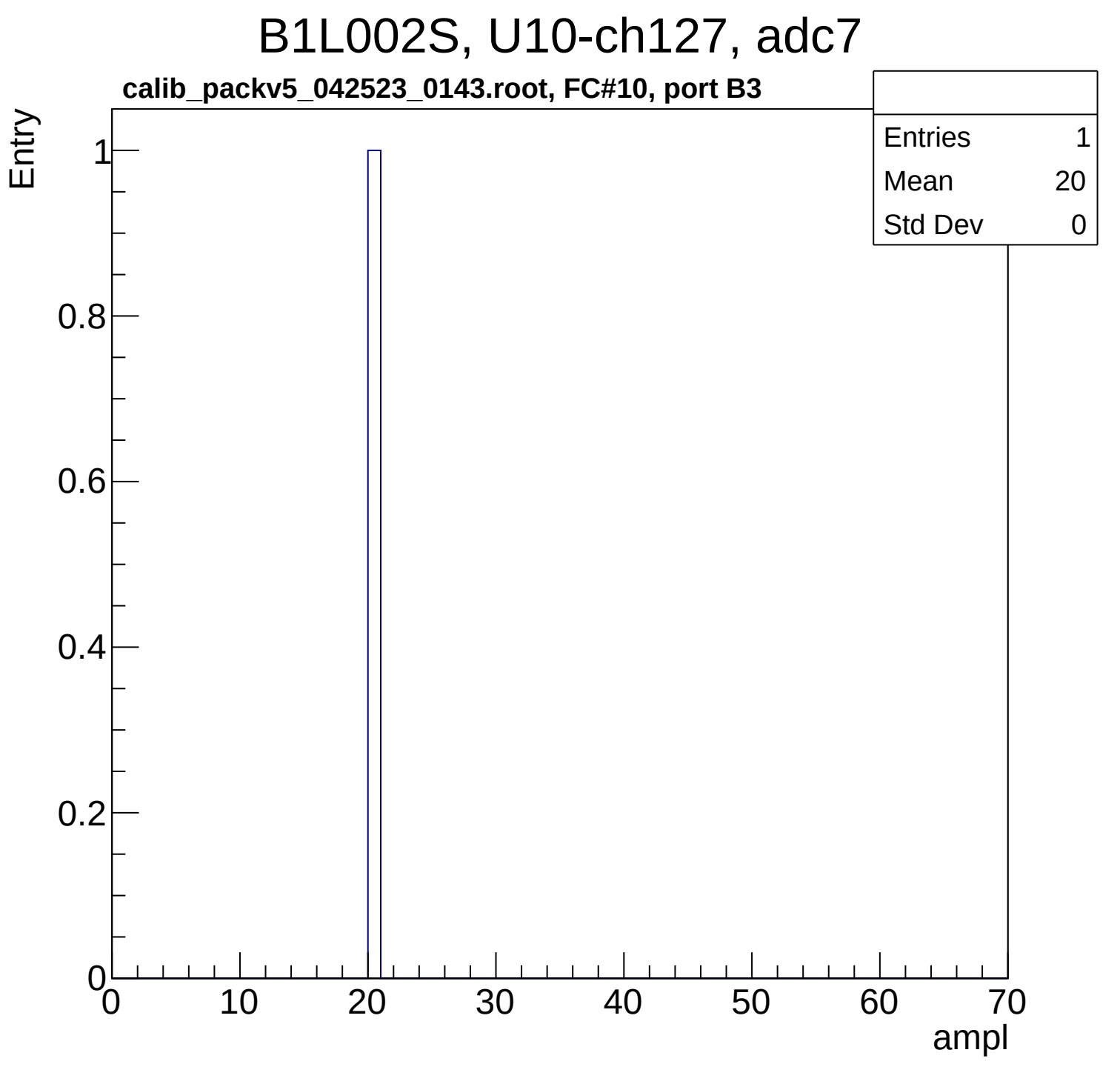

B1L002S, U10-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.91
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U10-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.52
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U10-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.36
Std Dev 2.645
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U10-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.82
Std Dev 12.93
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl