

B1L102S, U10-ch0, adc0

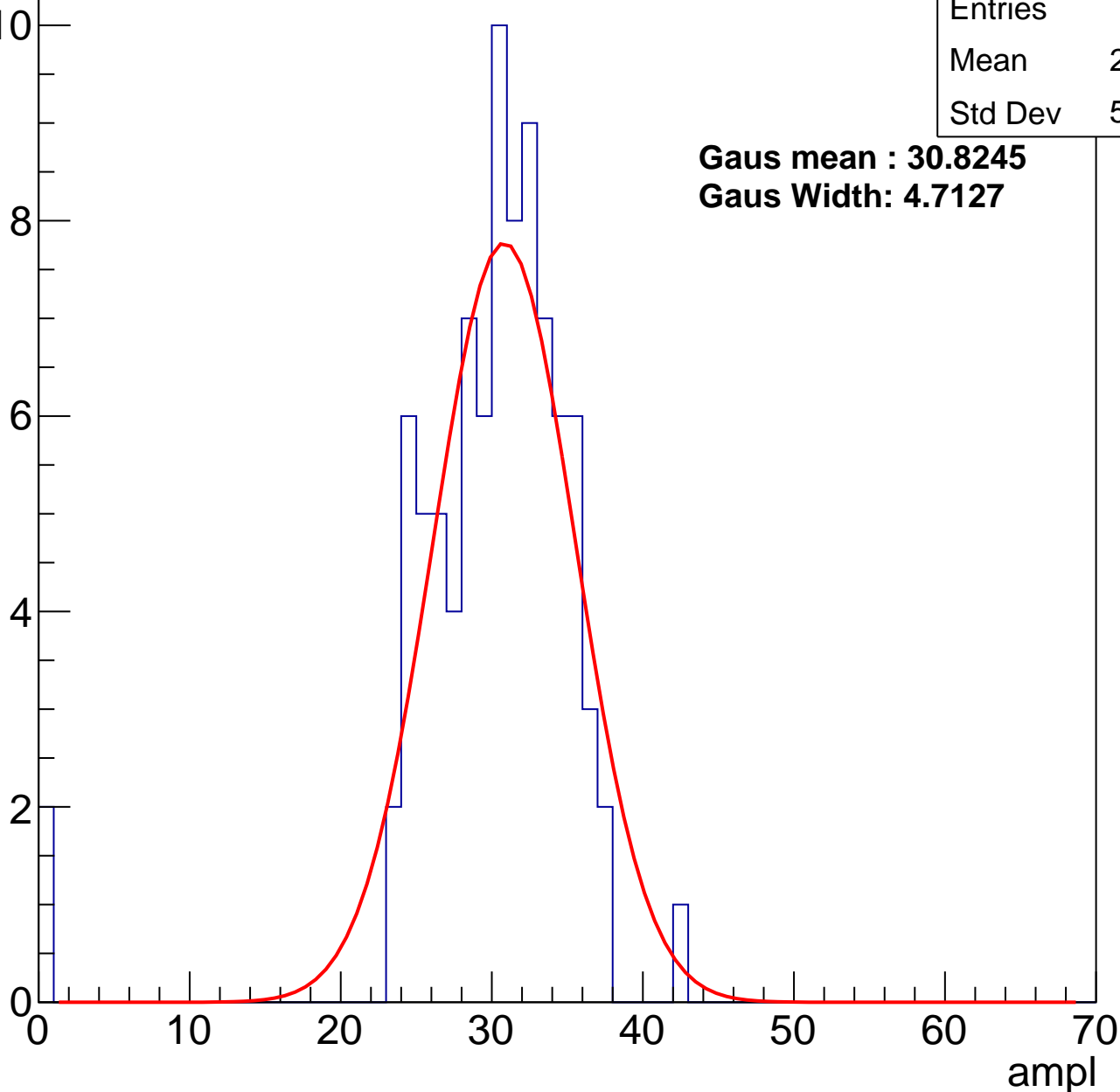
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	29.53
Std Dev	5.875

Gaus mean : 30.8245

Gaus Width: 4.7127



B1L102S, U10-ch0, adc1

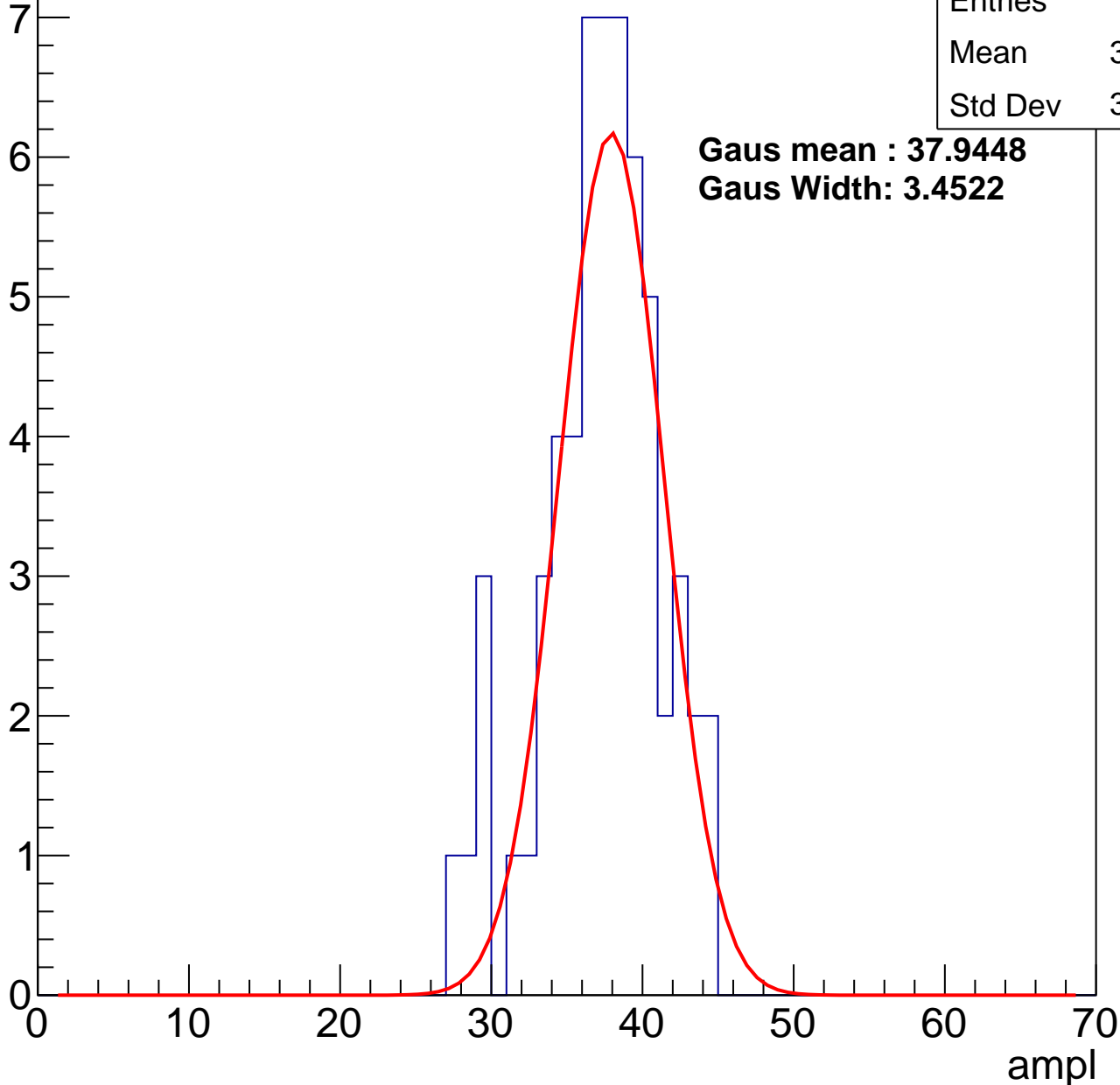
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.83
Std Dev	3.889

Gaus mean : 37.9448

Gaus Width: 3.4522



B1L102S, U10-ch0, adc2

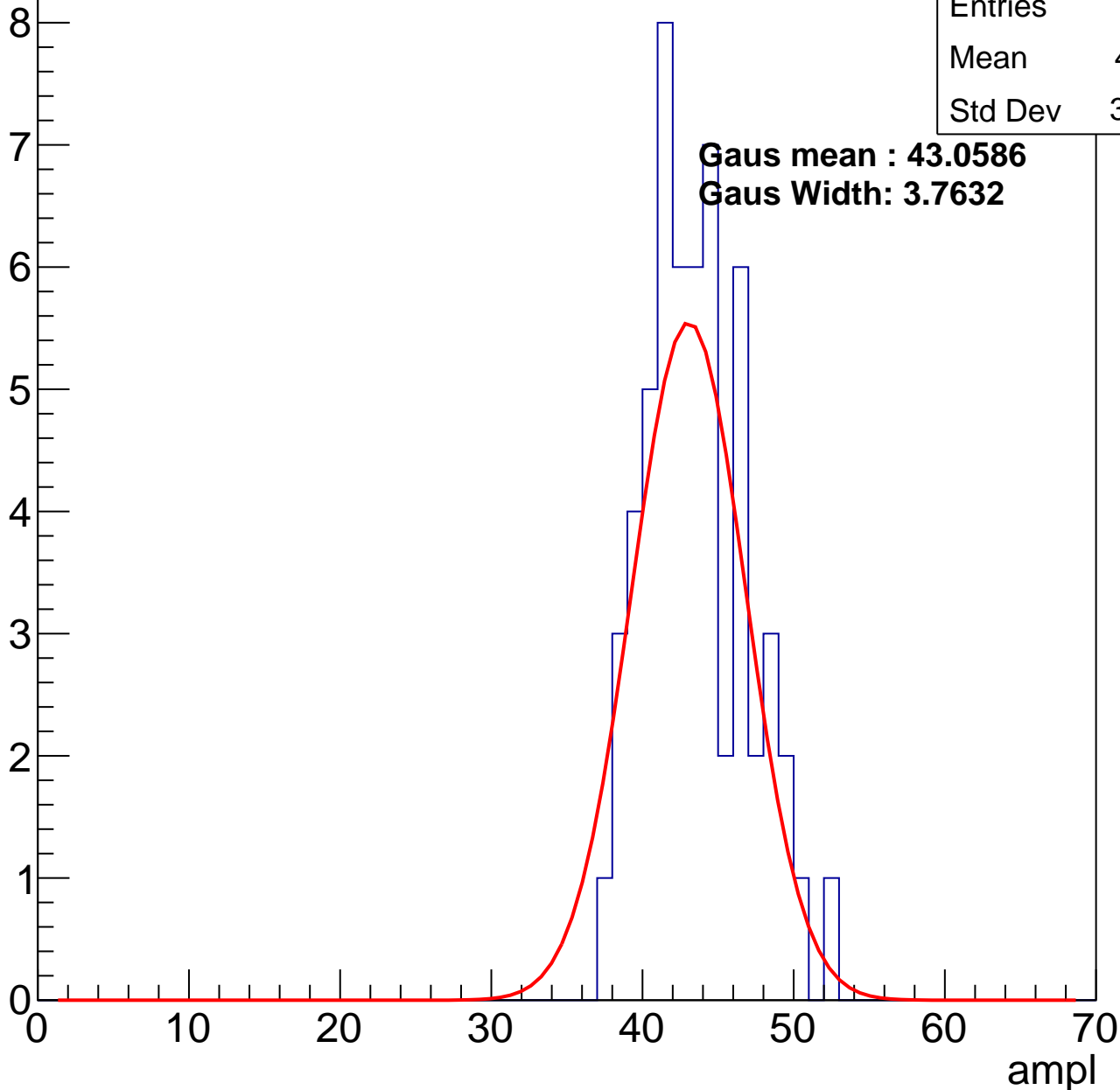
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.11
Std Dev	3.339

Gaus mean : 43.0586

Gaus Width: 3.7632

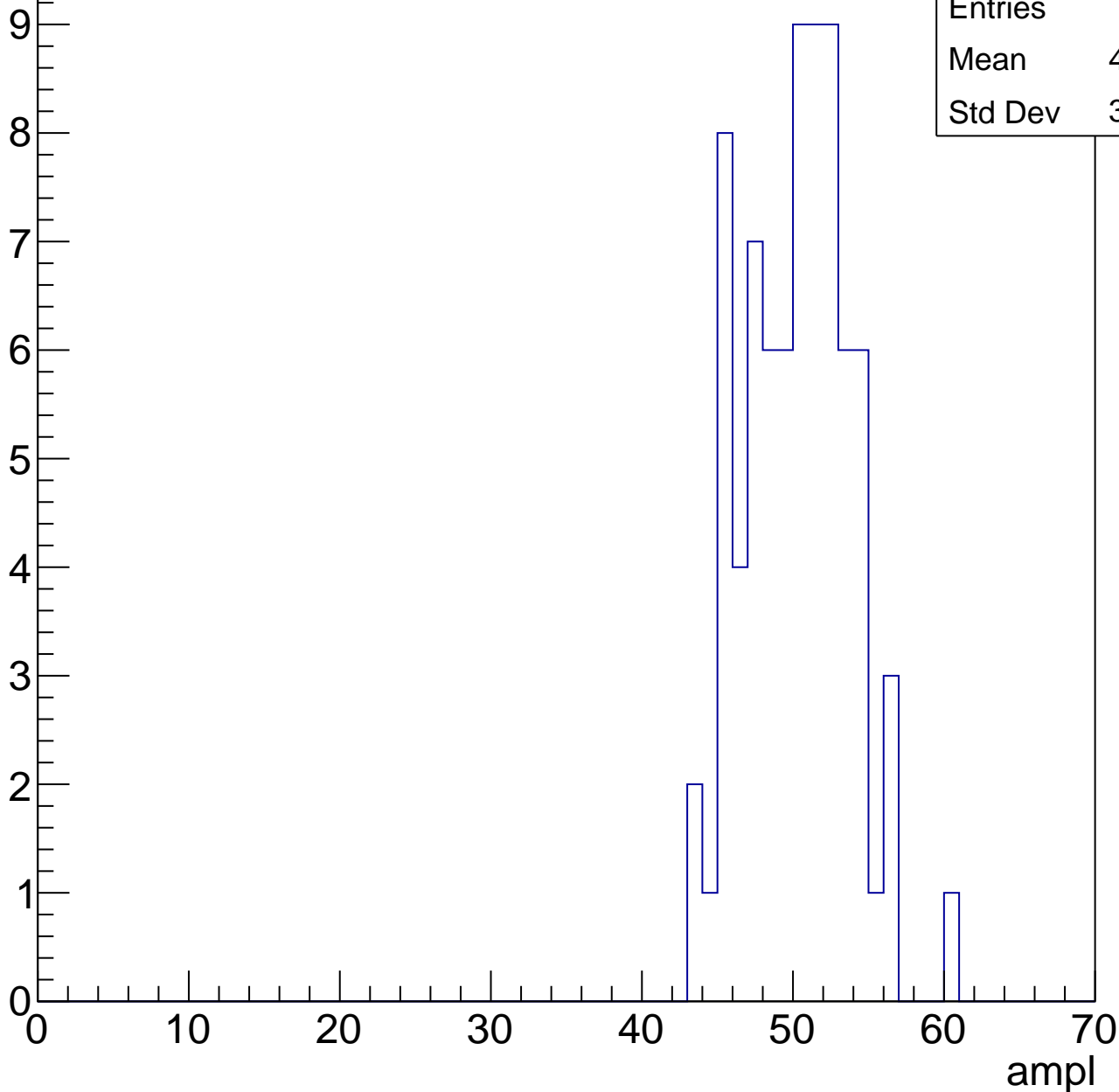


B1L102S, U10-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

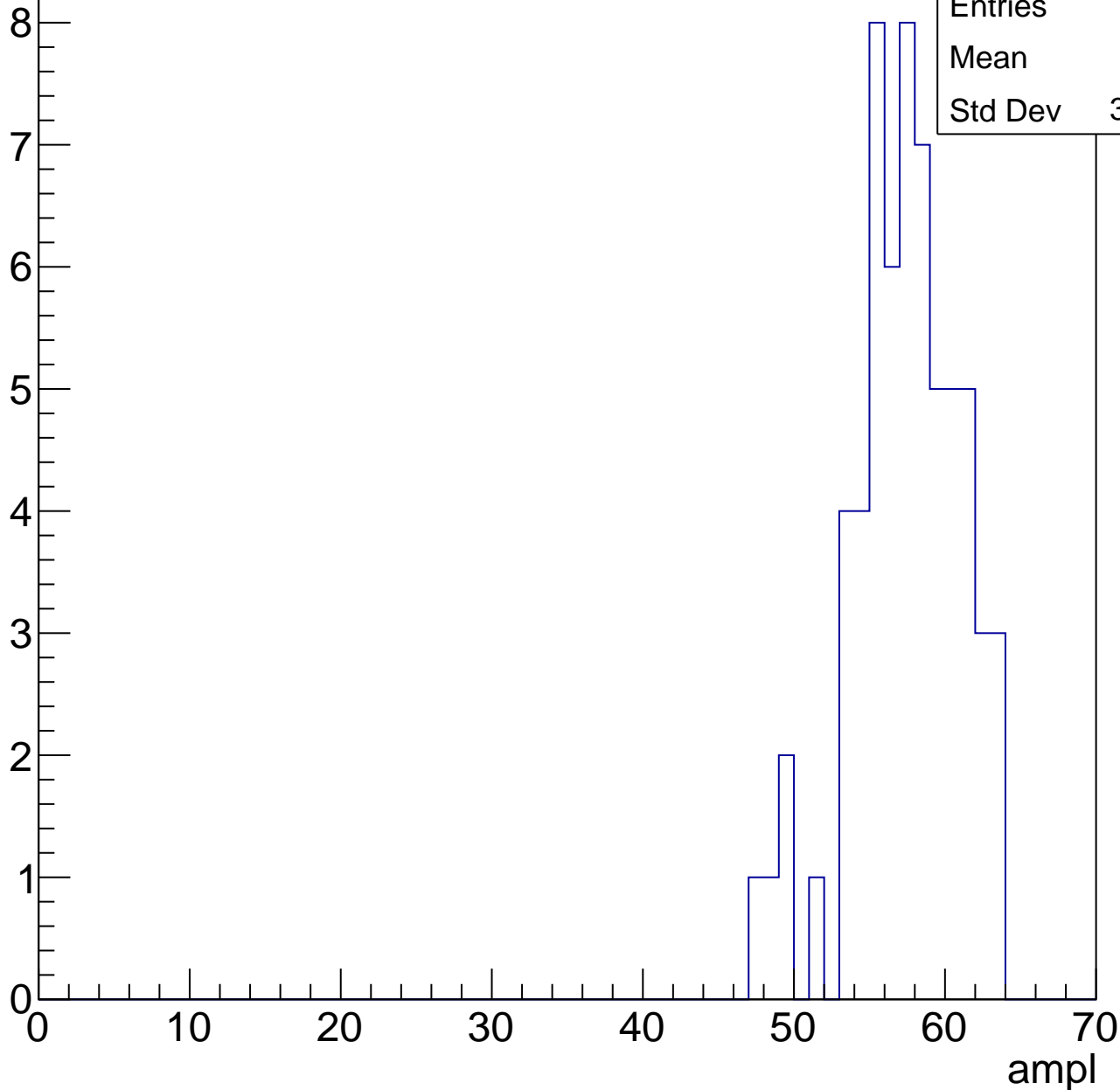
Entries	78
Mean	49.83
Std Dev	3.428



B1L102S, U10-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	33
Mean	58.55
Std Dev	10.56

ampl

0

10

20

30

40

50

60

70

B1L102S, U10-ch0, adc6

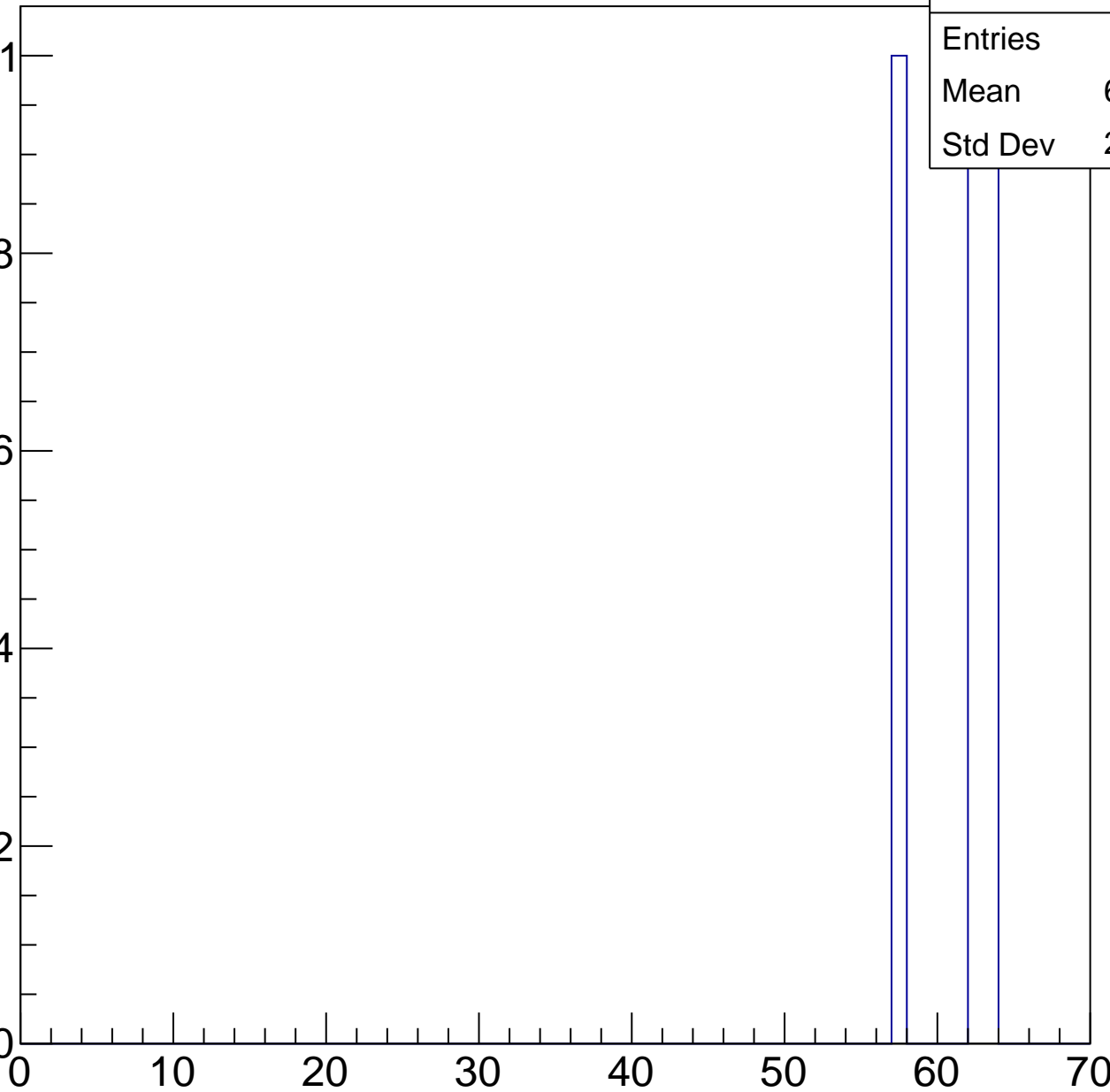
calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.67
Std Dev	2.625

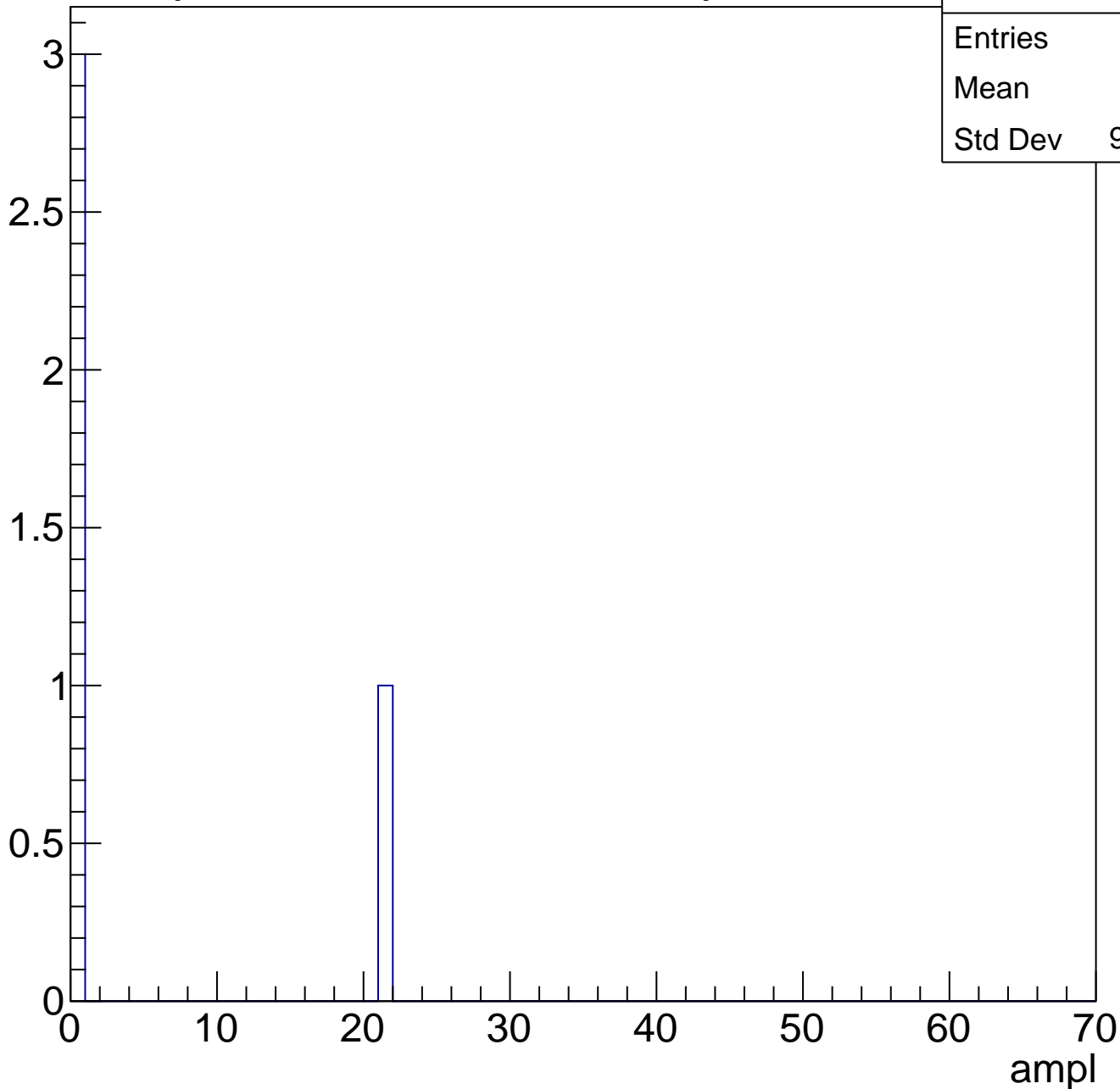
ampl



B1L102S, U10-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch1, adc0

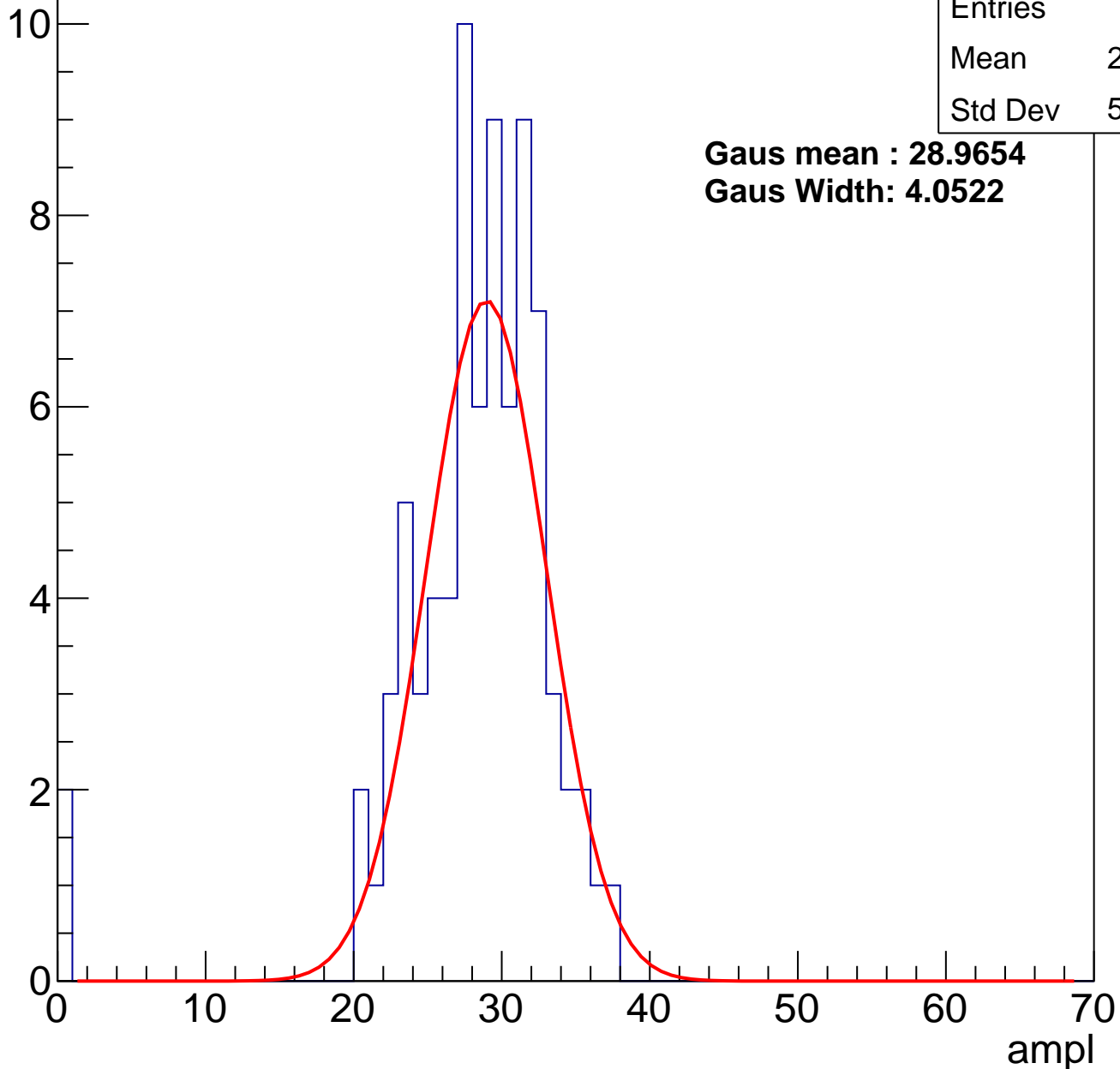
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	27.62
Std Dev	5.802

Gaus mean : 28.9654

Gaus Width: 4.0522

Entry



B1L102S, U10-ch1, adc1

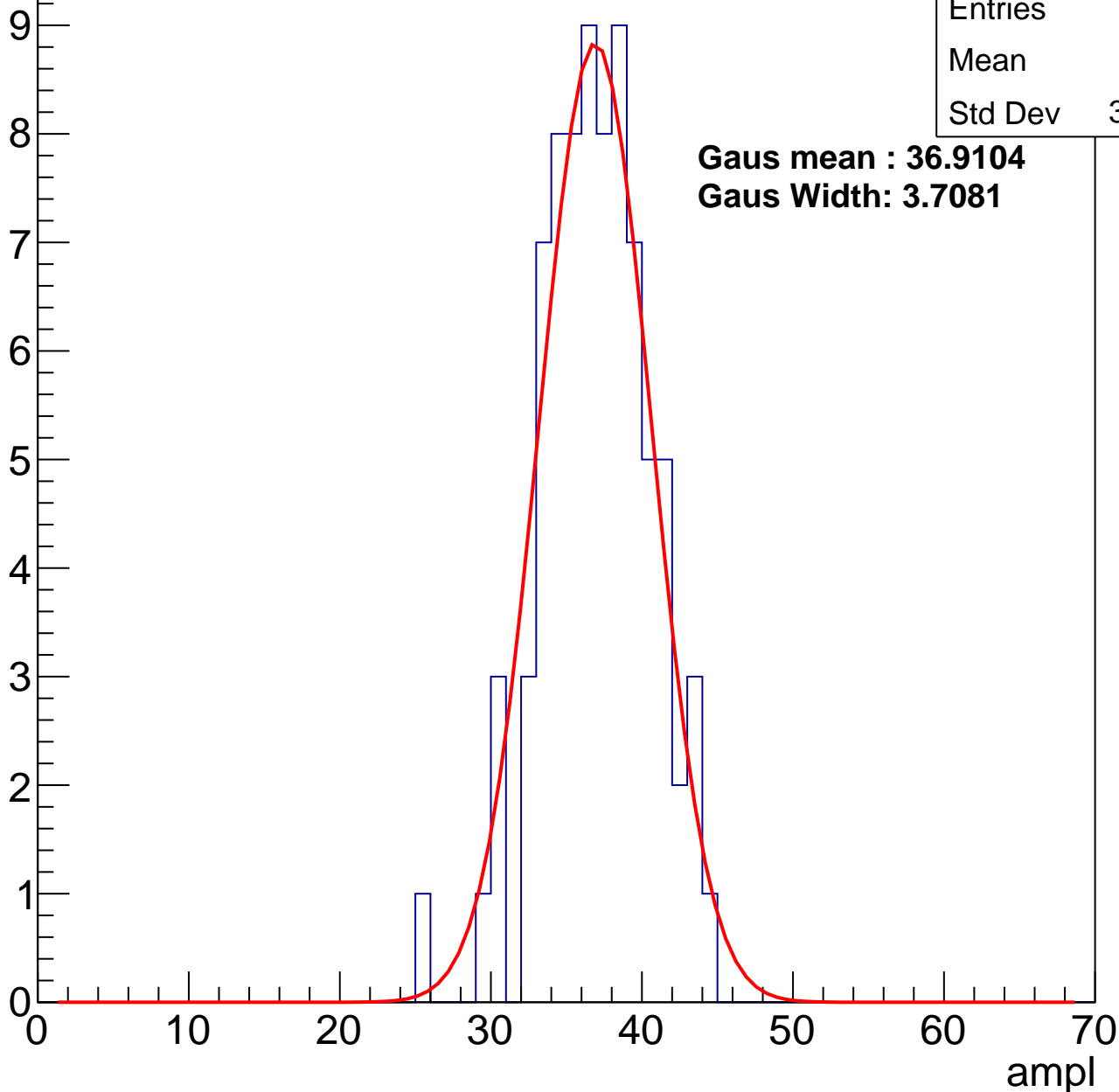
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.5
Std Dev	3.543

Gaus mean : 36.9104

Gaus Width: 3.7081



B1L102S, U10-ch1, adc2

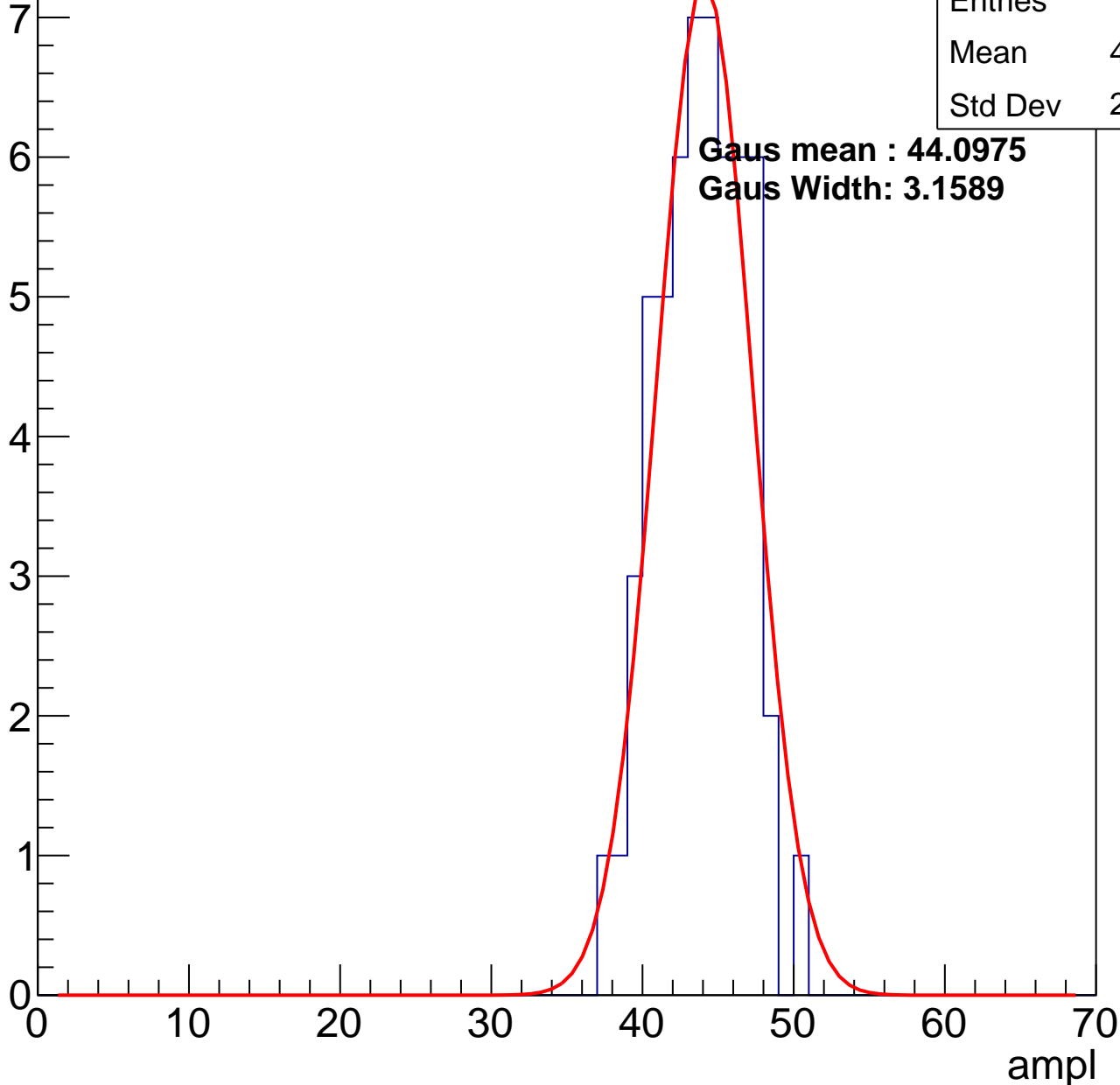
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.43
Std Dev	2.834

Gaus mean : 44.0975

Gaus Width: 3.1589



B1L102S, U10-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

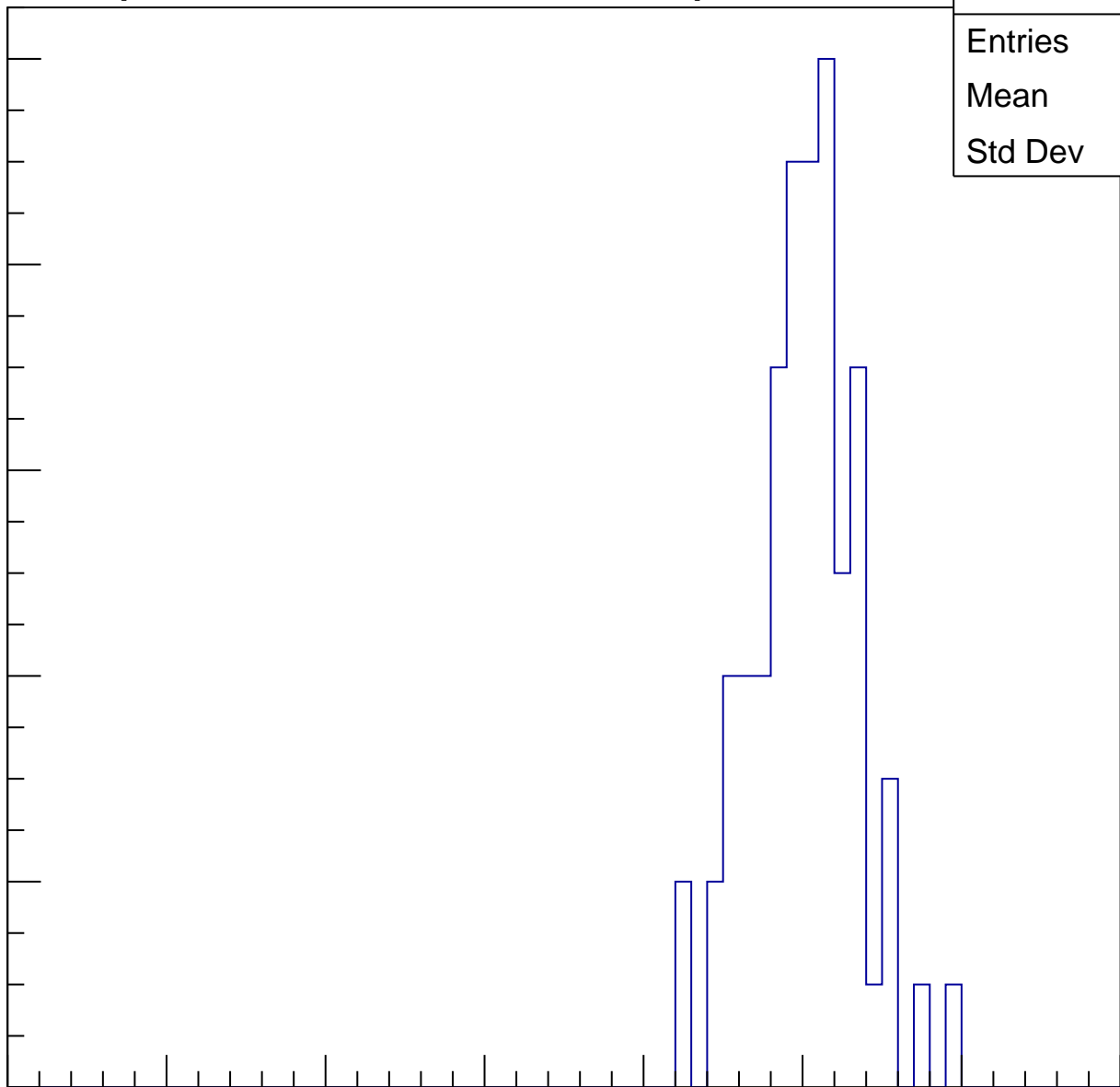
Entries	69
Mean	49.67
Std Dev	3.282

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

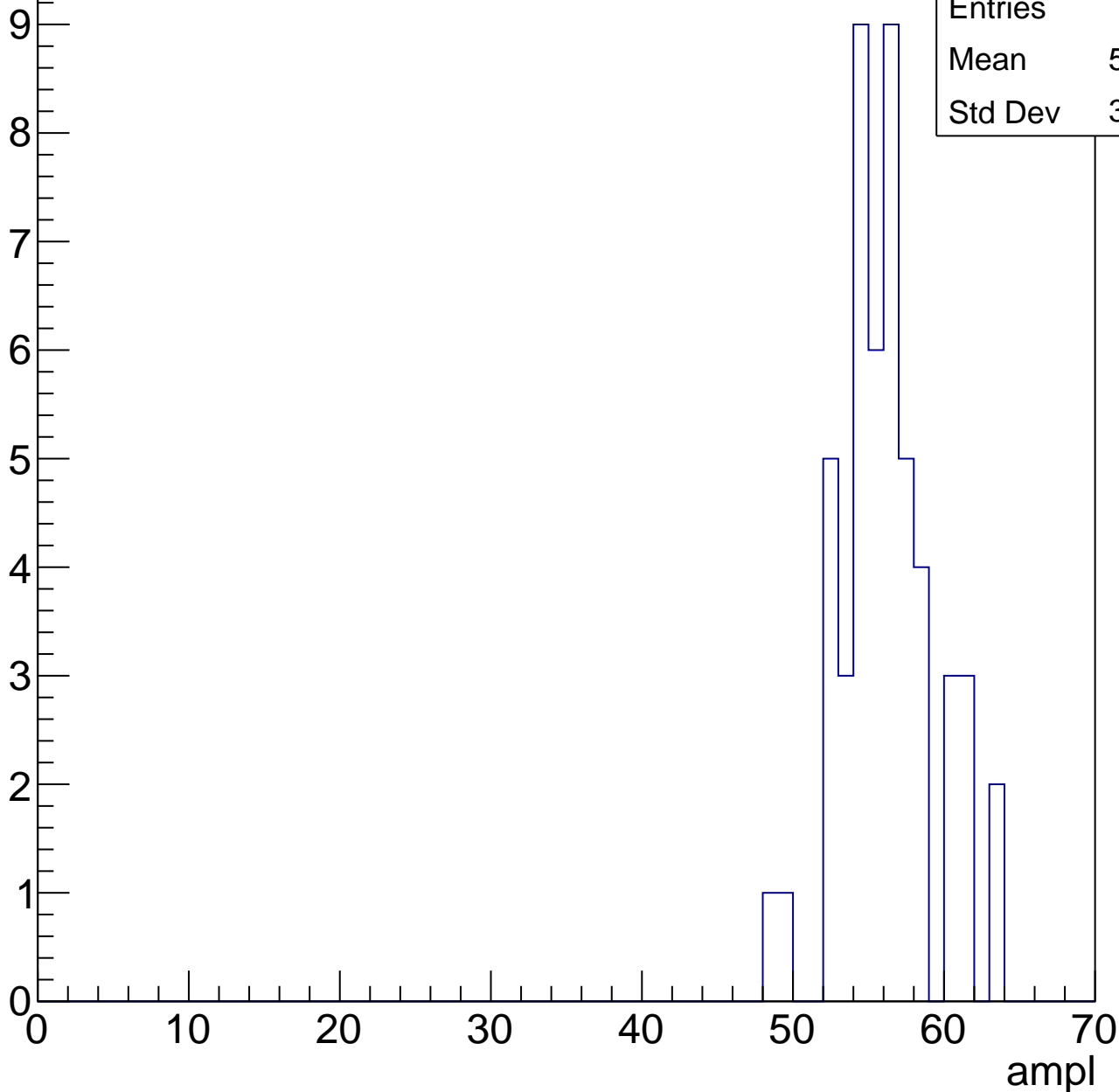


B1L102S, U10-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.73
Std Dev	3.132

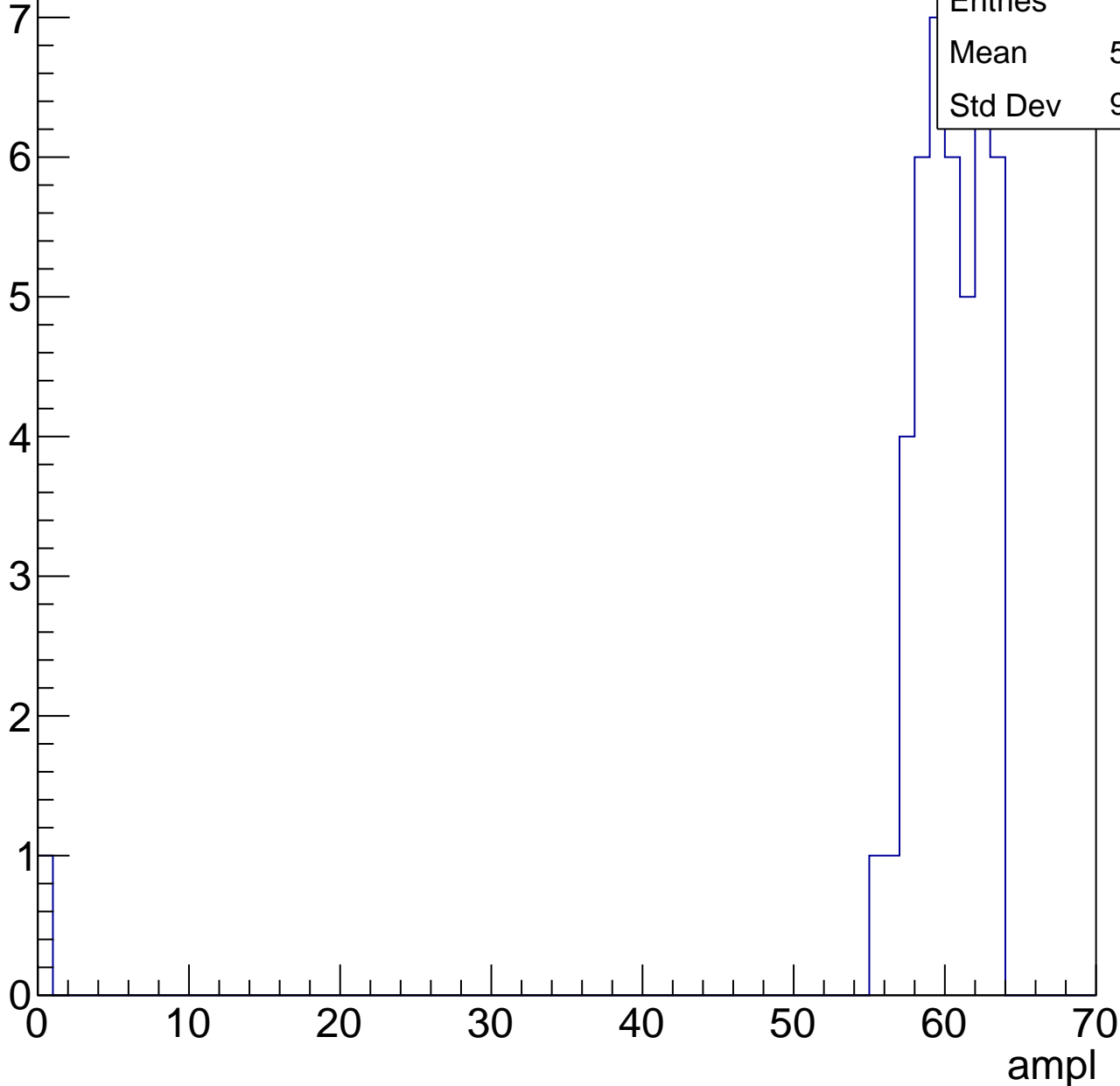


B1L102S, U10-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.57
Std Dev	9.176



B1L102S, U10-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

10

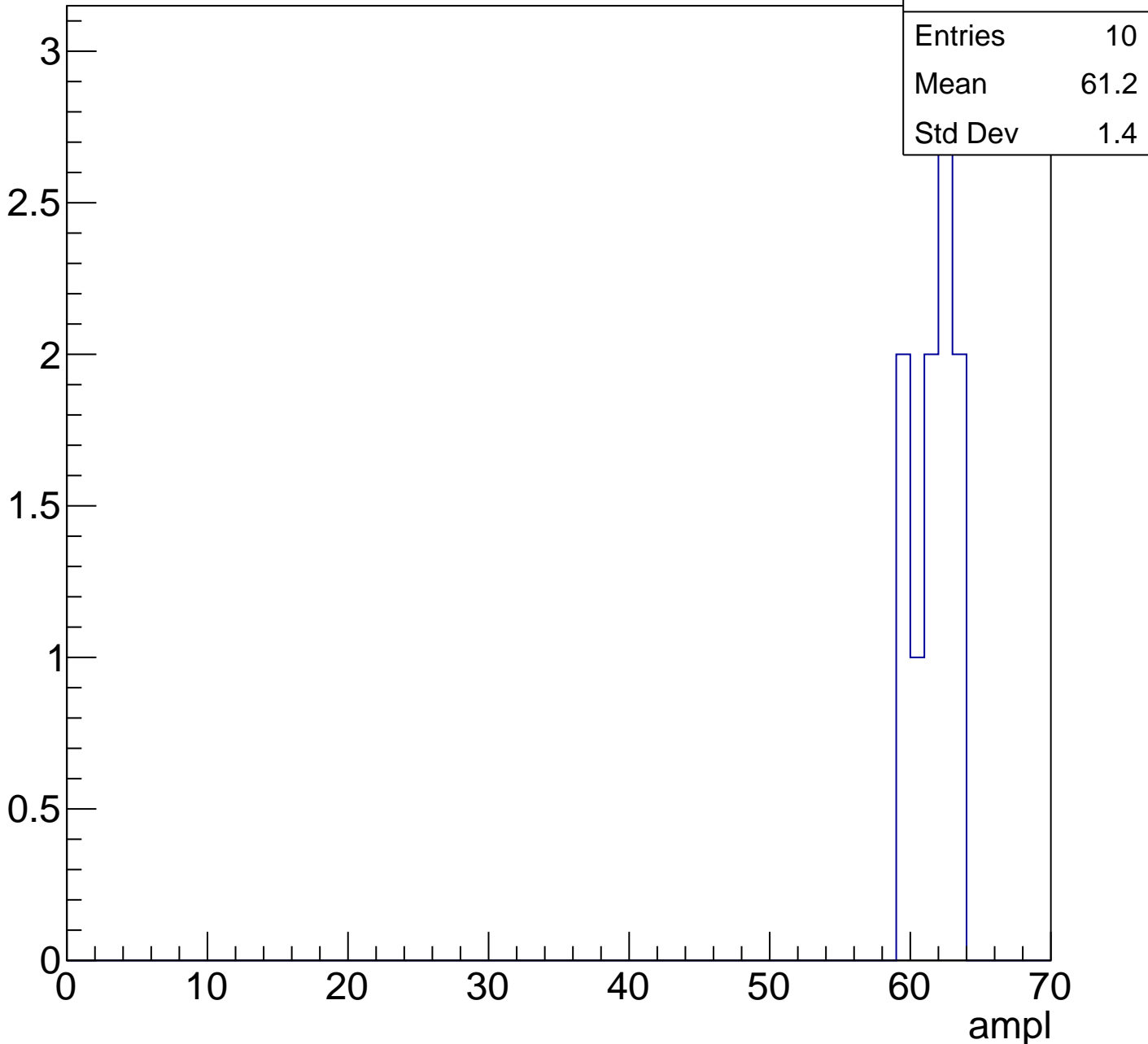
Mean

61.2

Std Dev

1.4

ampl



B1L102S, U10-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch2, adc0

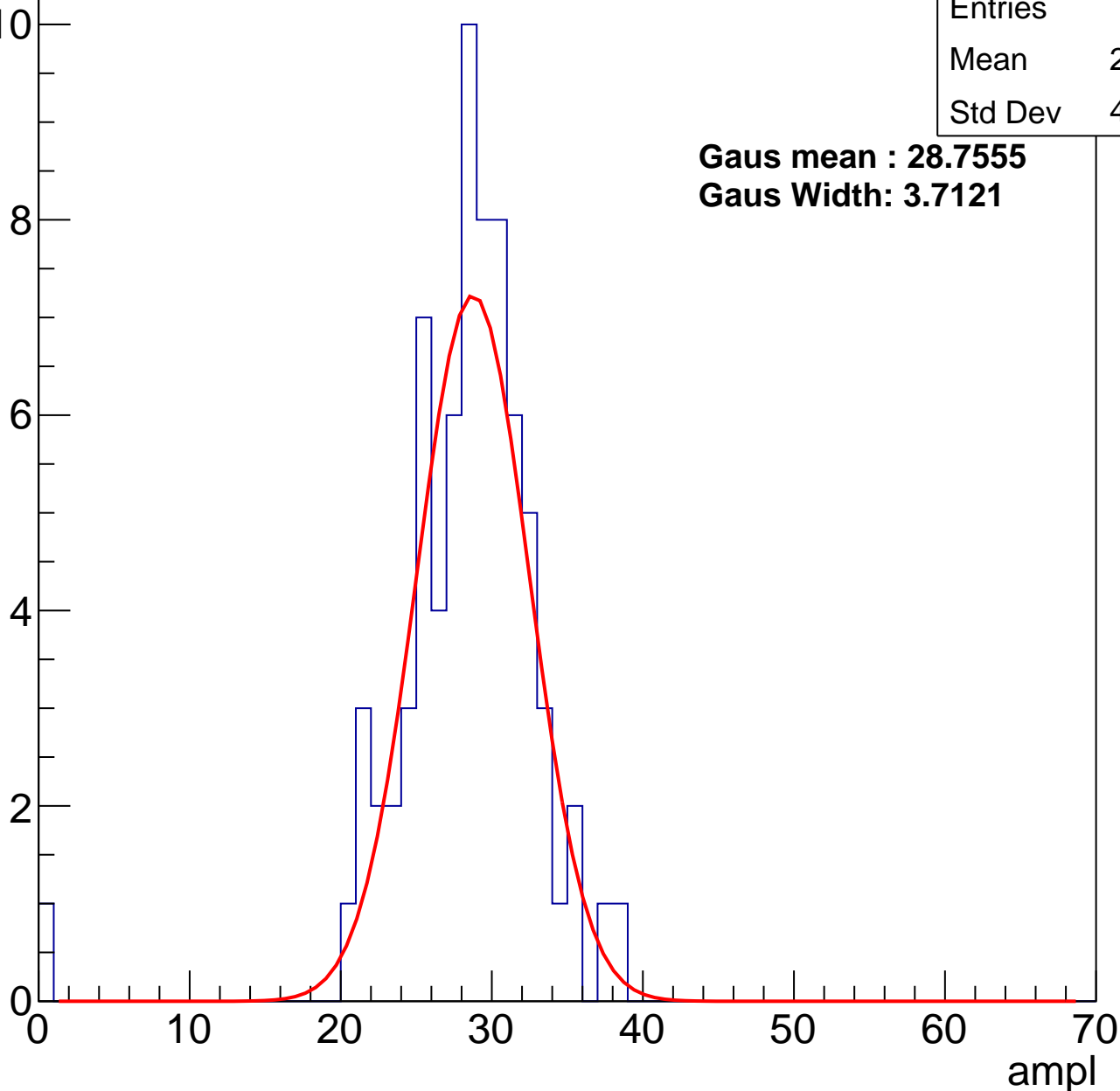
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.86
Std Dev	4.949

Gaus mean : 28.7555

Gaus Width: 3.7121



B1L102S, U10-ch2, adc1

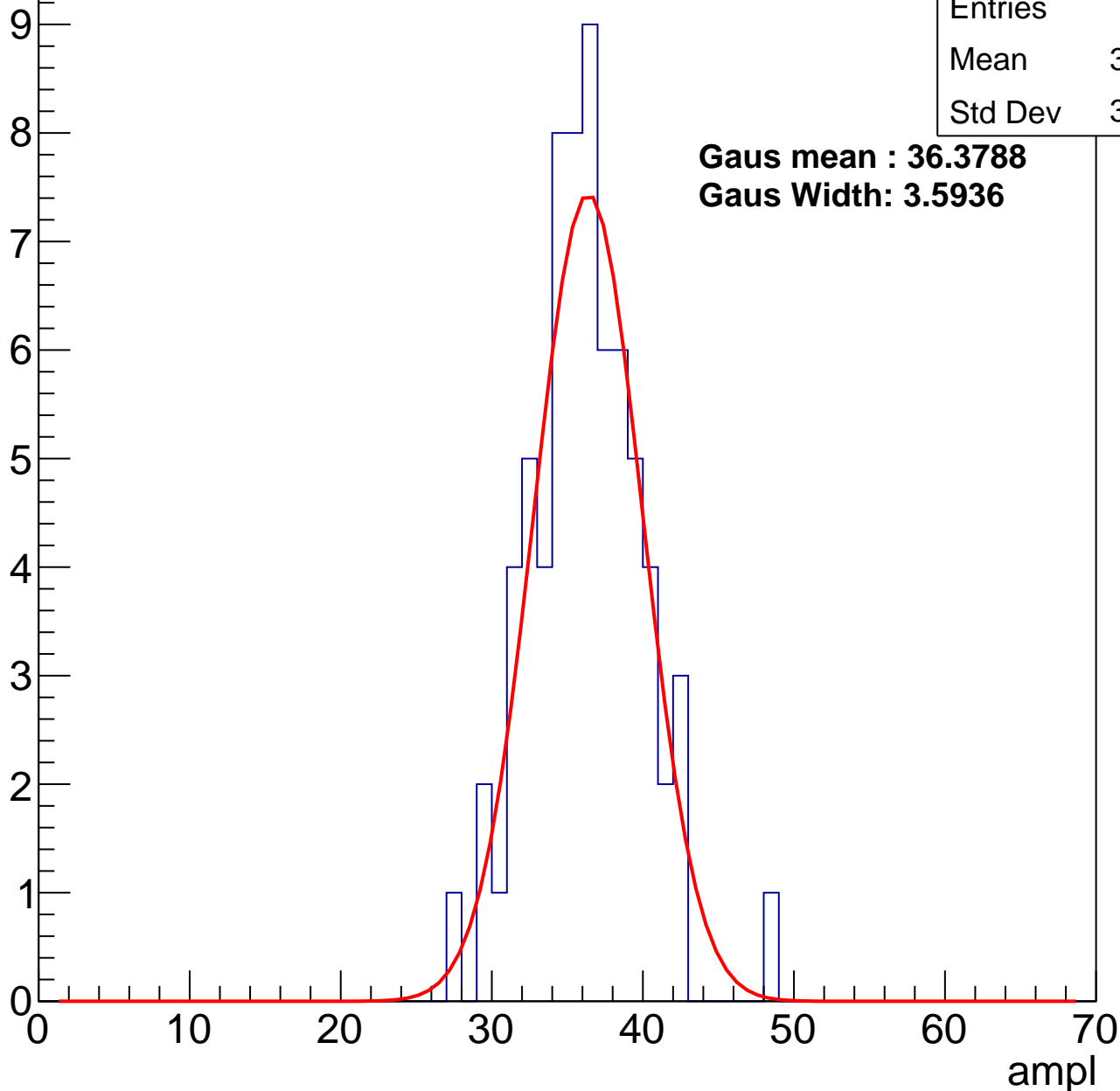
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.77
Std Dev	3.628

Gaus mean : 36.3788

Gaus Width: 3.5936



B1L102S, U10-ch2, adc2

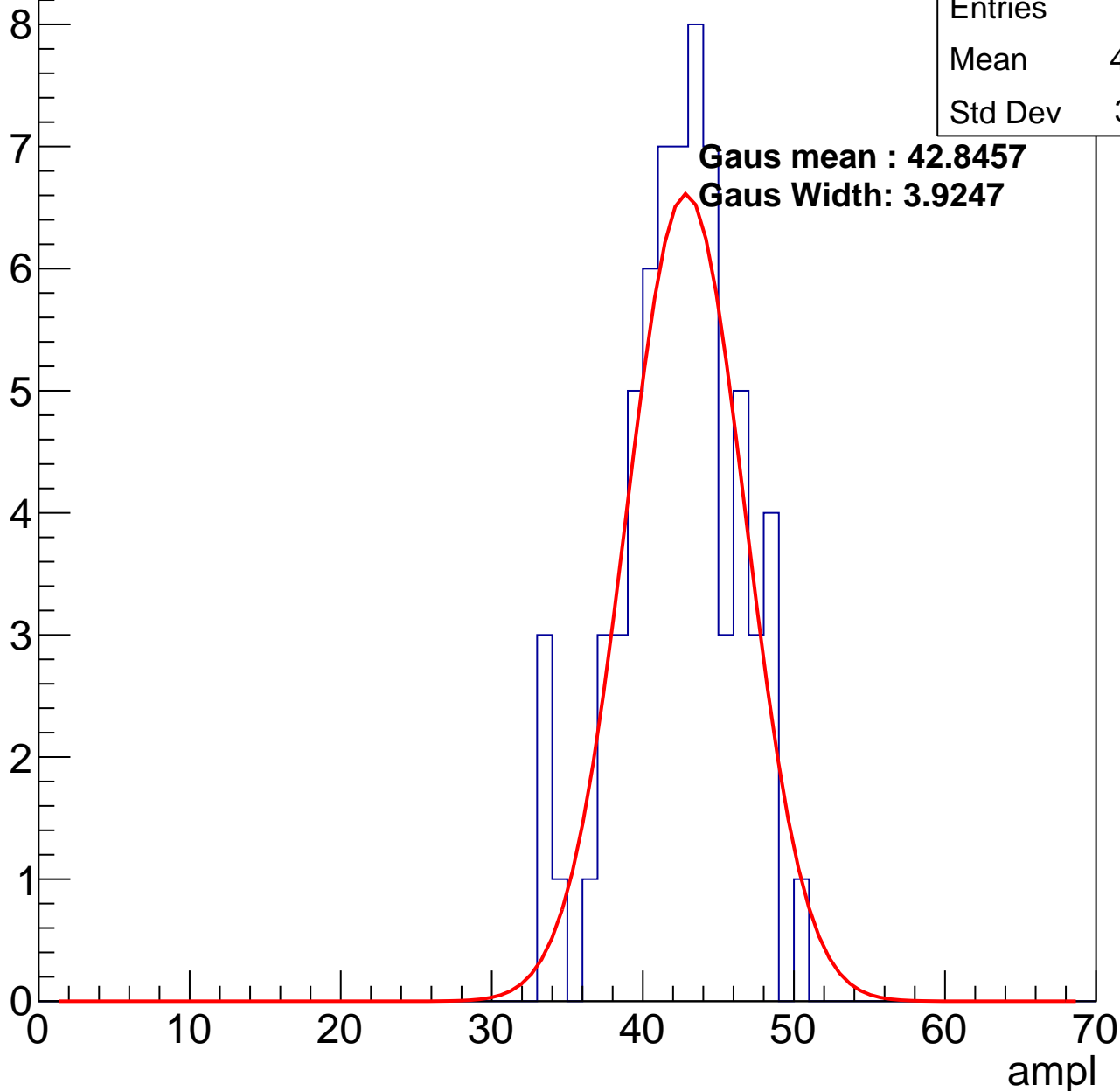
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.94
Std Dev	3.801

Gaus mean : 42.8457

Gaus Width: 3.9247

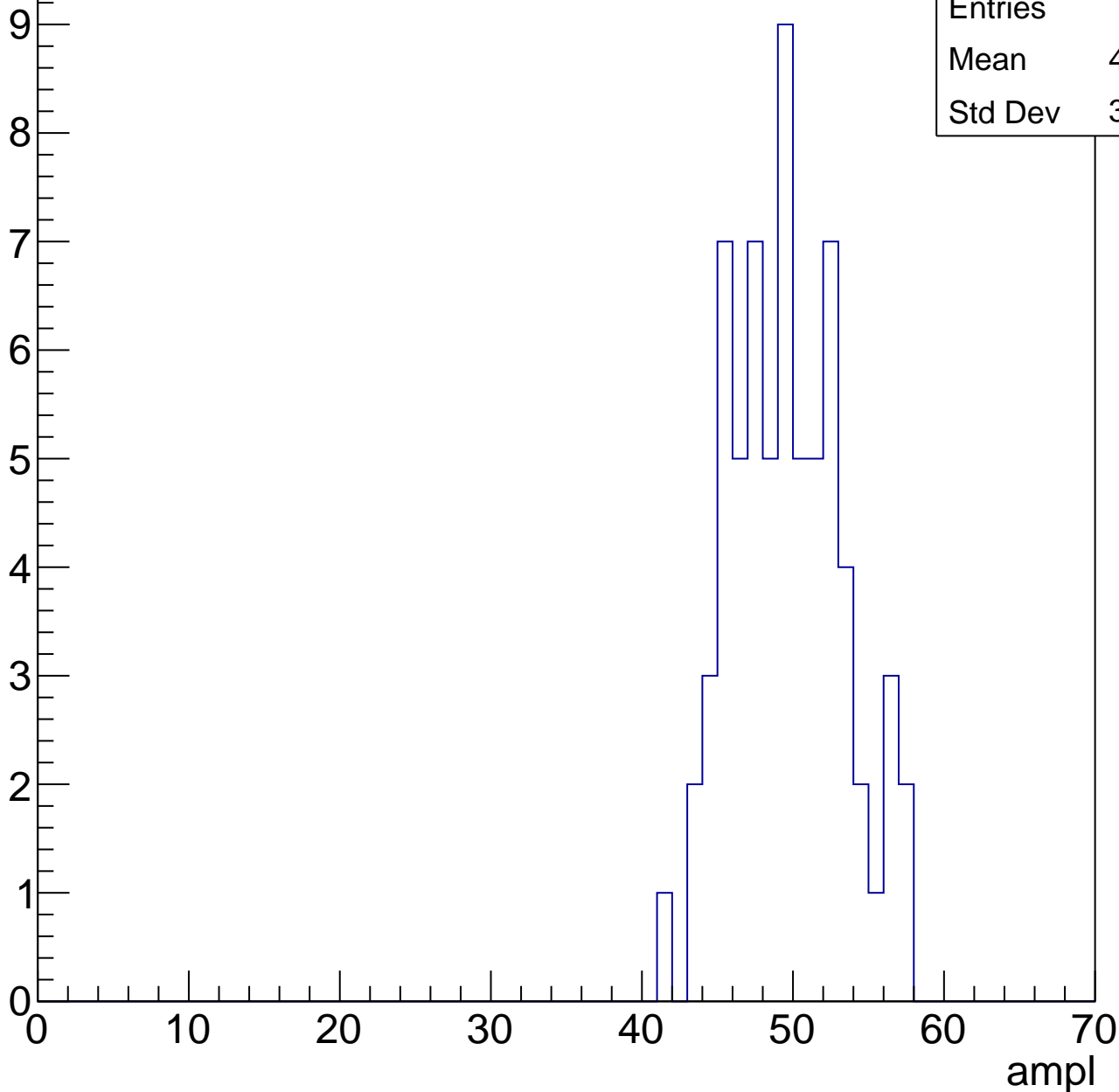


B1L102S, U10-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.12
Std Dev	3.664

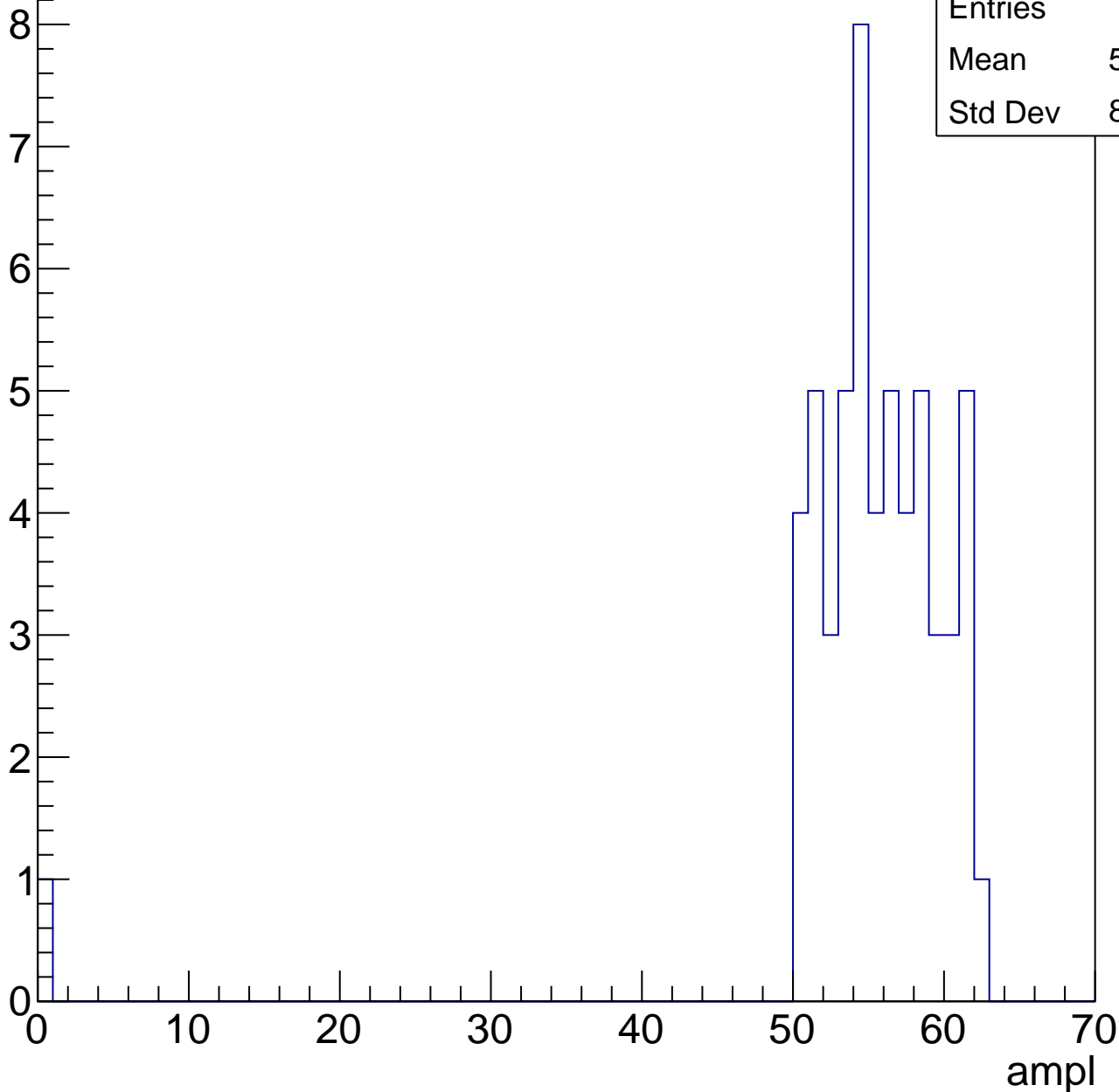


B1L102S, U10-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.46
Std Dev	8.087

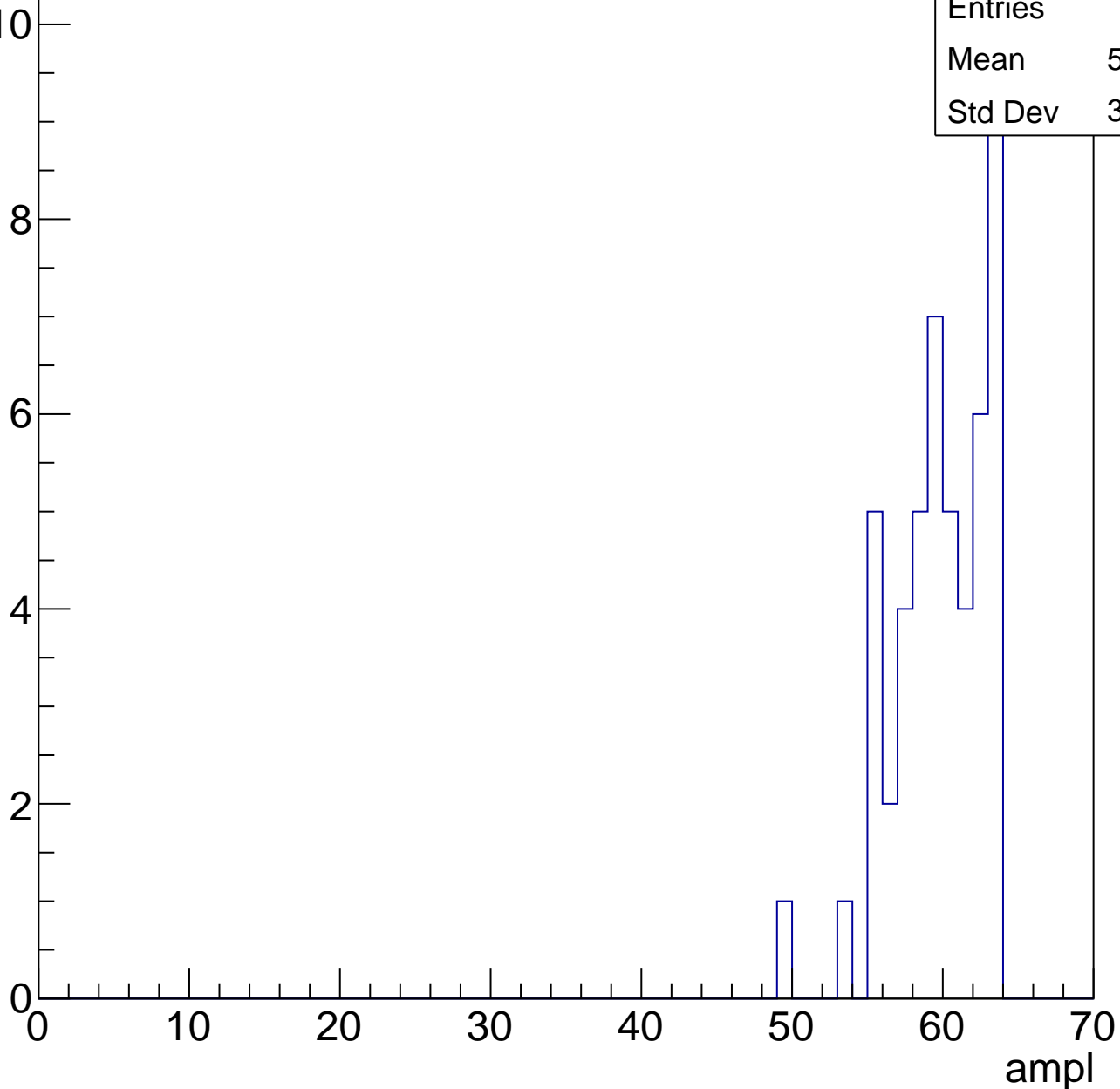


B1L102S, U10-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

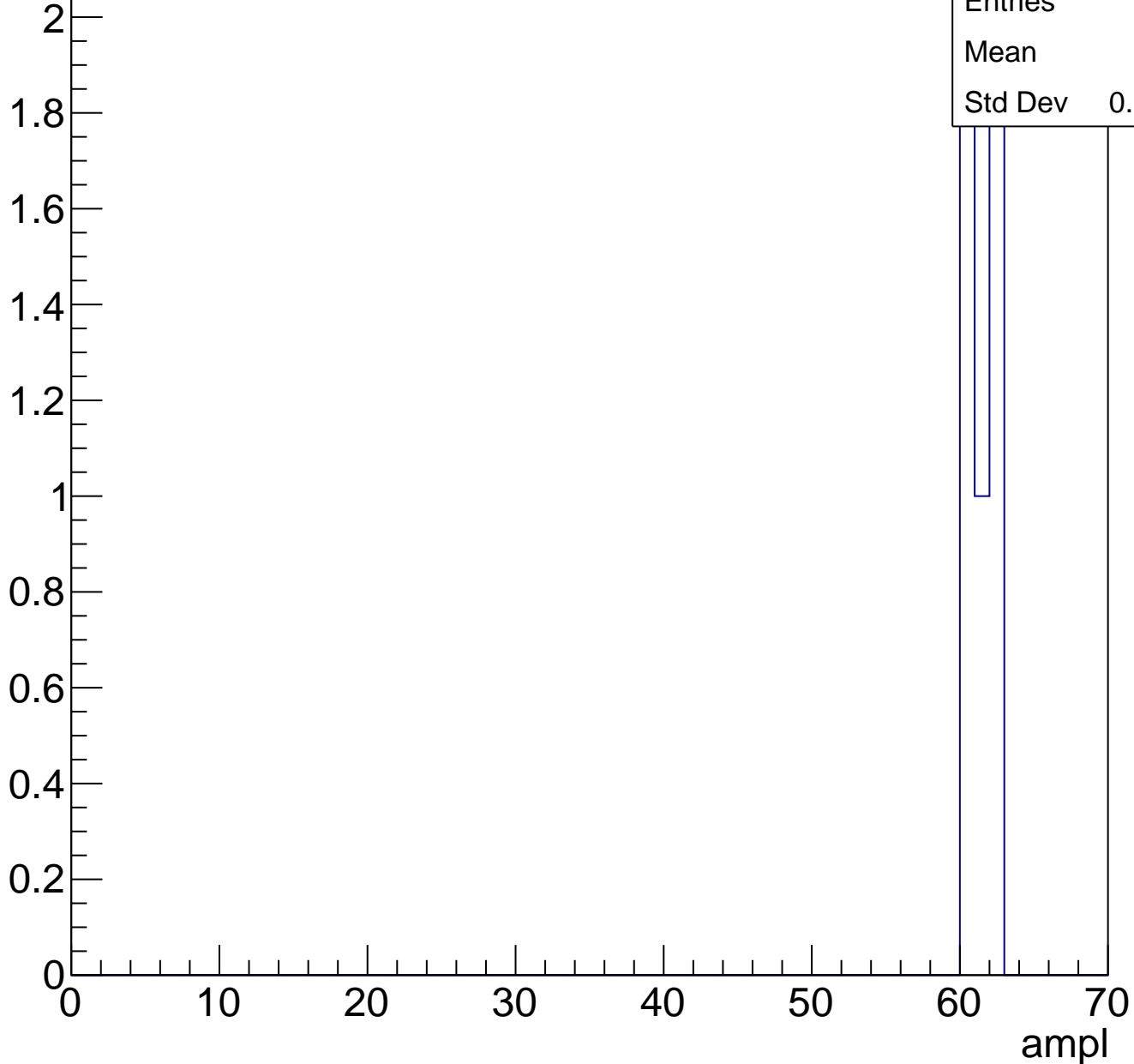
Entries	50
Mean	59.32
Std Dev	3.114



B1L102S, U10-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

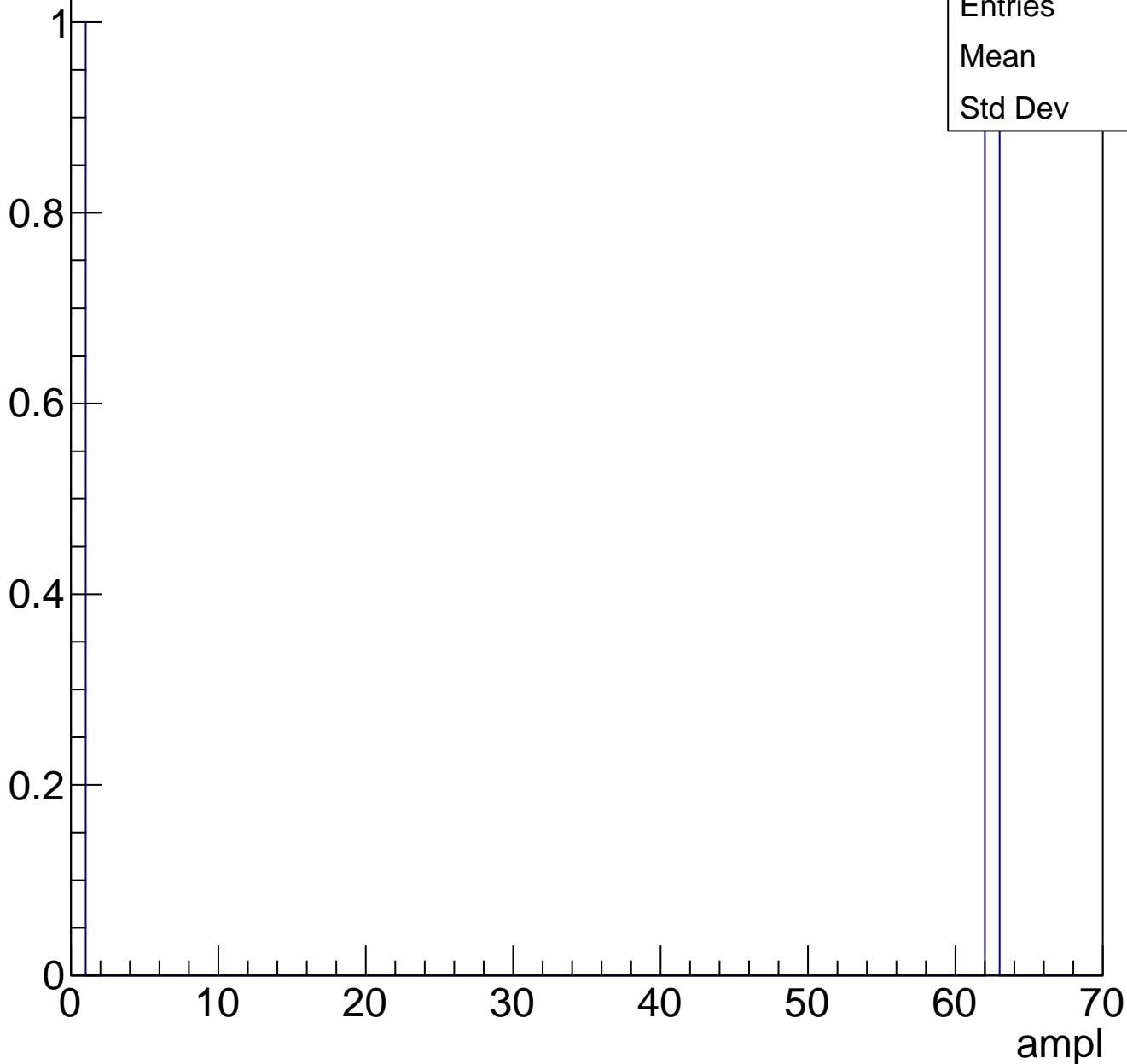
Entry



B1L102S, U10-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch3, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.86
Std Dev	3.554

Gaus mean : 29.5742

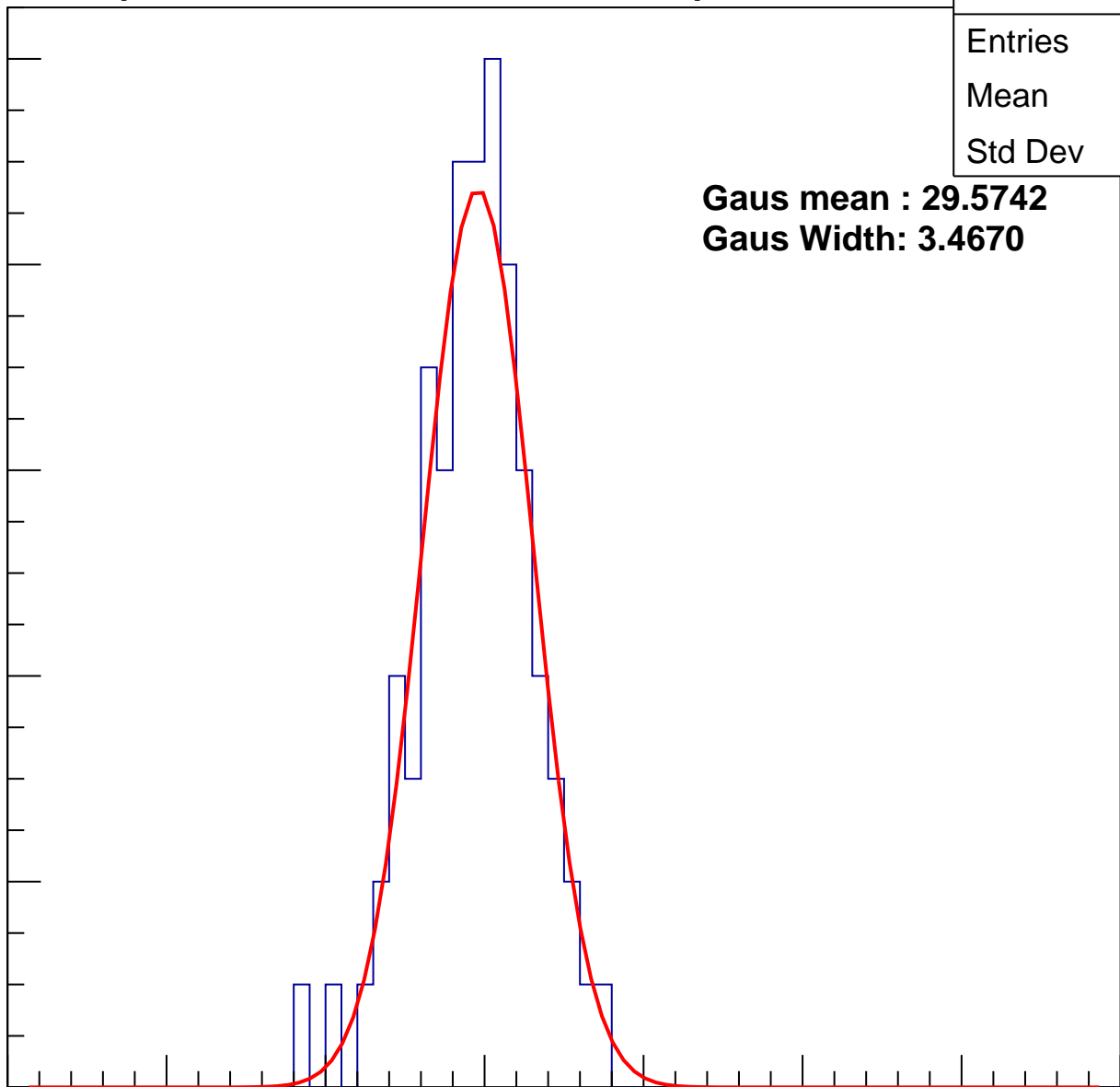
Gaus Width: 3.4670

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch3, adc1

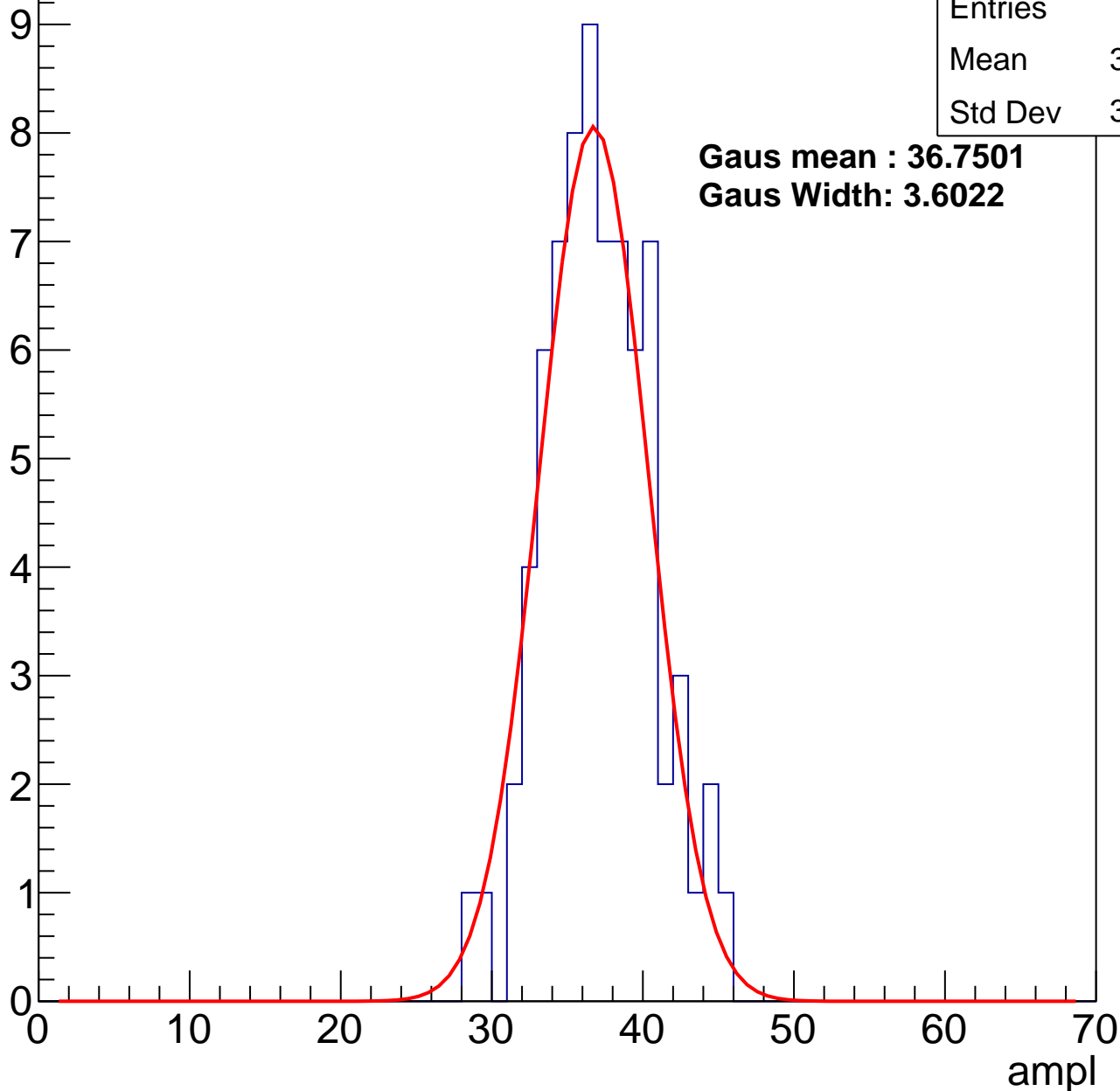
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.62
Std Dev	3.498

Gaus mean : 36.7501

Gaus Width: 3.6022



B1L102S, U10-ch3, adc2

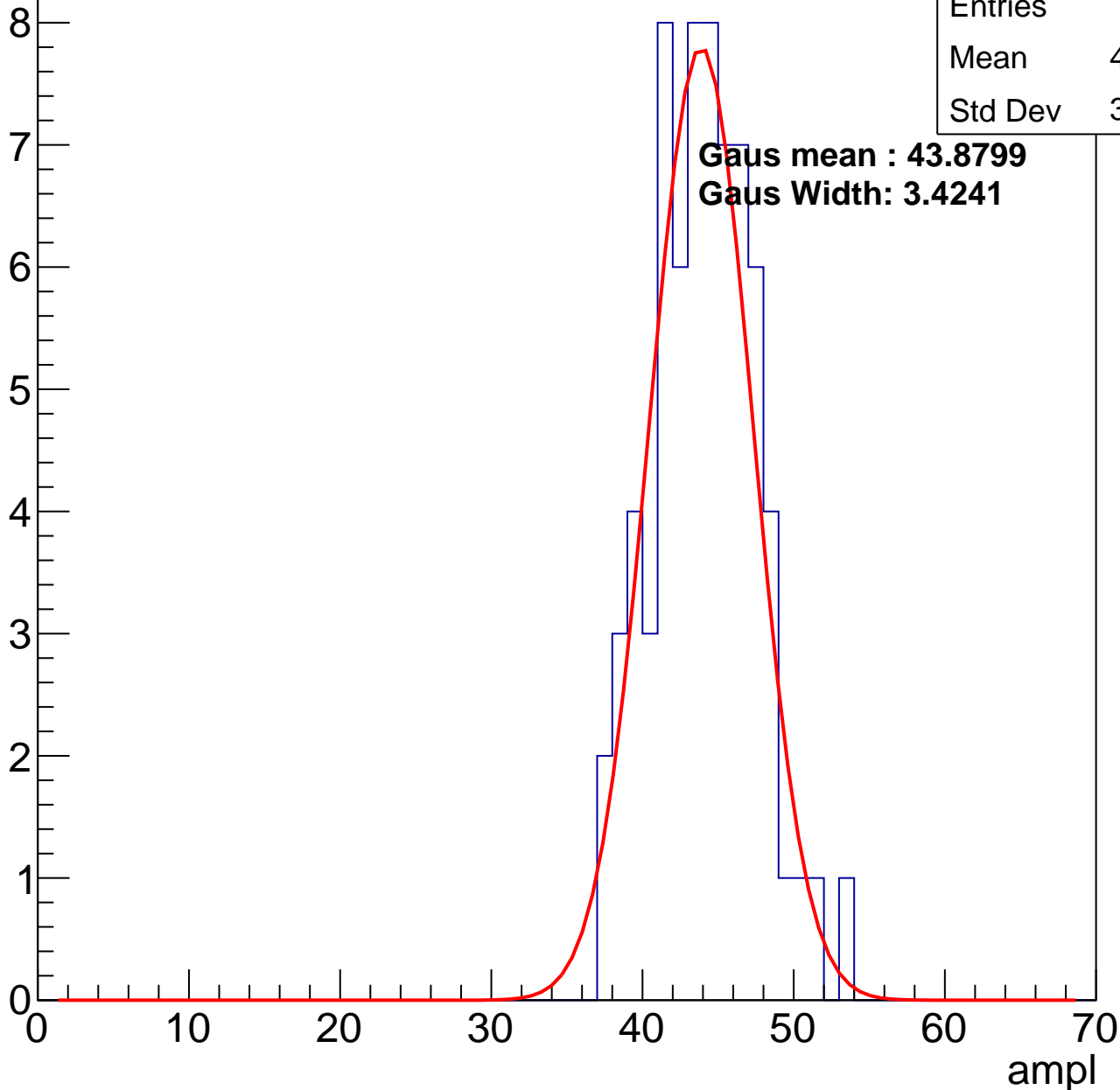
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.63
Std Dev	3.364

Gaus mean : 43.8799

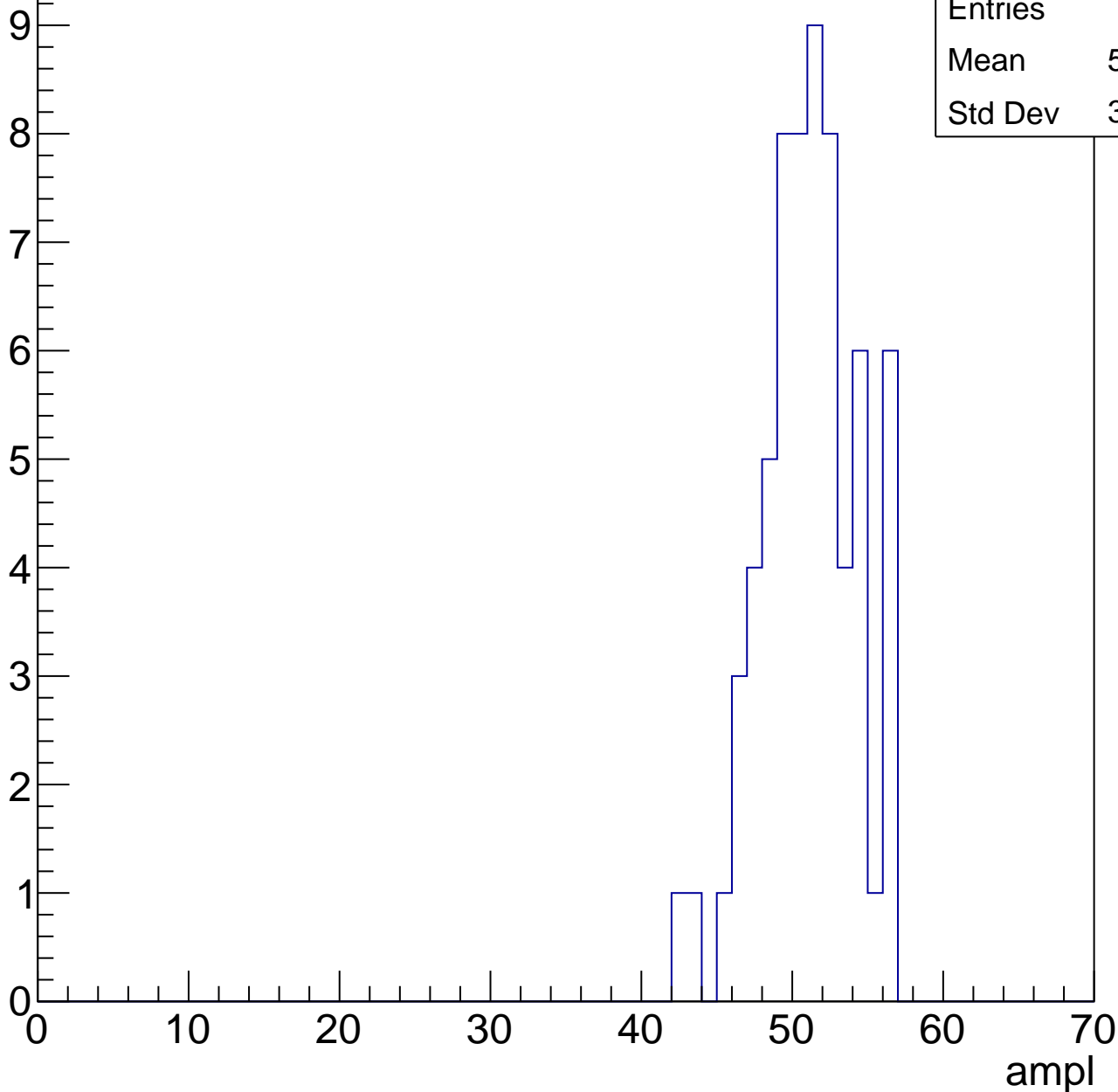
Gaus Width: 3.4241



B1L102S, U10-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



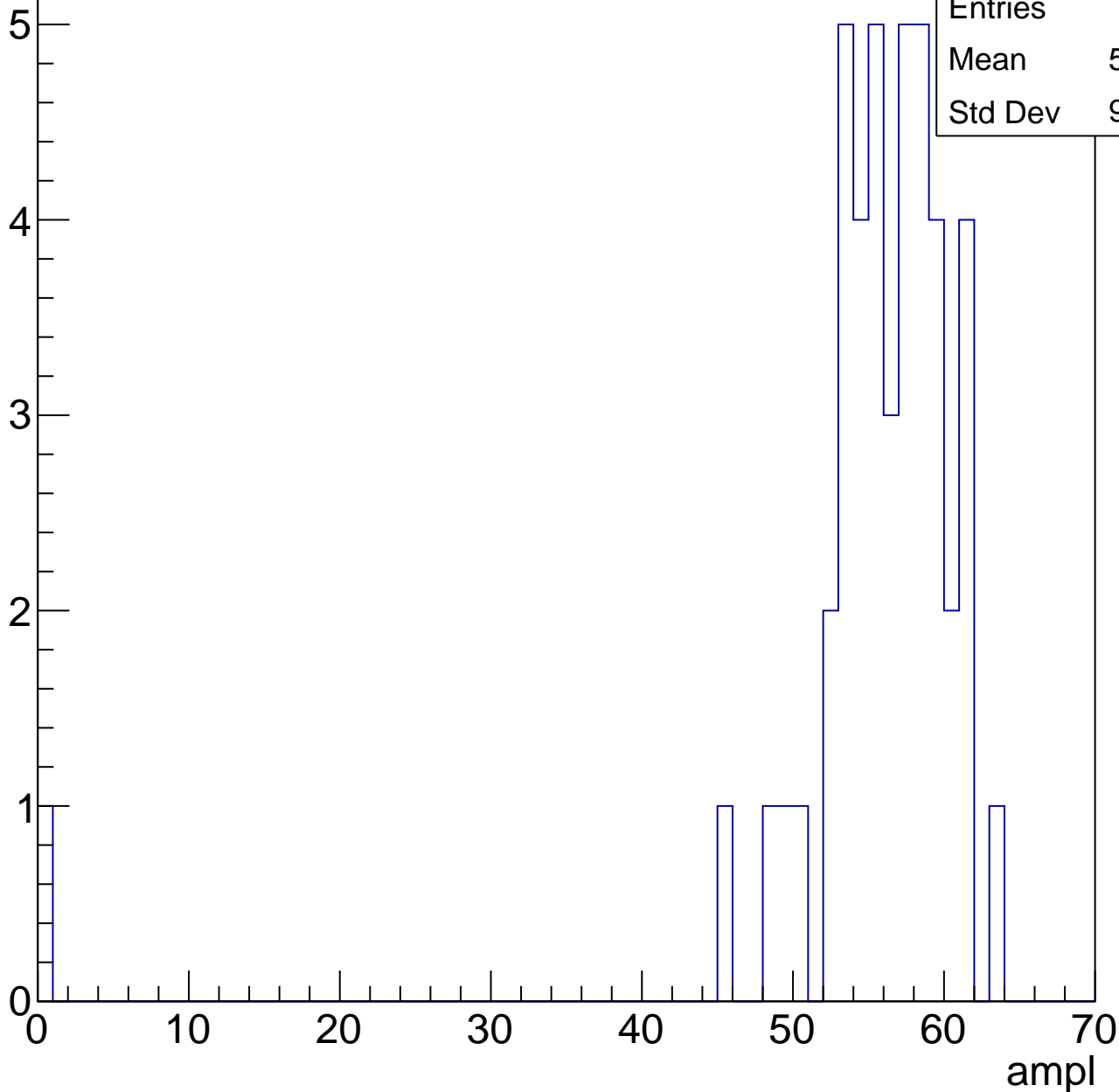
Entries	65
Mean	50.62
Std Dev	3.136

B1L102S, U10-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	54.62
Std Dev	9.019



B1L102S, U10-ch3, adc5

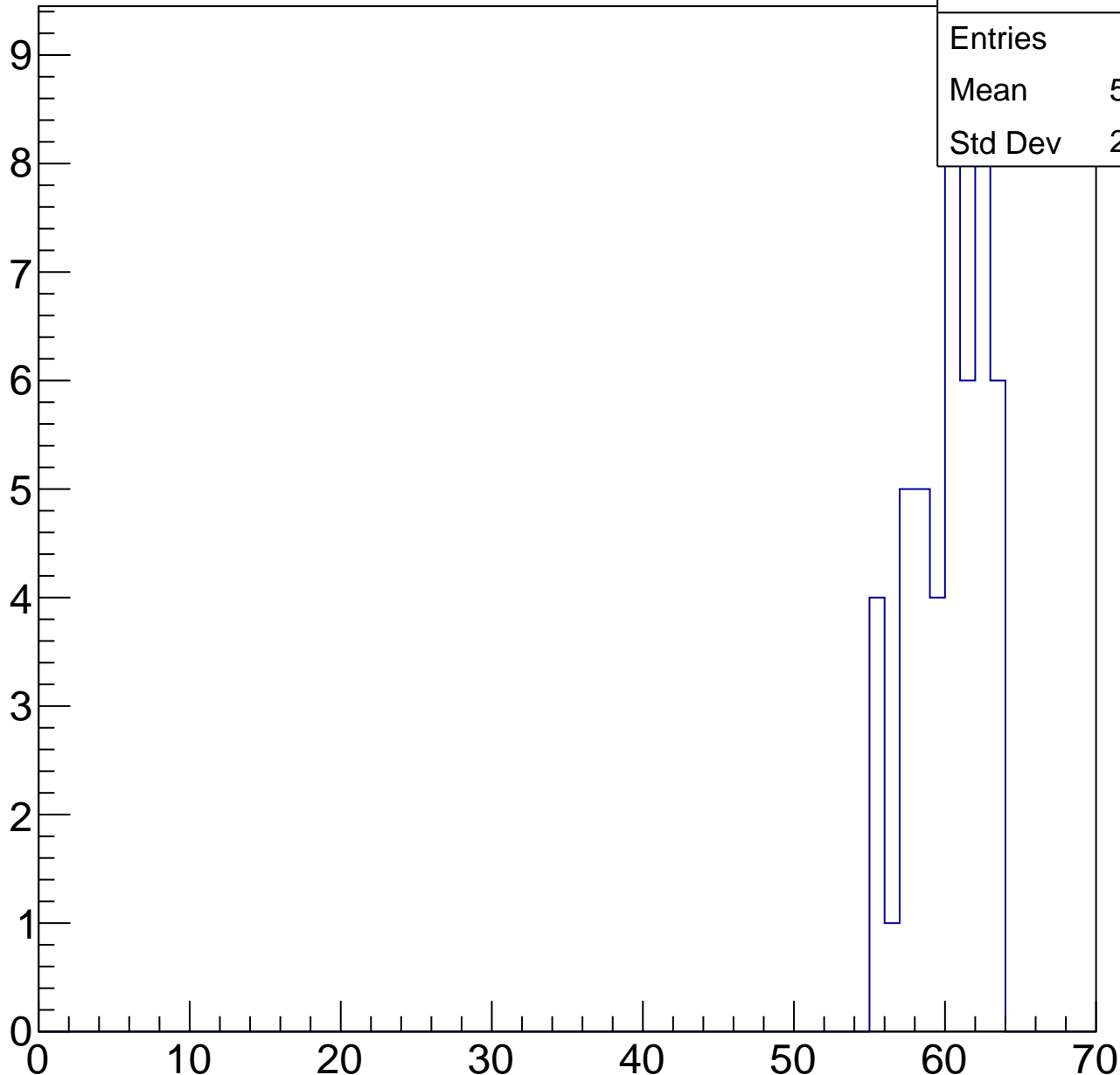
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.77
Std Dev	2.409

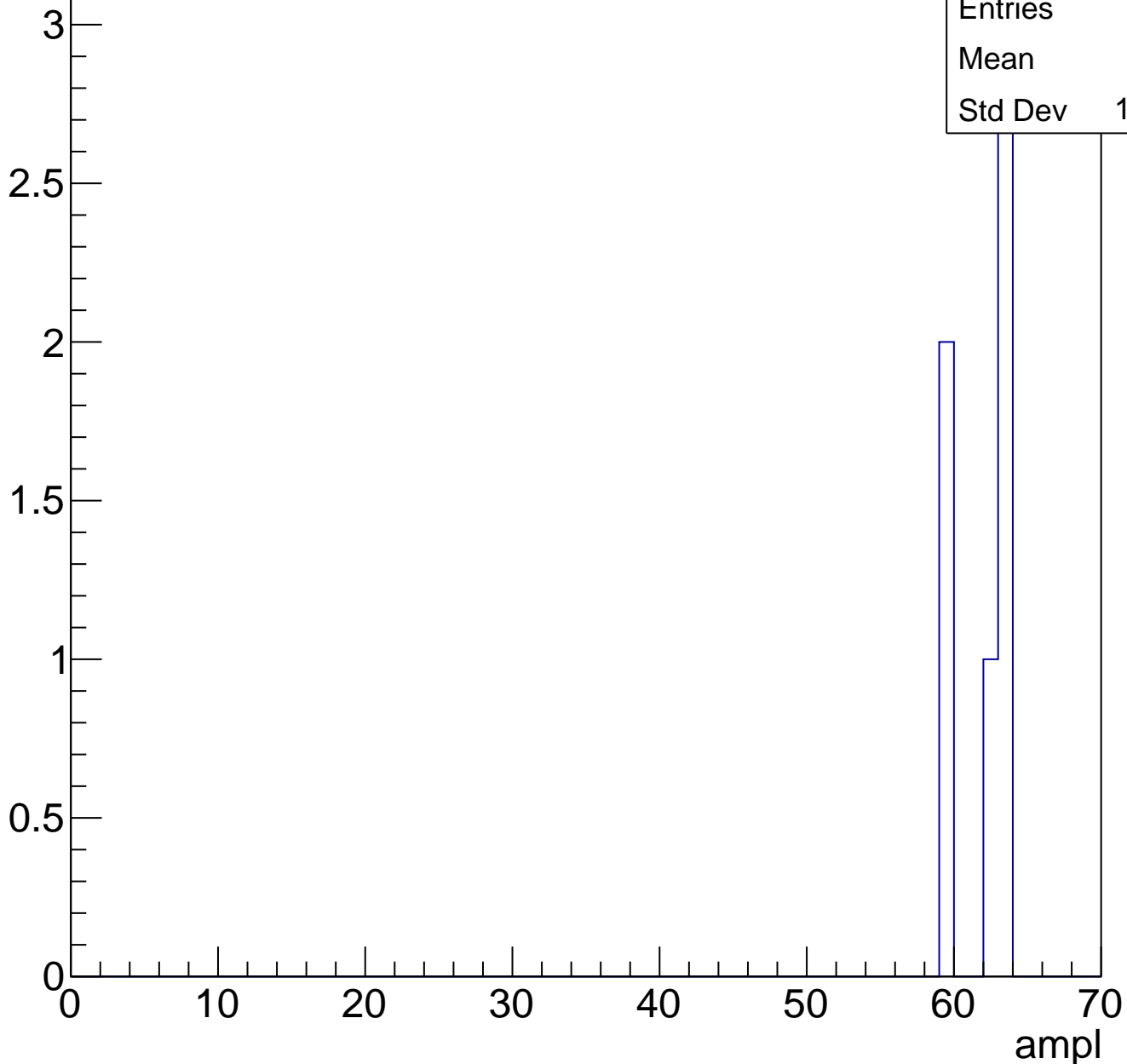
ampl



B1L102S, U10-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	61.5
Std Dev	1.803

B1L102S, U10-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U10-ch4, adc0

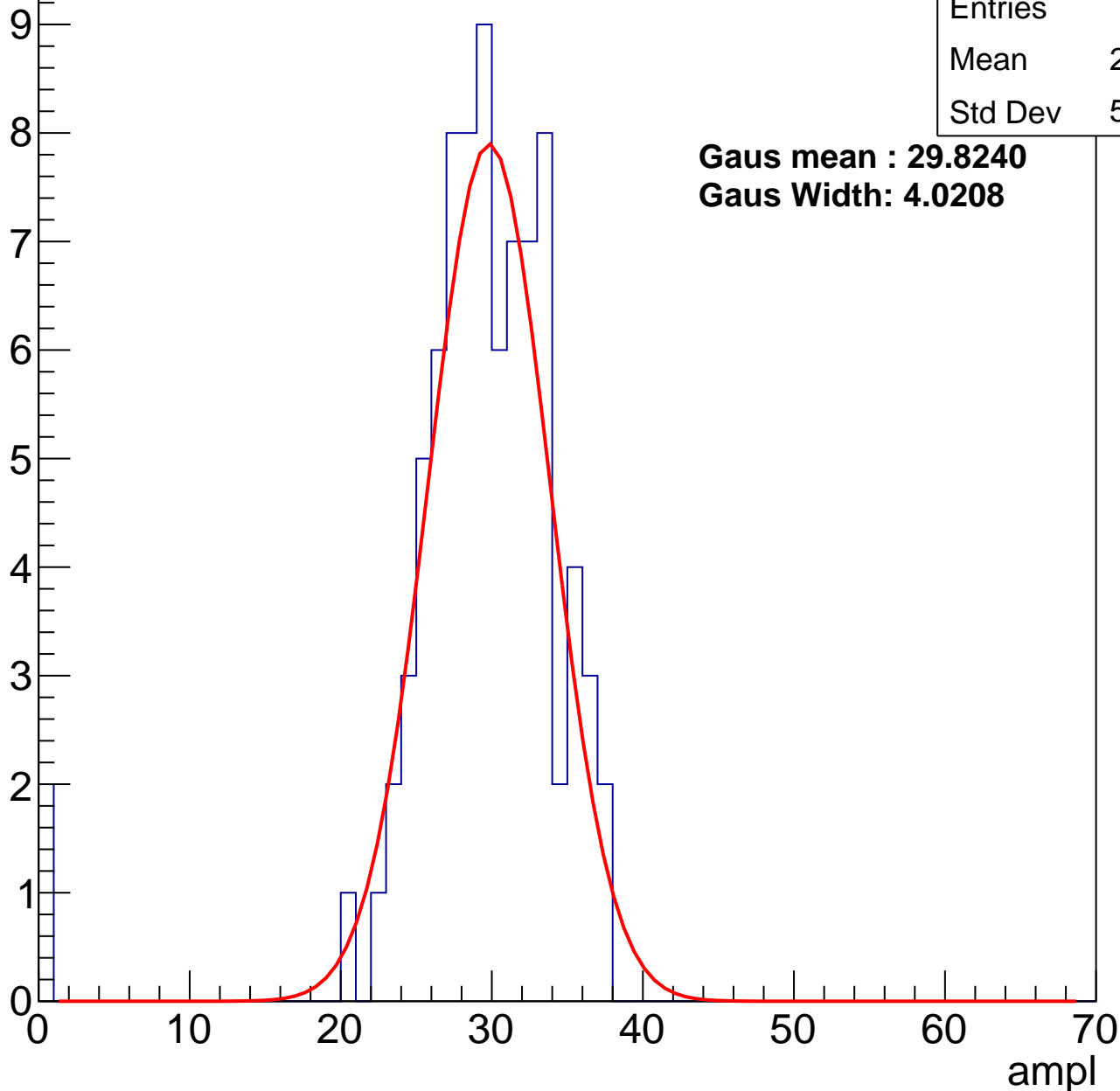
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.77
Std Dev	5.797

Gaus mean : 29.8240

Gaus Width: 4.0208



B1L102S, U10-ch4, adc1

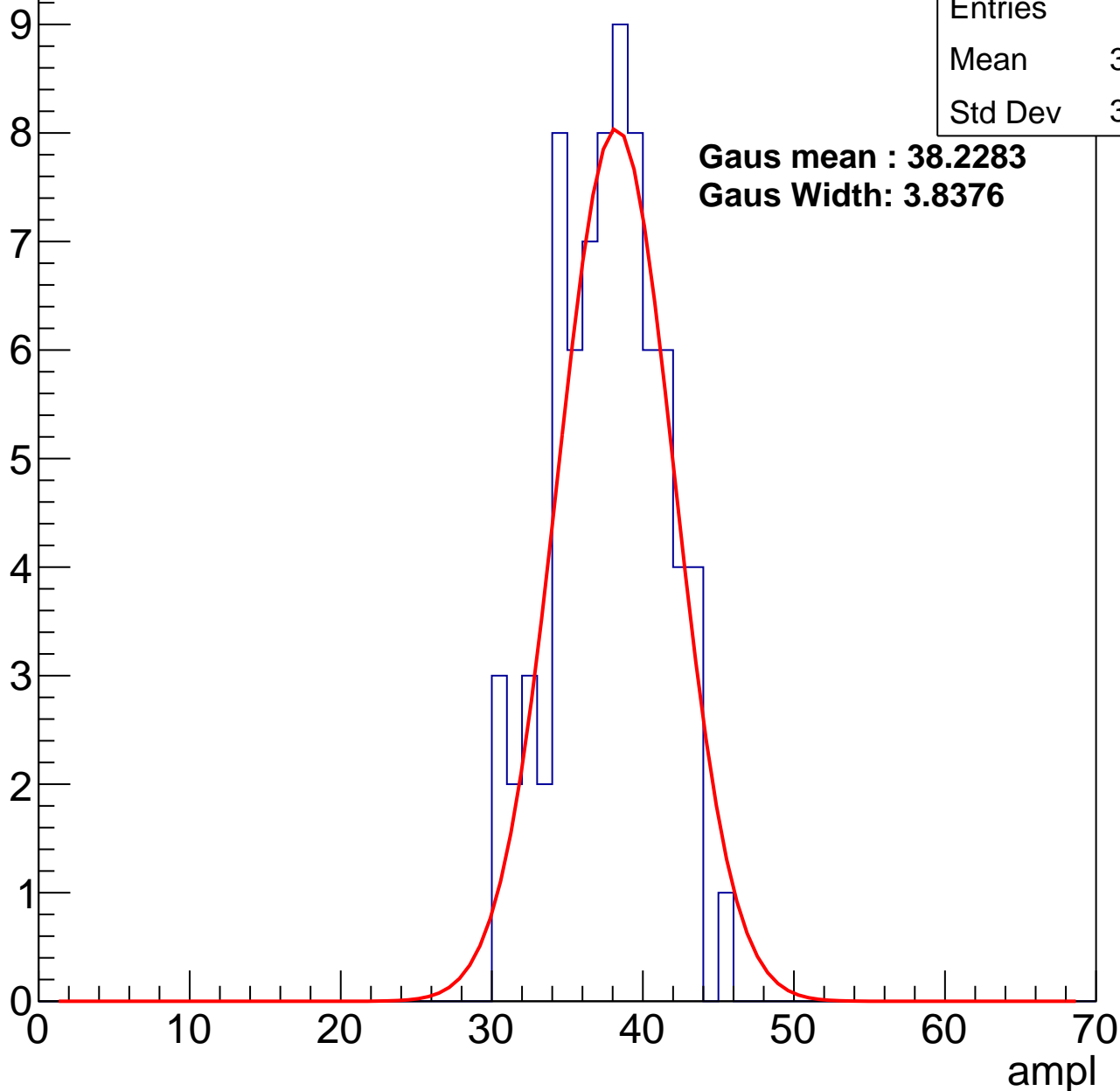
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	37.26
Std Dev	3.462

Gaus mean : 38.2283

Gaus Width: 3.8376



B1L102S, U10-ch4, adc2

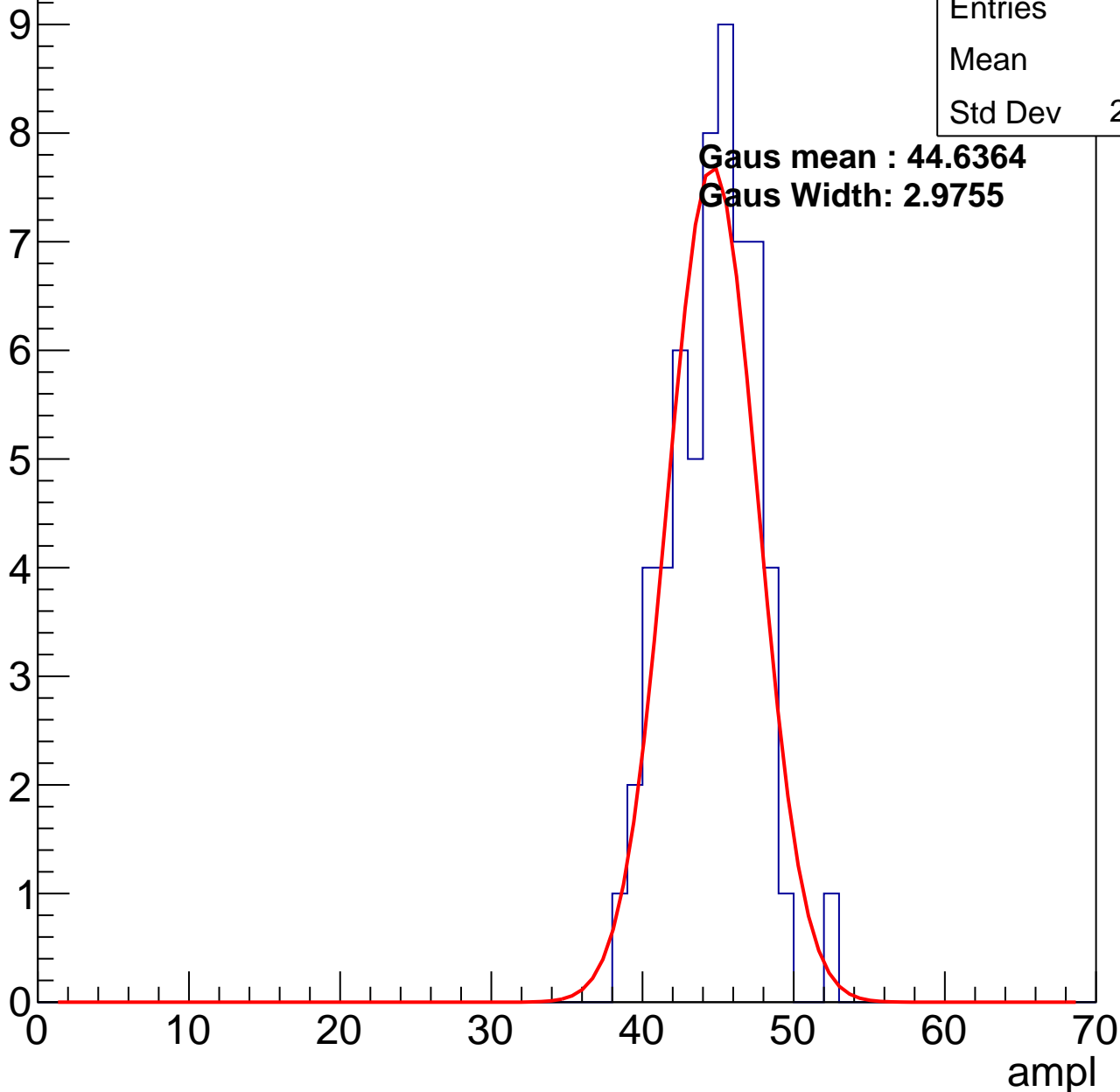
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.2
Std Dev	2.809

Gaus mean : 44.6364

Gaus Width: 2.9755

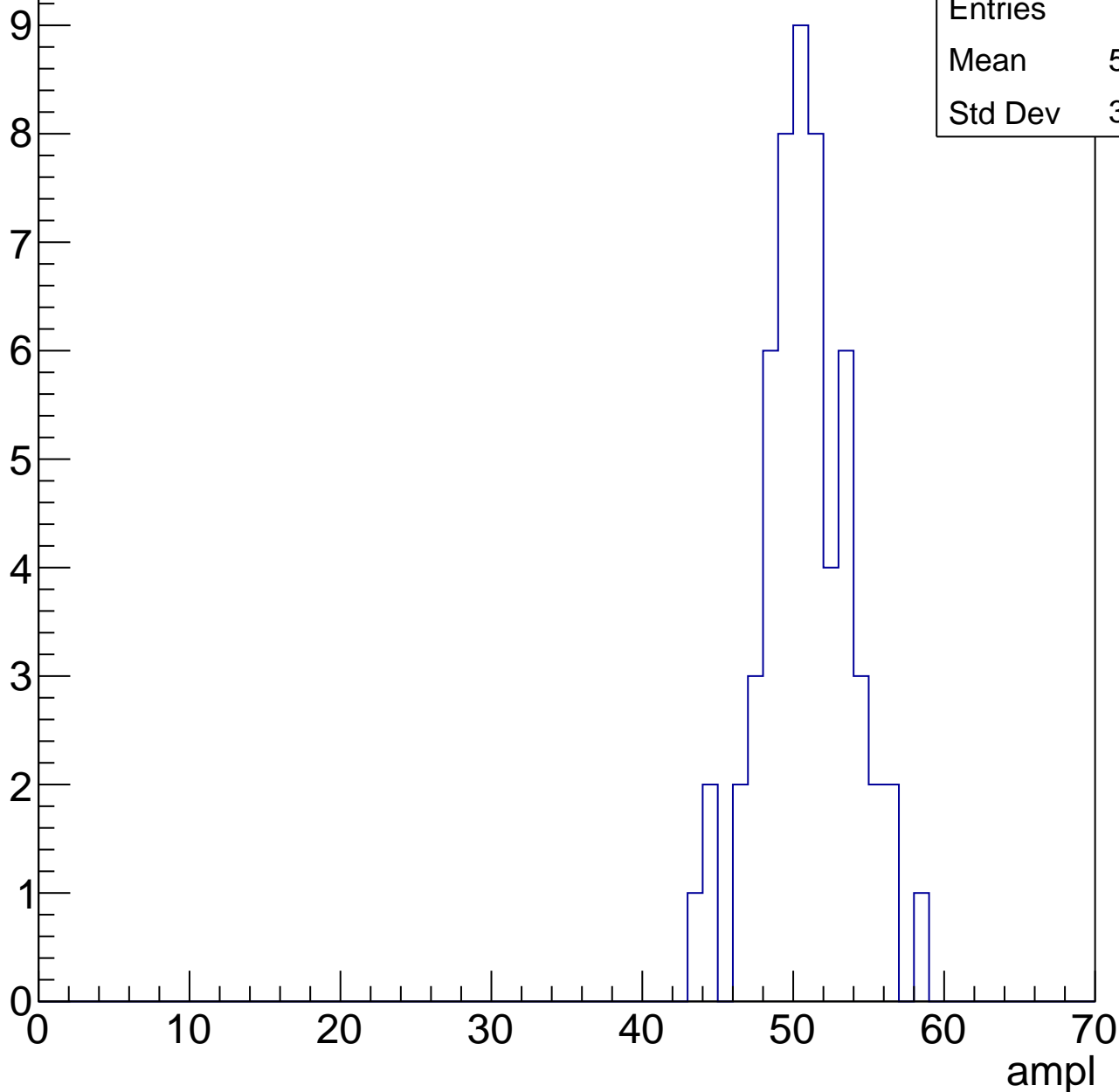


B1L102S, U10-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

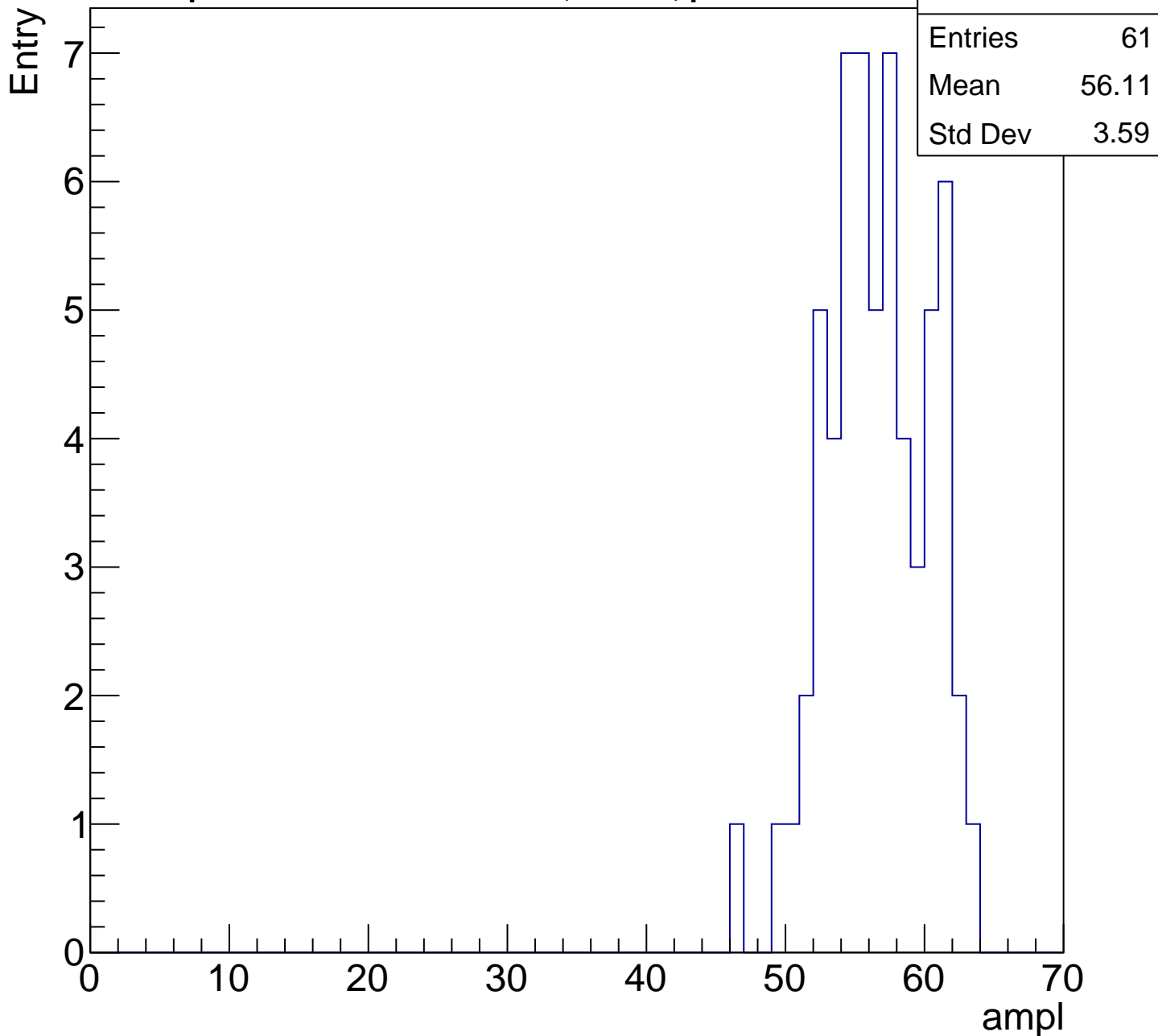
Entry

Entries	57
Mean	50.35
Std Dev	3.012



B1L102S, U10-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

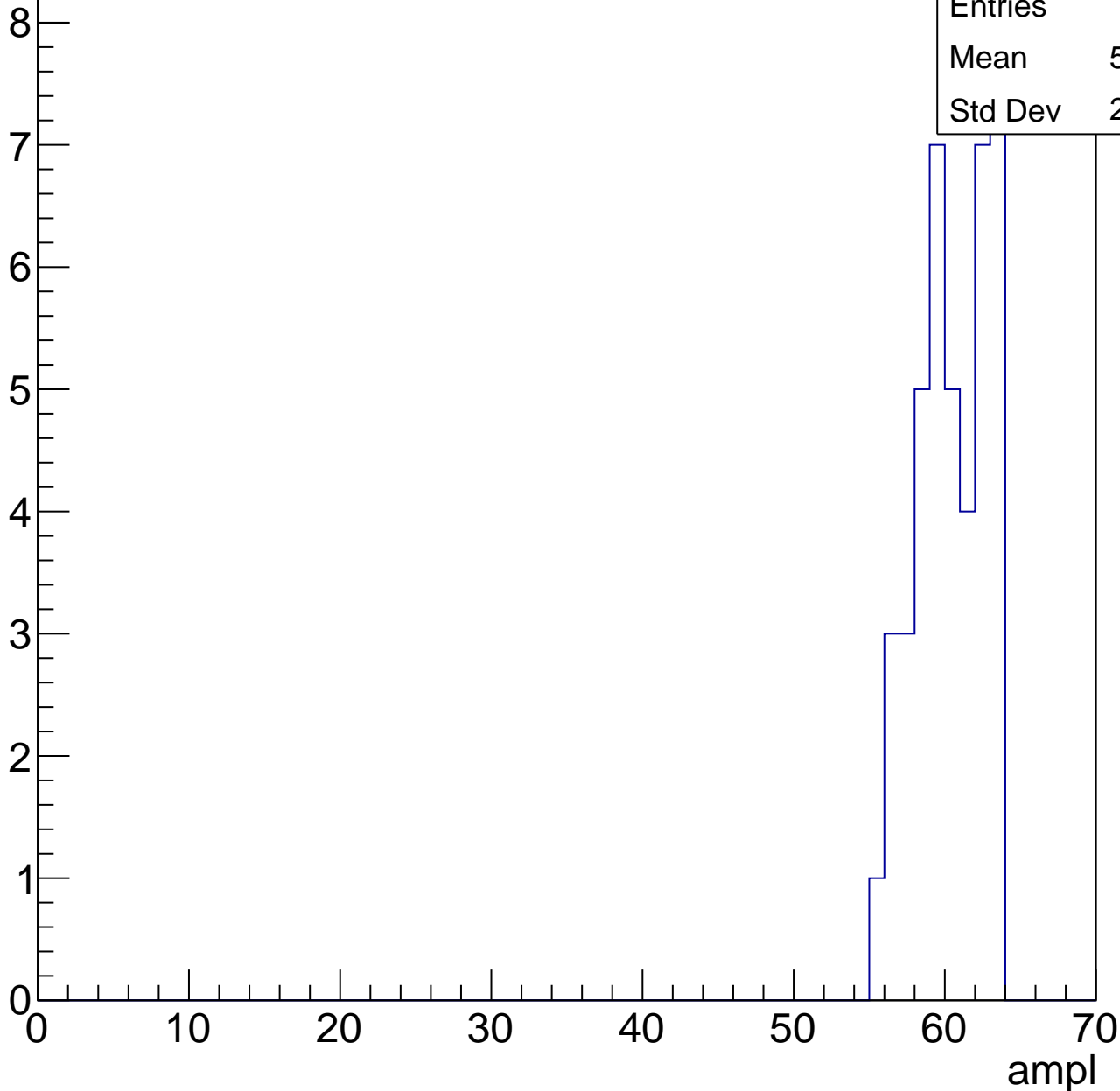


B1L102S, U10-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.98
Std Dev	2.318



B1L102S, U10-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch5, adc0

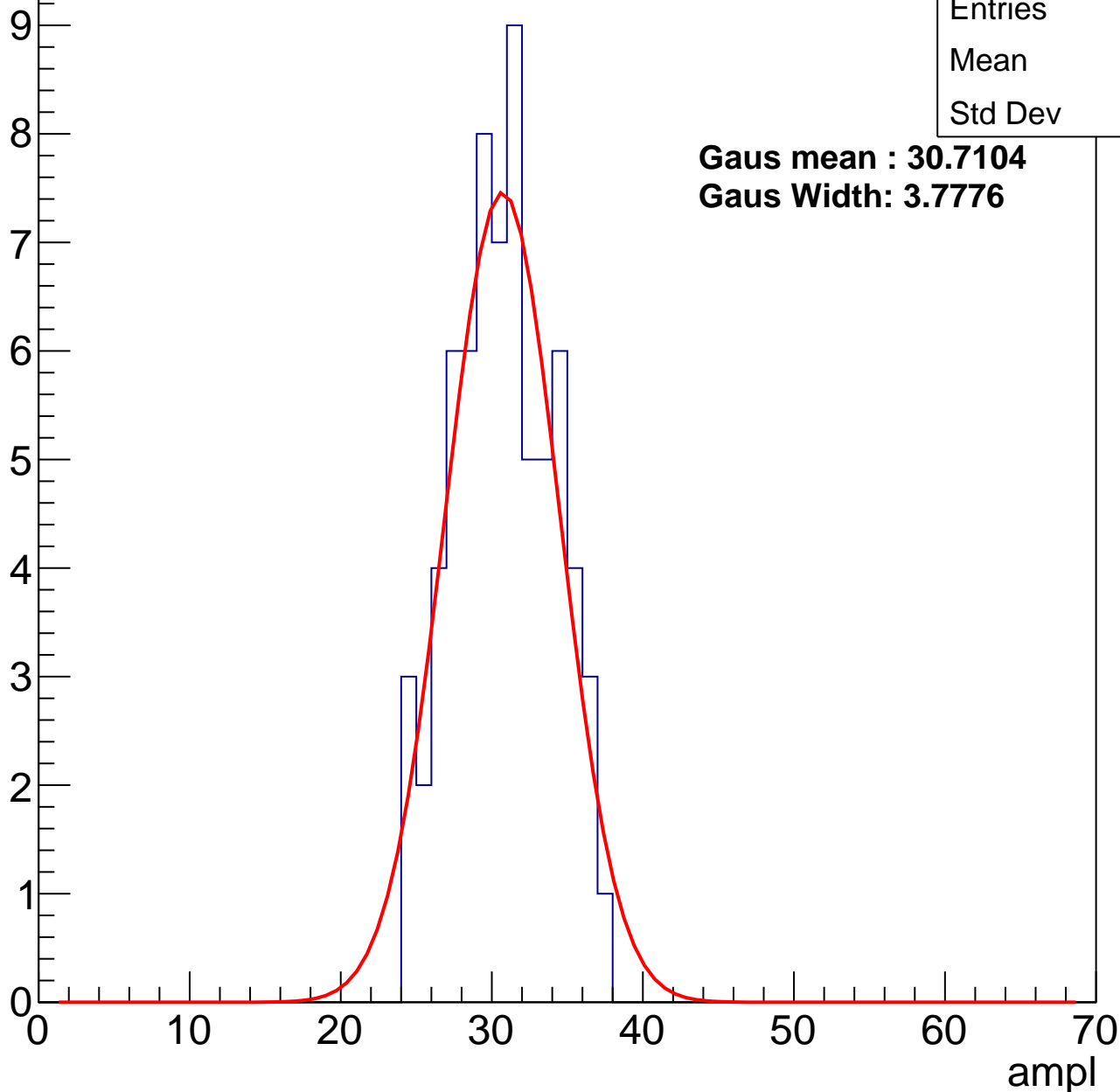
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.3
Std Dev	3.25

Gaus mean : 30.7104

Gaus Width: 3.7776



B1L102S, U10-ch5, adc1

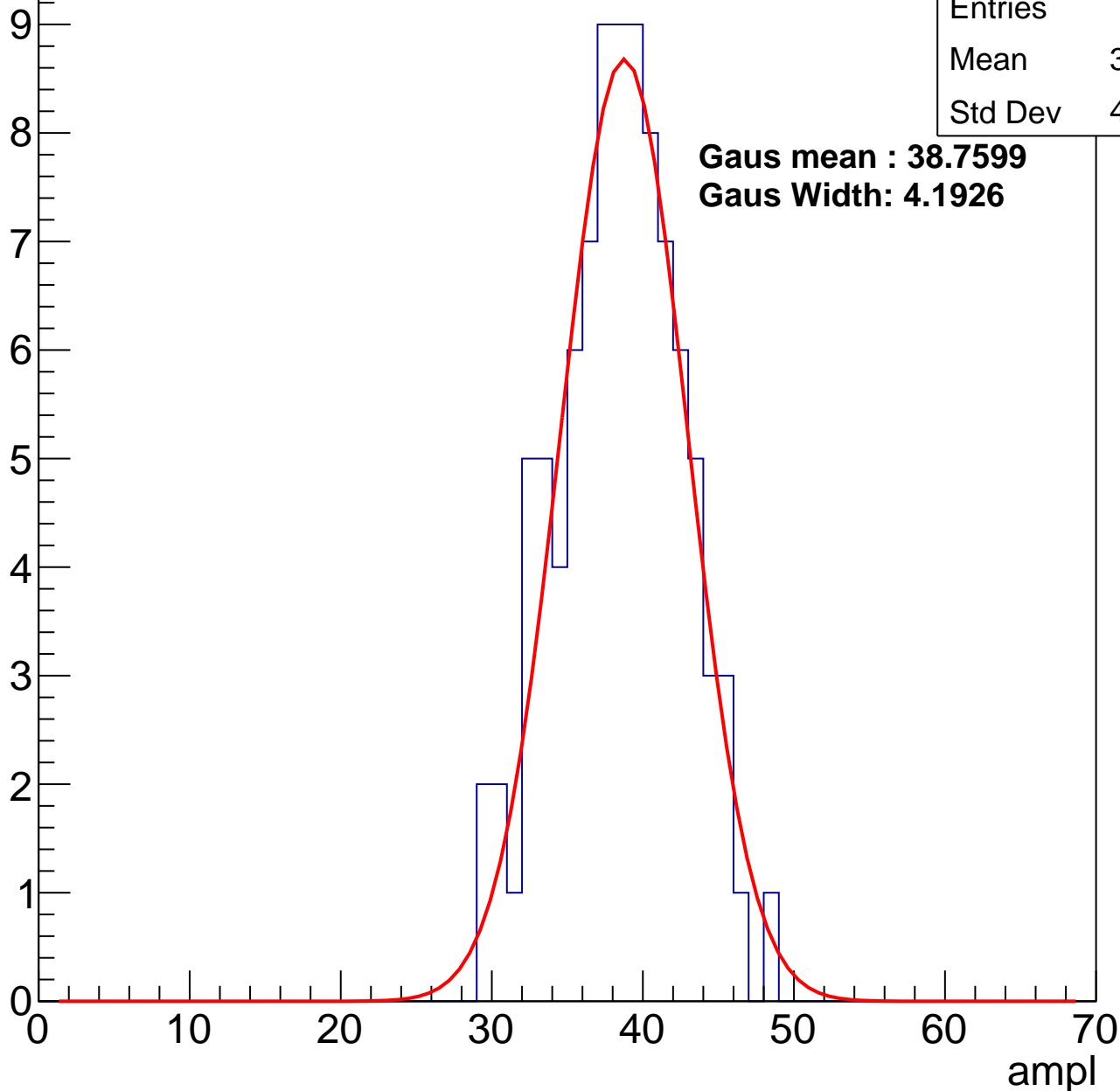
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	37.99
Std Dev	4.073

Gaus mean : 38.7599

Gaus Width: 4.1926



B1L102S, U10-ch5, adc2

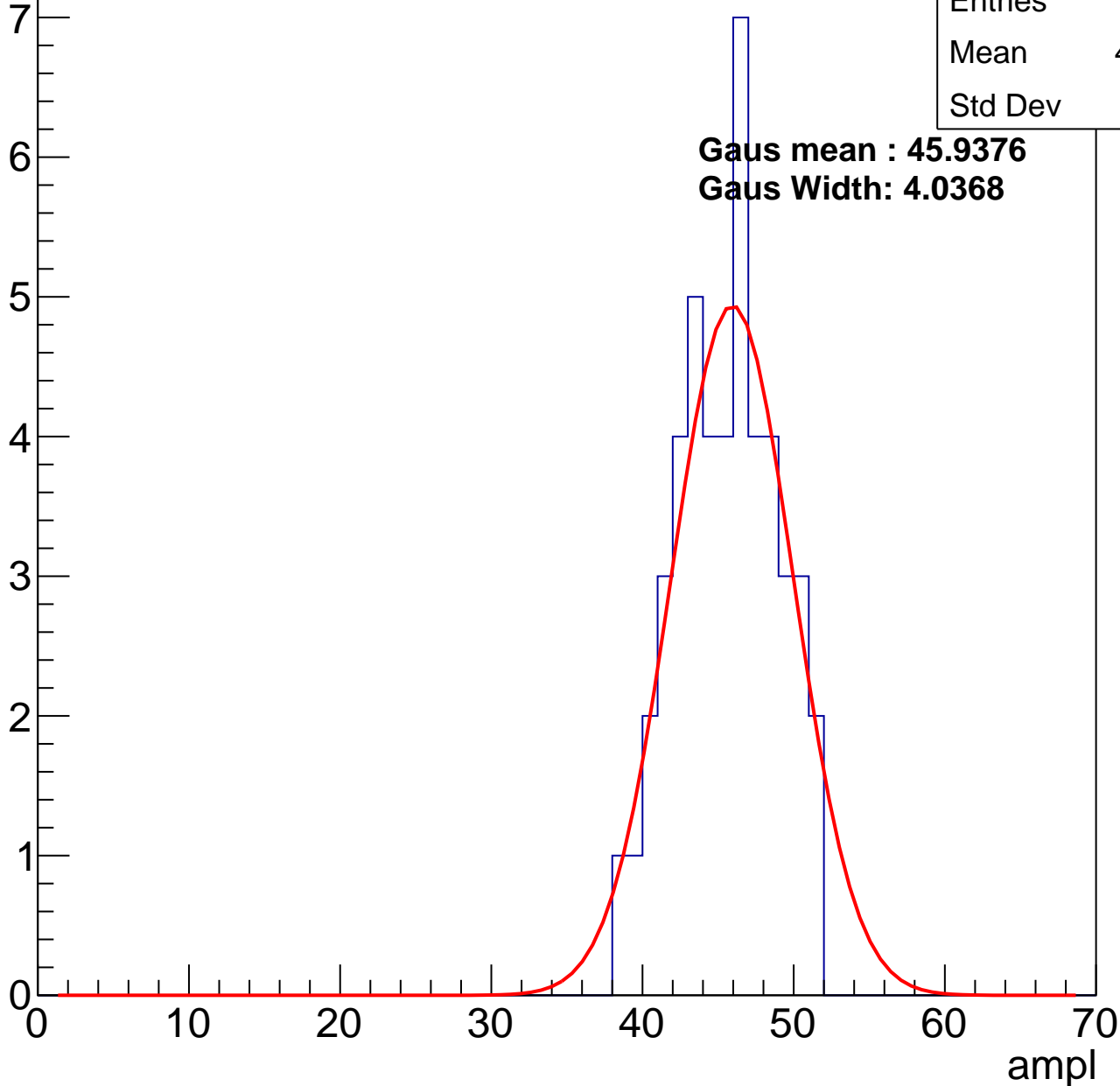
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	45.11
Std Dev	3.25

Gaus mean : 45.9376

Gaus Width: 4.0368

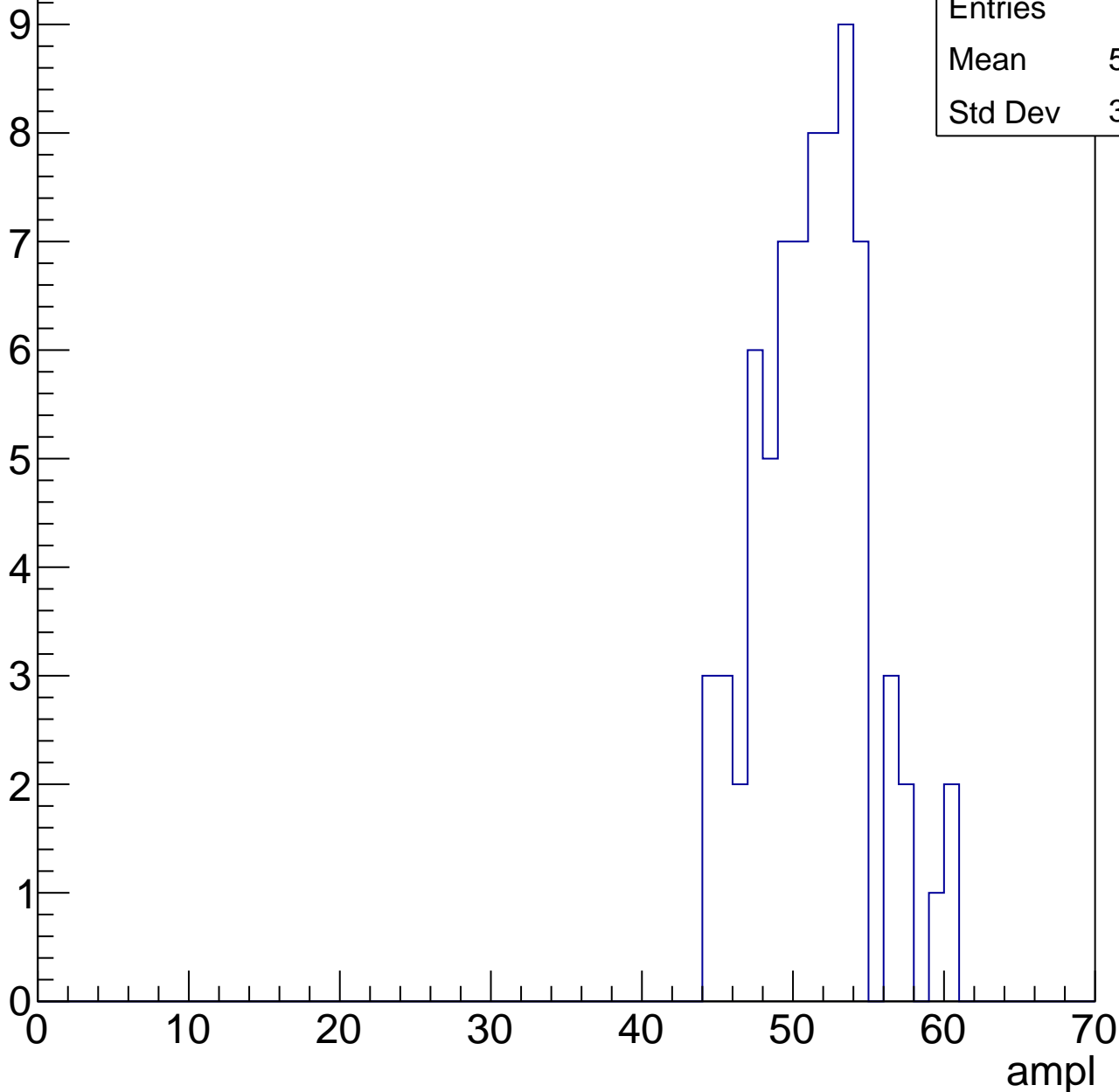


B1L102S, U10-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.88
Std Dev	3.626

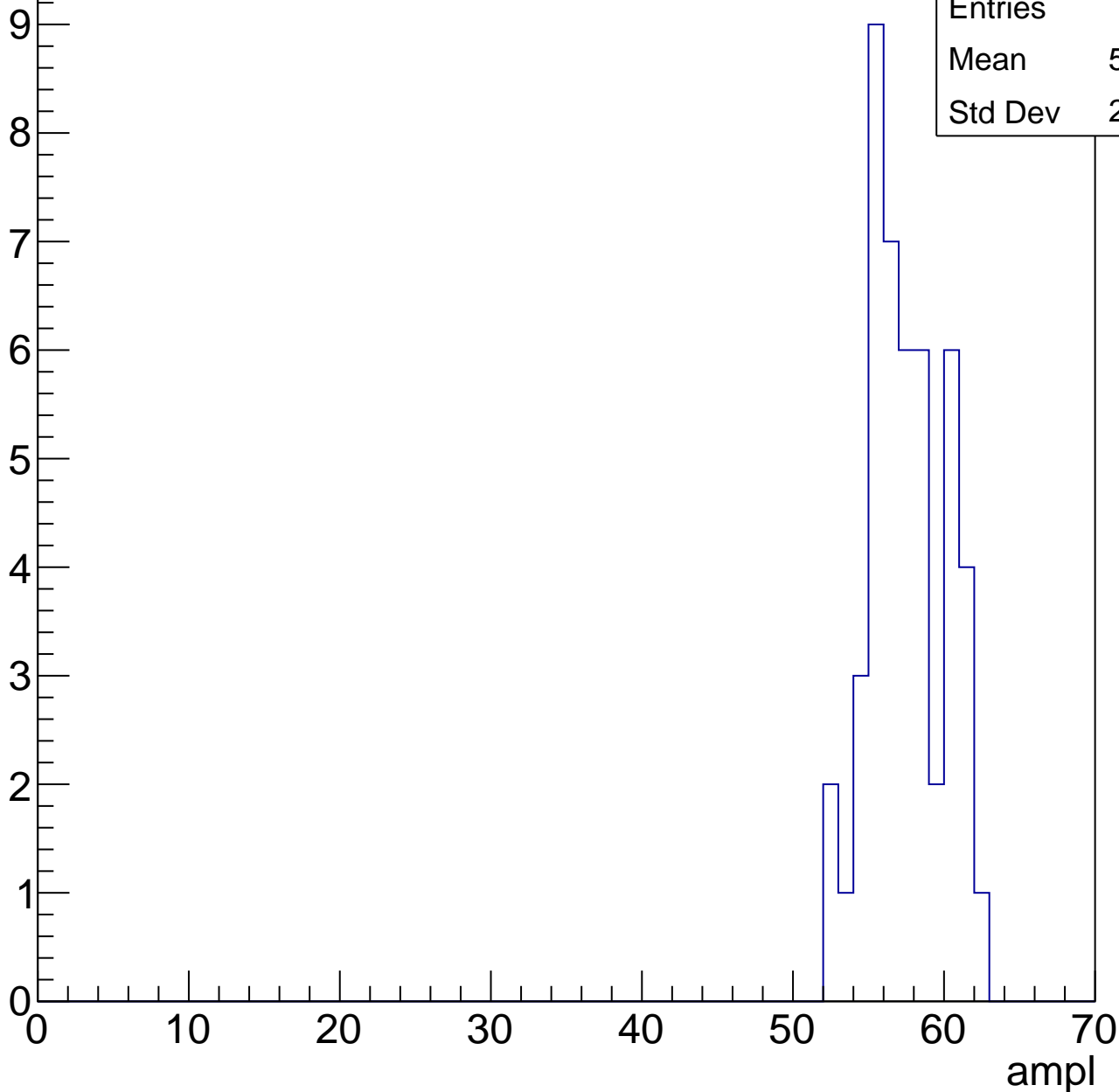


B1L102S, U10-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.02
Std Dev	2.497

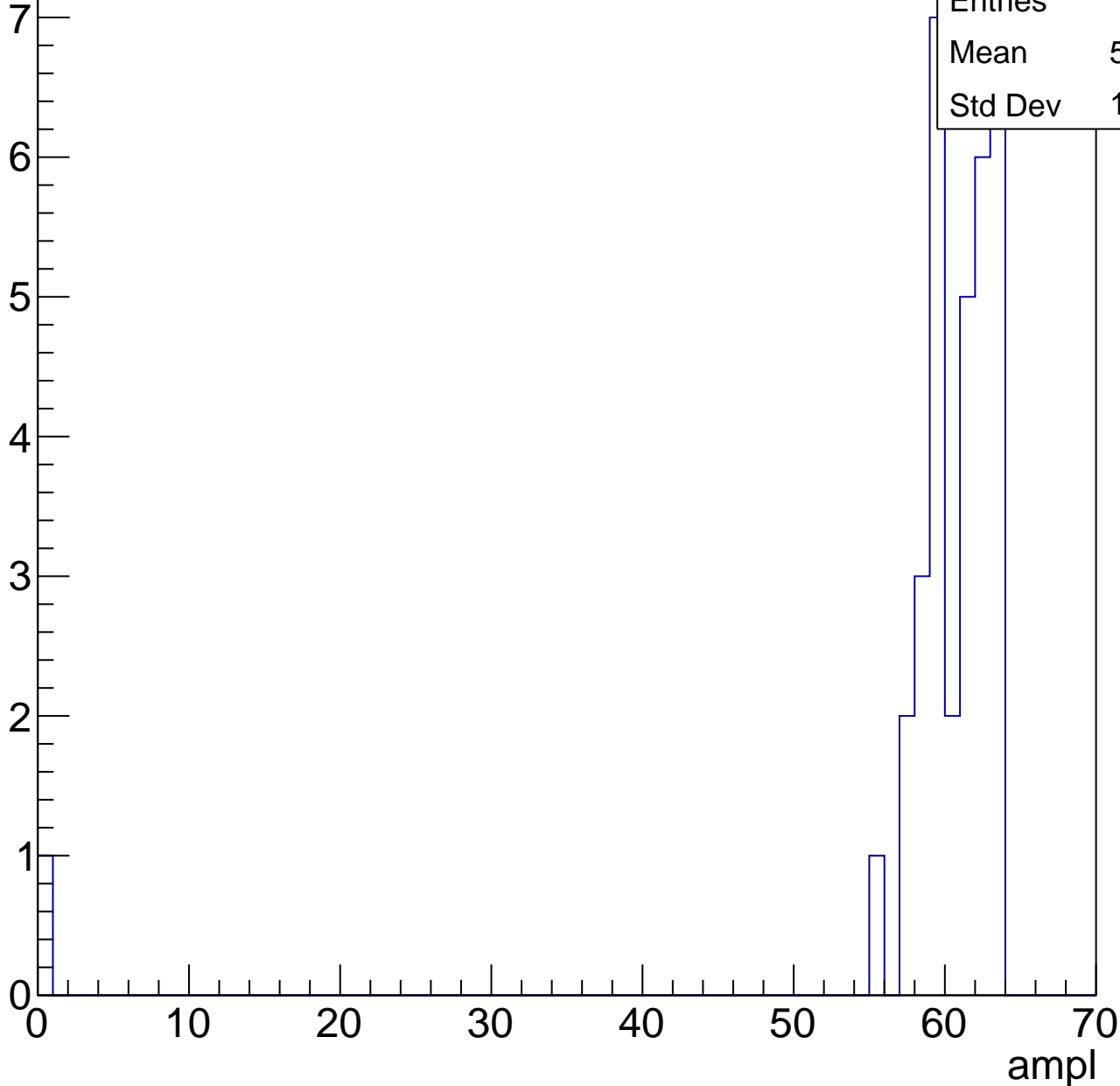


B1L102S, U10-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

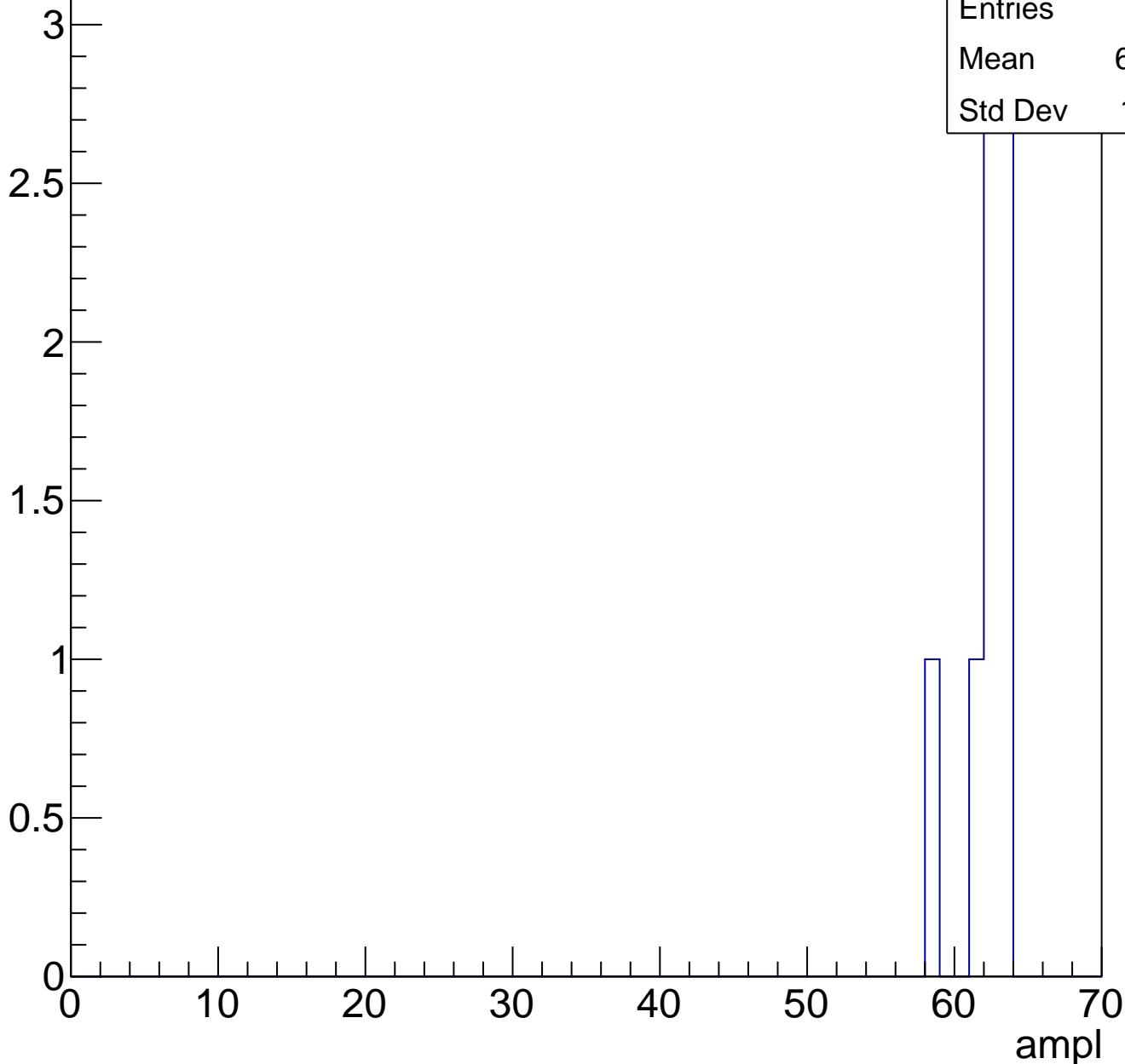
Entries	34
Mean	58.65
Std Dev	10.42



B1L102S, U10-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch6, adc0

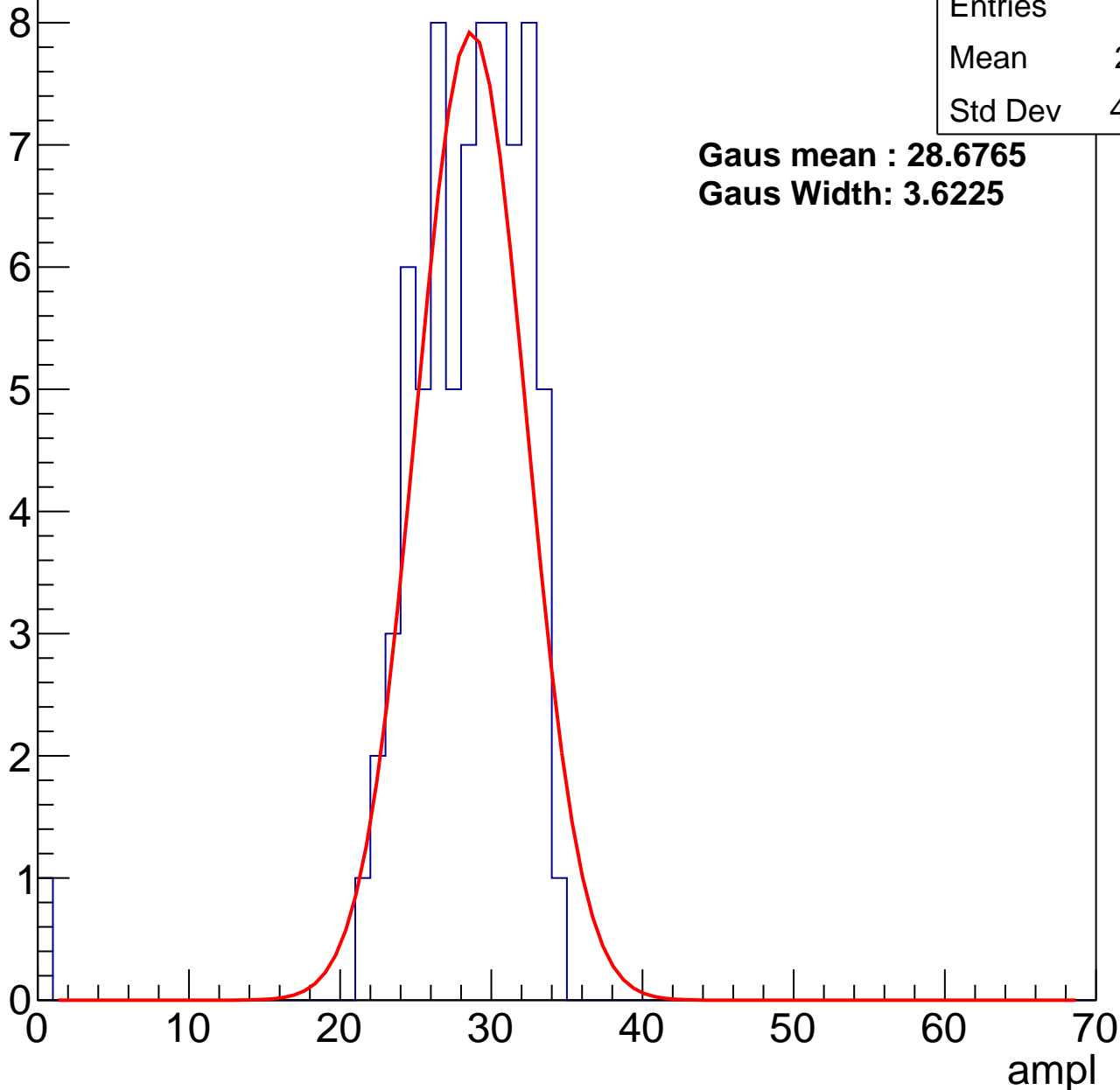
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.81
Std Dev	4.542

Gaus mean : 28.6765

Gaus Width: 3.6225



B1L102S, U10-ch6, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	36.3
Std Dev	3.352

Gaus mean : 36.6037

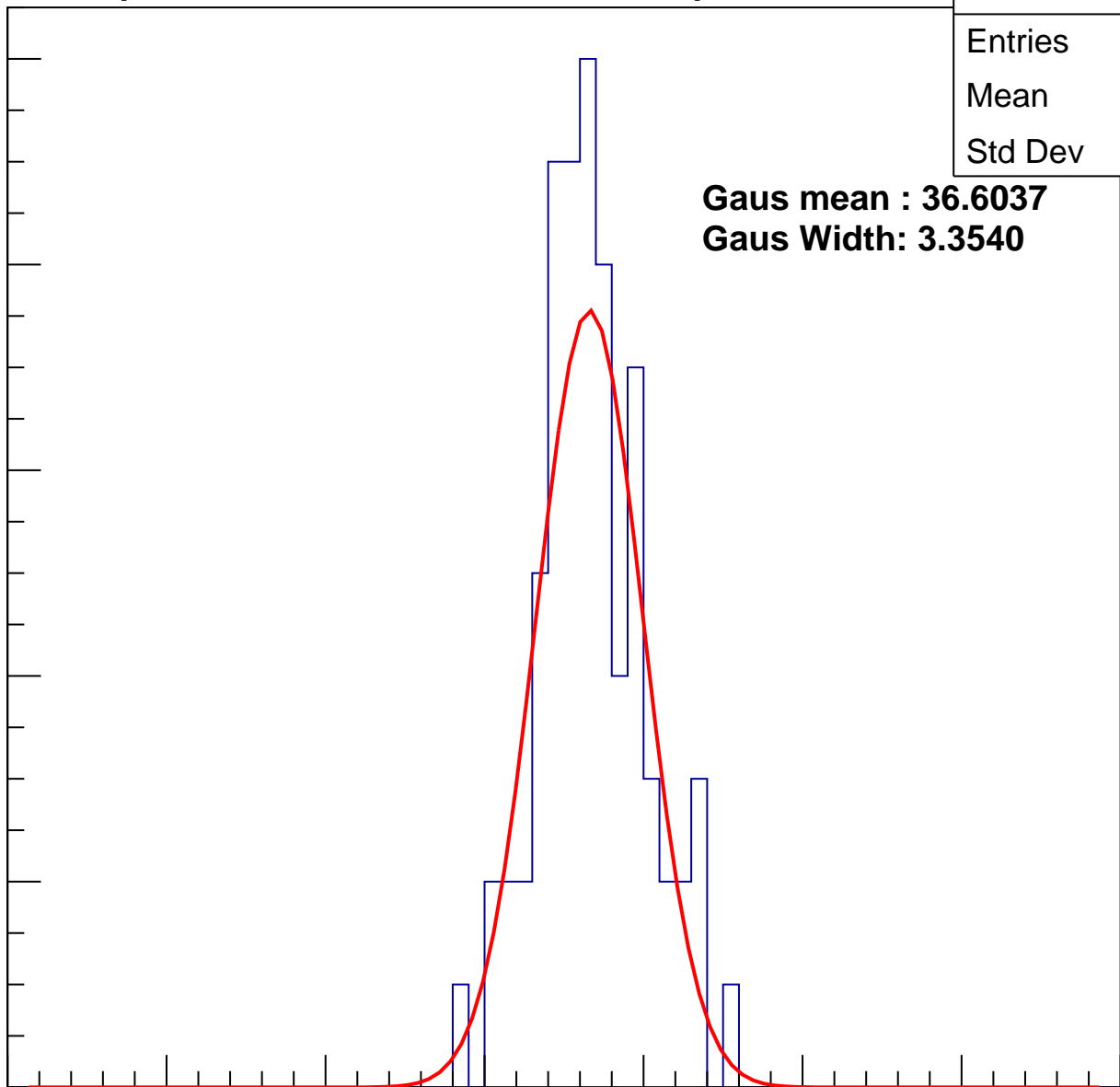
Gaus Width: 3.3540

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch6, adc2

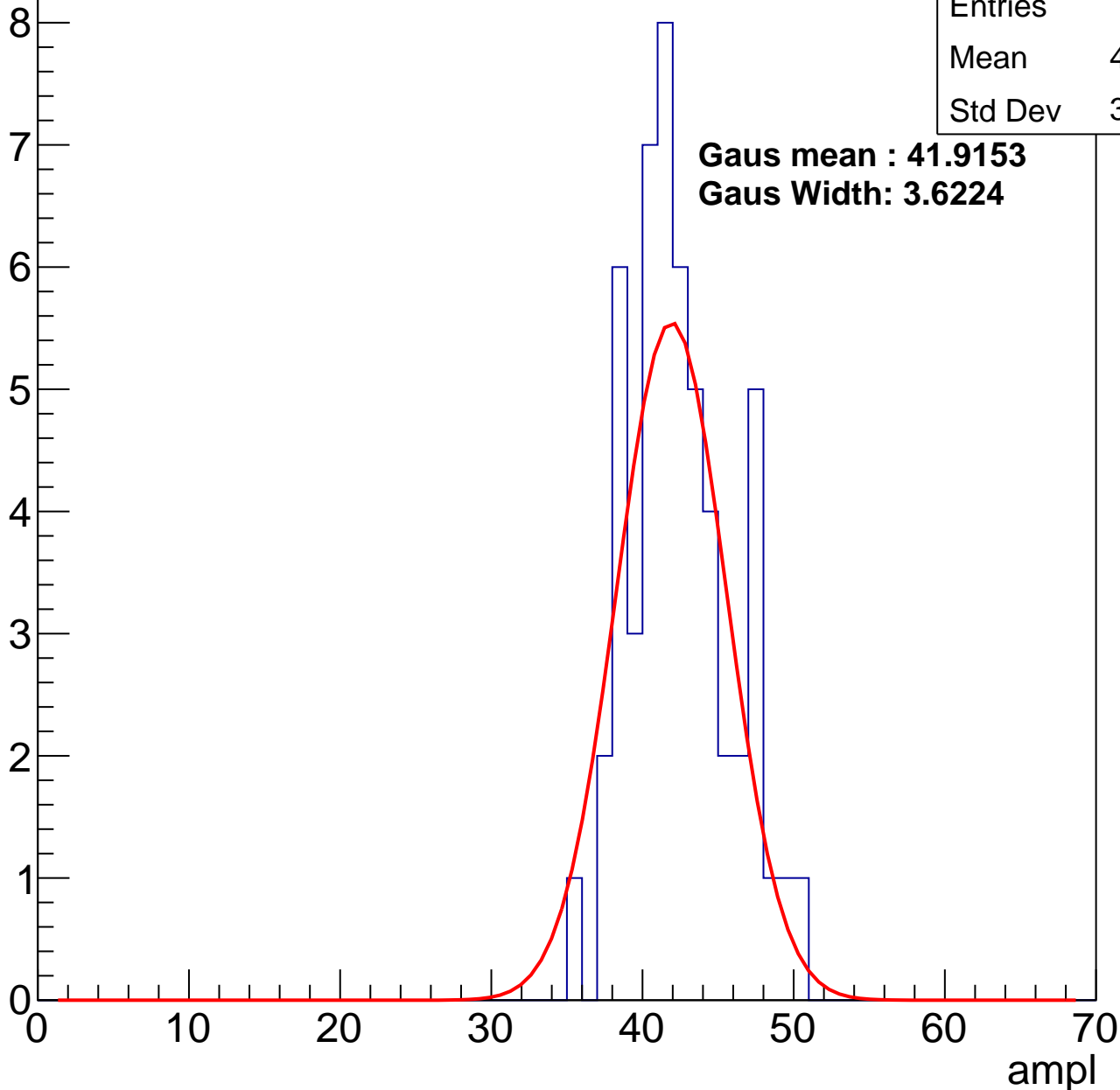
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.02
Std Dev	3.342

Gaus mean : 41.9153

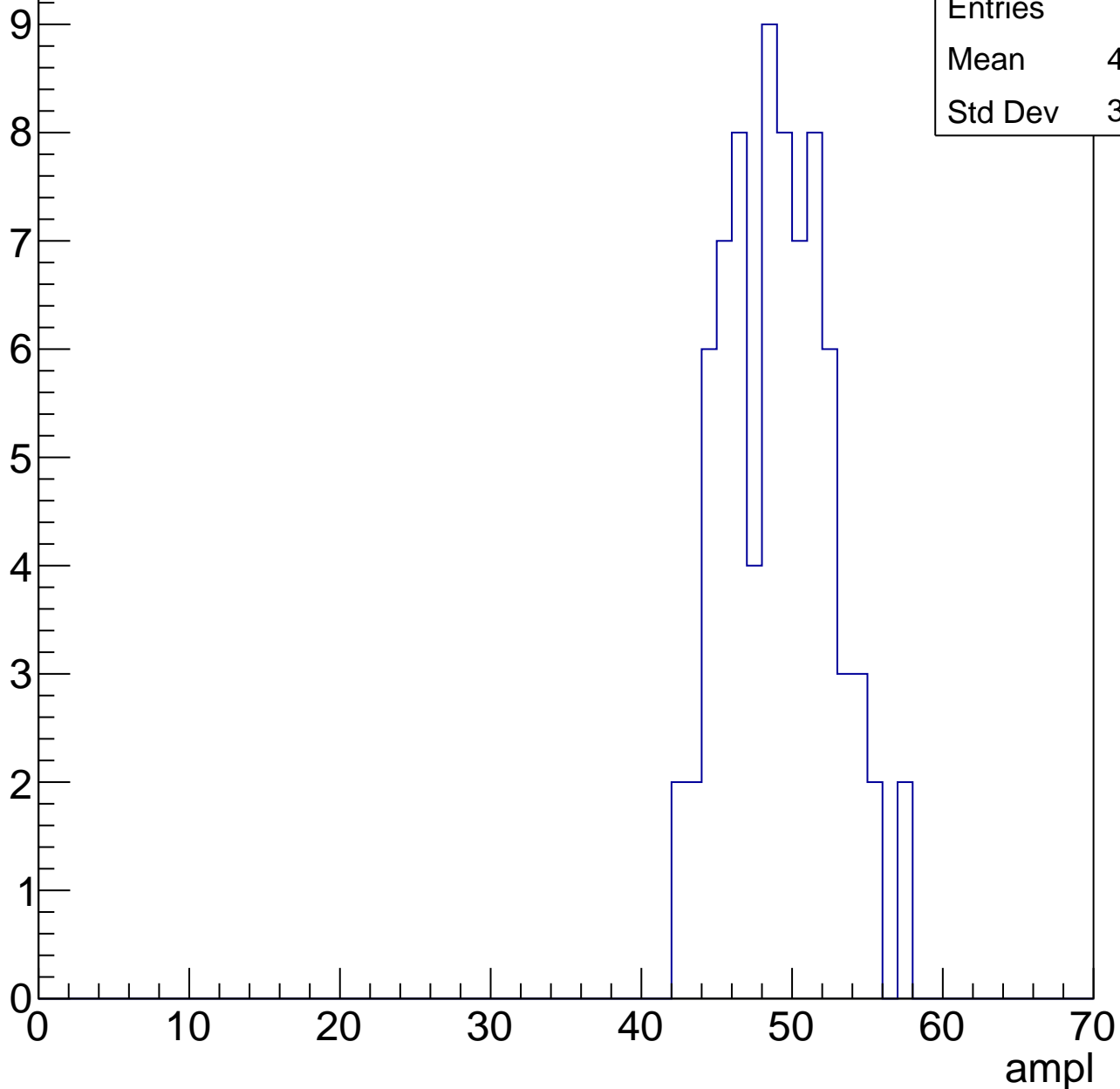
Gaus Width: 3.6224



B1L102S, U10-ch6, adc3

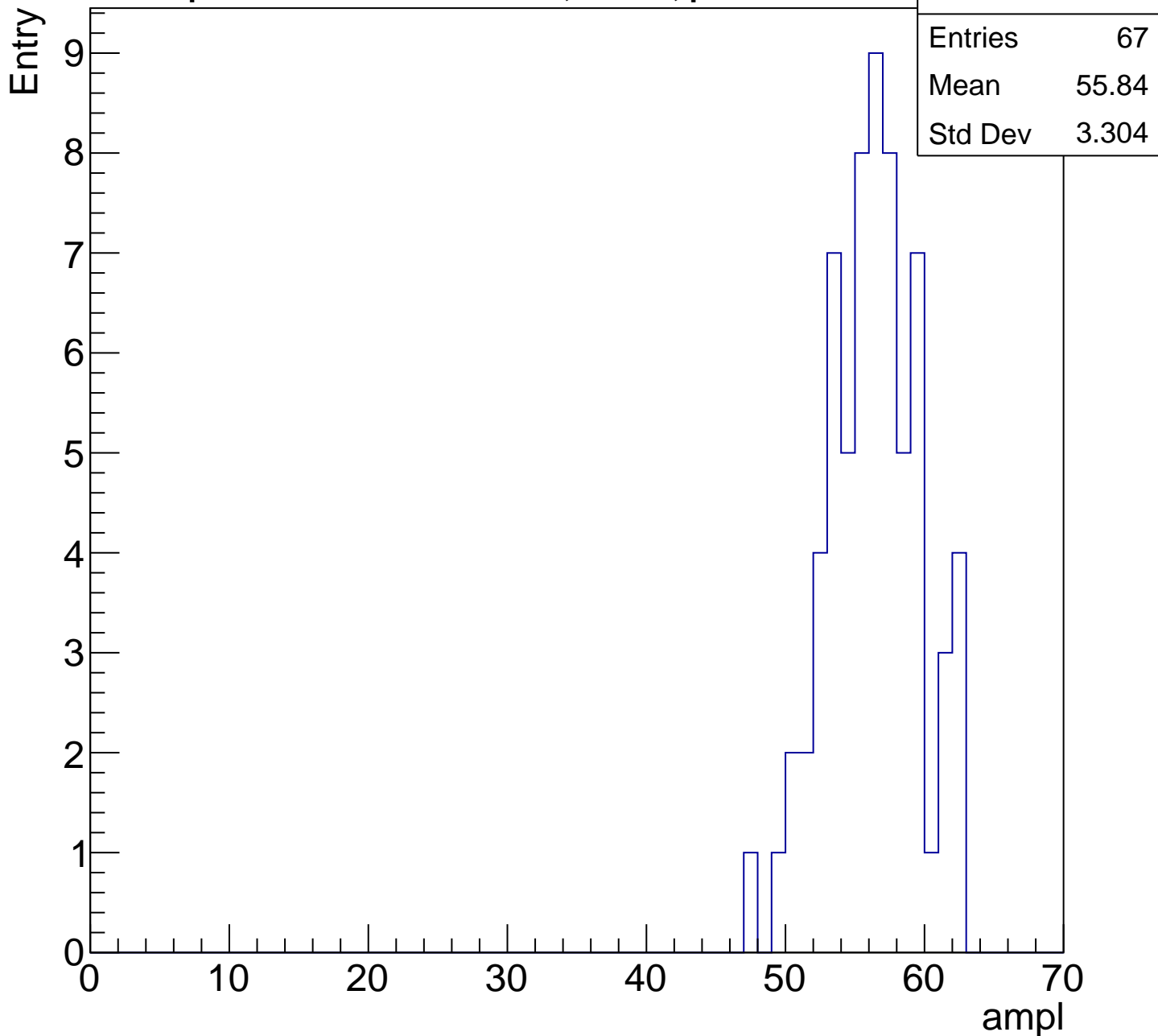
calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

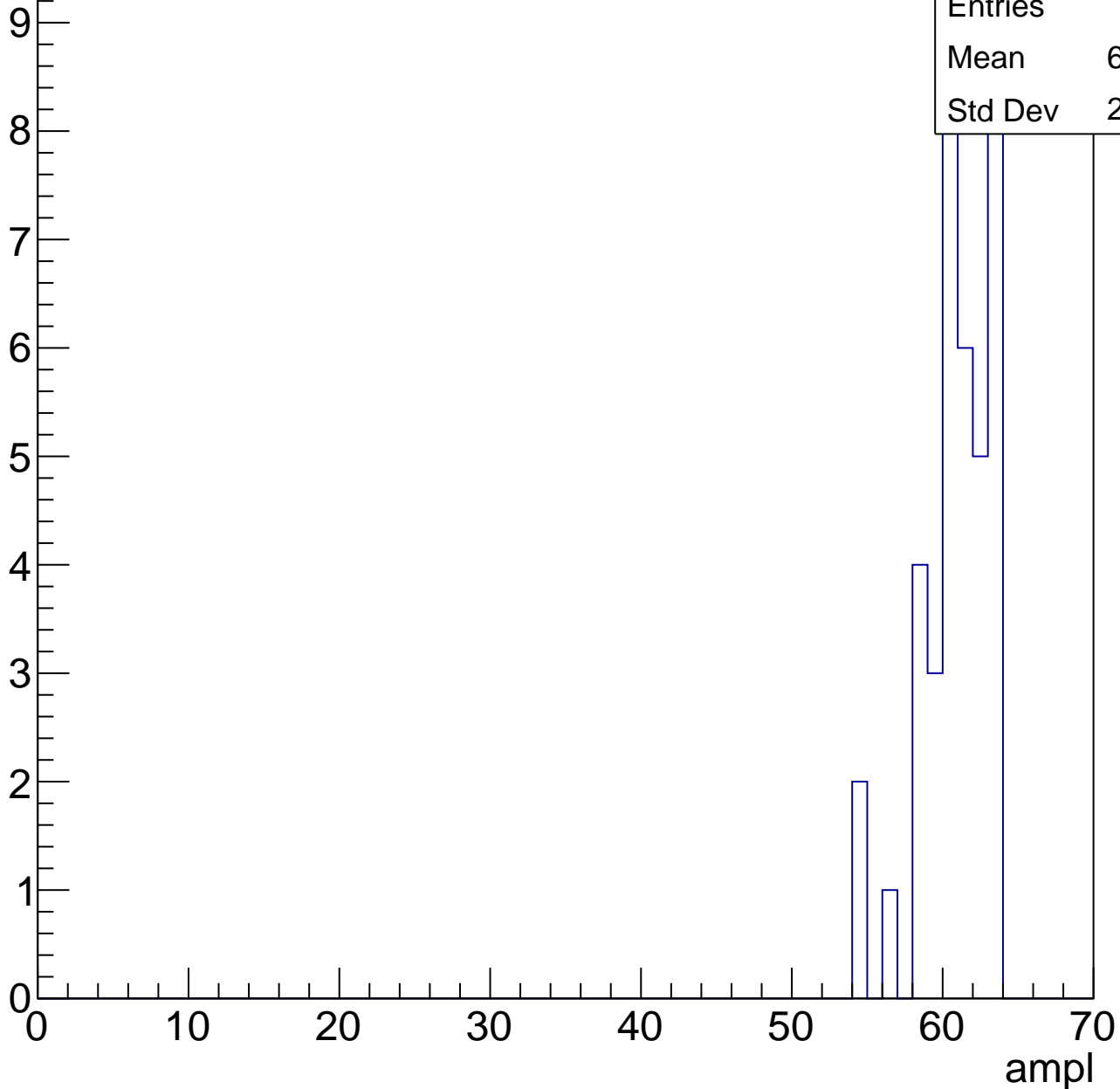


B1L102S, U10-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

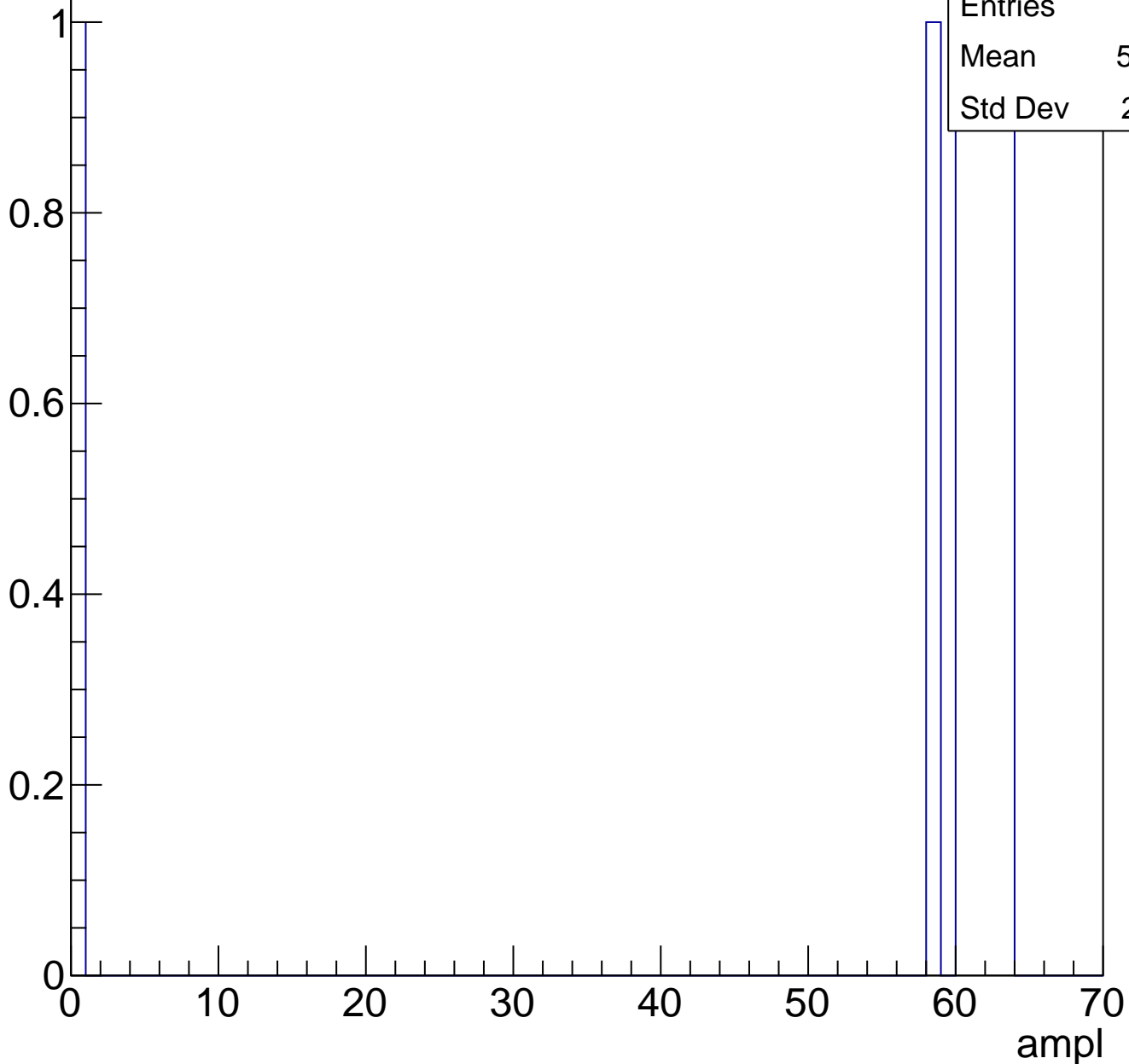
Entries	38
Mean	60.42
Std Dev	2.335



B1L102S, U10-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U10-ch7, adc0

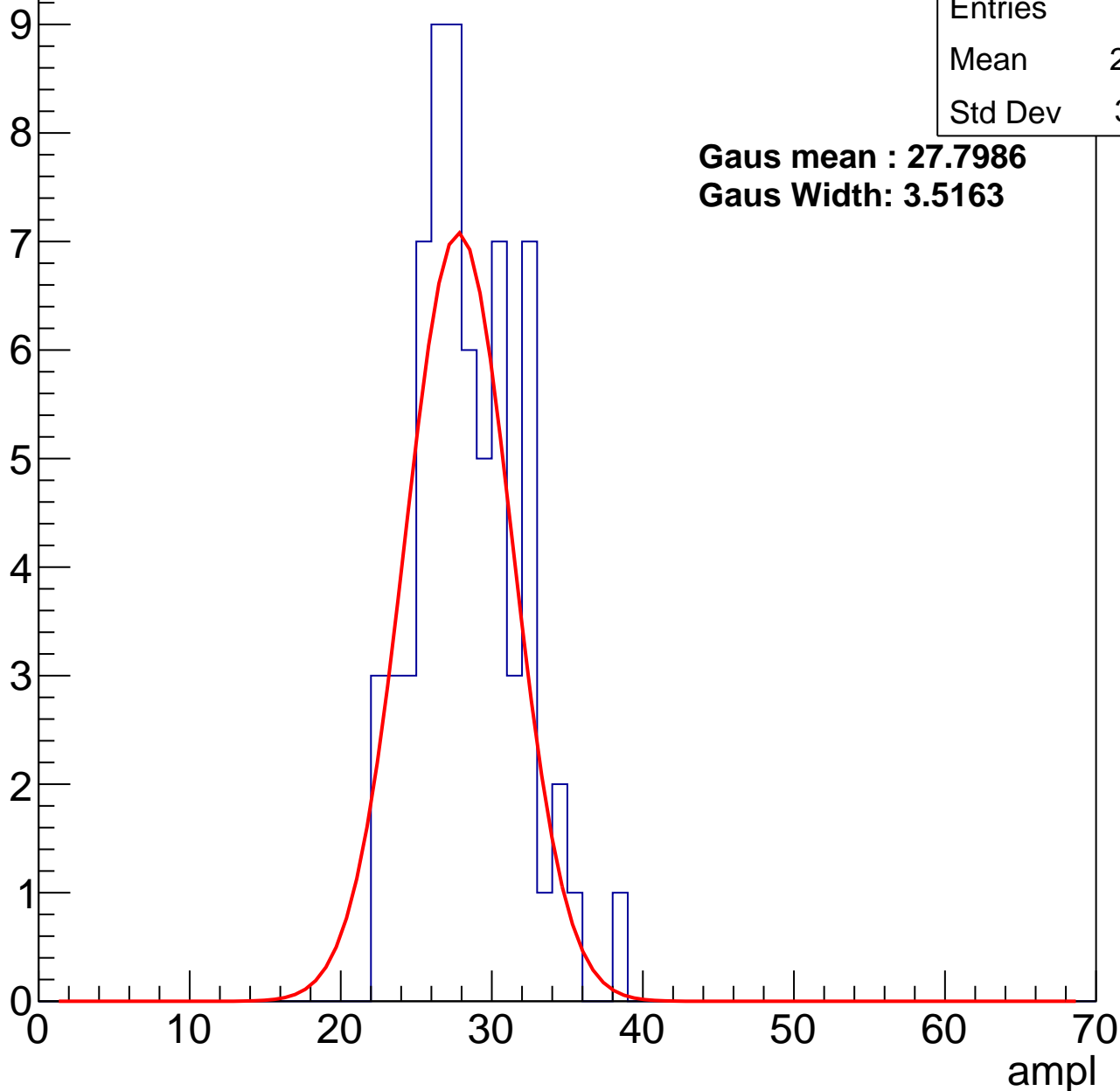
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.96
Std Dev	3.361

Gaus mean : 27.7986

Gaus Width: 3.5163



B1L102S, U10-ch7, adc1

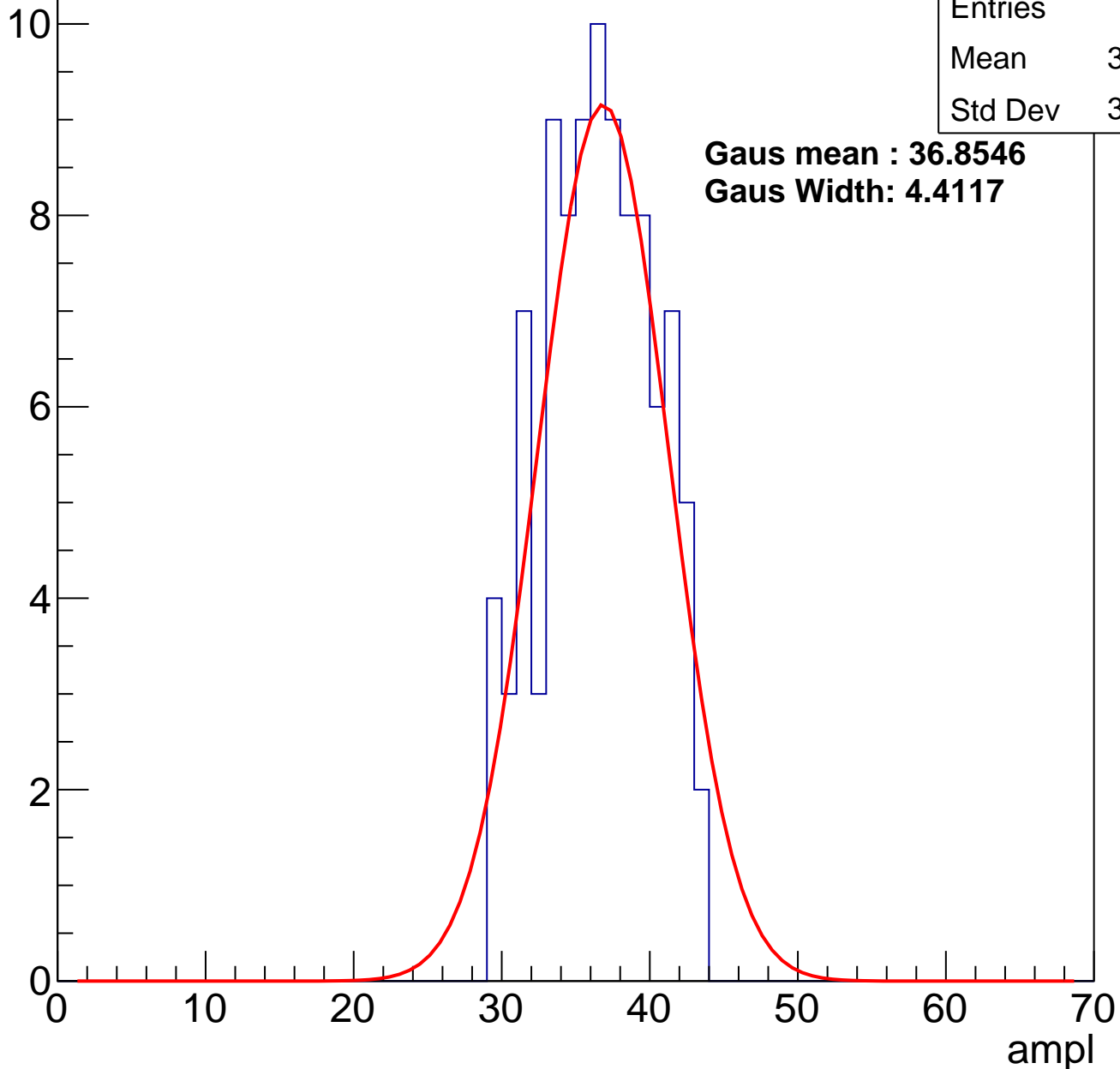
calib_packv5_042523_0143.root, FC#11, port A2

Entries	98
Mean	36.07
Std Dev	3.657

Gaus mean : 36.8546

Gaus Width: 4.4117

Entry



B1L102S, U10-ch7, adc2

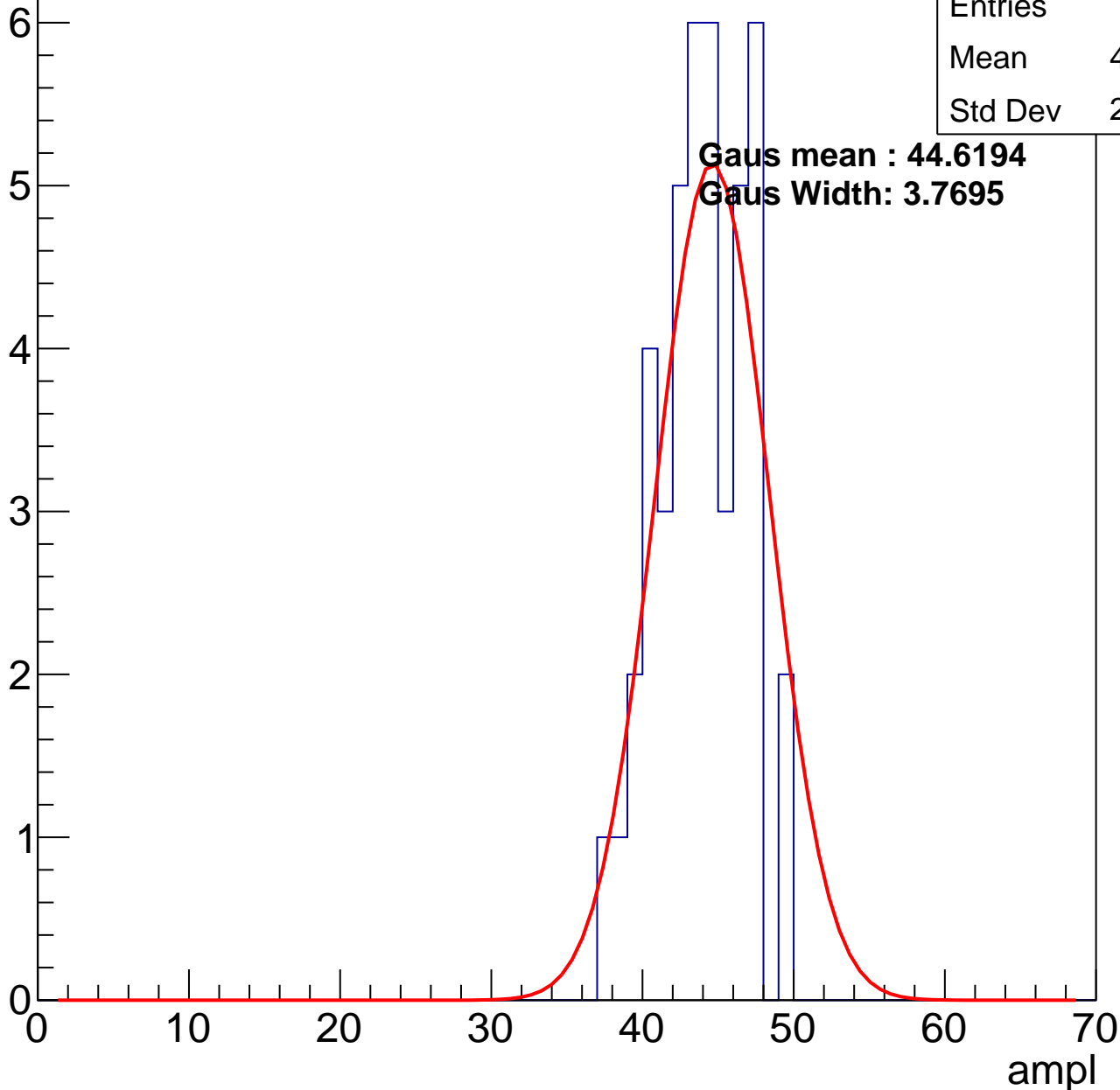
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	43.48
Std Dev	2.888

Gaus mean : 44.6194

Gaus Width: 3.7695



B1L102S, U10-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	49.98
Std Dev	3.766

Entry

10

8

6

4

2

0

0

10

20

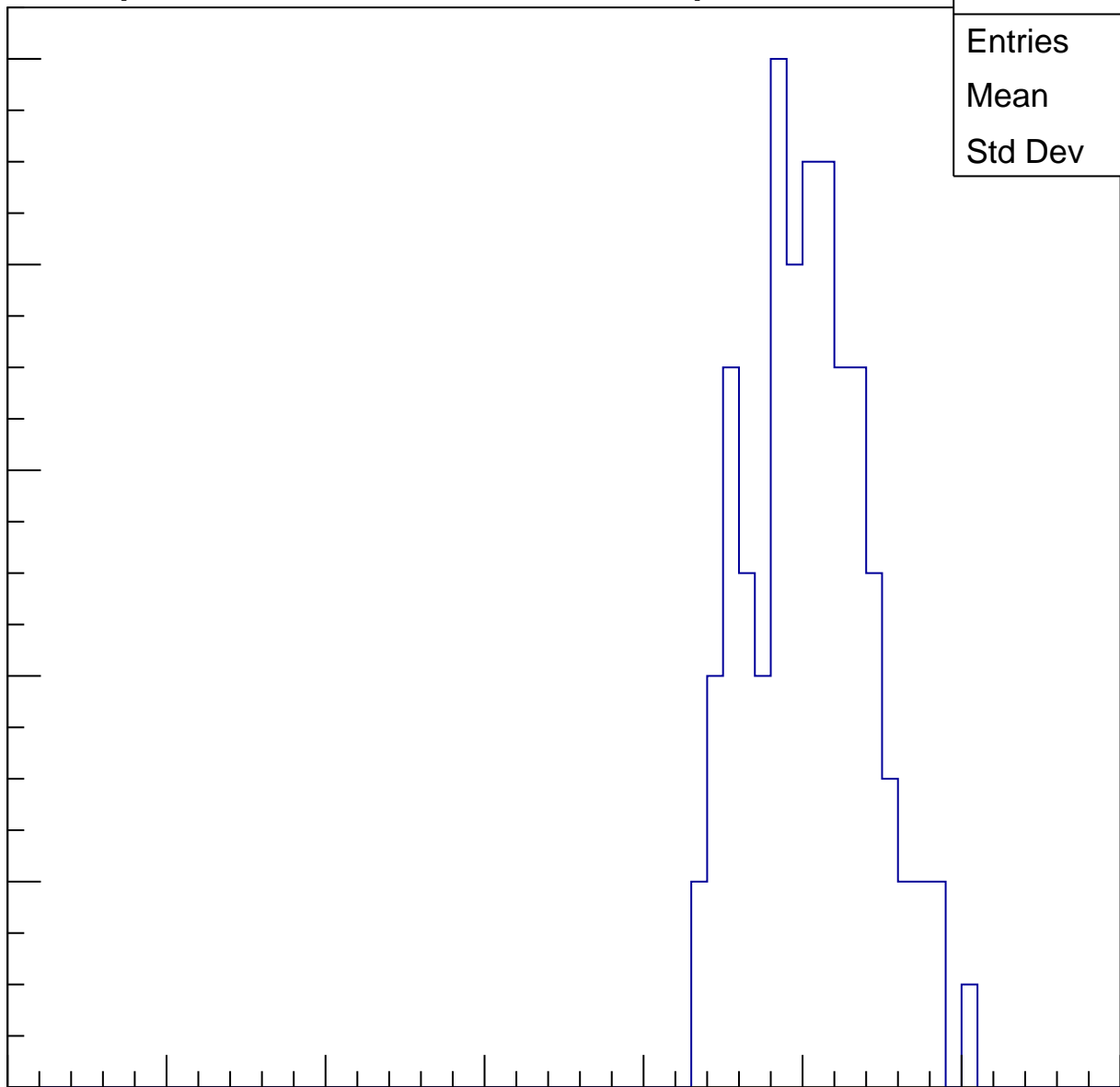
30

40

50

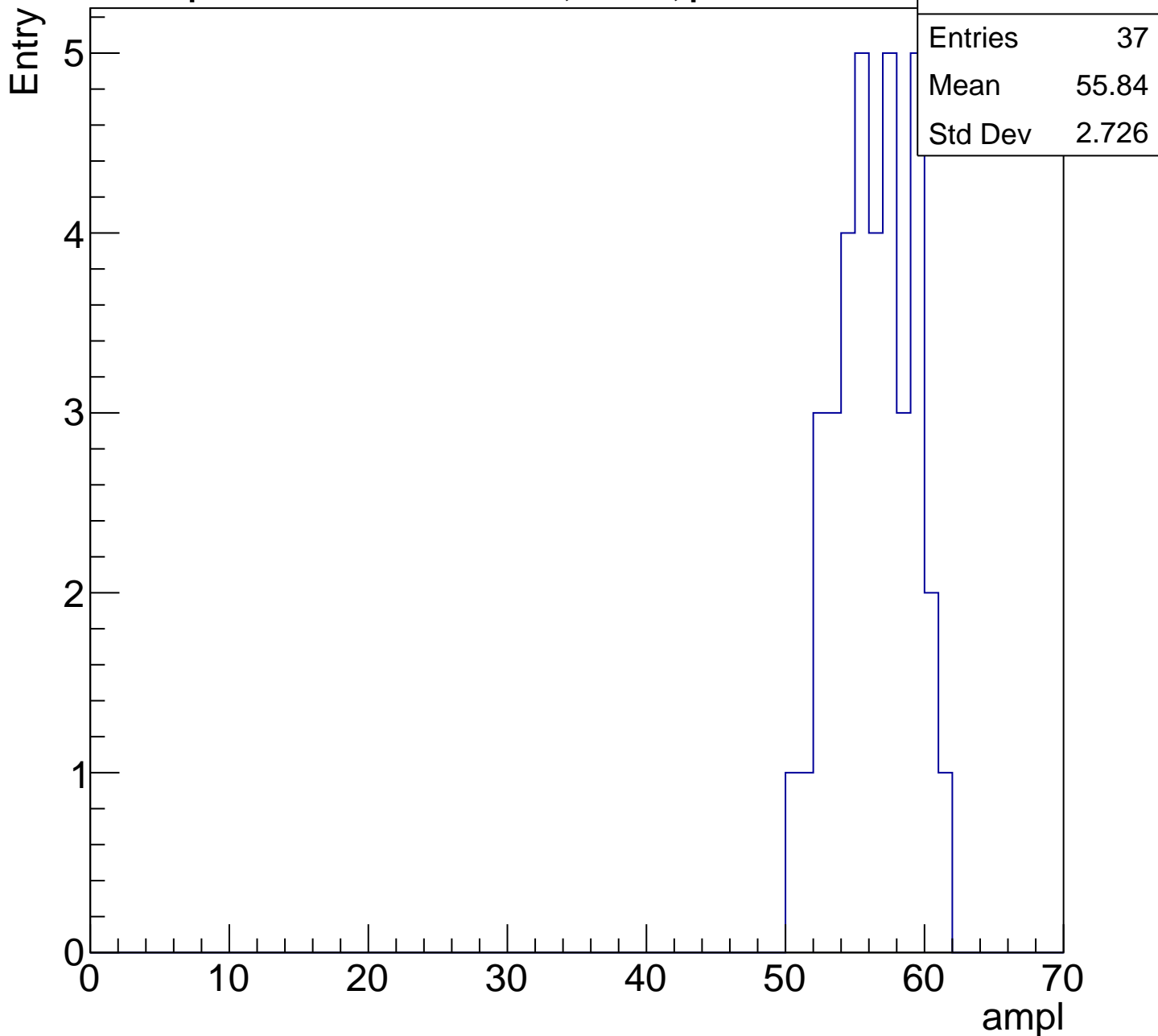
60

ampl



B1L102S, U10-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U10-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.8
Std Dev	8.434

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L102S, U10-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	16
Std Dev	0

ampl

B1L102S, U10-ch8, adc0

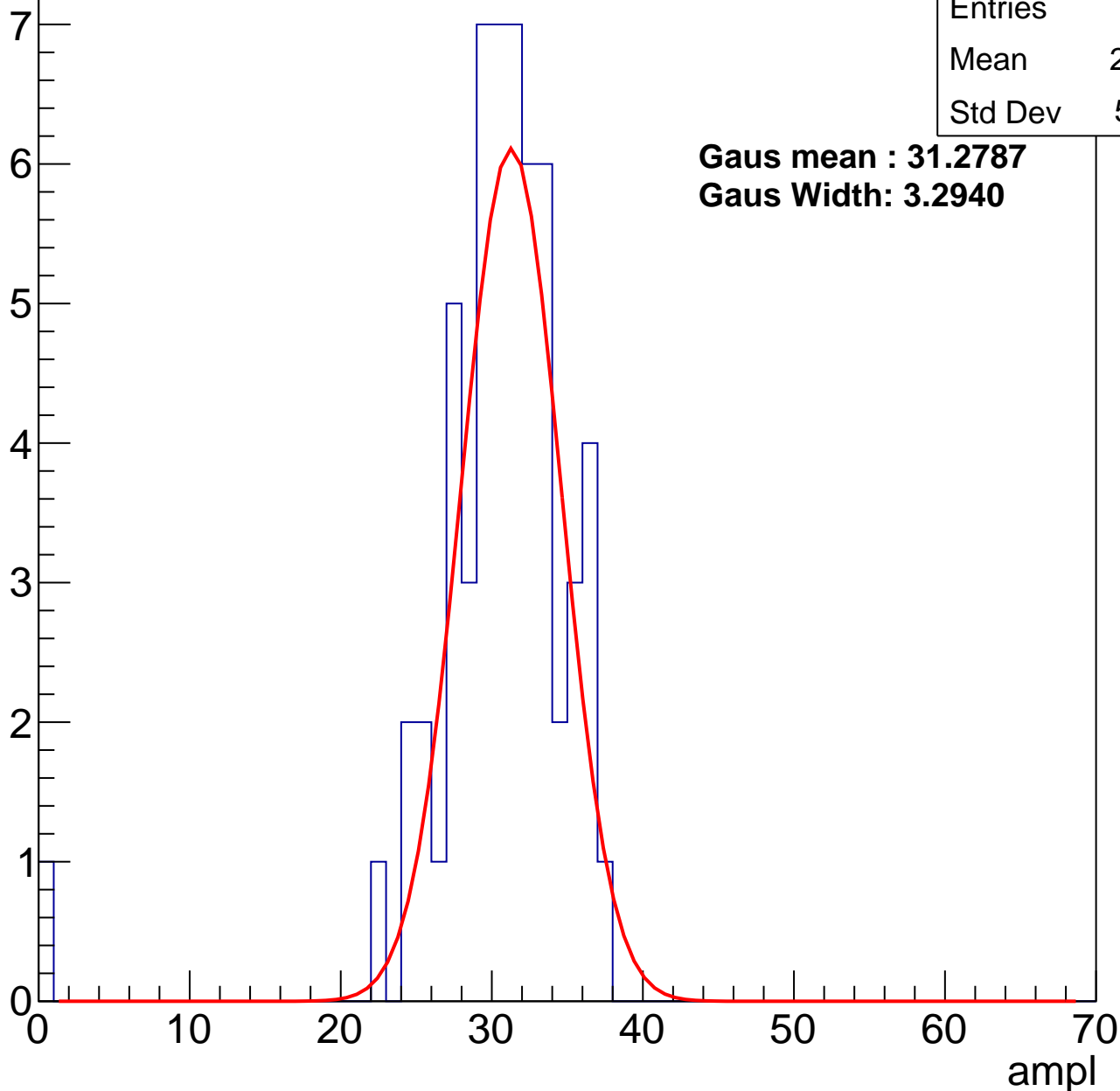
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.98
Std Dev	5.171

Gaus mean : 31.2787

Gaus Width: 3.2940



B1L102S, U10-ch8, adc1

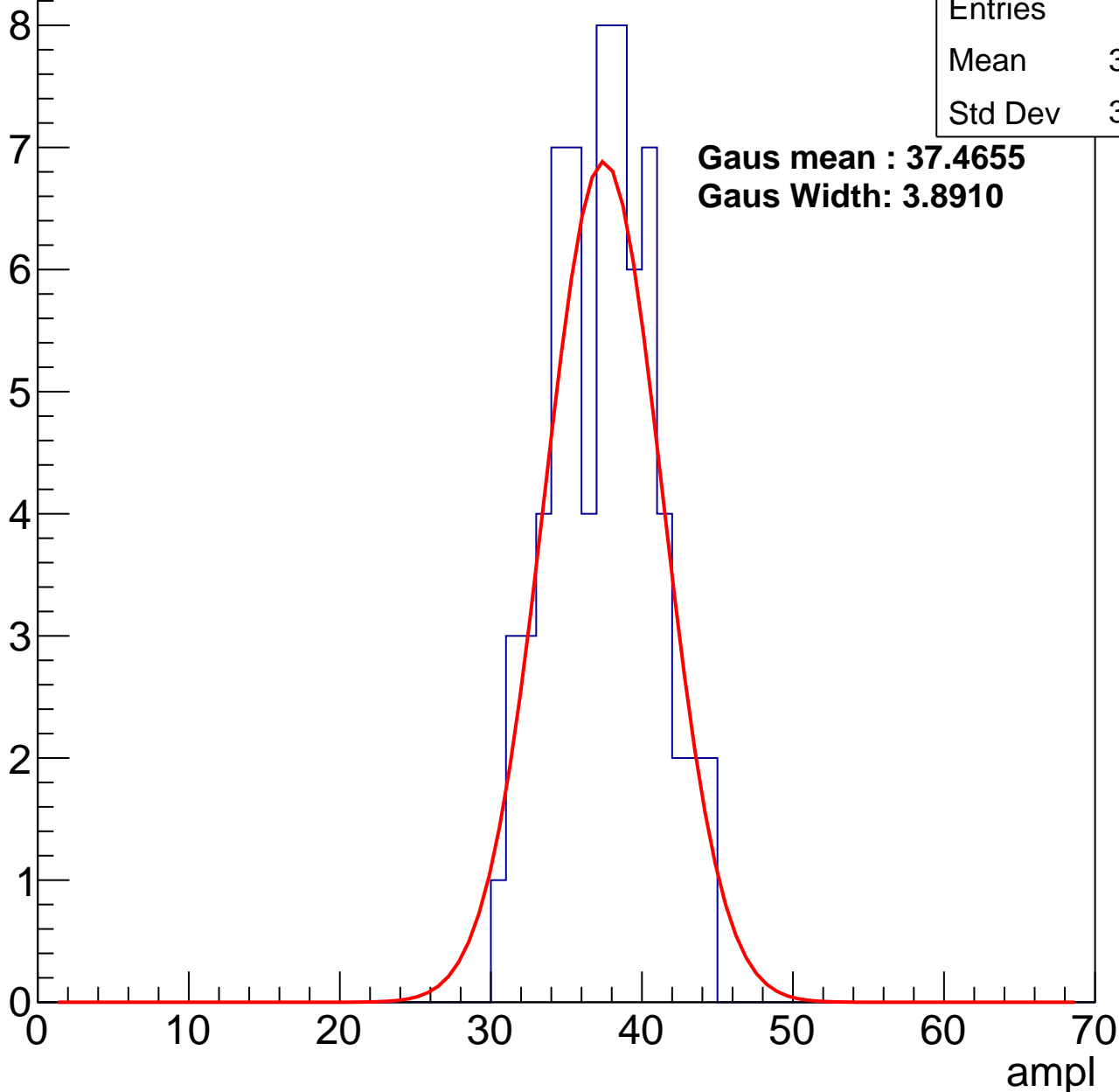
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.97
Std Dev	3.365

Gaus mean : 37.4655

Gaus Width: 3.8910



B1L102S, U10-ch8, adc2

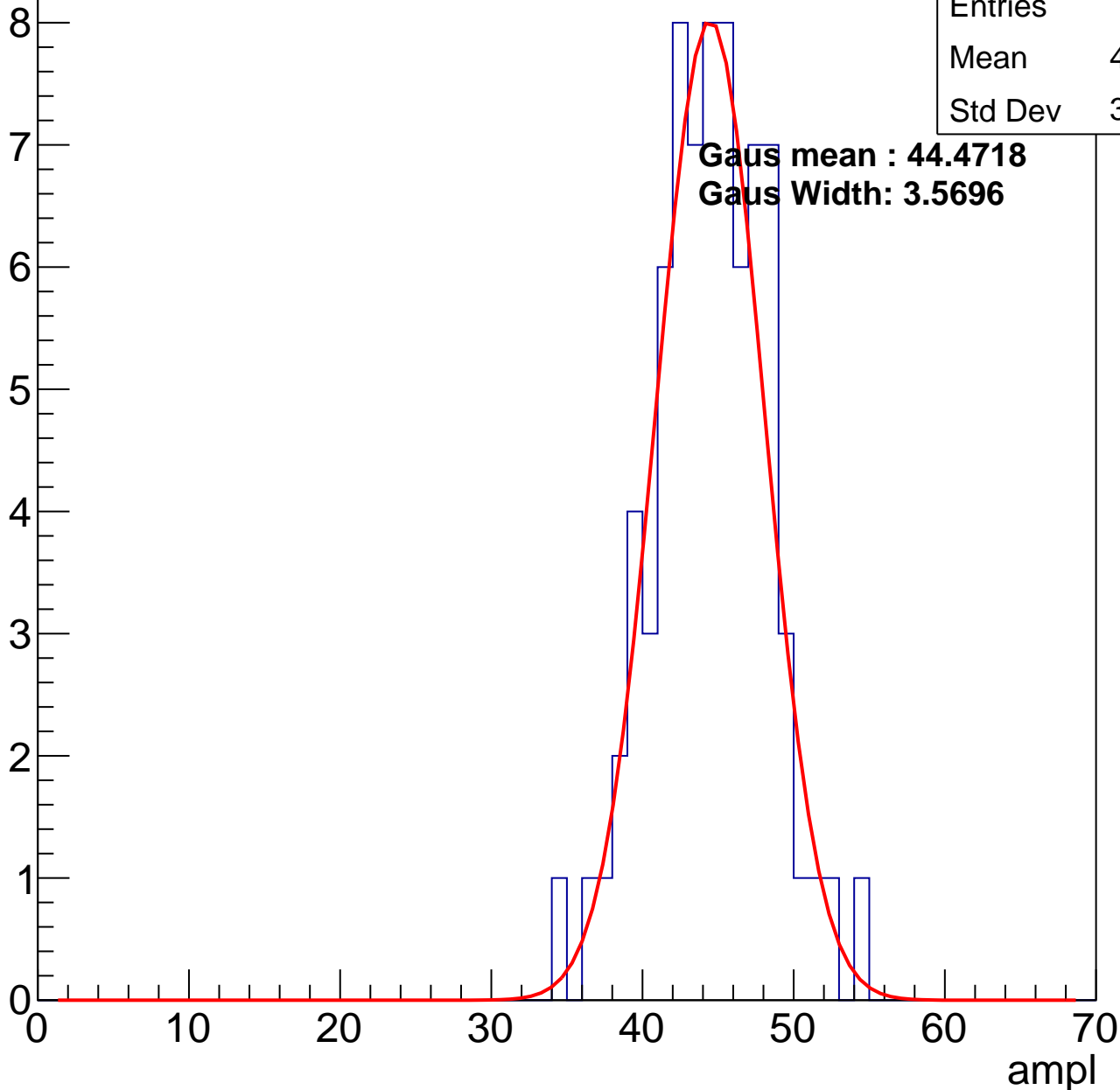
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	44.07
Std Dev	3.729

Gaus mean : 44.4718

Gaus Width: 3.5696

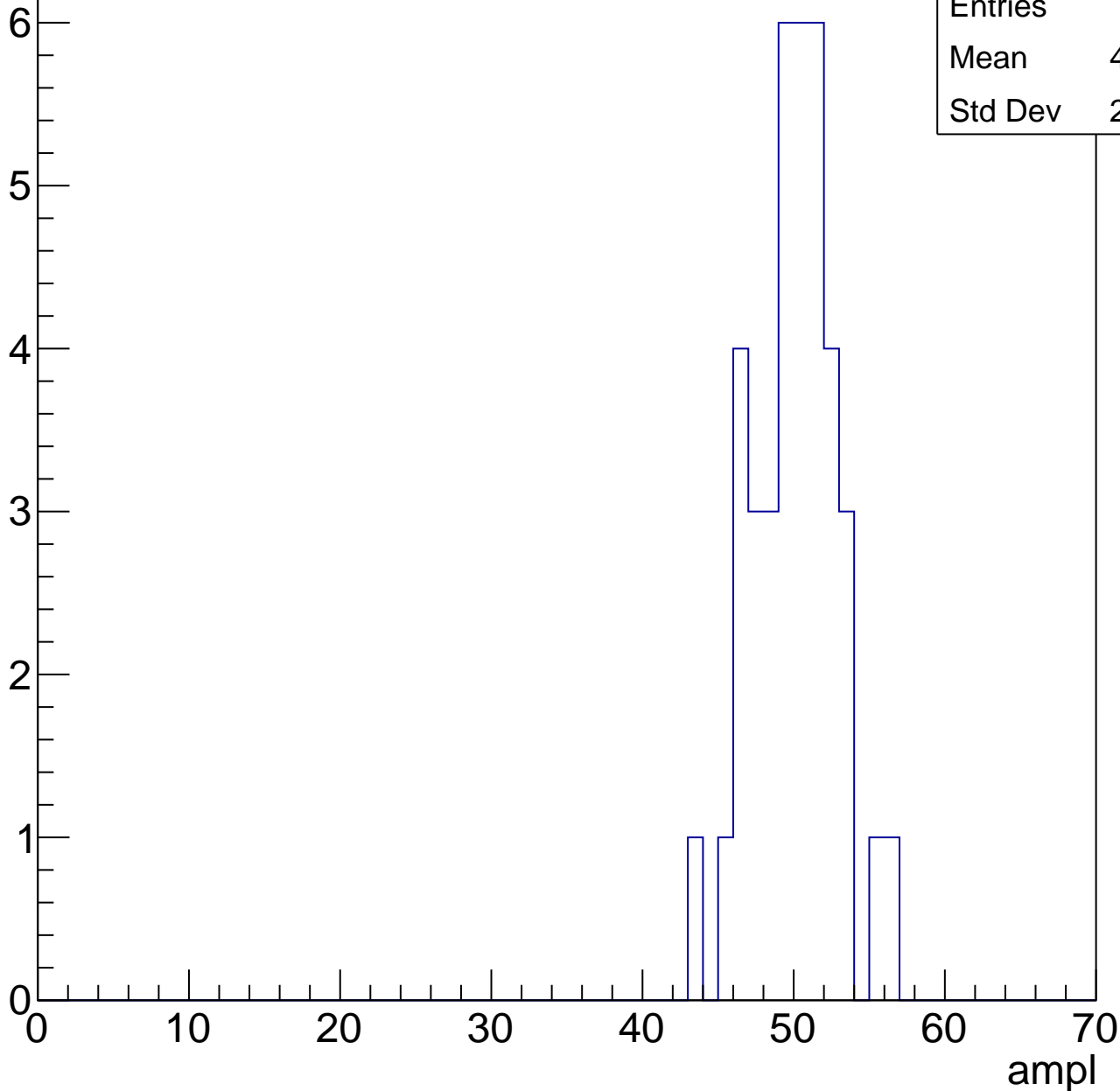


B1L102S, U10-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	49.62
Std Dev	2.714

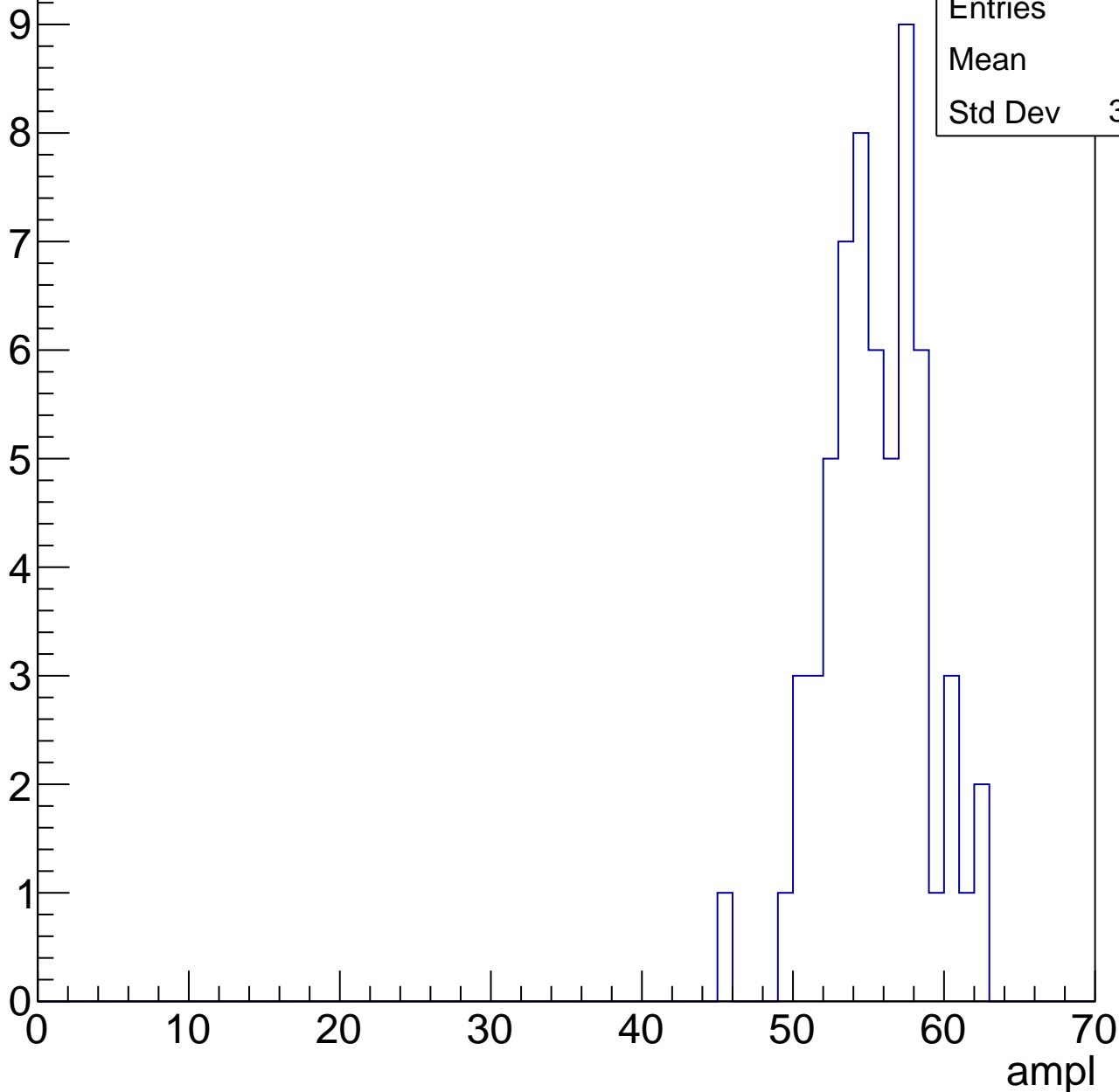


B1L102S, U10-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

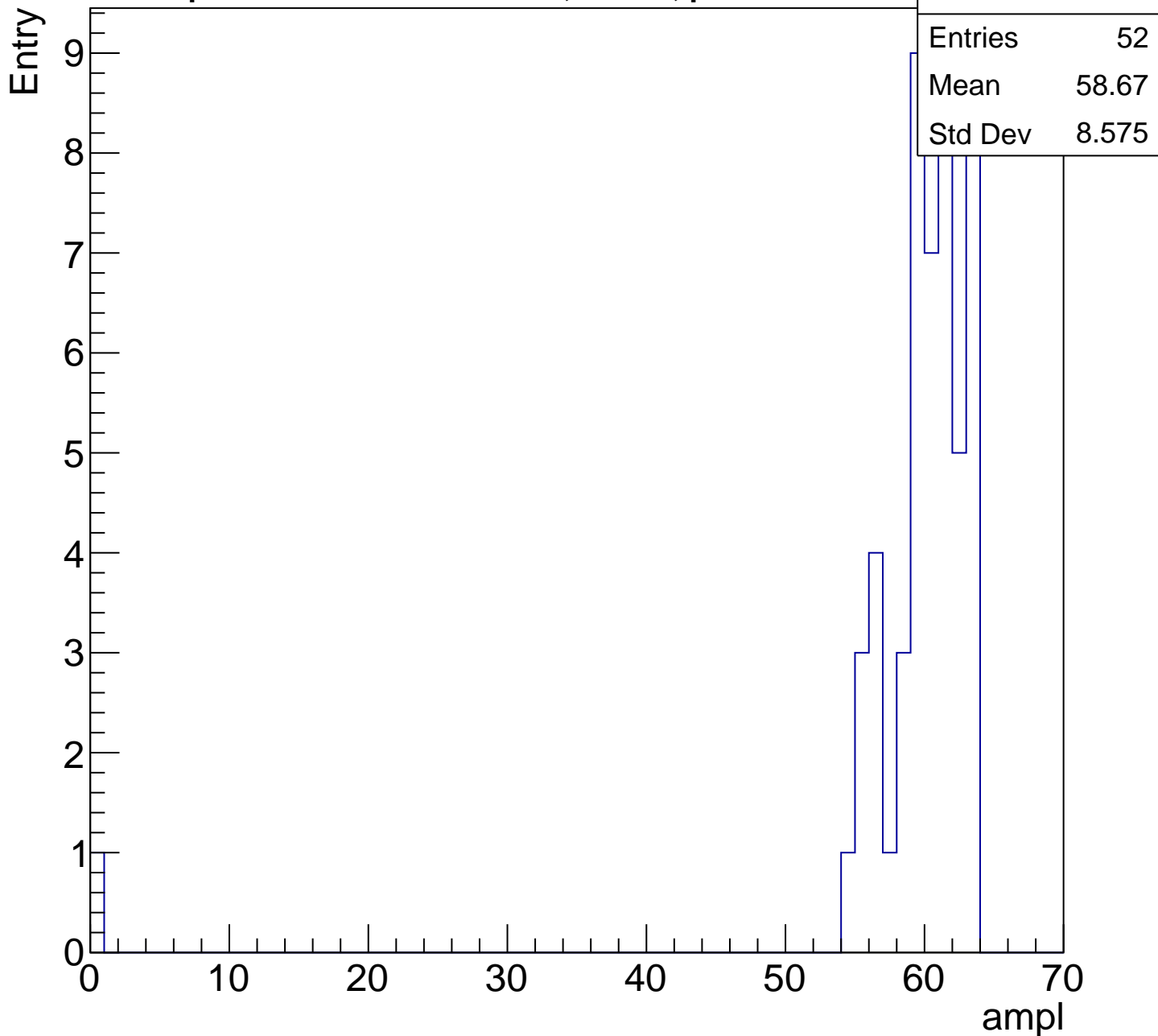
Entry

Entries	61
Mean	55
Std Dev	3.289



B1L102S, U10-ch8, adc5

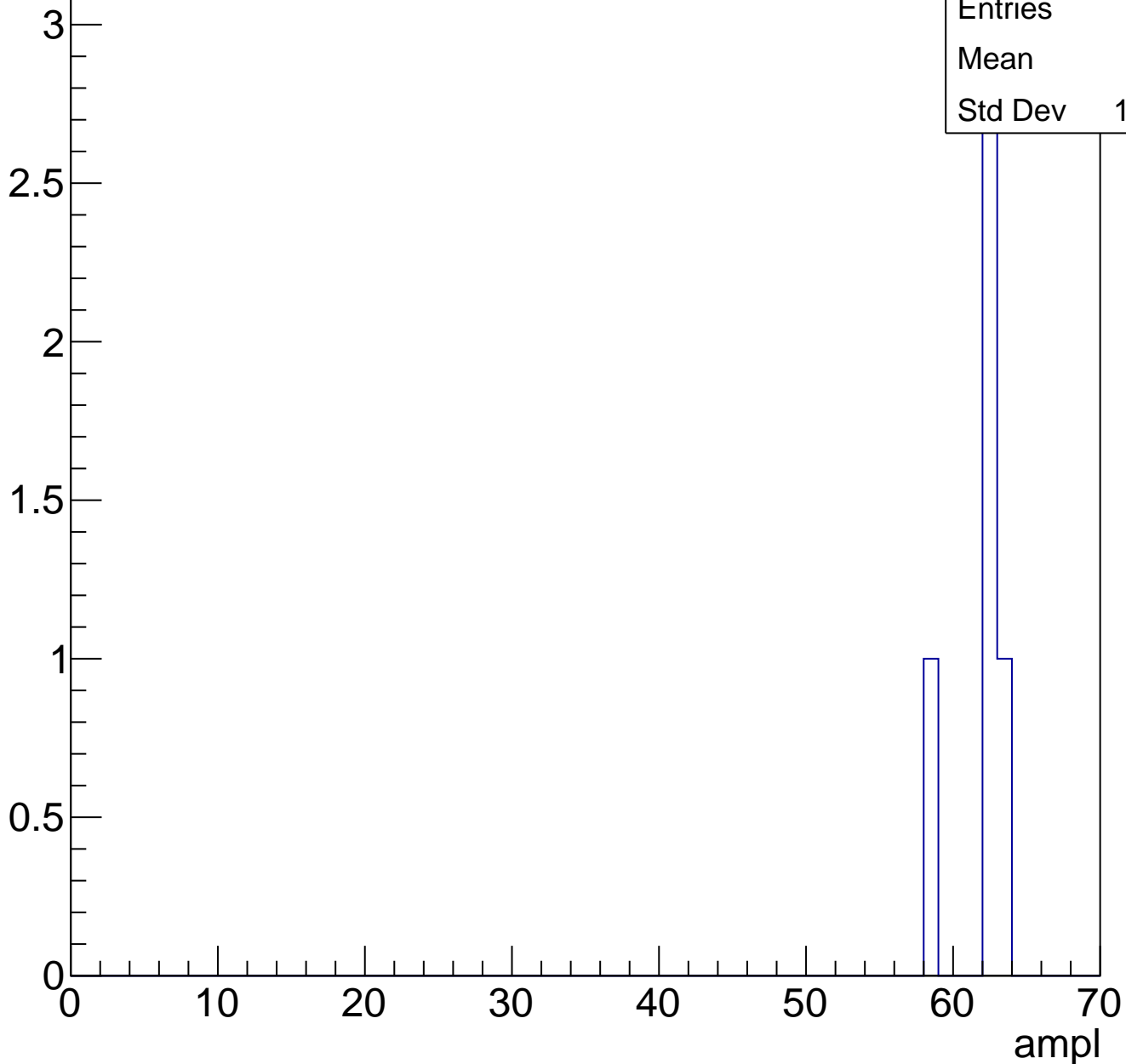
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U10-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch9, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	95
Mean	30.58
Std Dev	4.043

Gaus mean : 31.1695

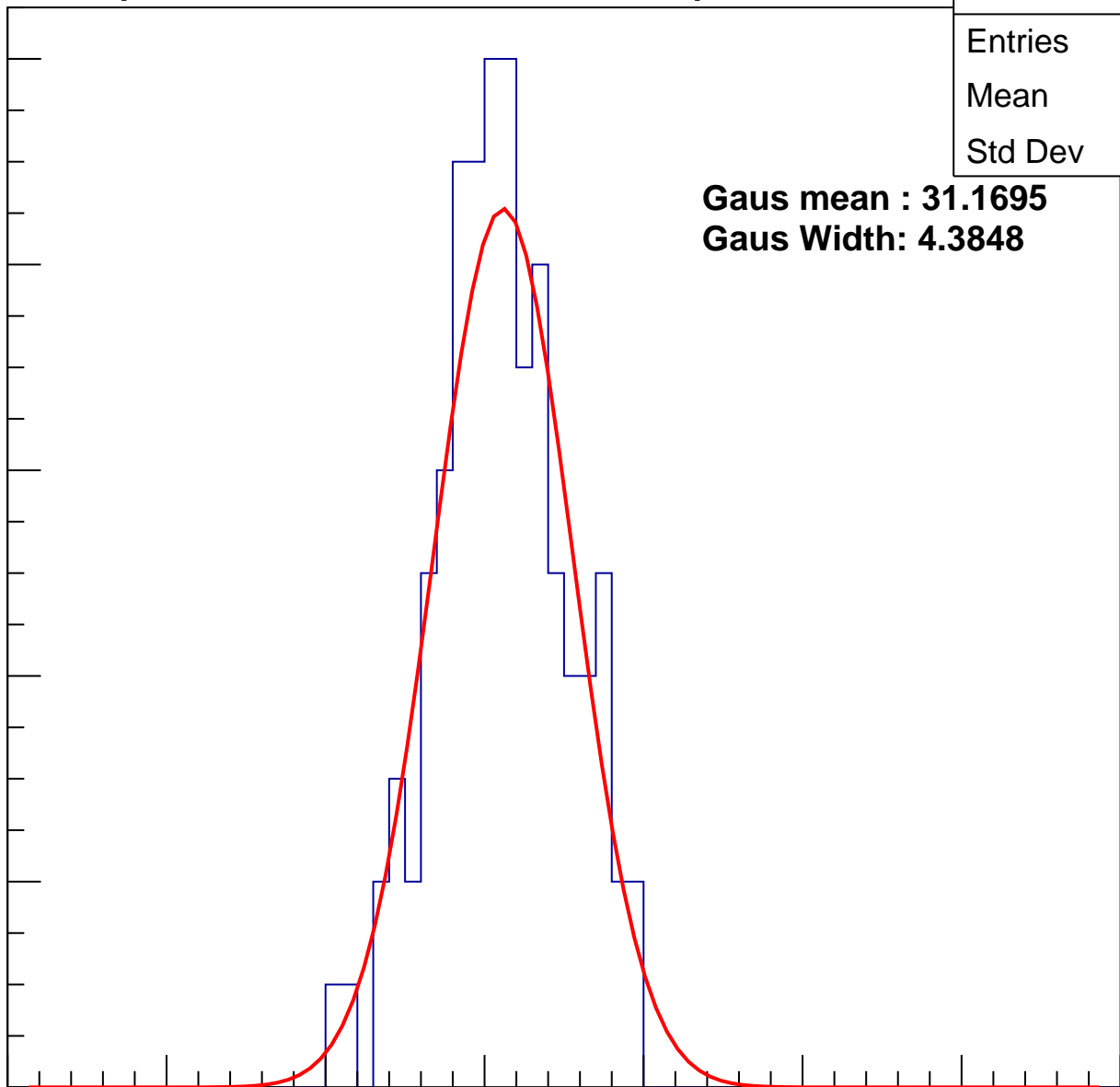
Gaus Width: 4.3848

Entry

10
8
6
4
2
0

ampl

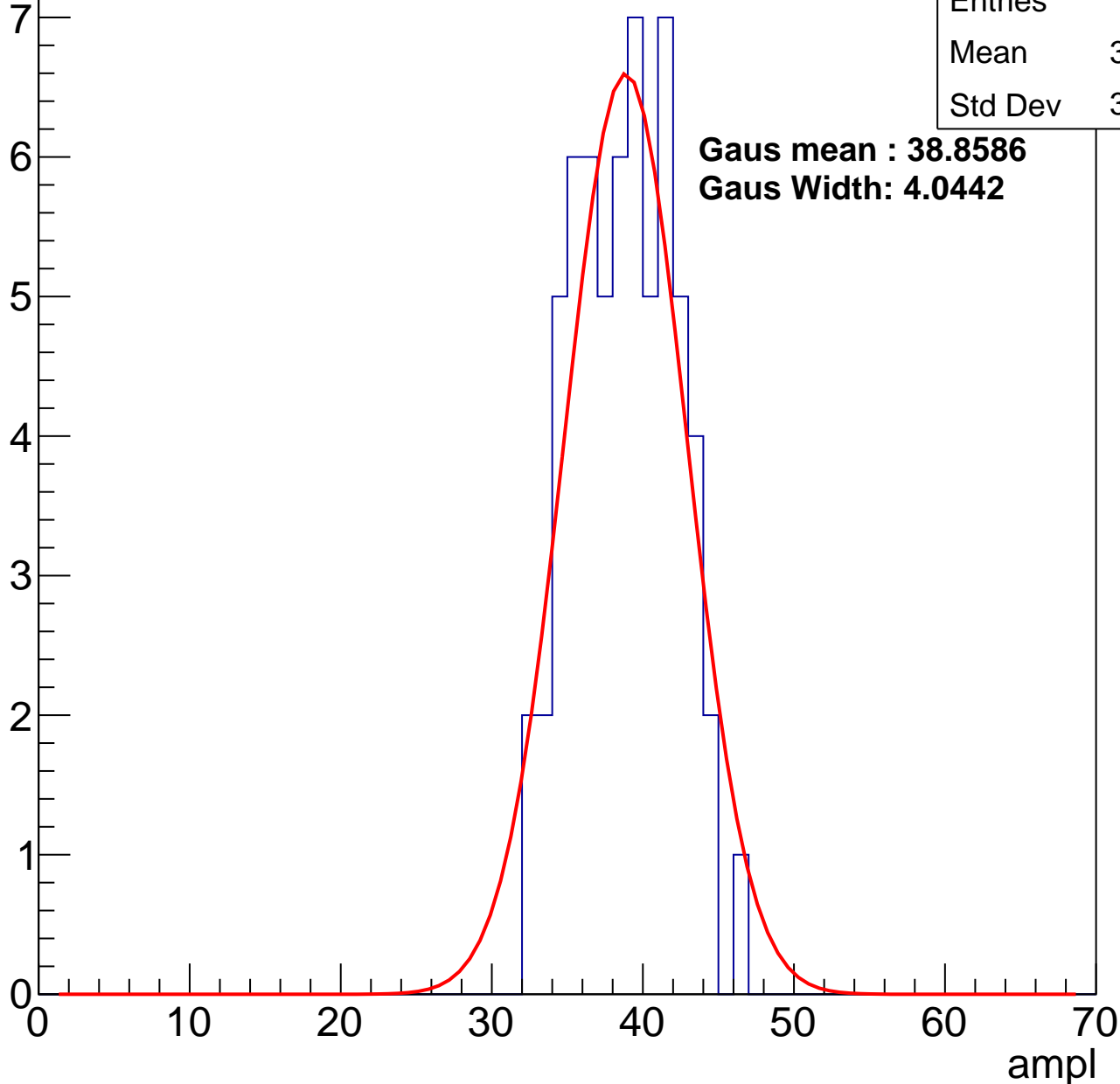
0 10 20 30 40 50 60 70



B1L102S, U10-ch9, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch9, adc2

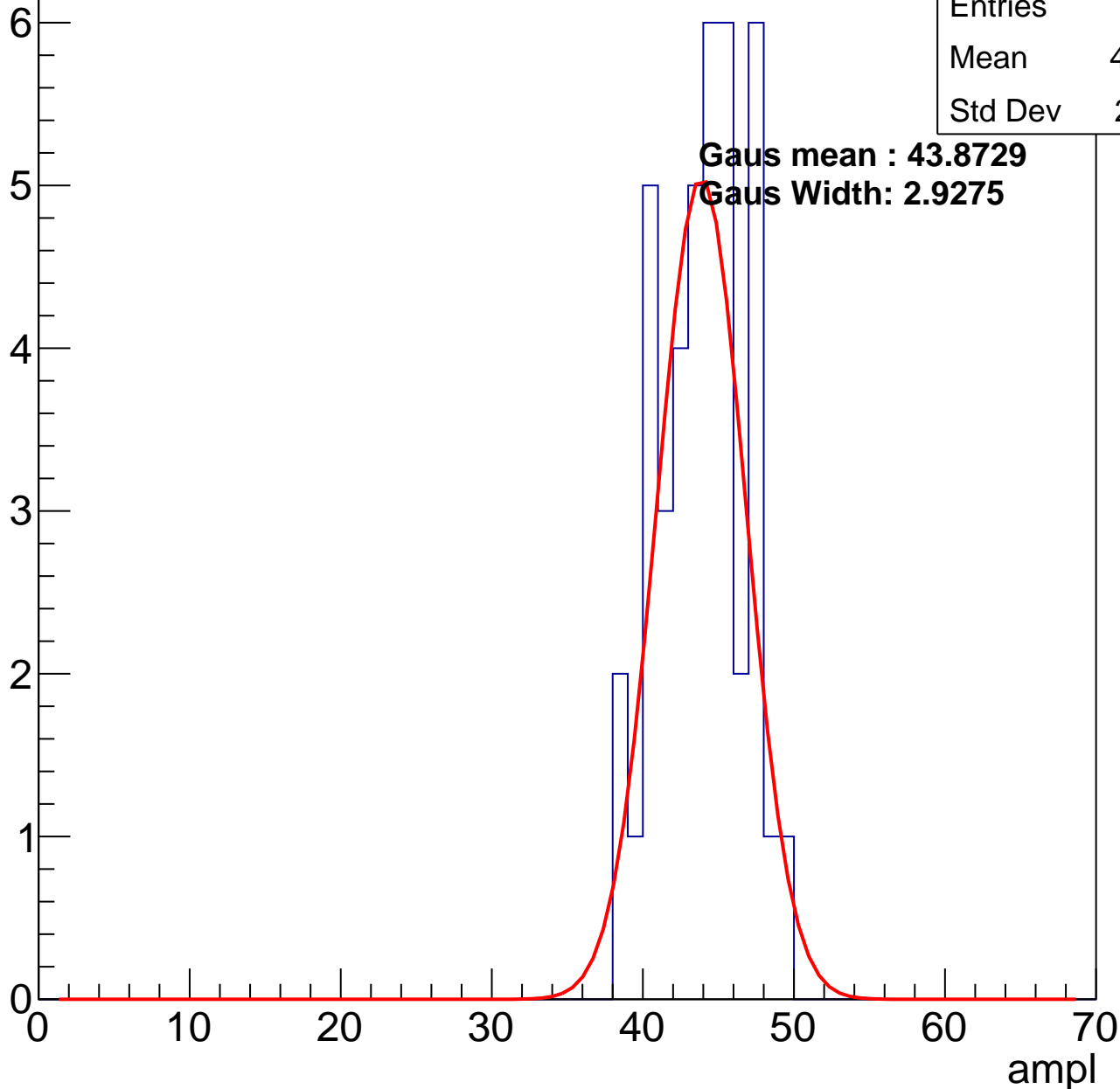
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	43.48
Std Dev	2.771

Gaus mean : 43.8729

Gaus Width: 2.9275

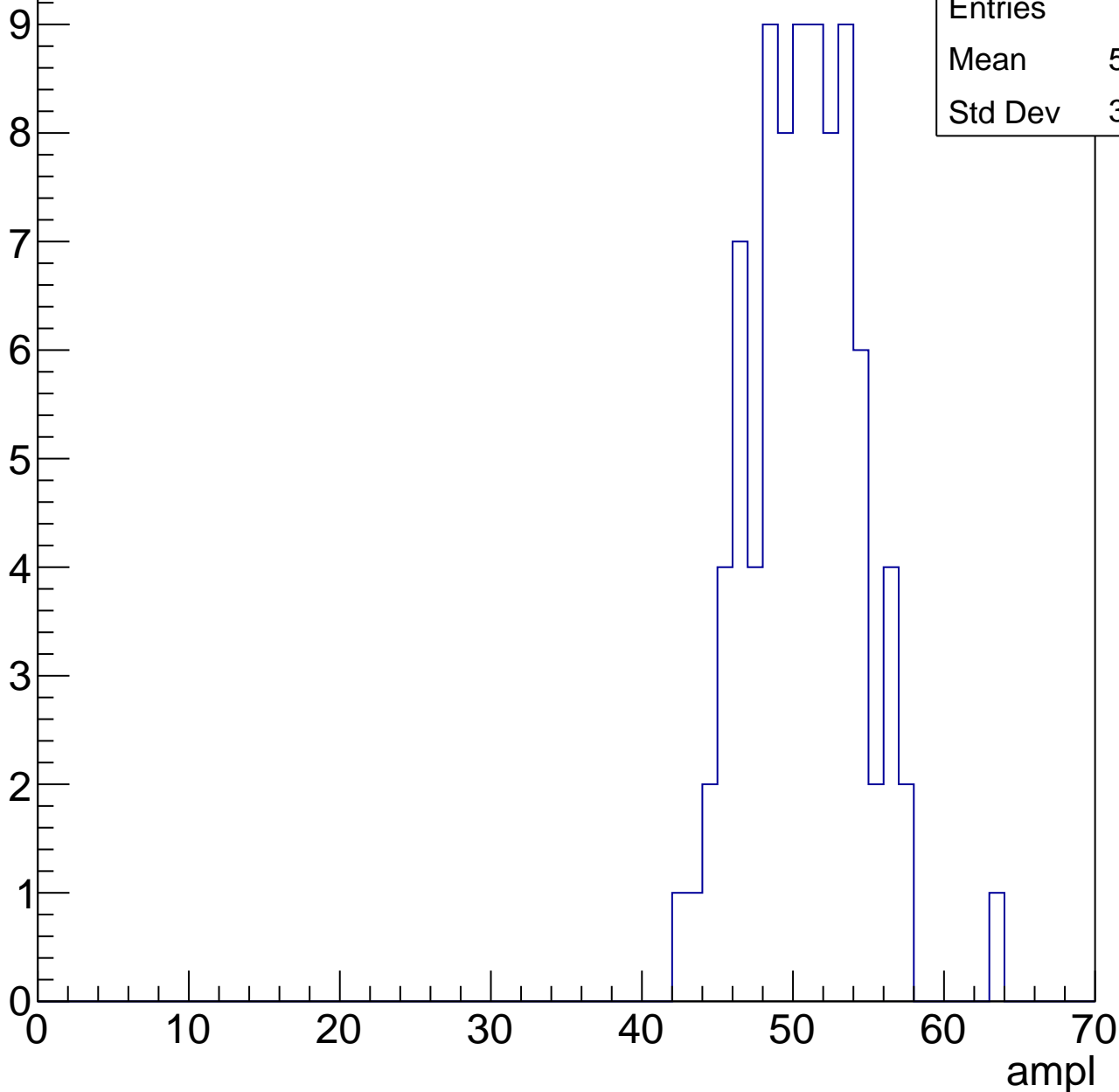


B1L102S, U10-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	50.28
Std Dev	3.656

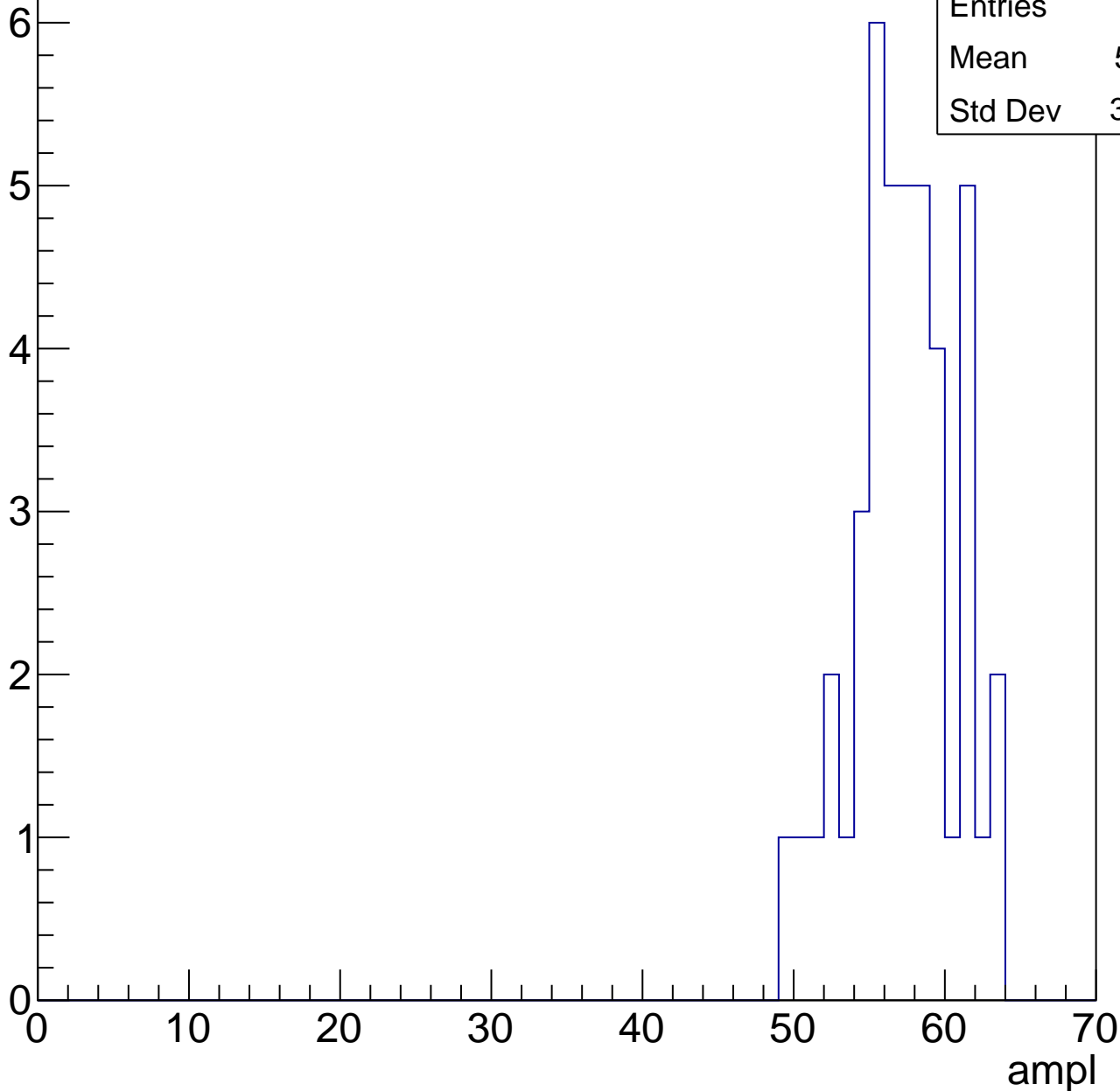


B1L102S, U10-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	56.81
Std Dev	3.329

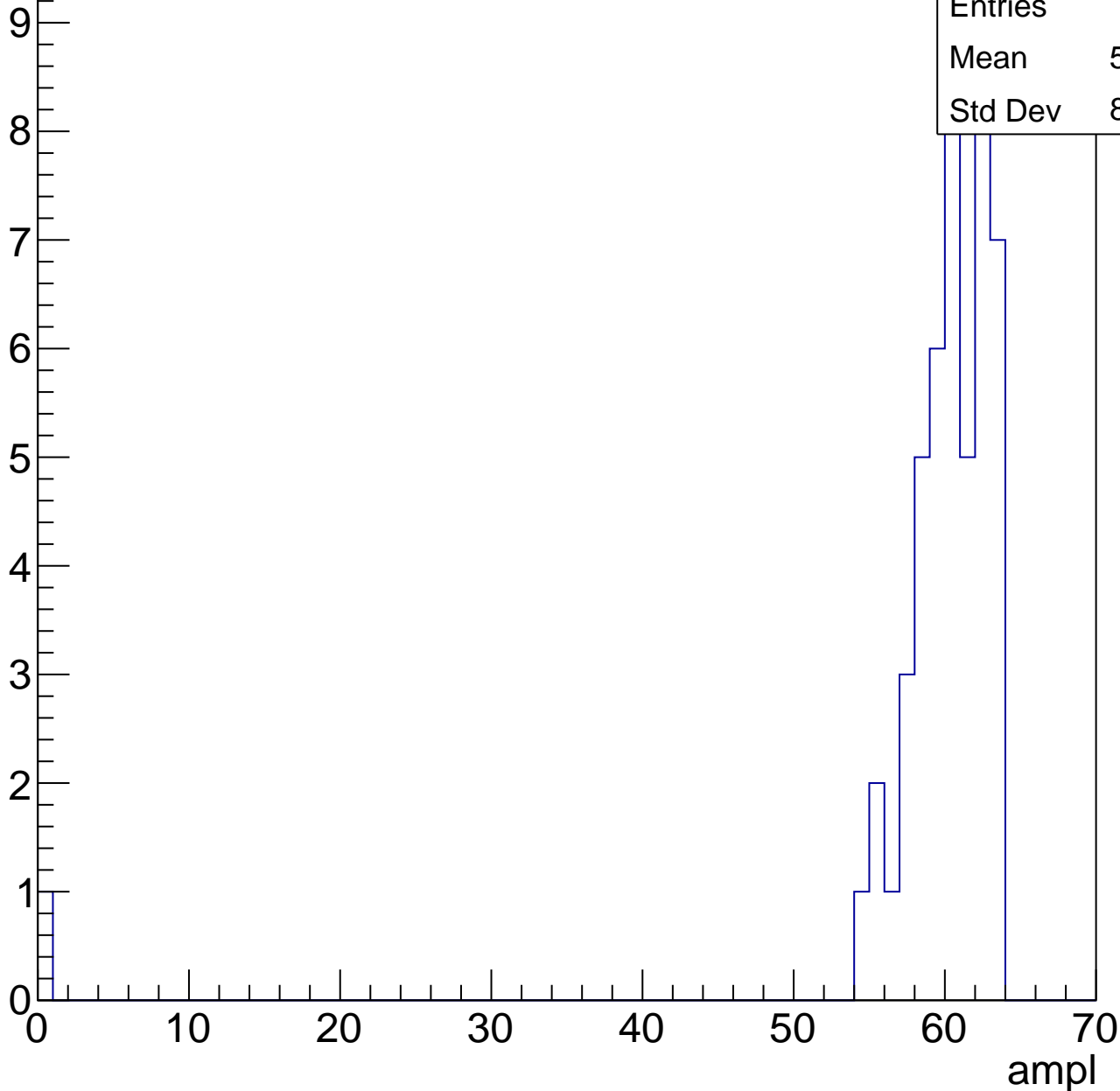


B1L102S, U10-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.69
Std Dev	8.865



B1L102S, U10-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch10, adc0

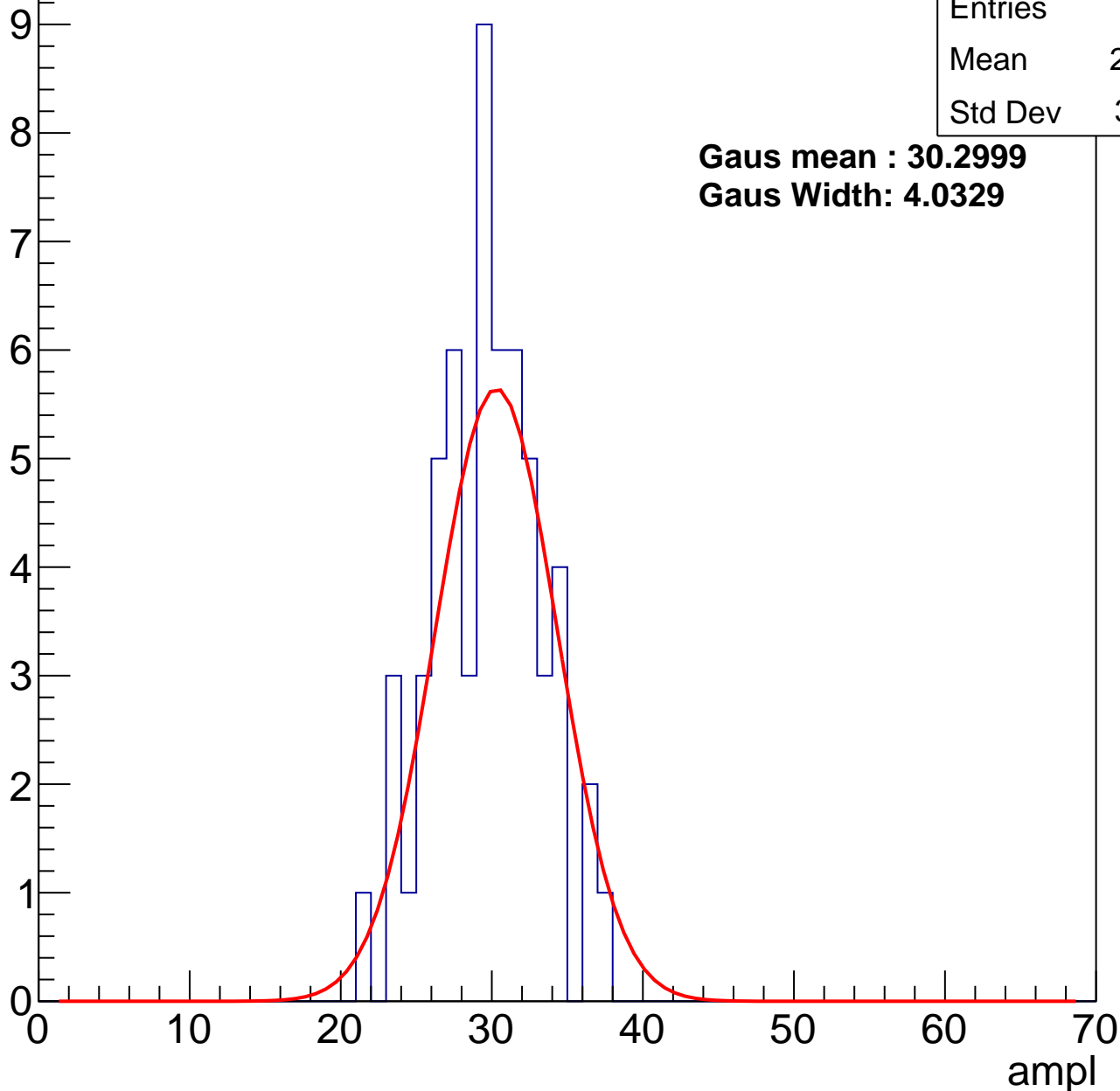
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.24
Std Dev	3.471

Gaus mean : 30.2999

Gaus Width: 4.0329



B1L102S, U10-ch10, adc1

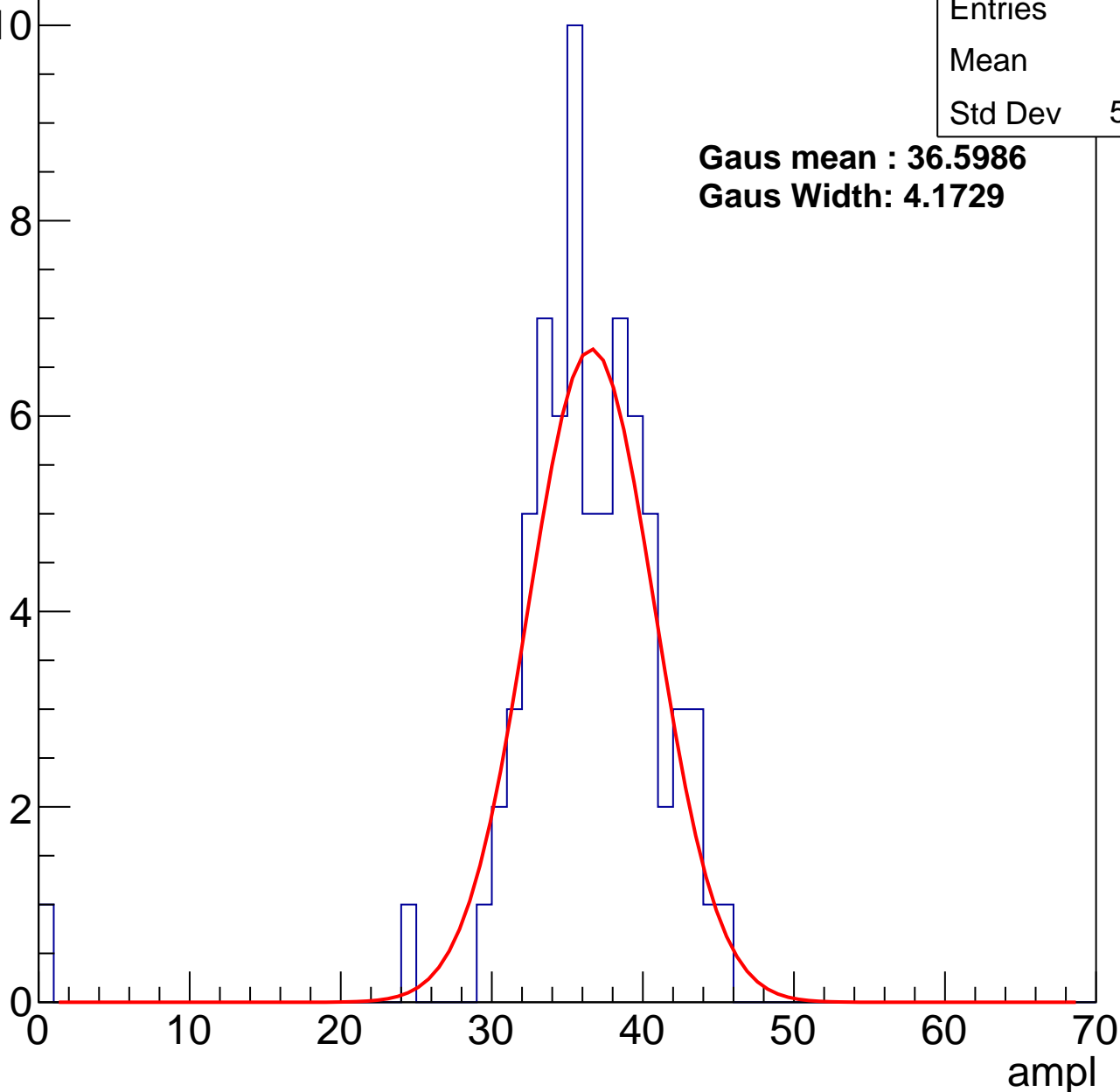
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.7
Std Dev	5.723

Gaus mean : 36.5986

Gaus Width: 4.1729



B1L102S, U10-ch10, adc2

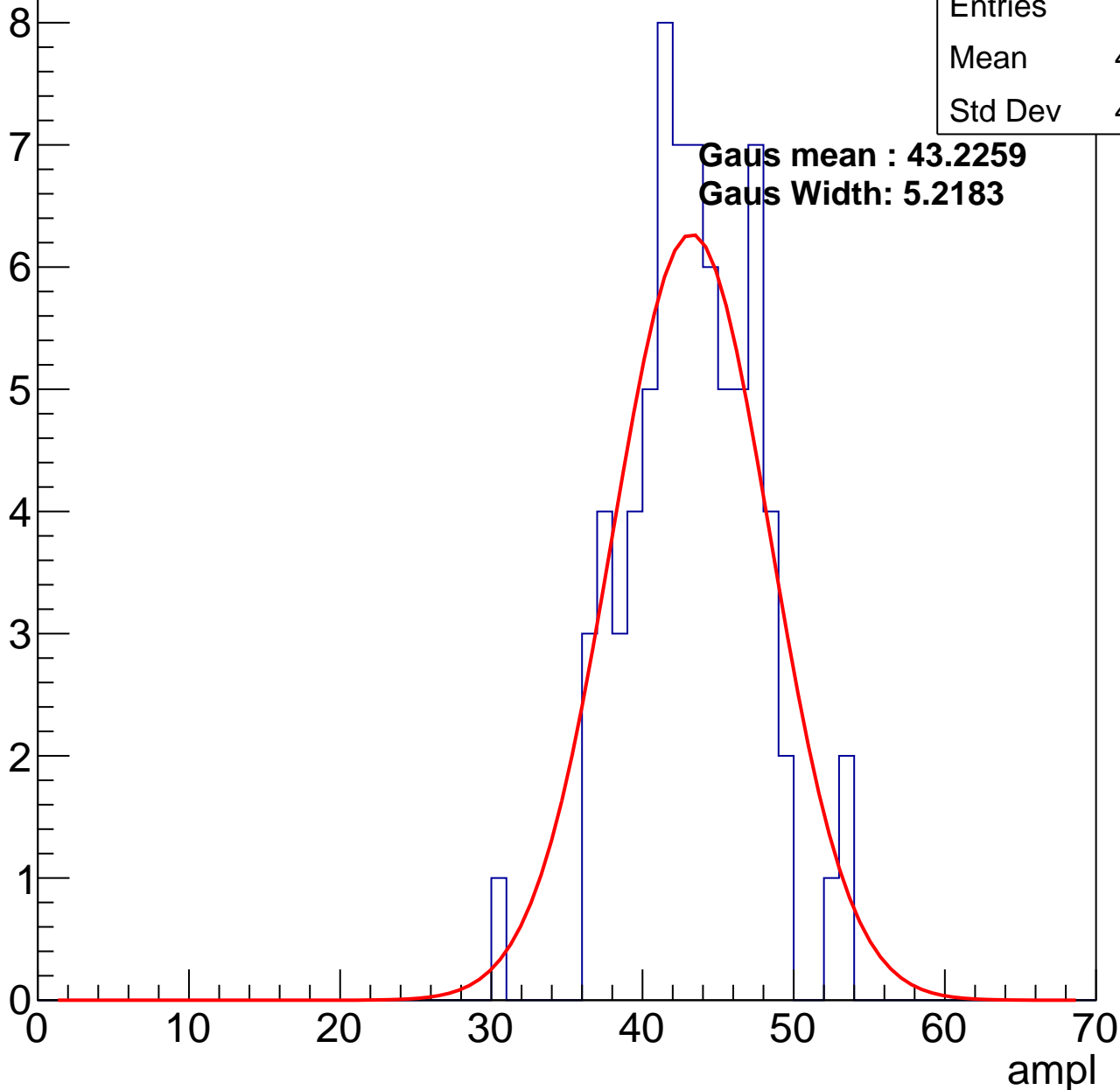
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.91
Std Dev	4.221

Gaus mean : 43.2259

Gaus Width: 5.2183

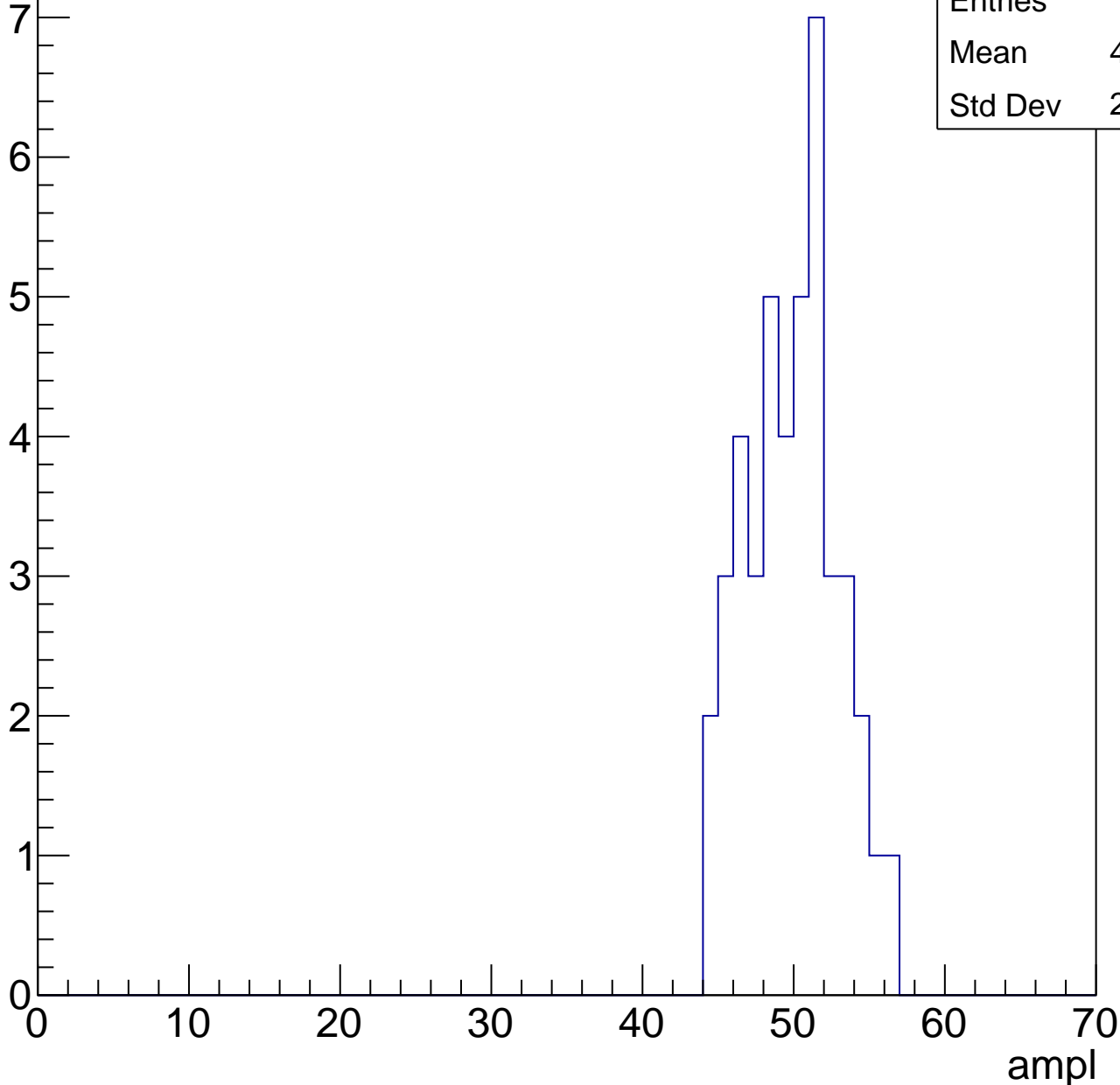


B1L102S, U10-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	49.42
Std Dev	2.998

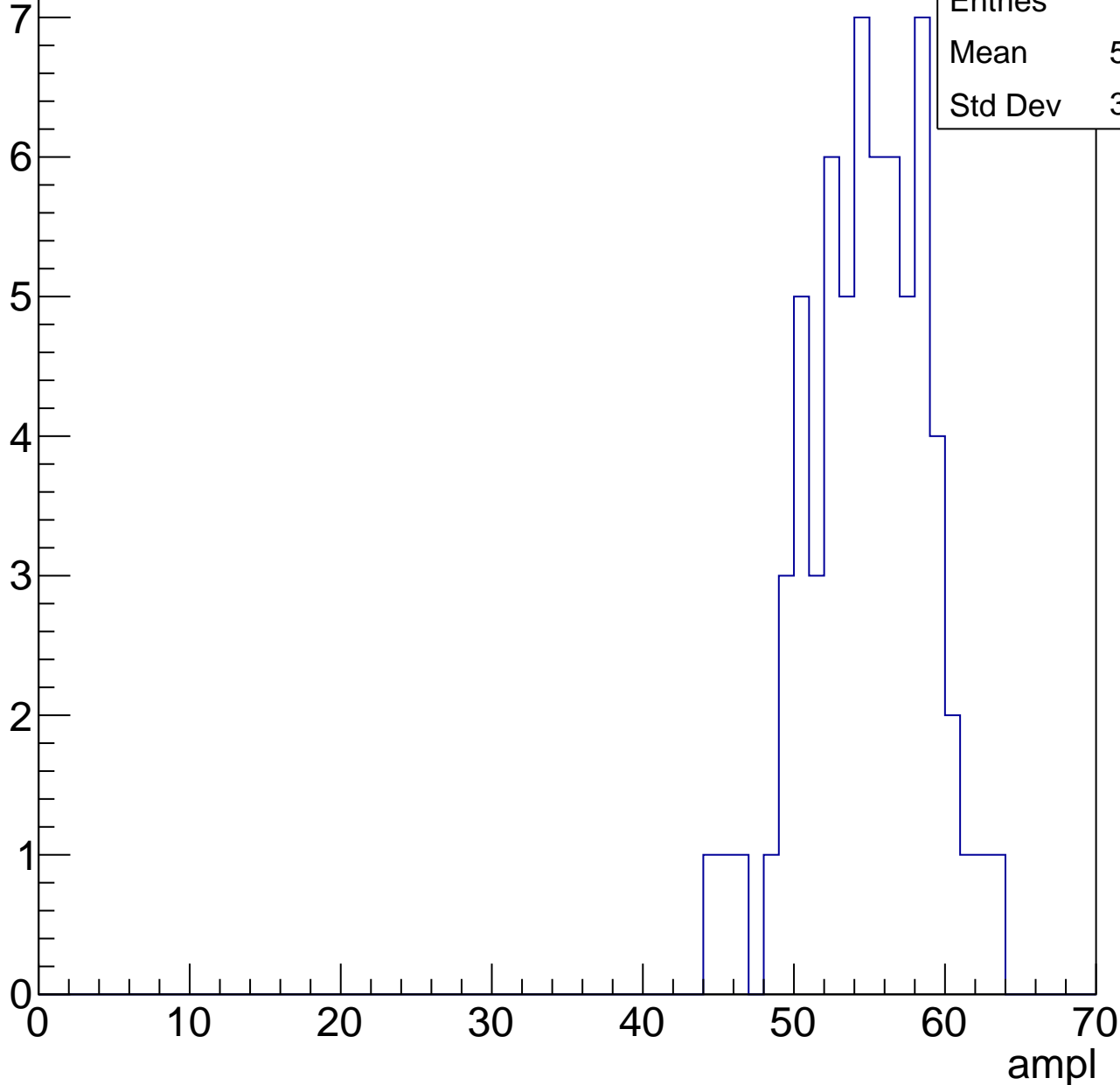


B1L102S, U10-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.35
Std Dev	3.964

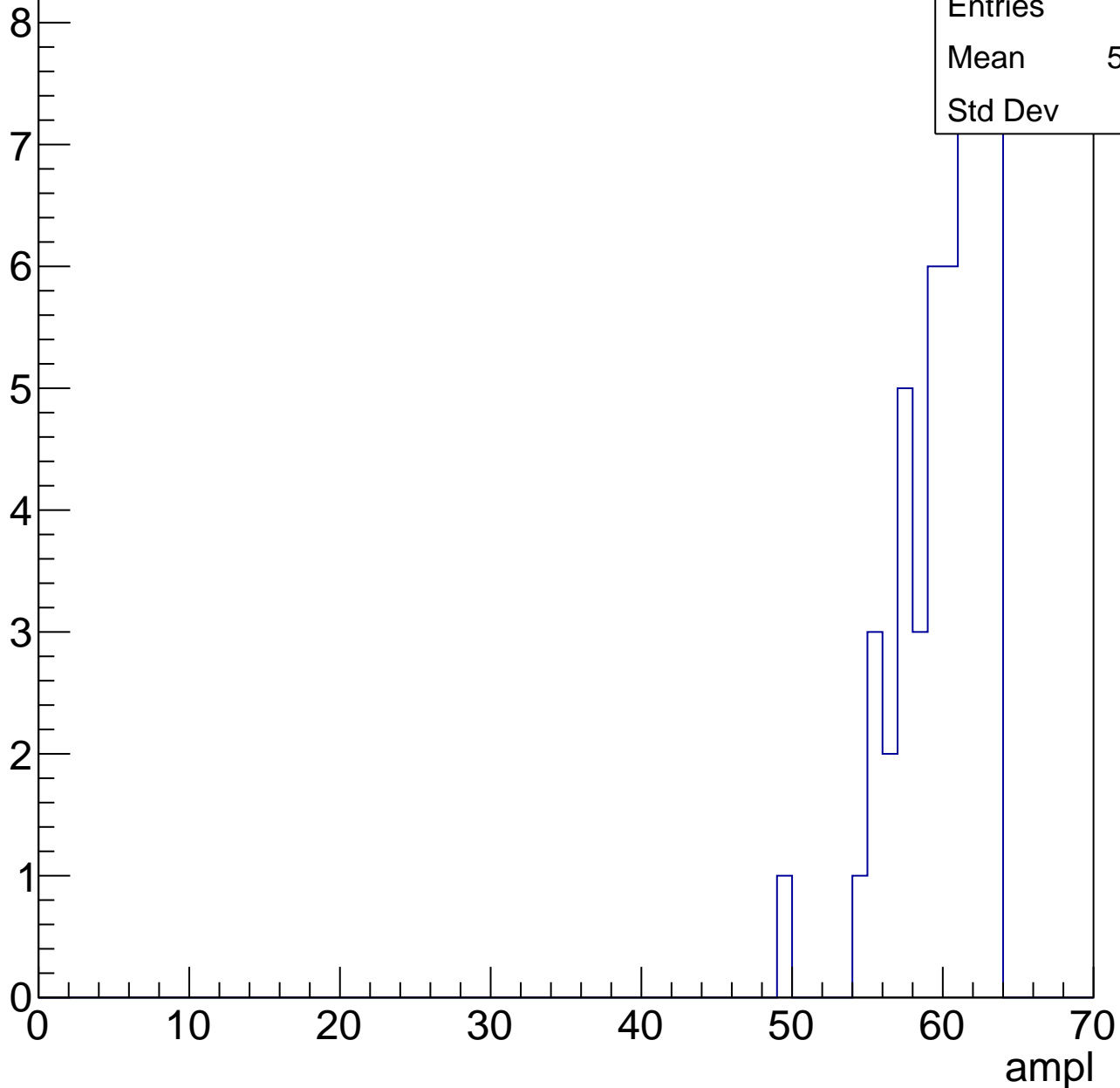


B1L102S, U10-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.63
Std Dev	2.91



B1L102S, U10-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

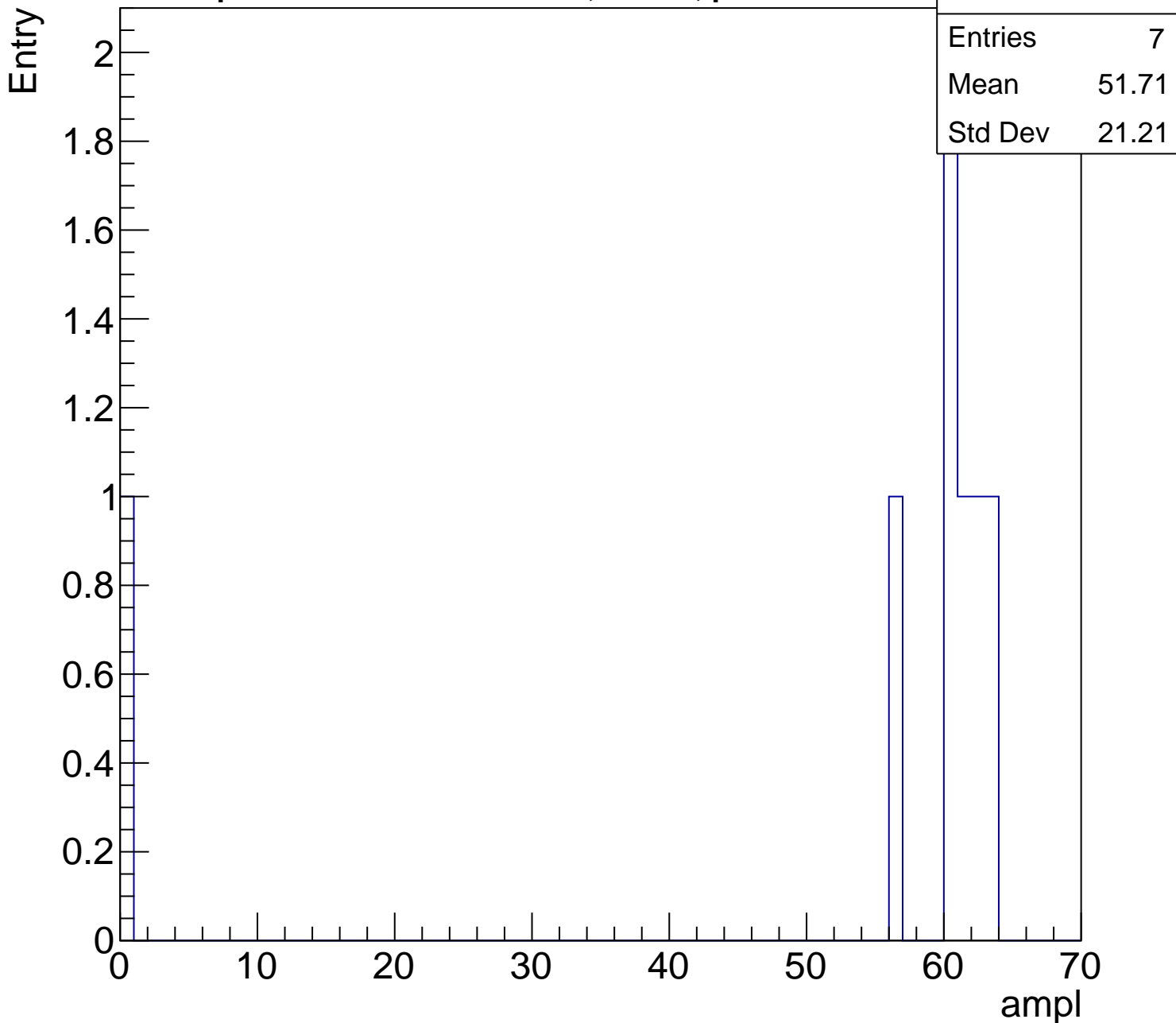
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	51.71
Std Dev	21.21

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U10-ch11, adc0

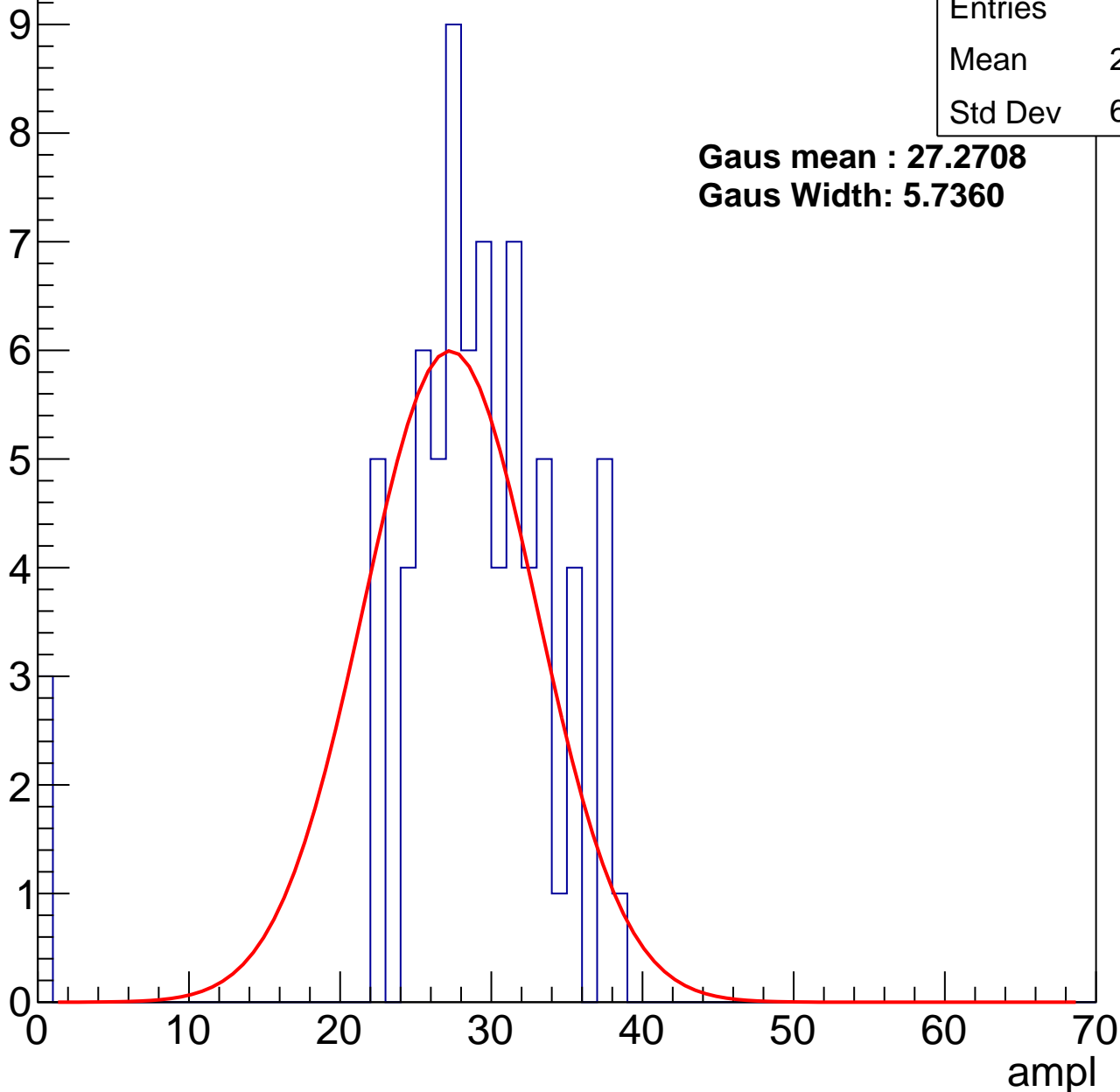
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.99
Std Dev	6.969

Gaus mean : 27.2708

Gaus Width: 5.7360



B1L102S, U10-ch11, adc1

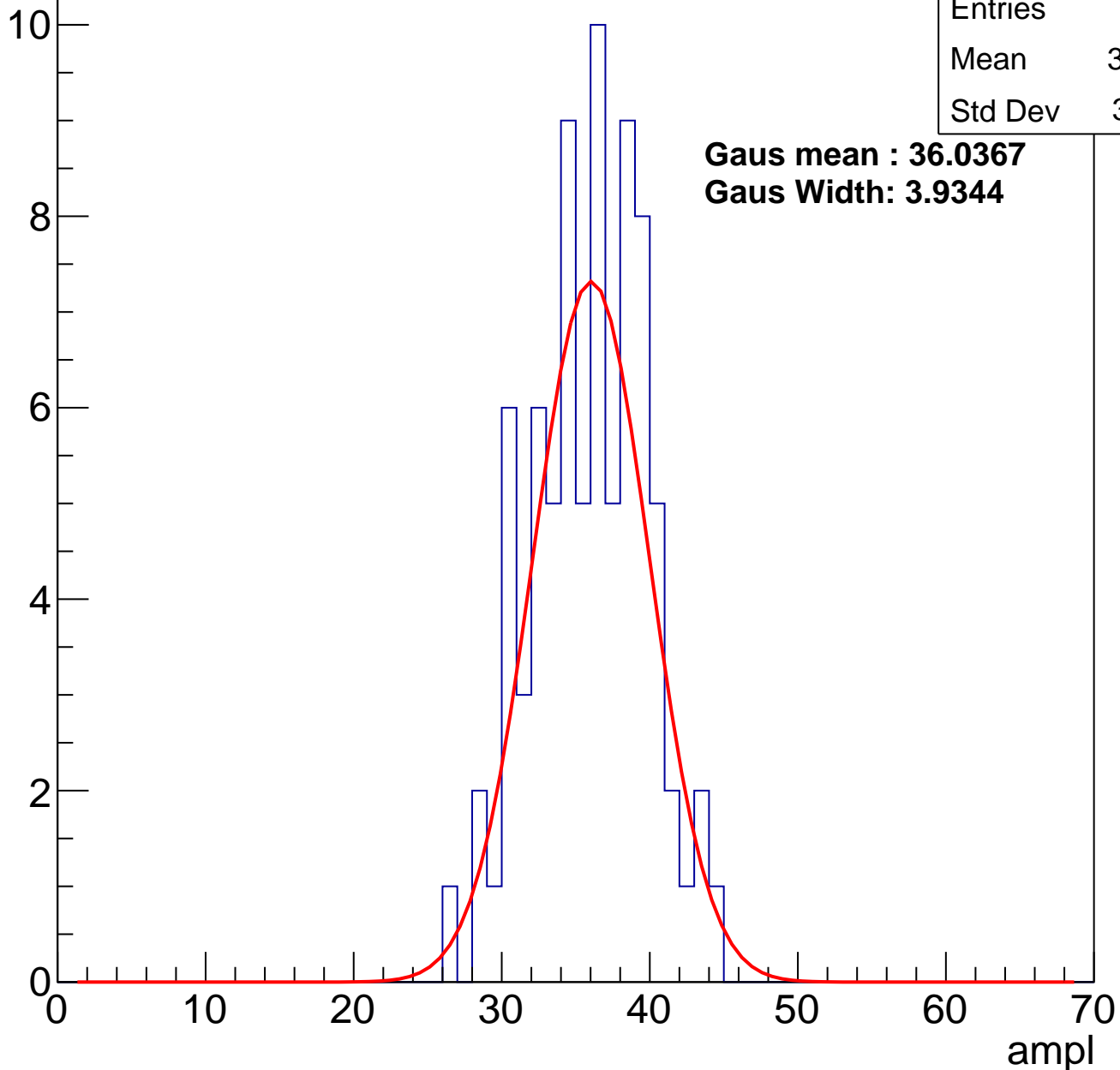
calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	35.49
Std Dev	3.801

Gaus mean : 36.0367

Gaus Width: 3.9344

Entry



B1L102S, U10-ch11, adc2

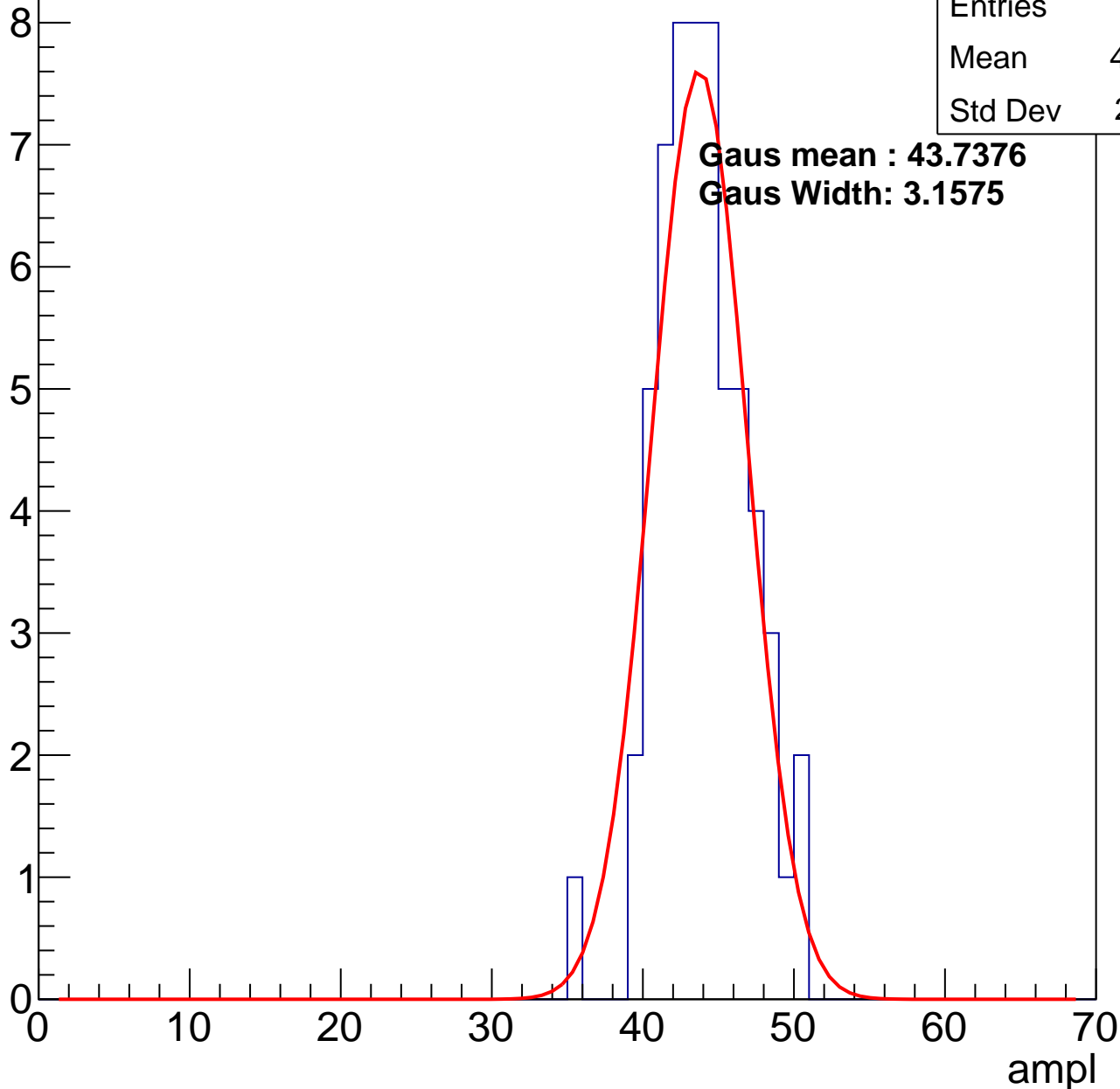
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	43.53
Std Dev	2.931

Gaus mean : 43.7376

Gaus Width: 3.1575

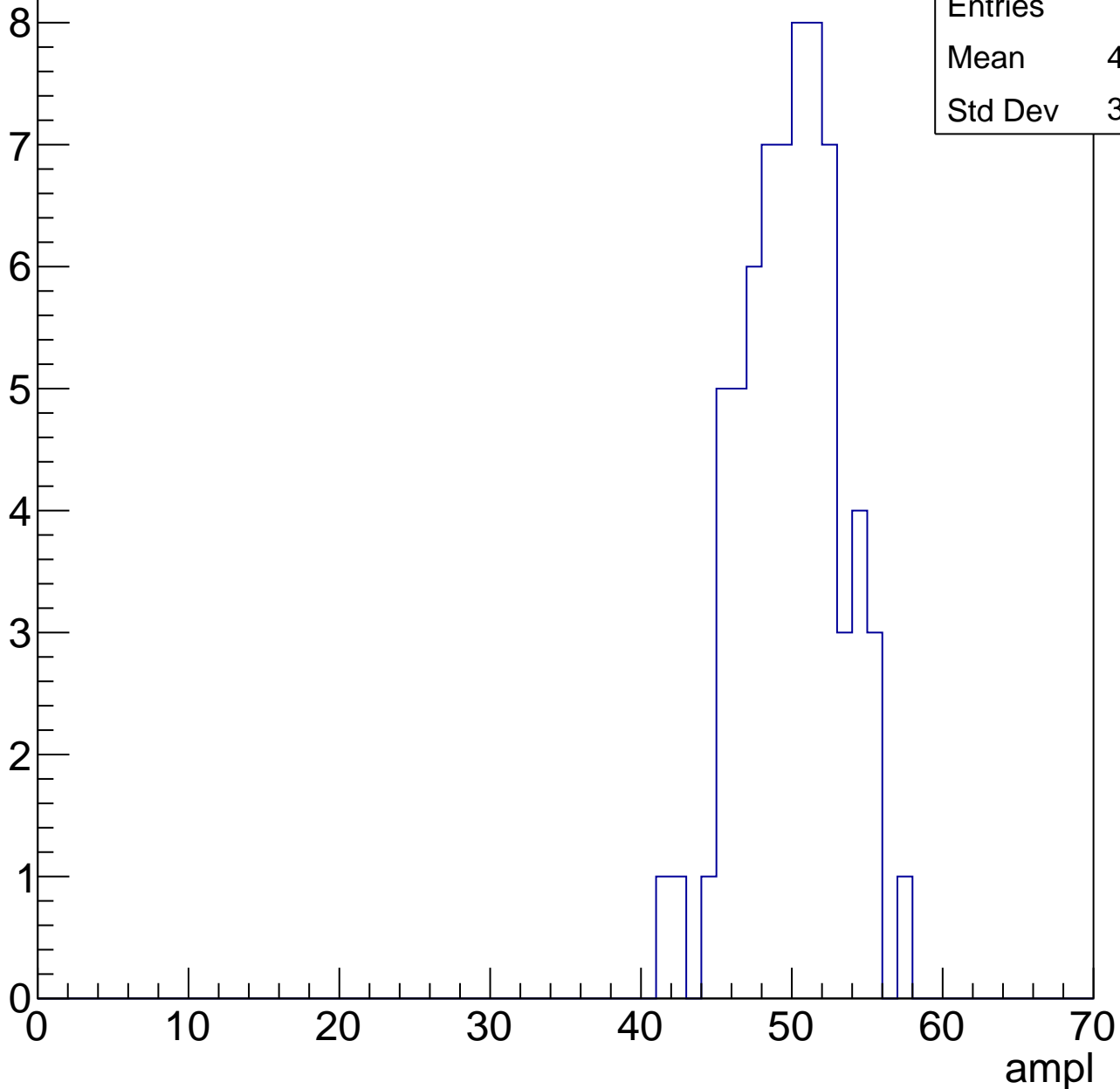


B1L102S, U10-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.43
Std Dev	3.243

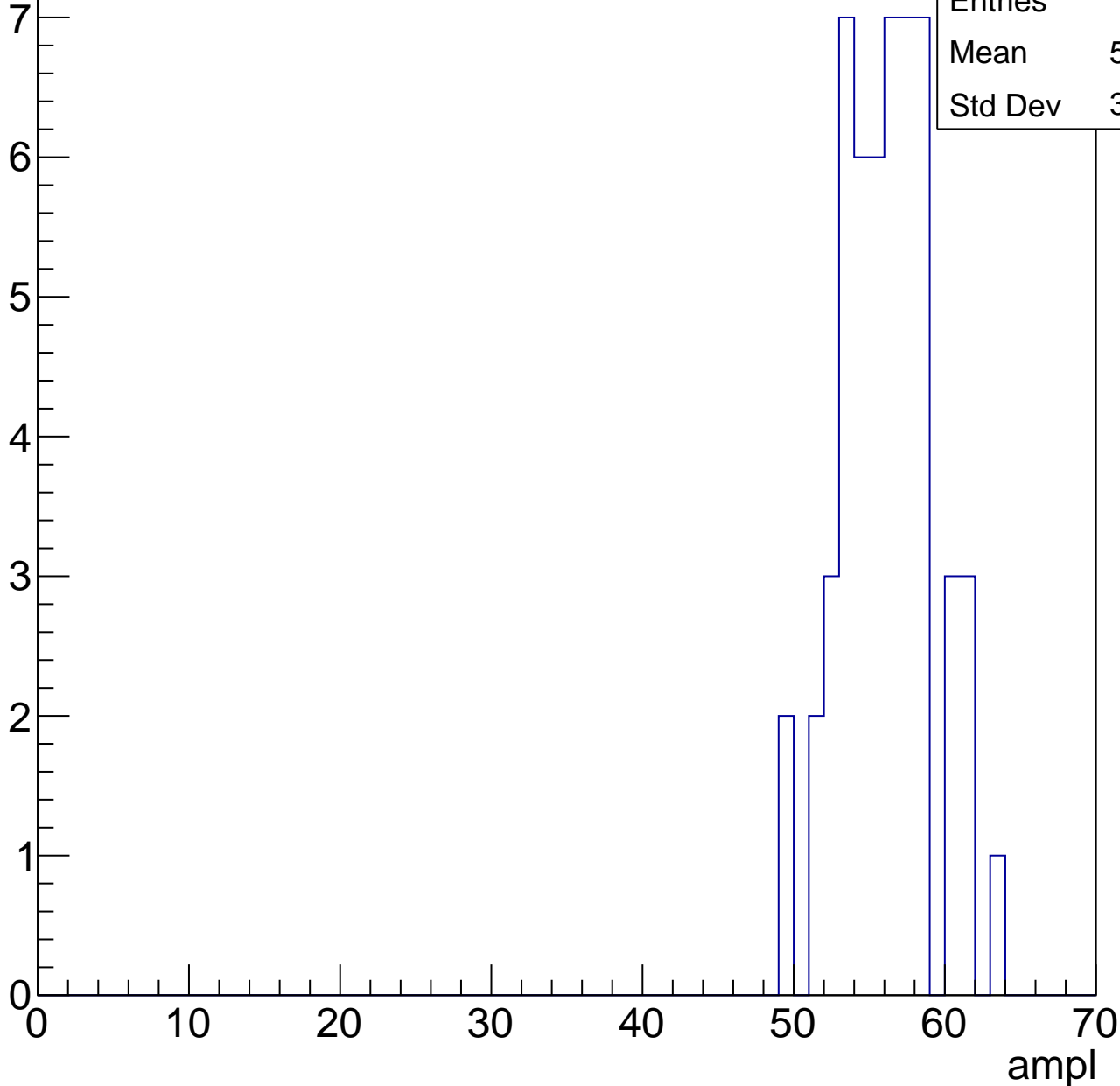


B1L102S, U10-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.63
Std Dev	3.008



B1L102S, U10-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	50
Mean	58.98
Std Dev	8.684

B1L102S, U10-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L102S, U10-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch12, adc0

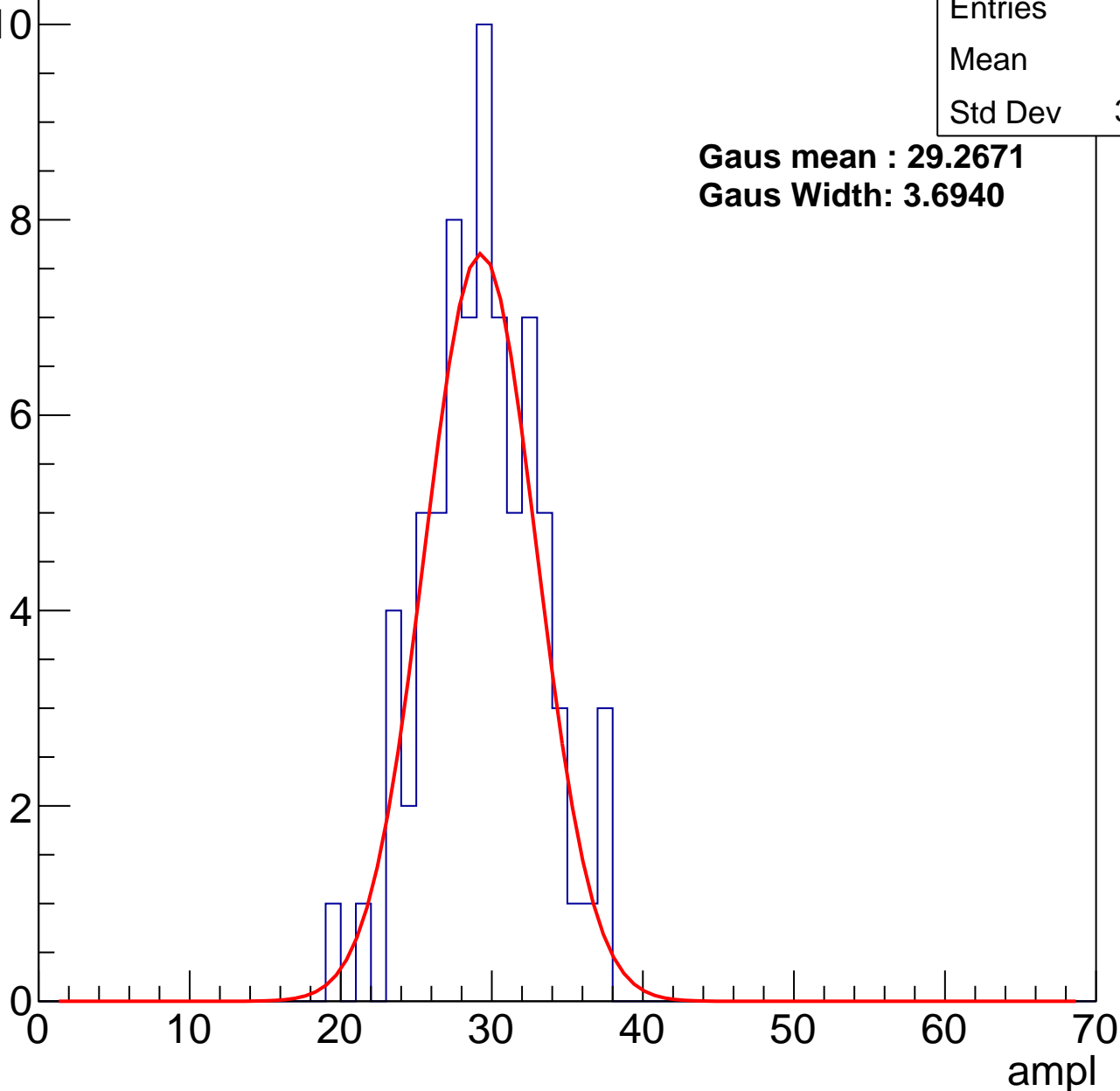
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29
Std Dev	3.731

Gaus mean : 29.2671

Gaus Width: 3.6940



B1L102S, U10-ch12, adc1

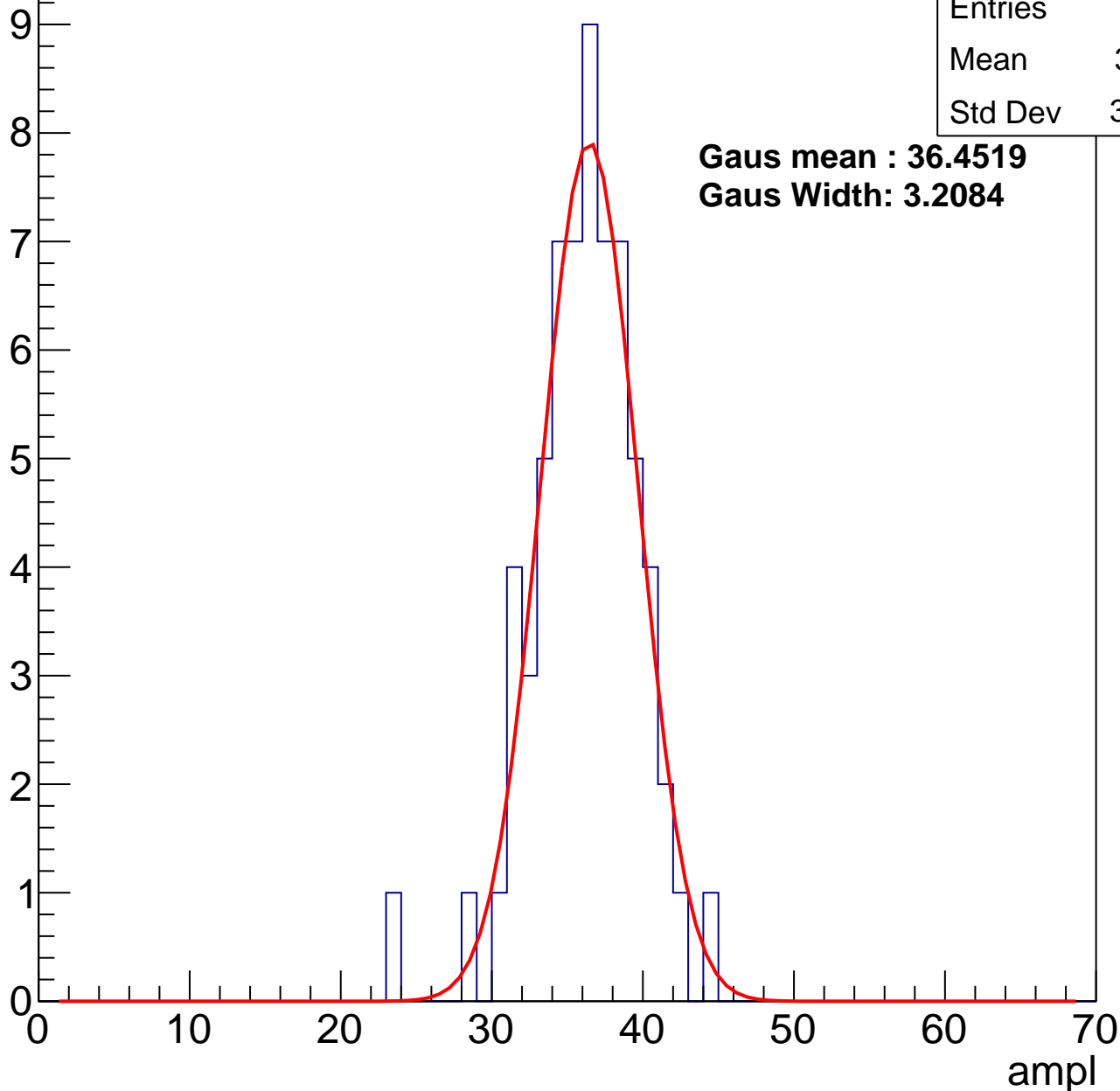
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.71
Std Dev	3.476

Gaus mean : 36.4519

Gaus Width: 3.2084



B1L102S, U10-ch12, adc2

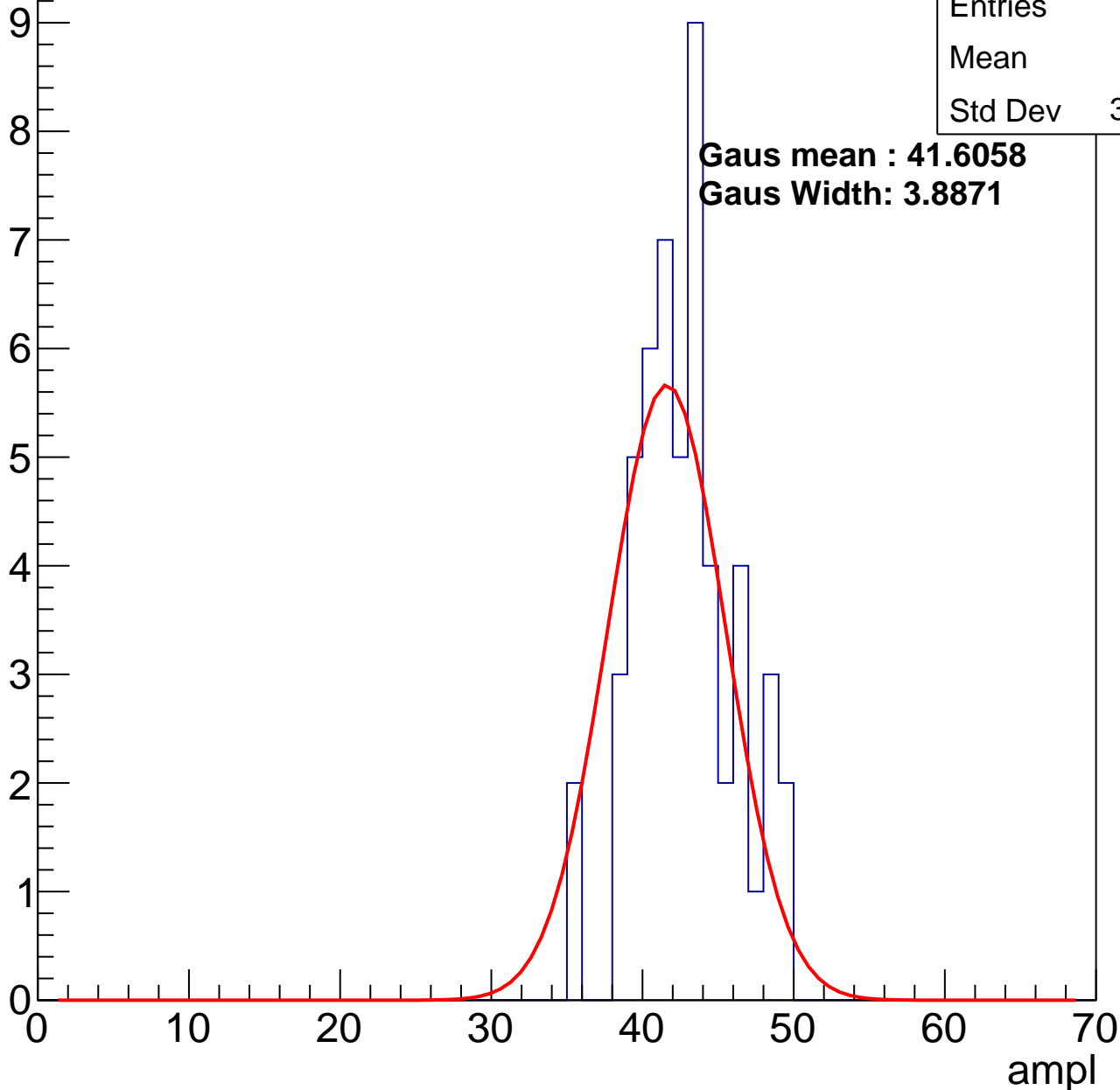
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.3
Std Dev	3.236

Gaus mean : 41.6058

Gaus Width: 3.8871

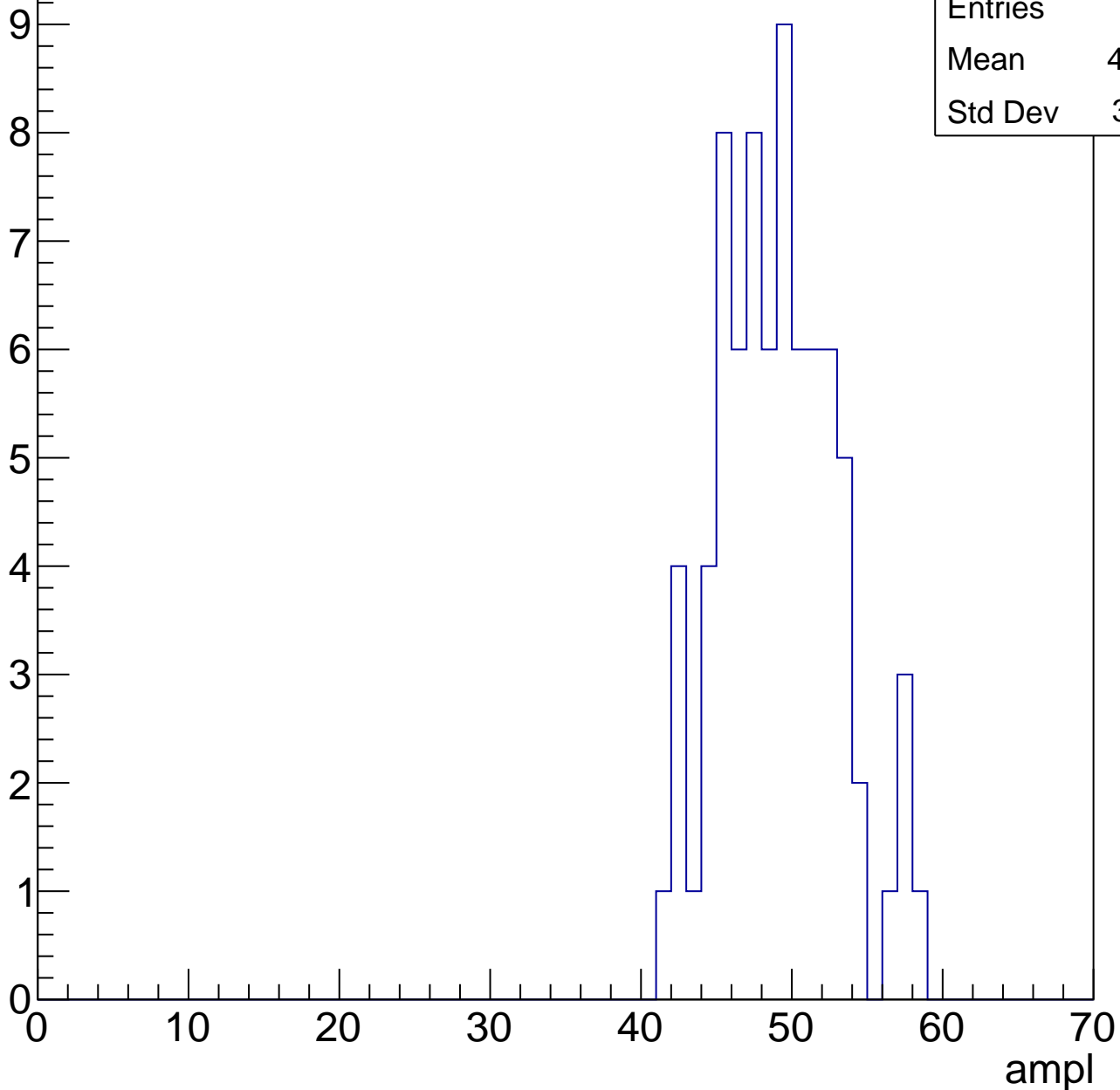


B1L102S, U10-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.64
Std Dev	3.851

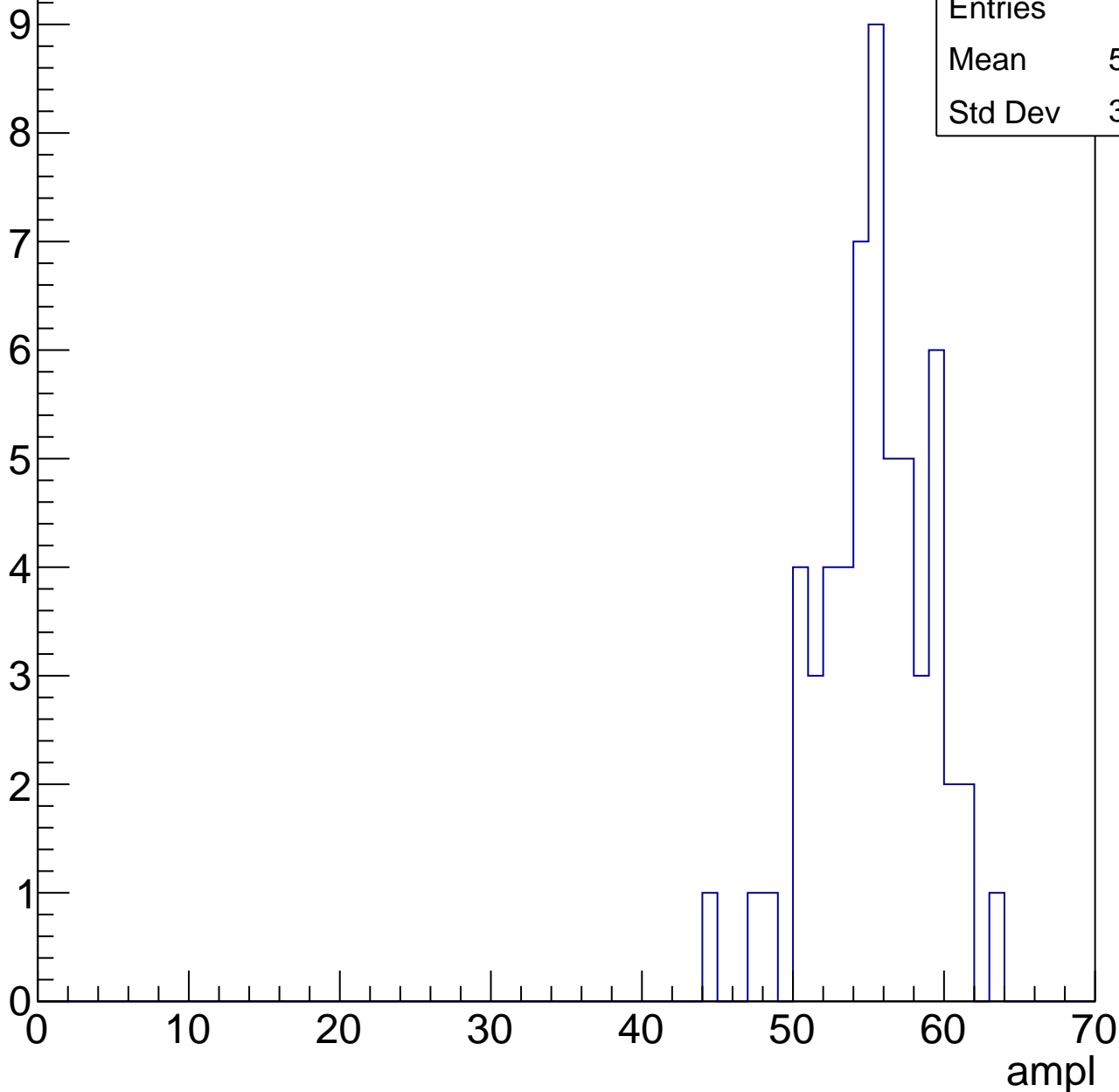


B1L102S, U10-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.88
Std Dev	3.653

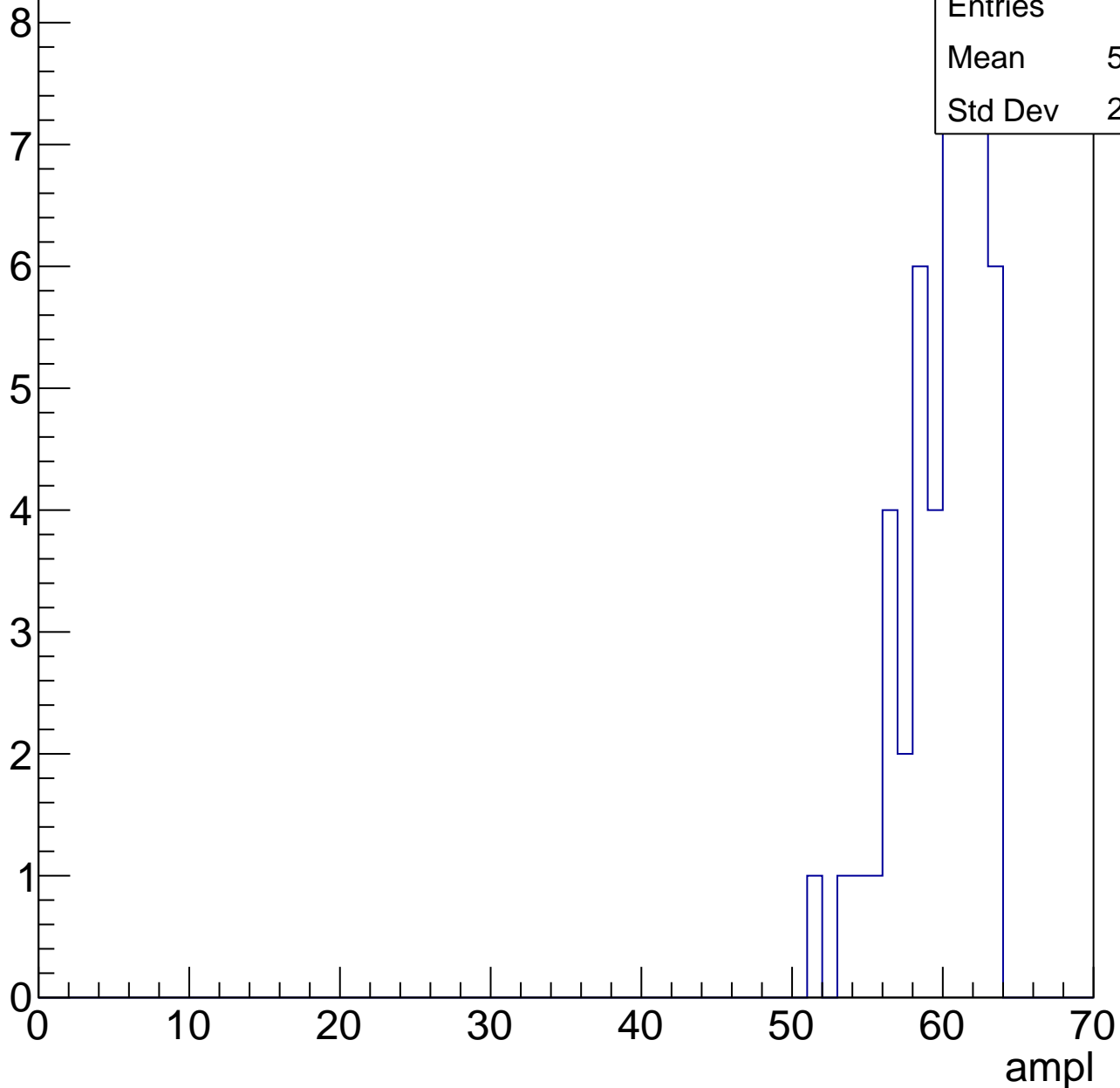


B1L102S, U10-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	59.54
Std Dev	2.773



B1L102S, U10-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch13, adc0

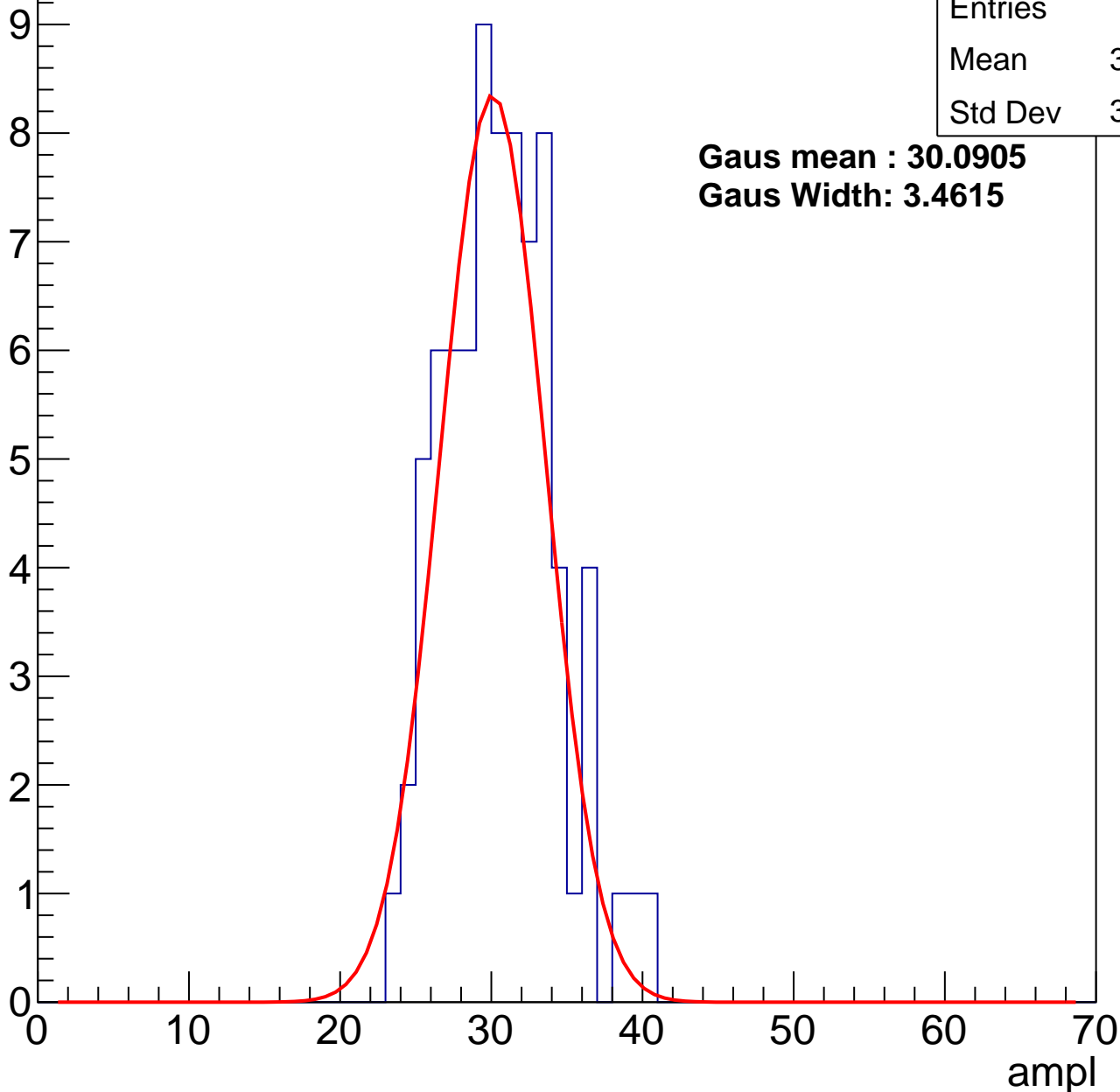
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	30.14
Std Dev	3.605

Gaus mean : 30.0905

Gaus Width: 3.4615



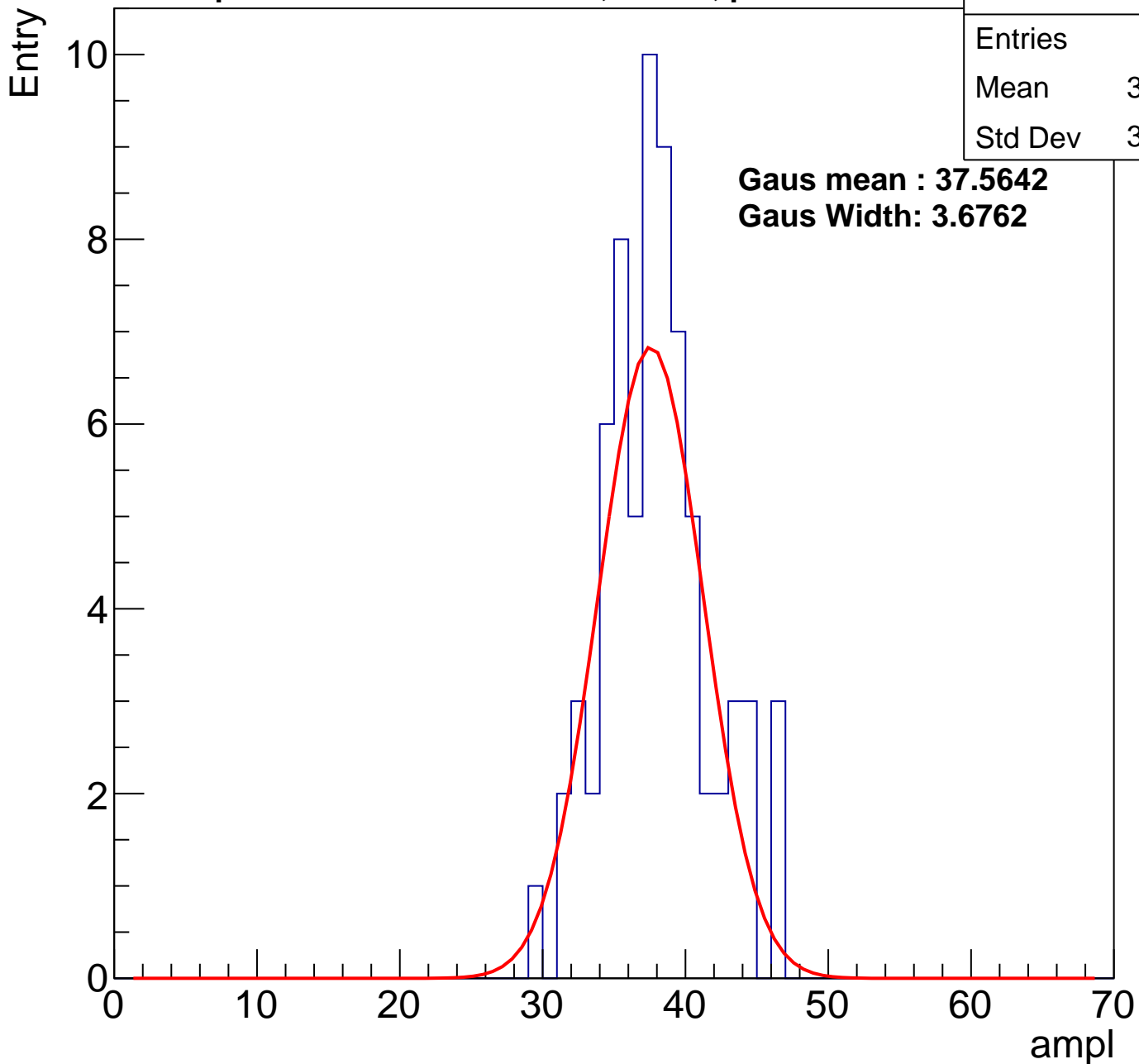
B1L102S, U10-ch13, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	37.56
Std Dev	3.703

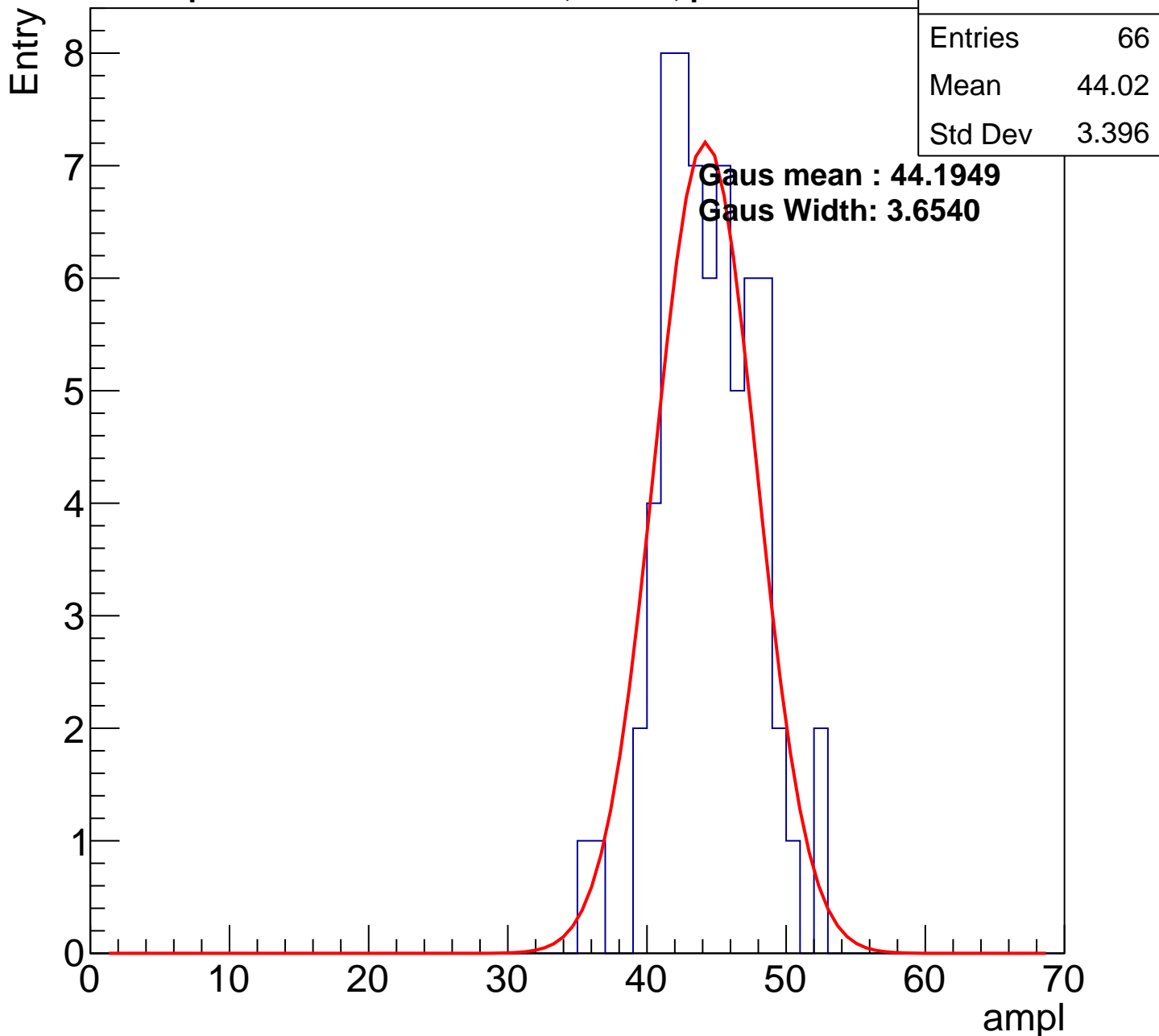
Gaus mean : 37.5642

Gaus Width: 3.6762



B1L102S, U10-ch13, adc2

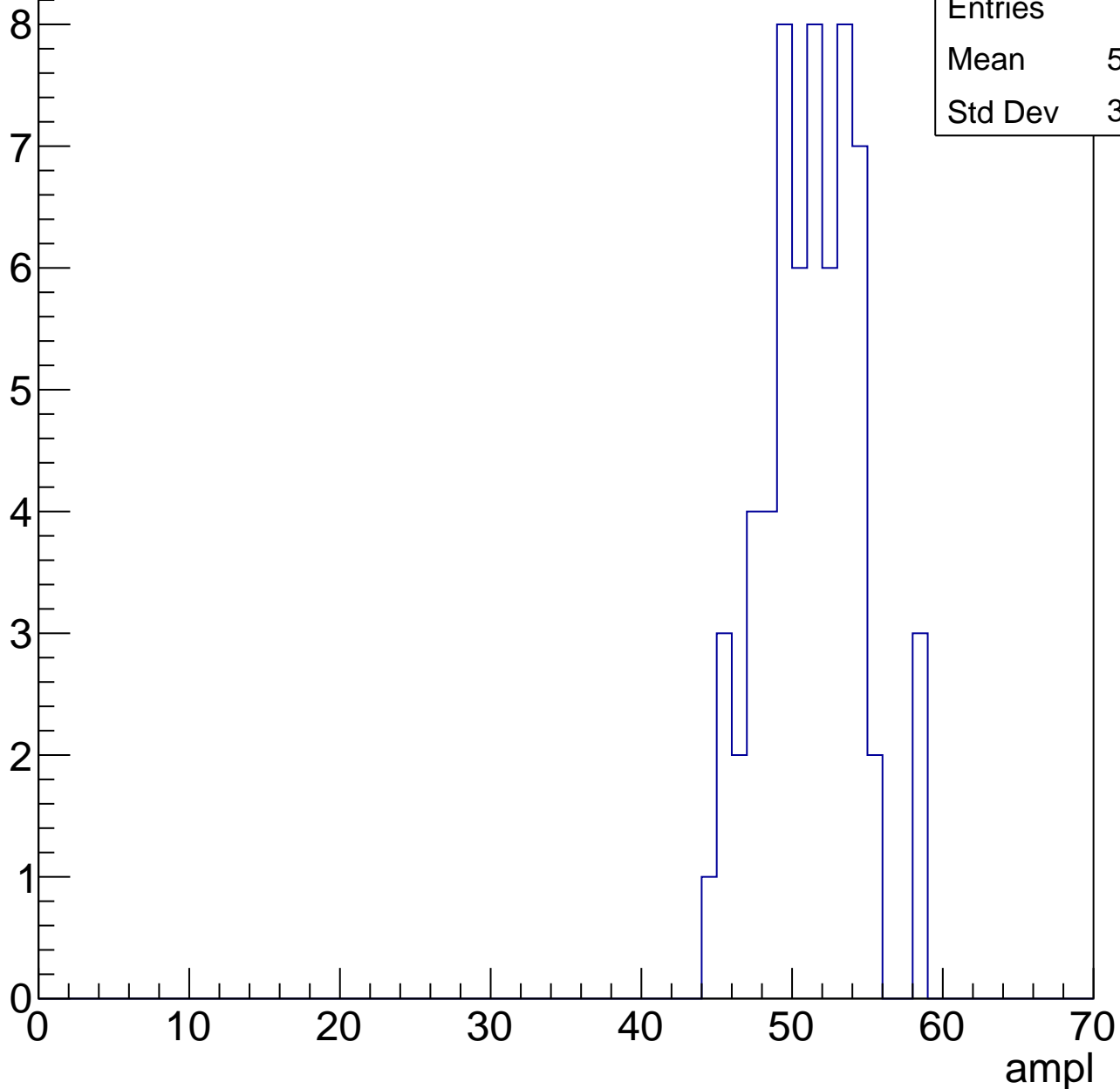
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U10-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



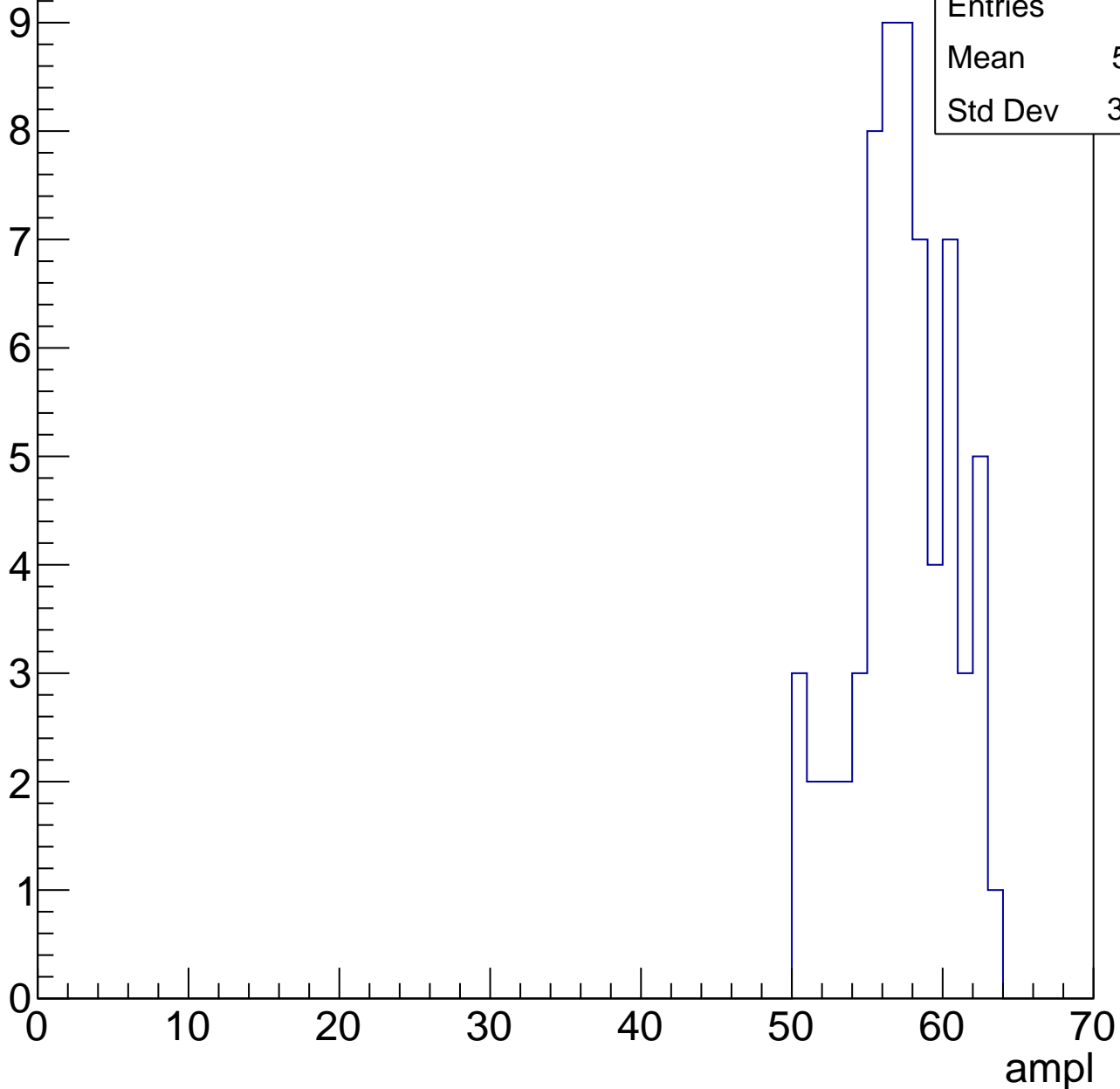
Entries	62
Mean	50.79
Std Dev	3.168

B1L102S, U10-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	56.91
Std Dev	3.195

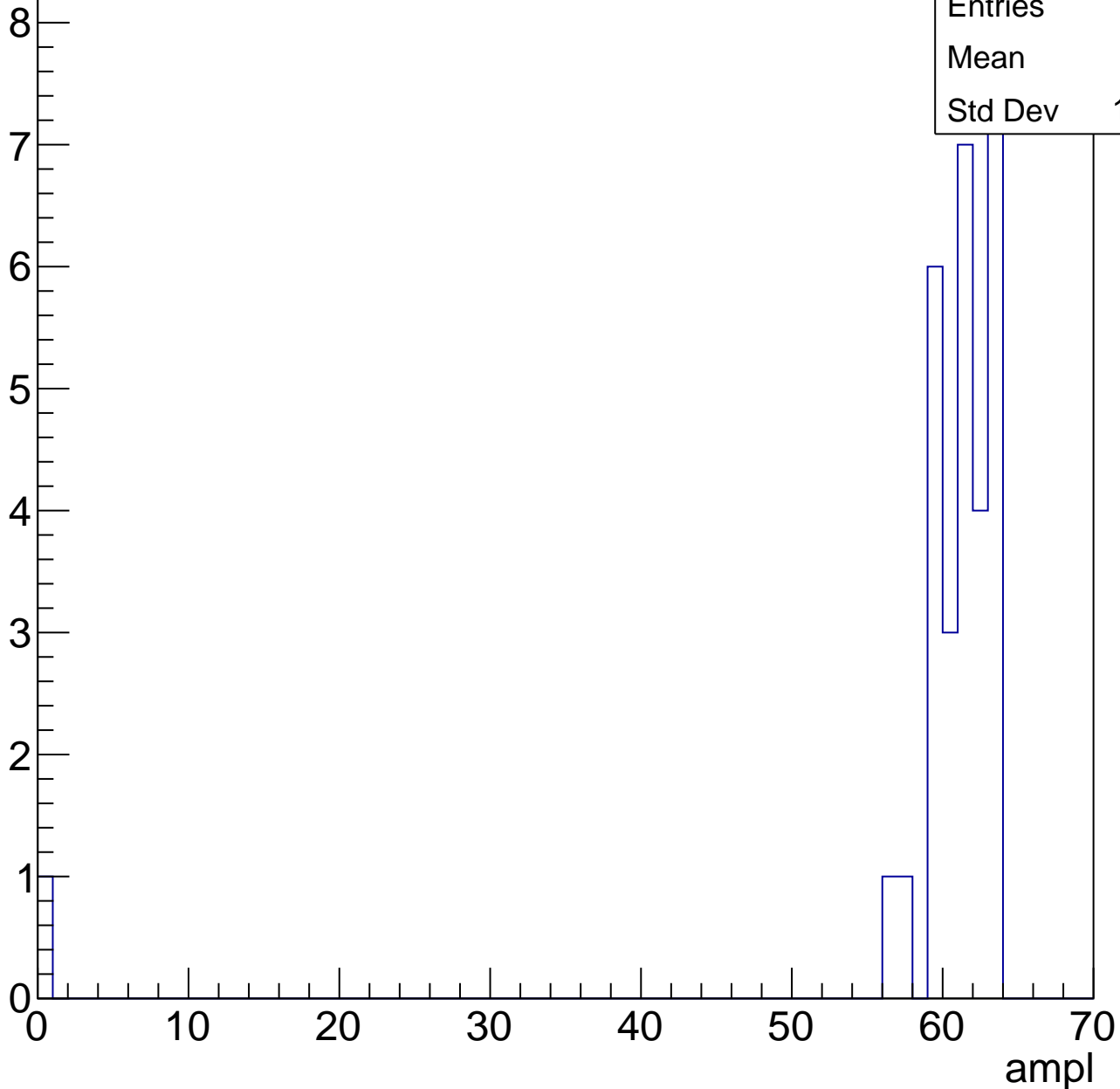


B1L102S, U10-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	58.9
Std Dev	10.91



B1L102S, U10-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L102S, U10-ch14, adc0

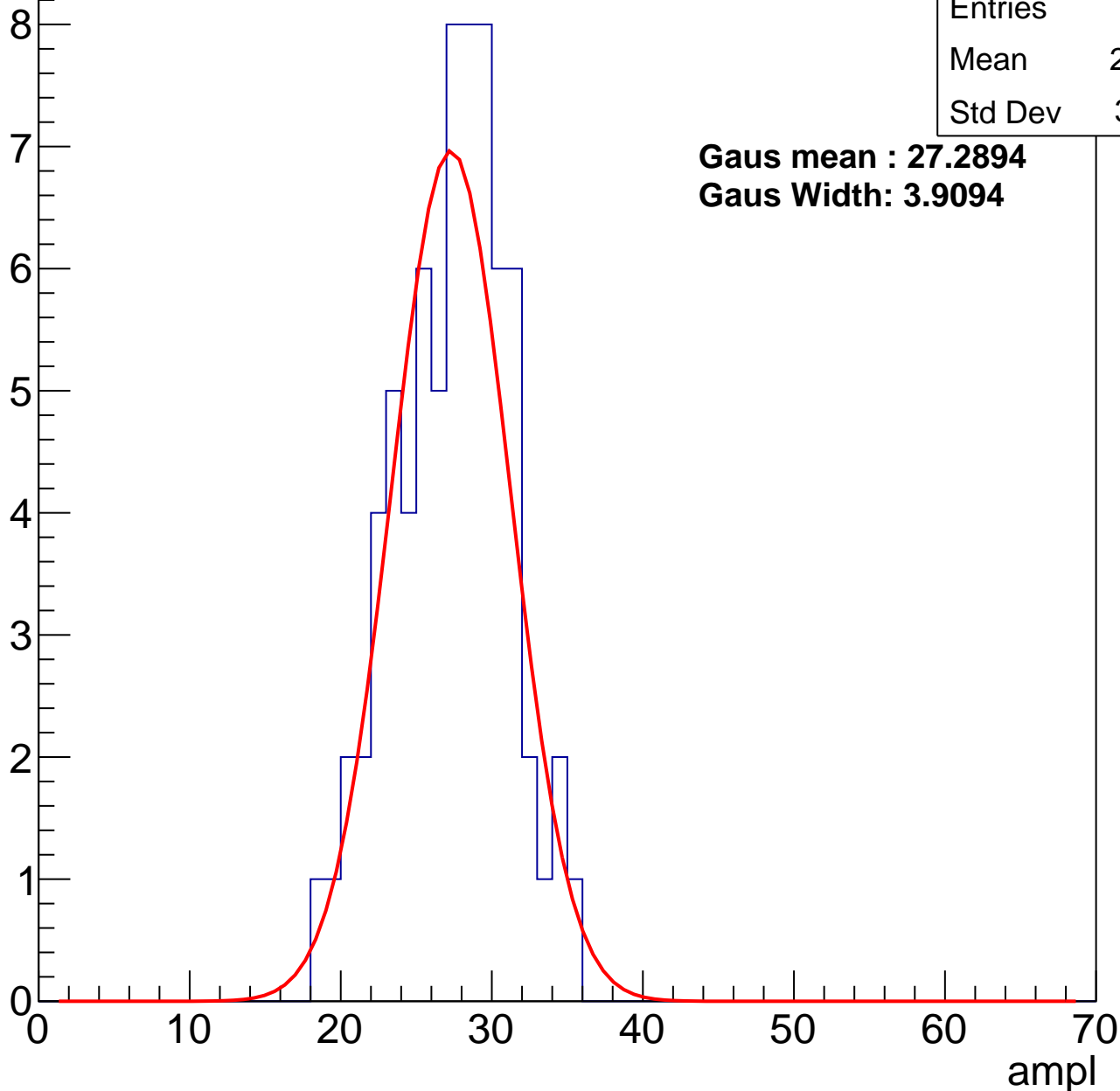
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.89
Std Dev	3.721

Gaus mean : 27.2894

Gaus Width: 3.9094



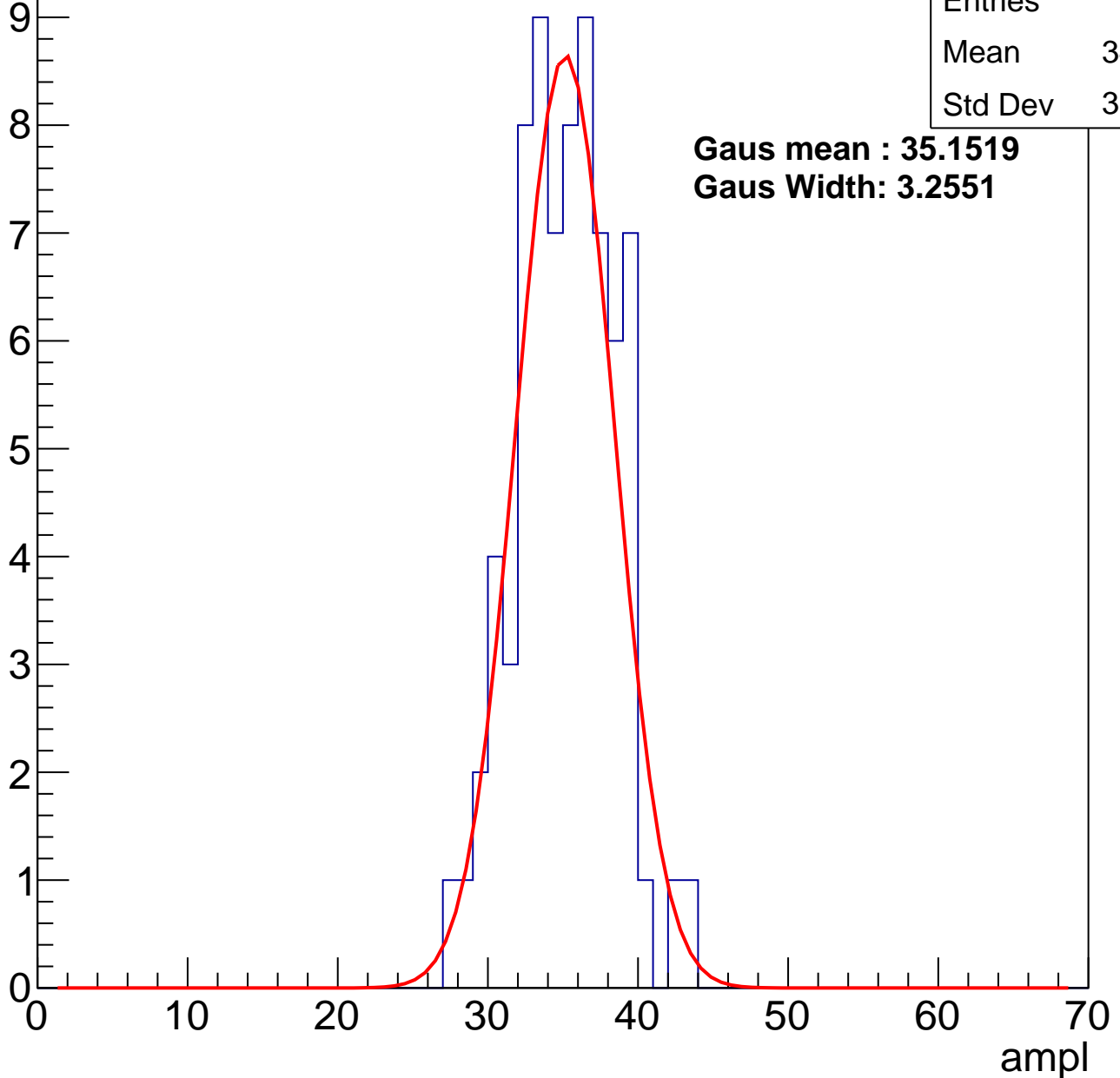
B1L102S, U10-ch14, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.75
Std Dev	3.234

Gaus mean : 35.1519
Gaus Width: 3.2551



B1L102S, U10-ch14, adc2

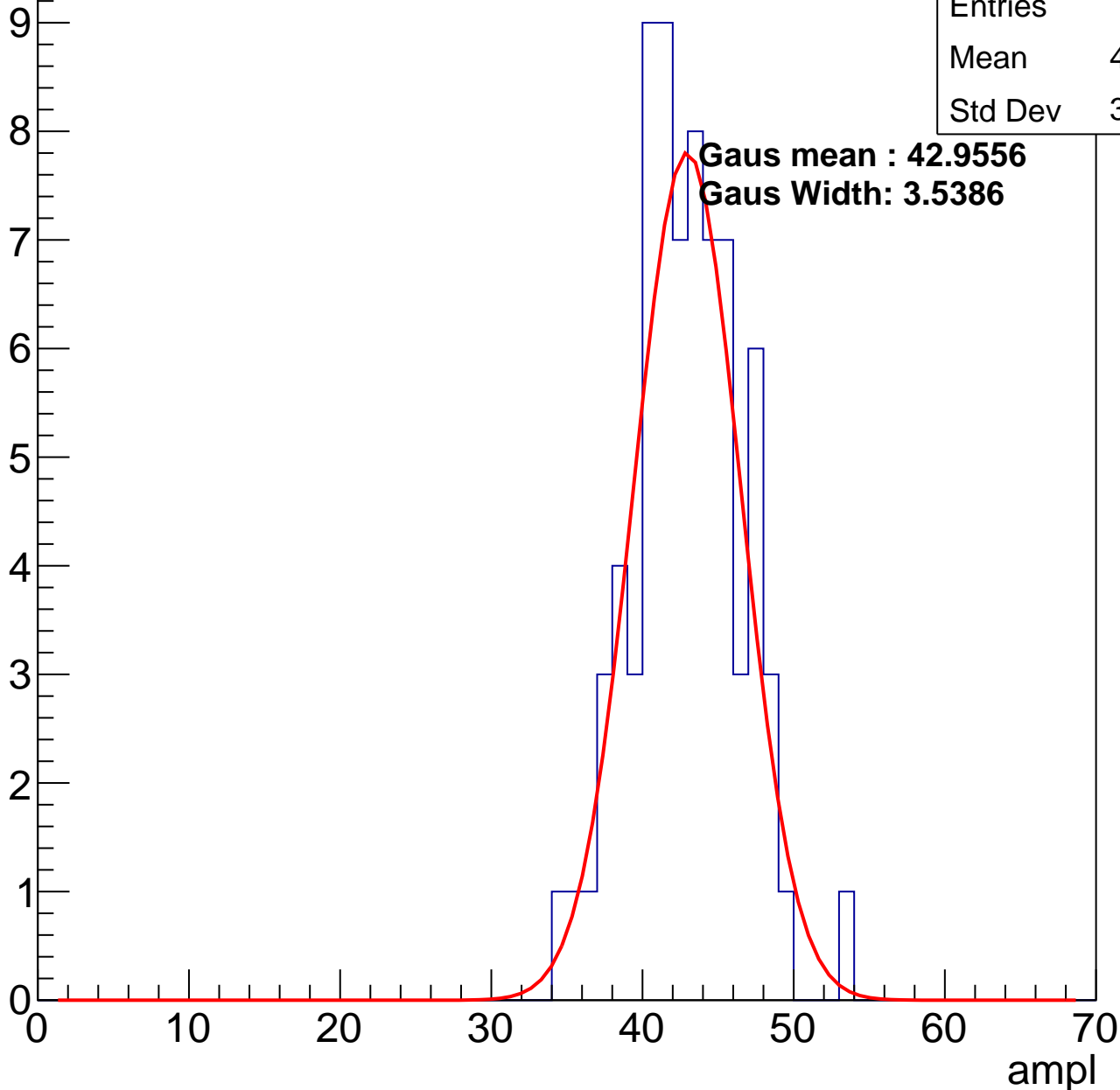
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.45
Std Dev	3.527

Gaus mean : 42.9556

Gaus Width: 3.5386

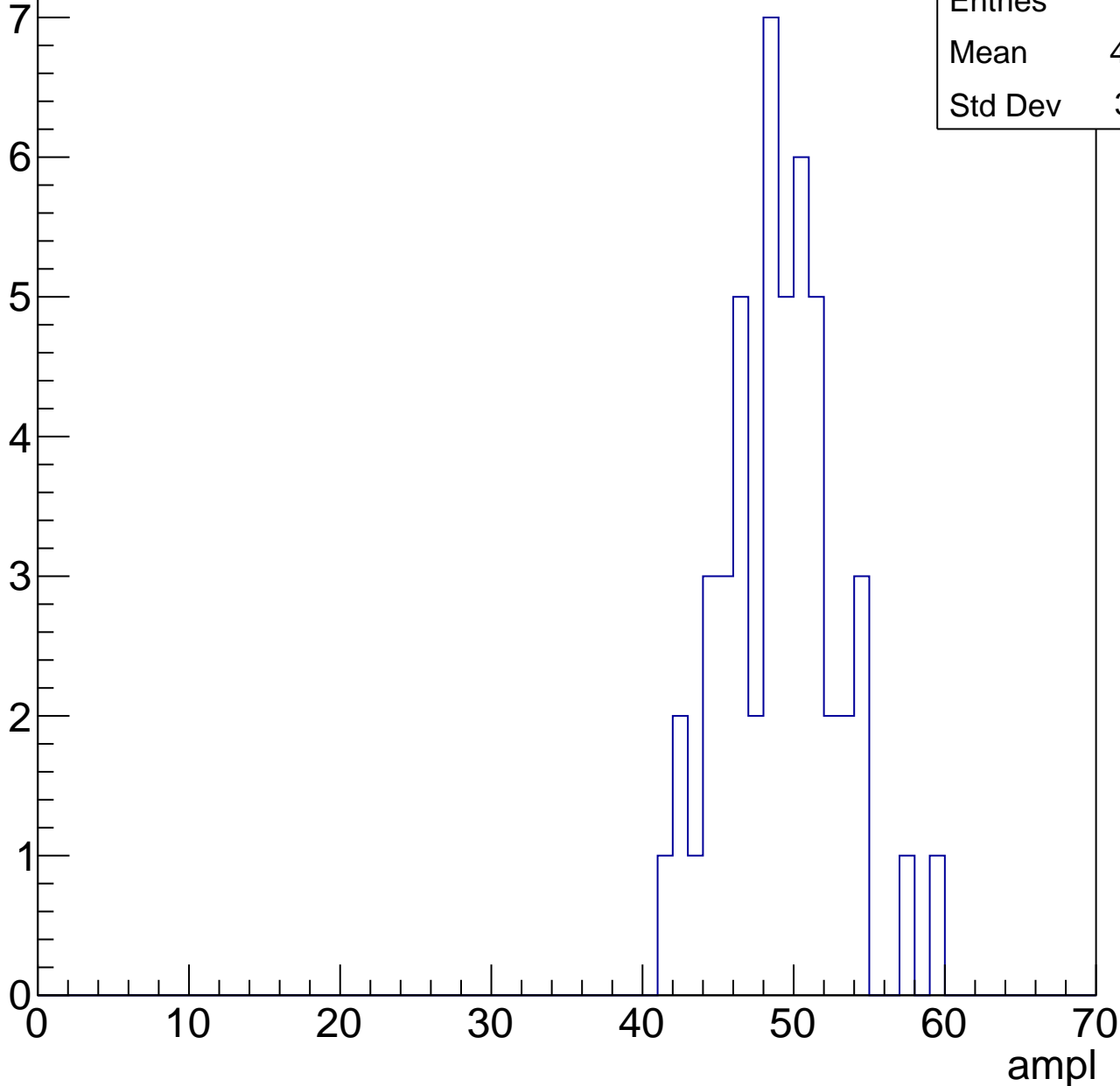


B1L102S, U10-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	48.63
Std Dev	3.751



B1L102S, U10-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	53.48
Std Dev	3.325

Entry

10

8

6

4

2

0

0

10

20

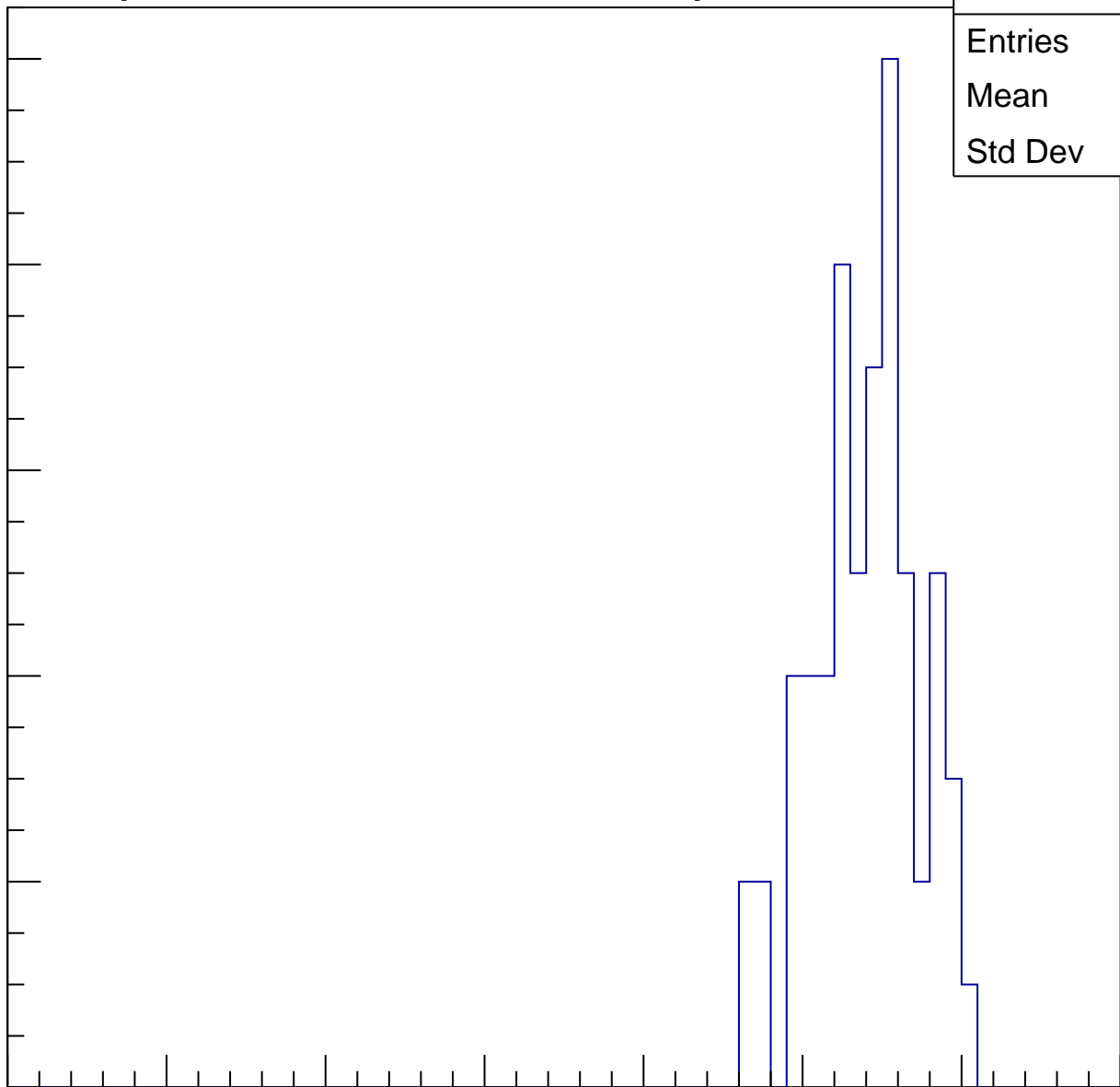
30

40

50

60

ampl

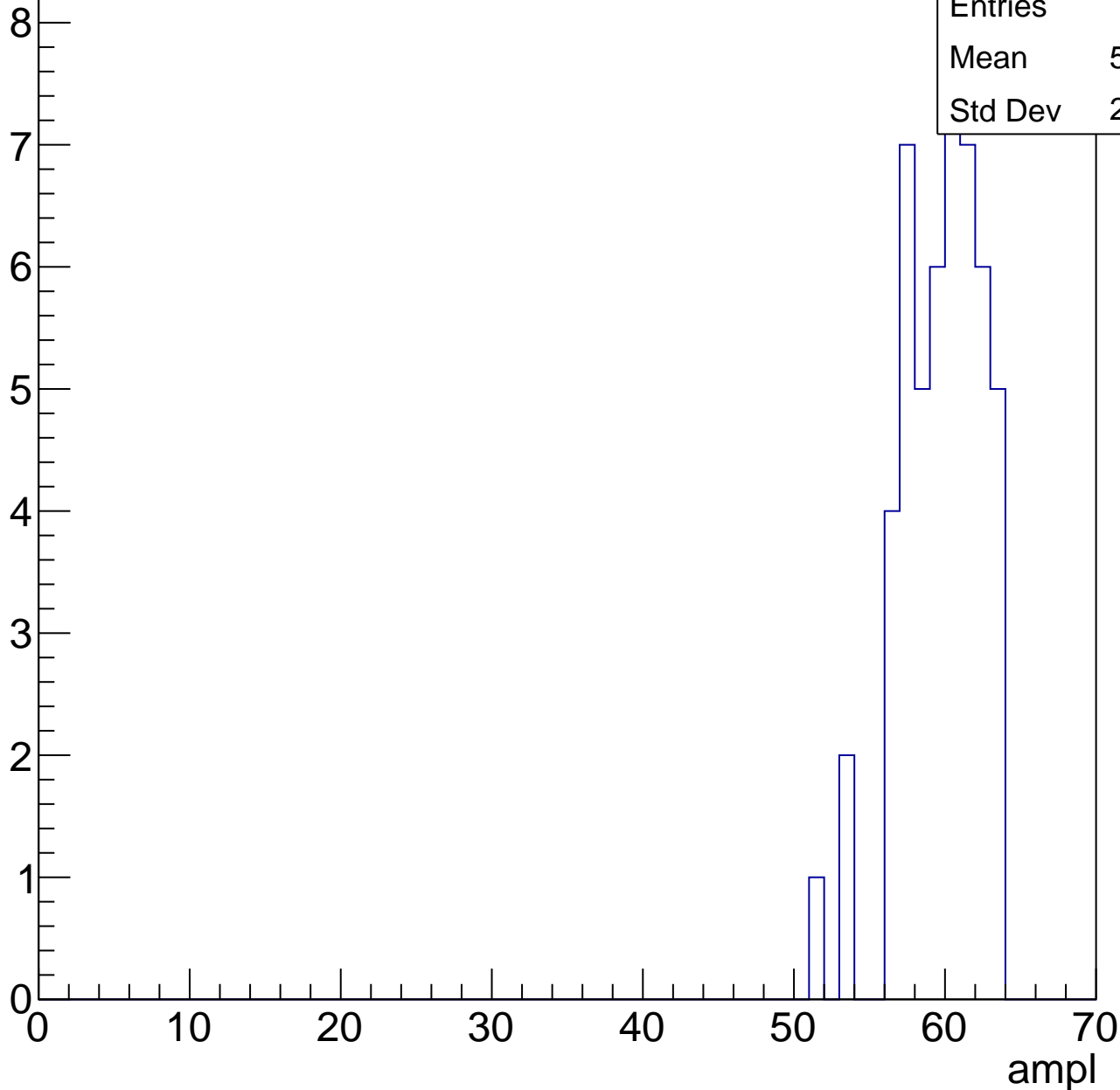


B1L102S, U10-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.18
Std Dev	2.706

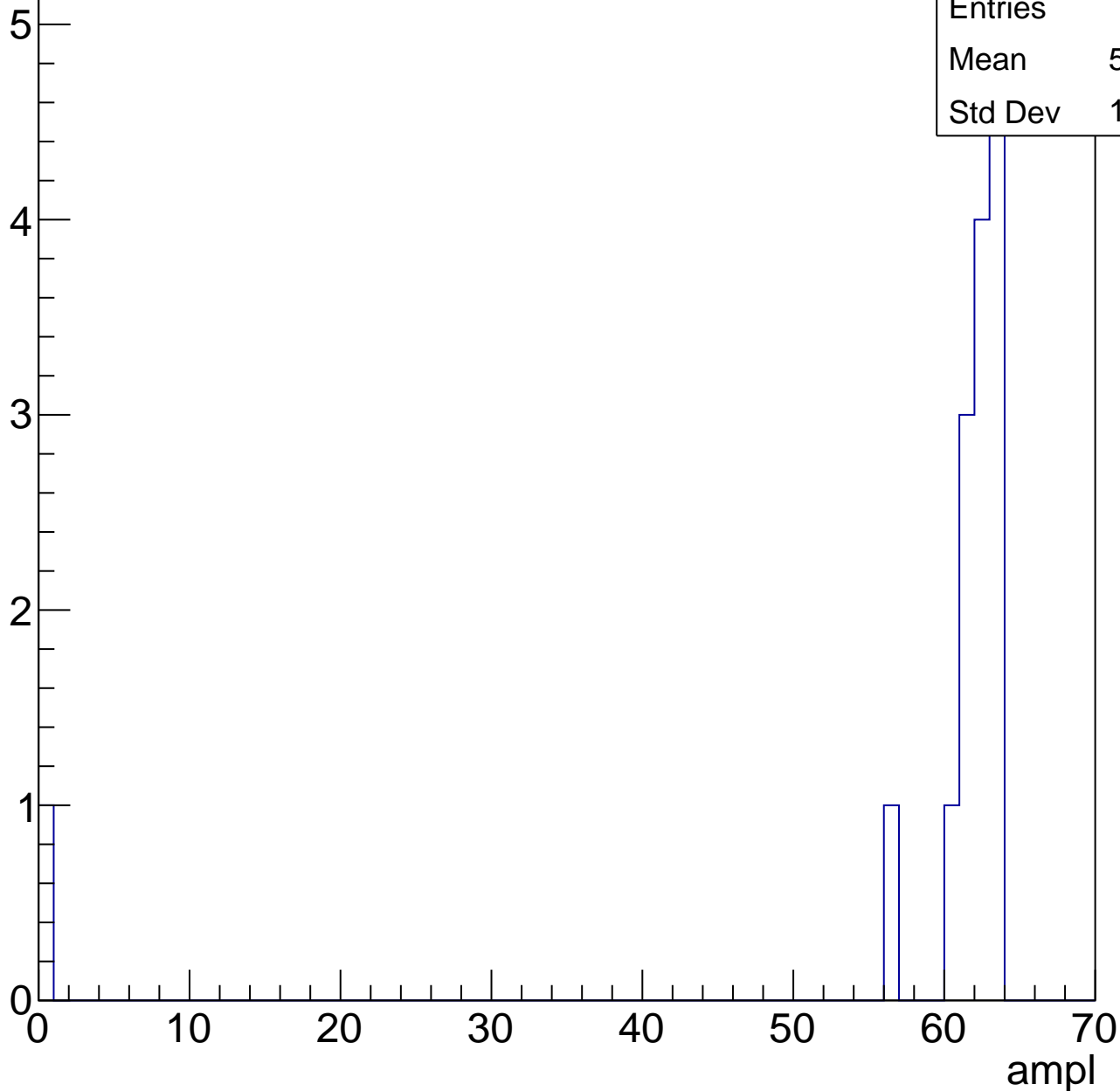


B1L102S, U10-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.47
Std Dev	15.46



B1L102S, U10-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch15, adc0

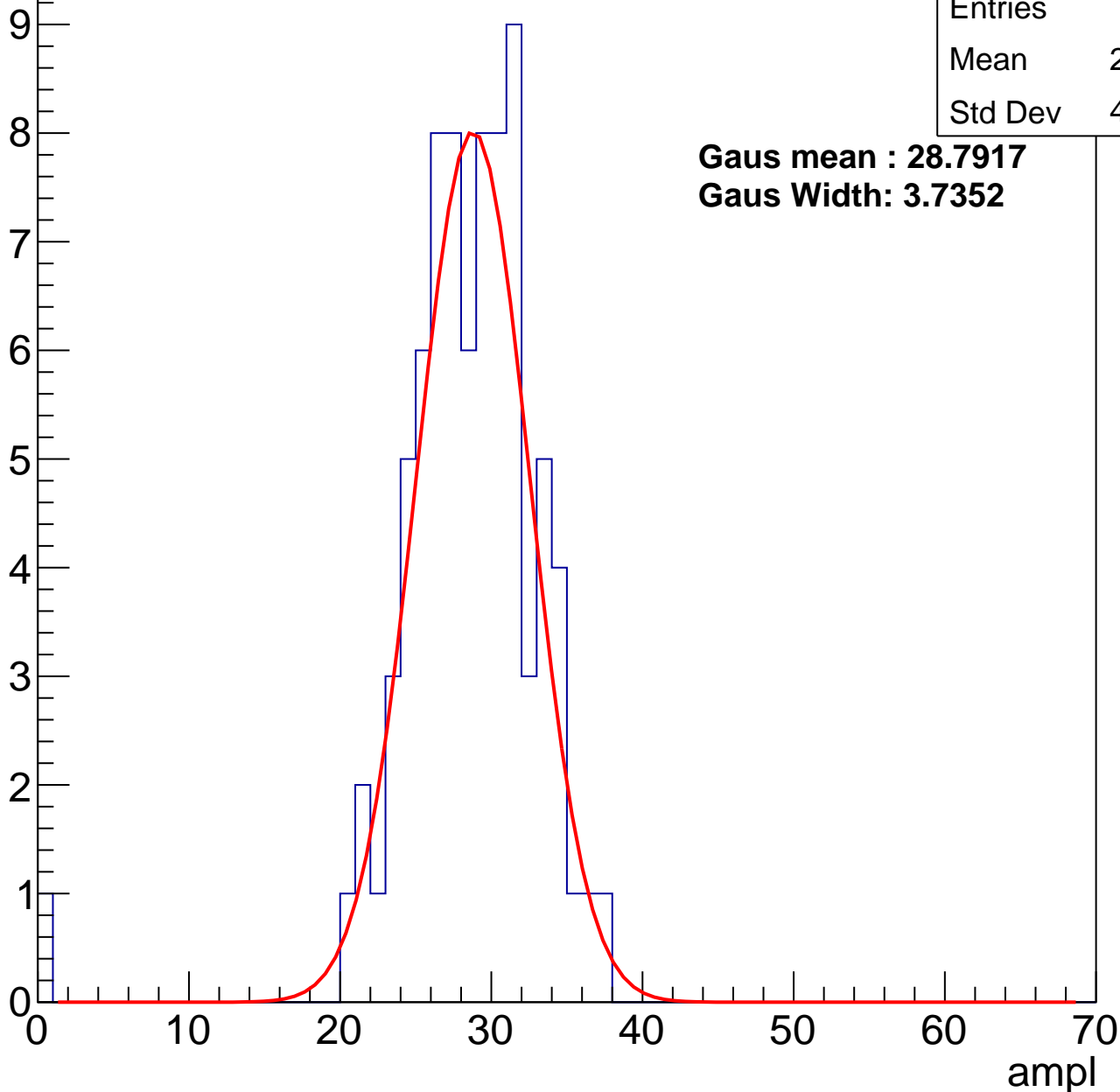
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.04
Std Dev	4.796

Gaus mean : 28.7917

Gaus Width: 3.7352



B1L102S, U10-ch15, adc1

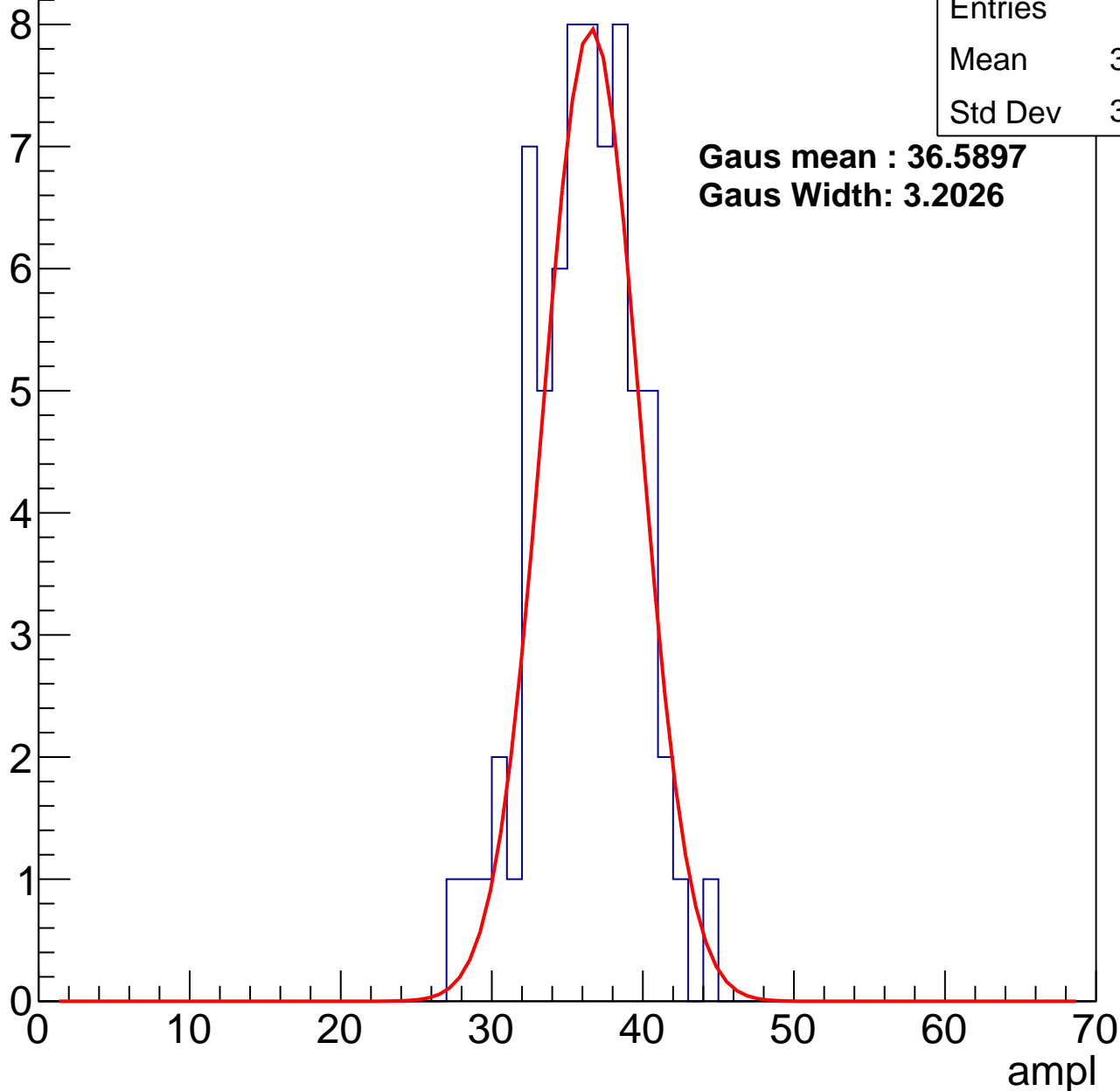
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.68
Std Dev	3.377

Gaus mean : 36.5897

Gaus Width: 3.2026



B1L102S, U10-ch15, adc2

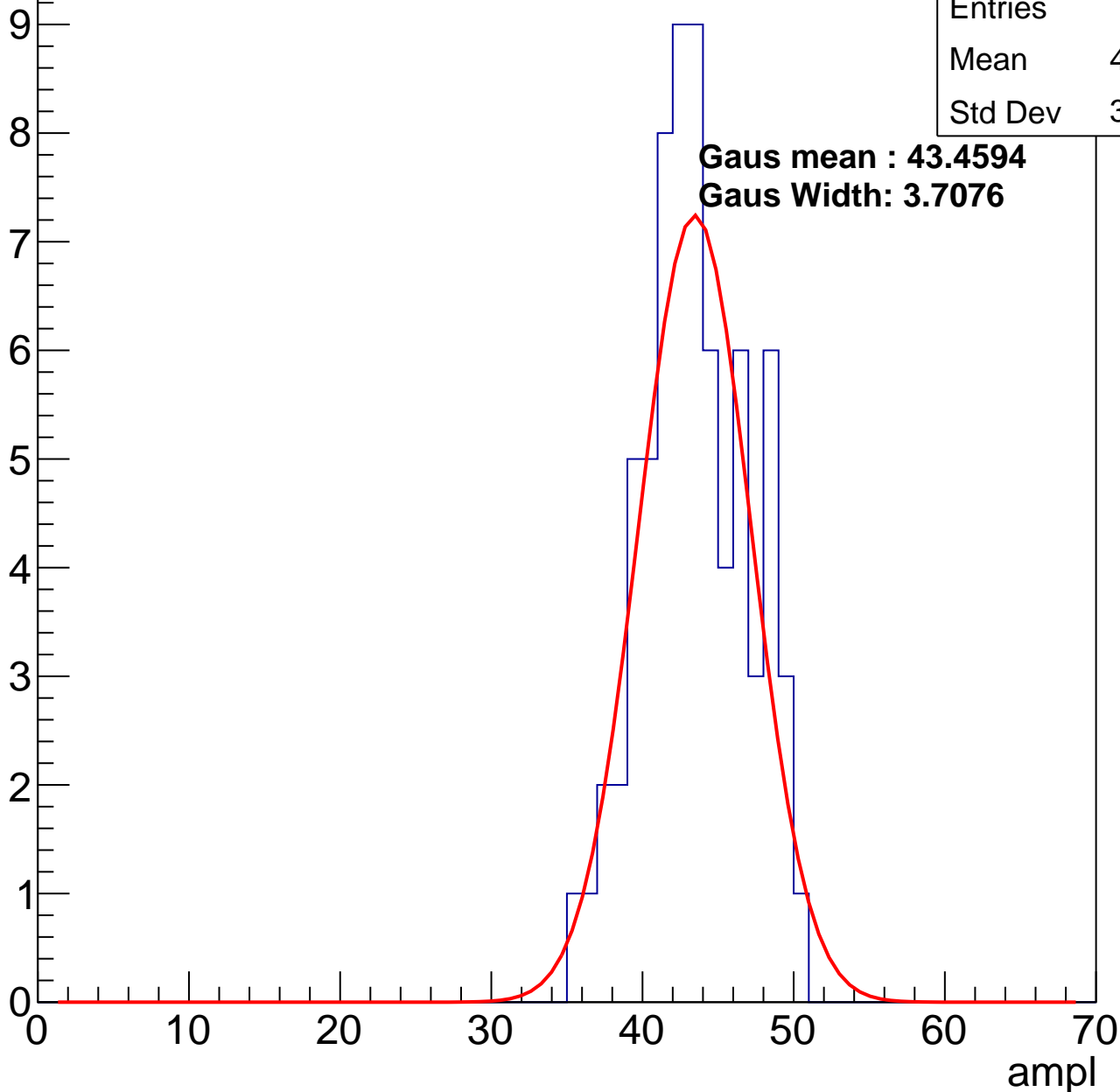
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.03
Std Dev	3.423

Gaus mean : 43.4594

Gaus Width: 3.7076

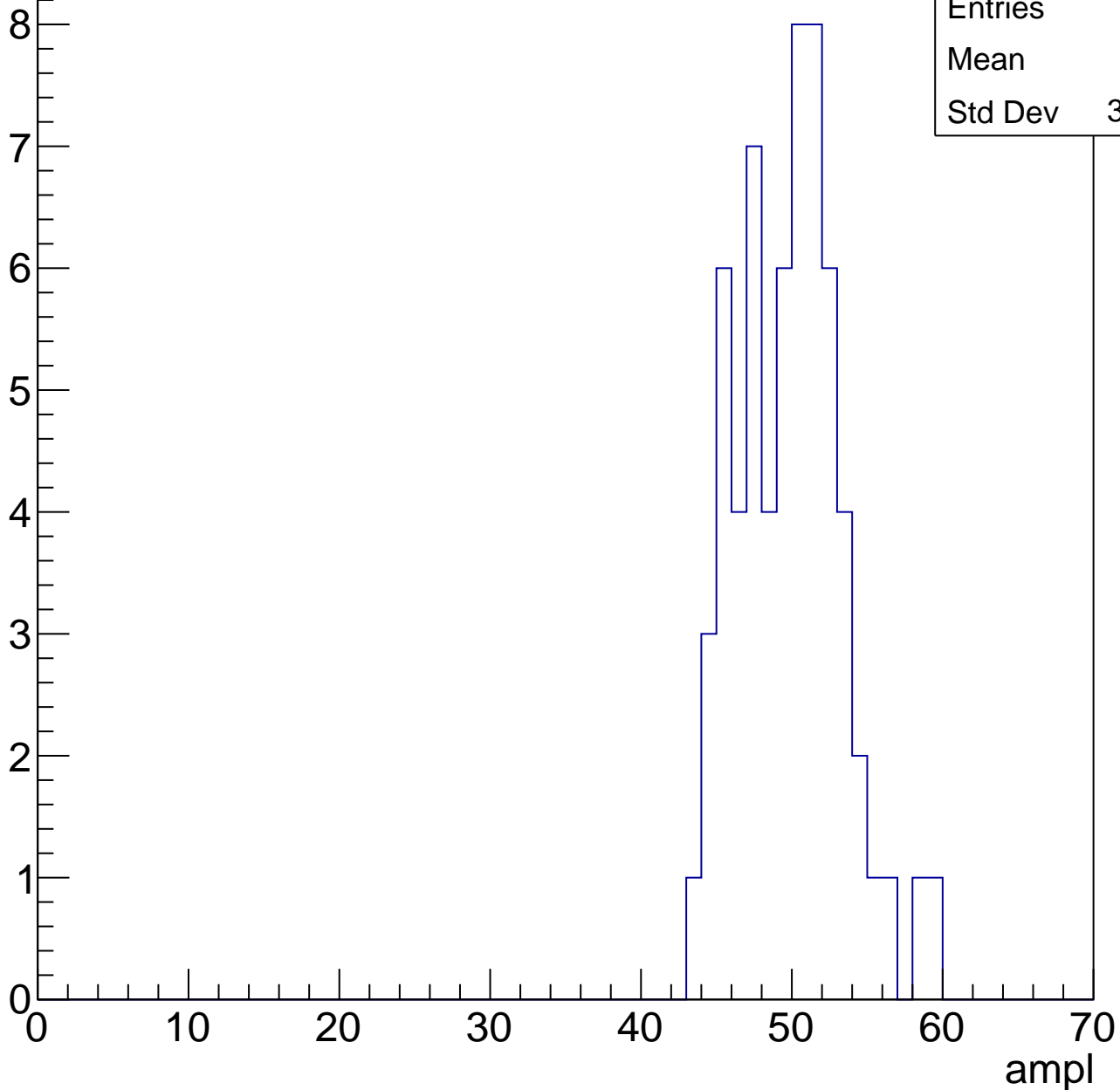


B1L102S, U10-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

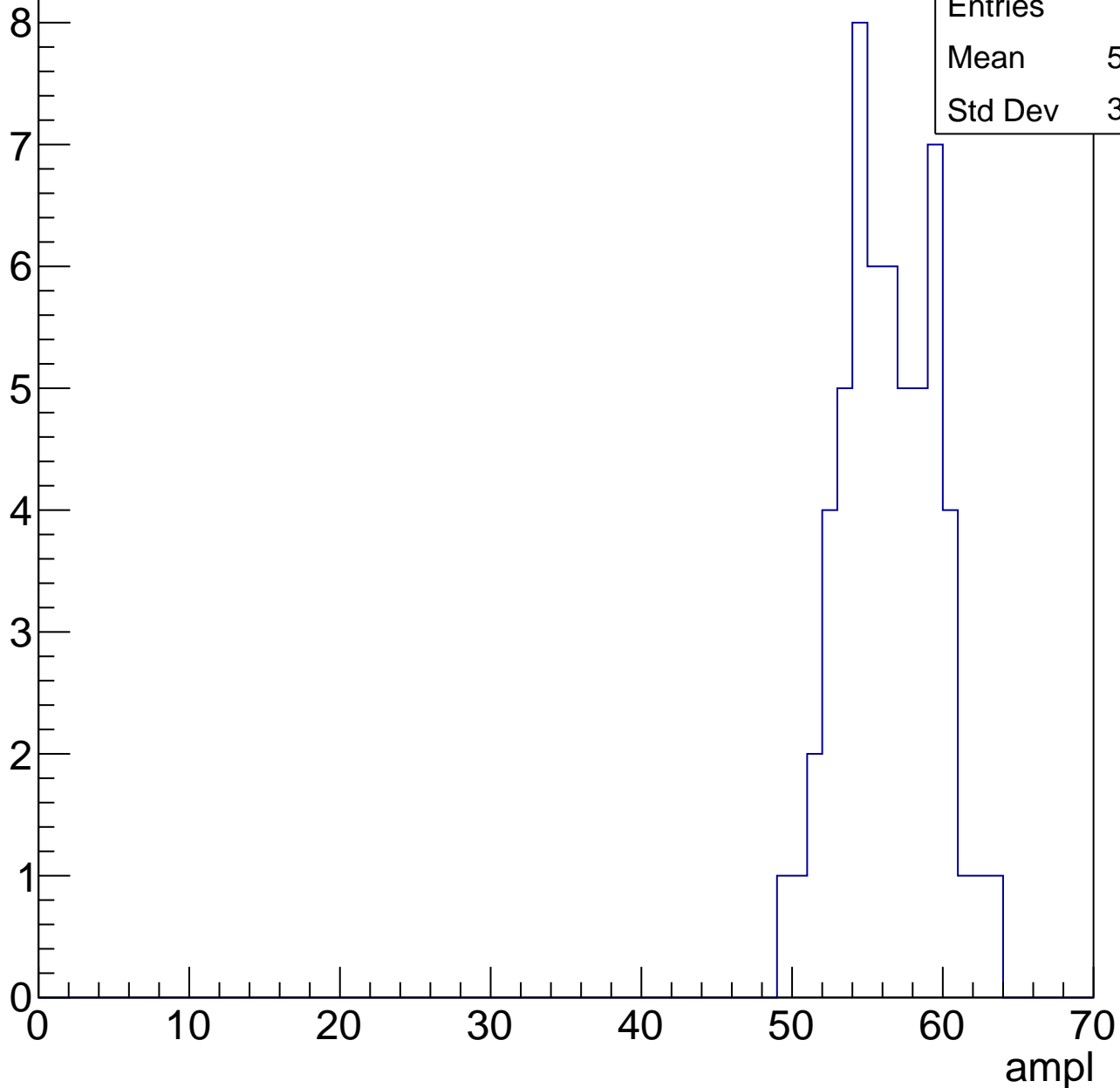
Entries	63
Mean	49.4
Std Dev	3.425



B1L102S, U10-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



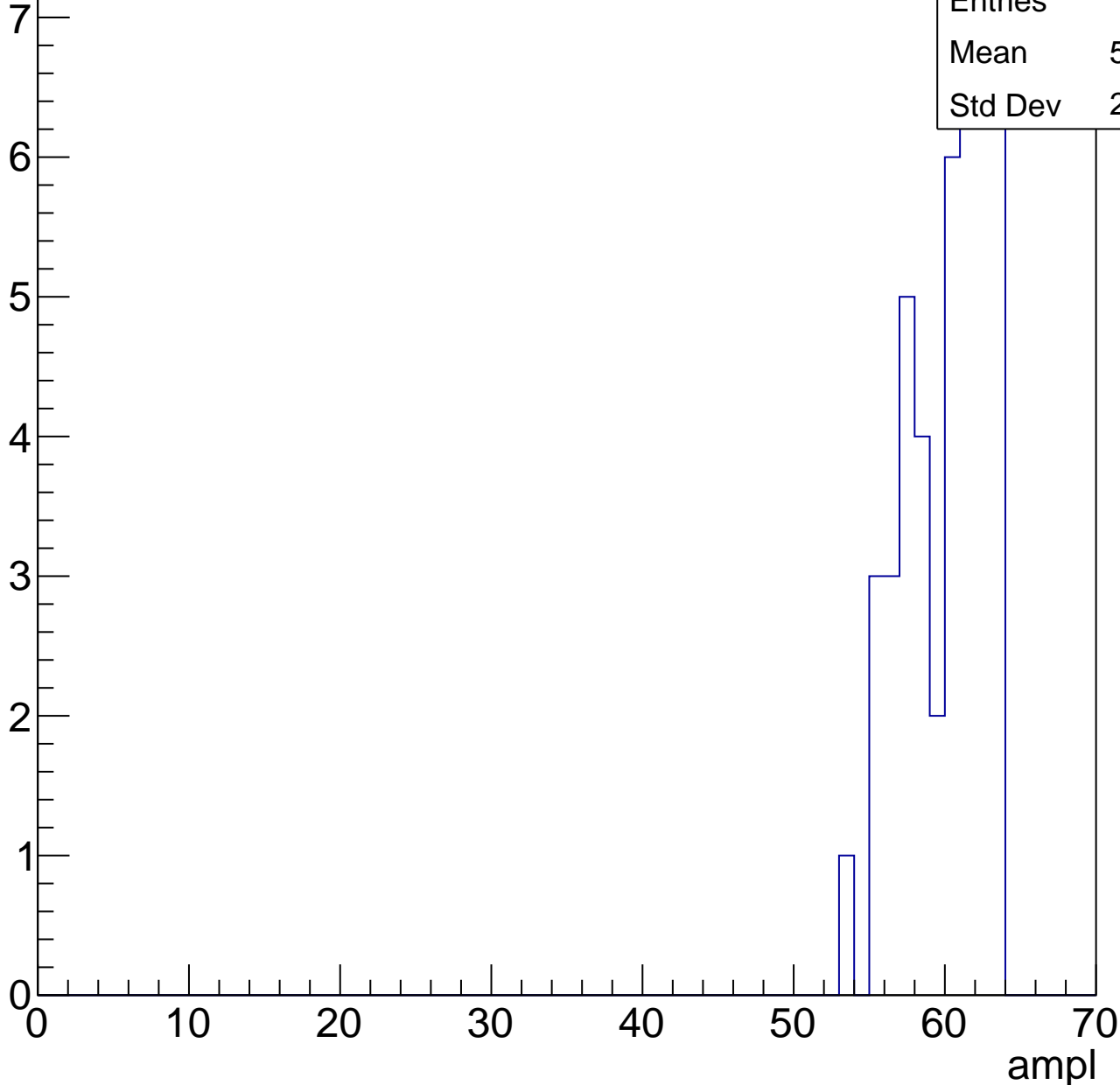
Entries	57
Mean	55.89
Std Dev	3.088

B1L102S, U10-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

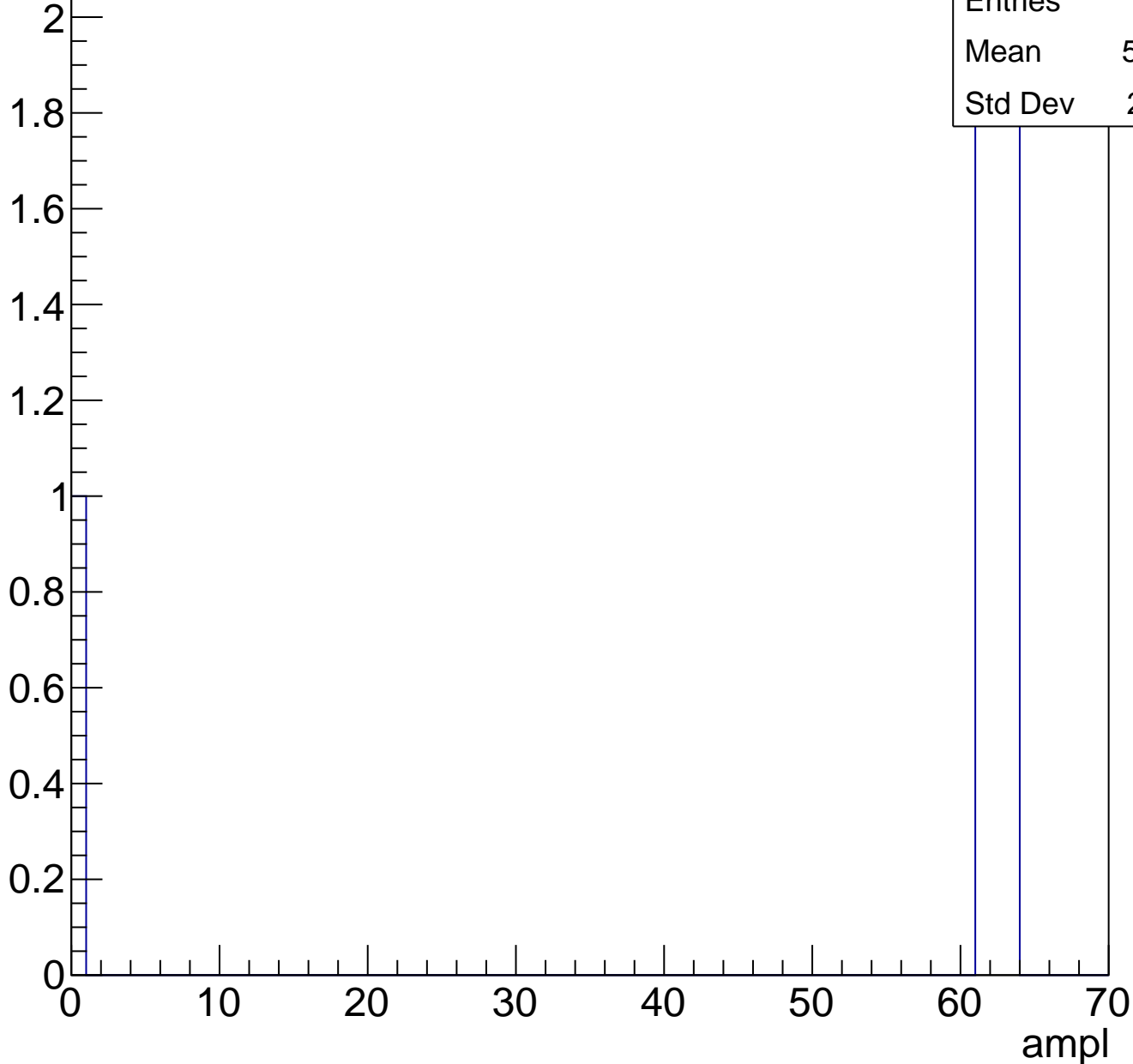
Entries	45
Mean	59.62
Std Dev	2.694



B1L102S, U10-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U10-ch16, adc0

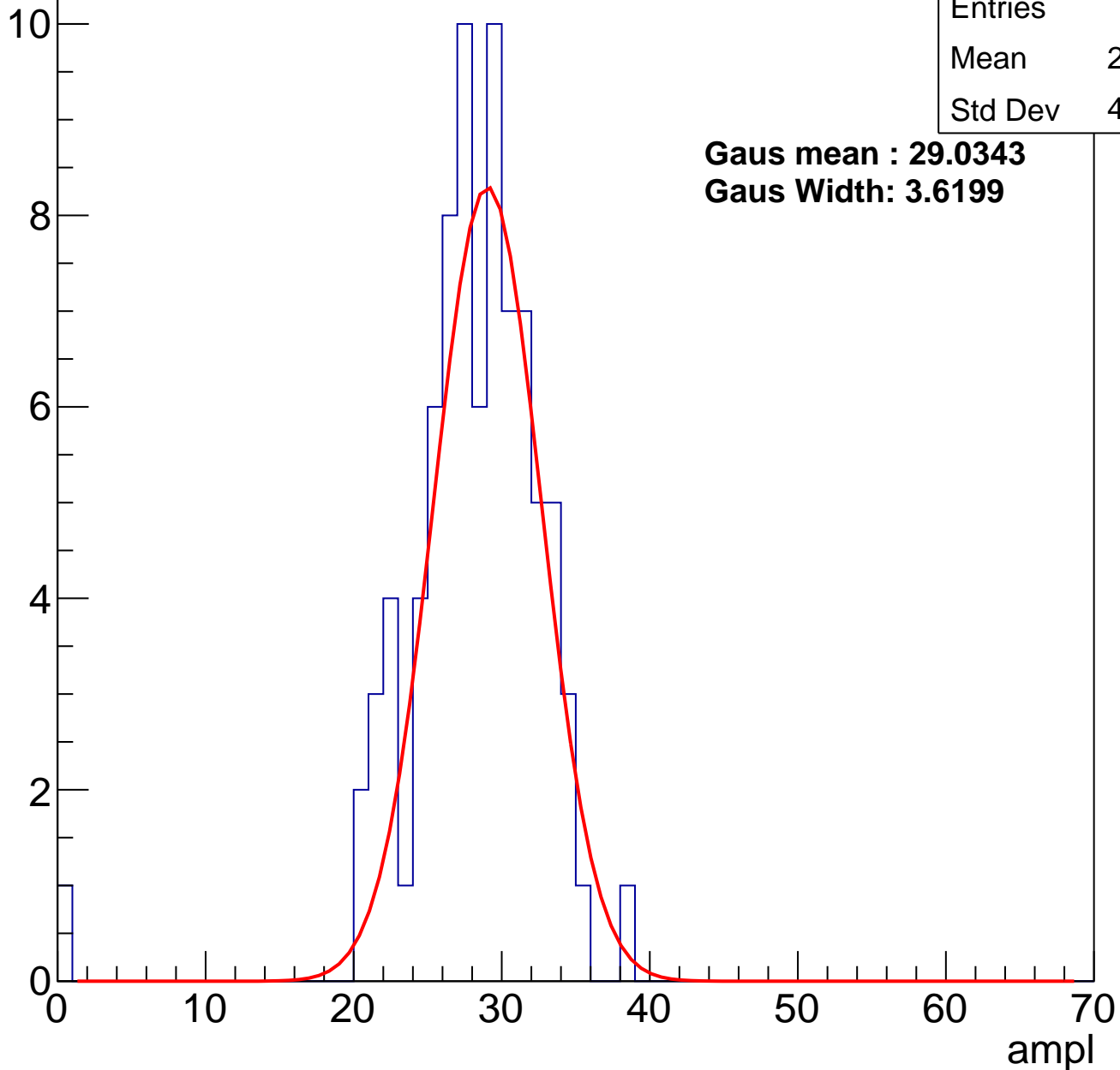
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	27.65
Std Dev	4.807

Gaus mean : 29.0343

Gaus Width: 3.6199

Entry



B1L102S, U10-ch16, adc1

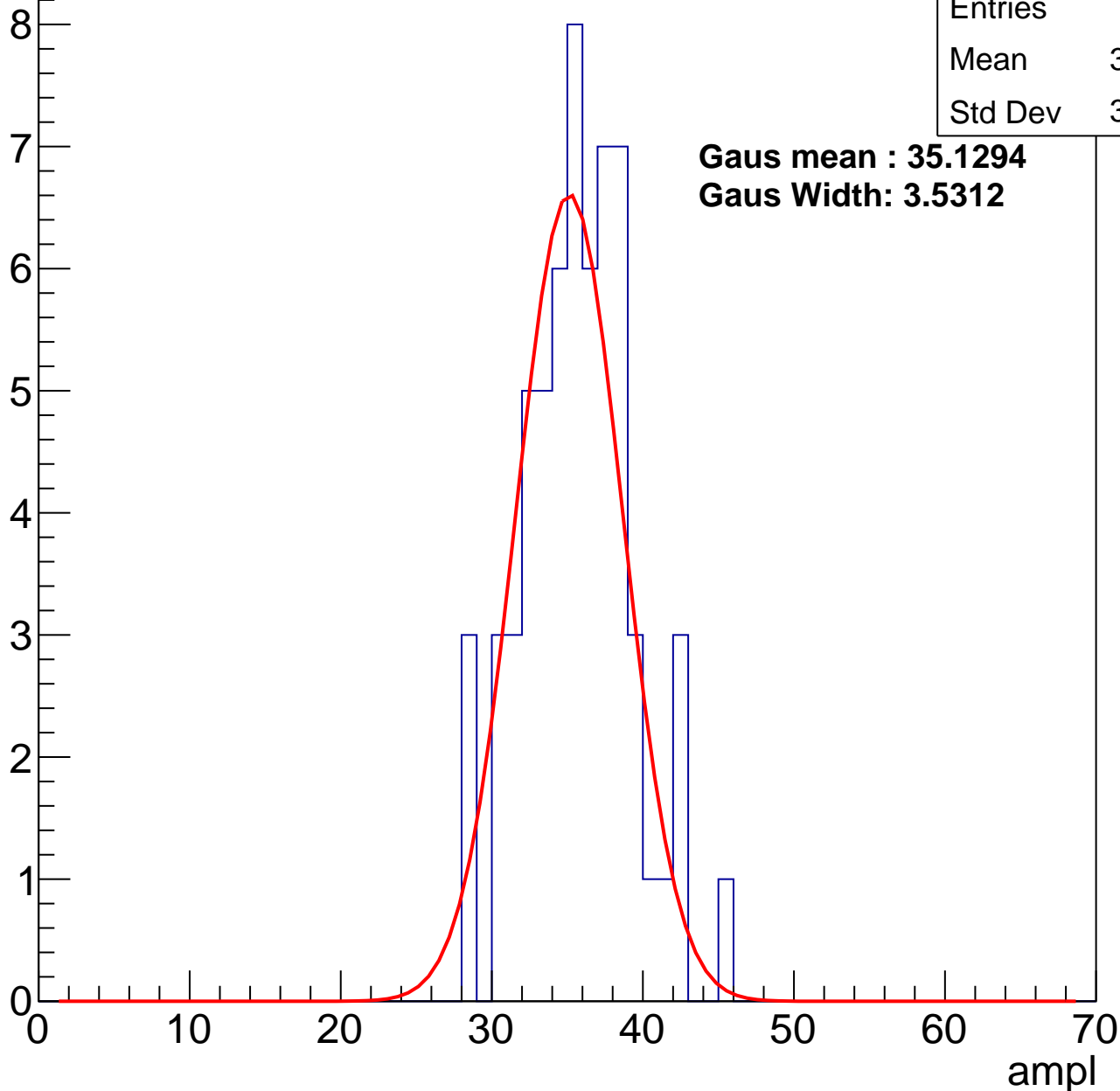
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.26
Std Dev	3.569

Gaus mean : 35.1294

Gaus Width: 3.5312



B1L102S, U10-ch16, adc2

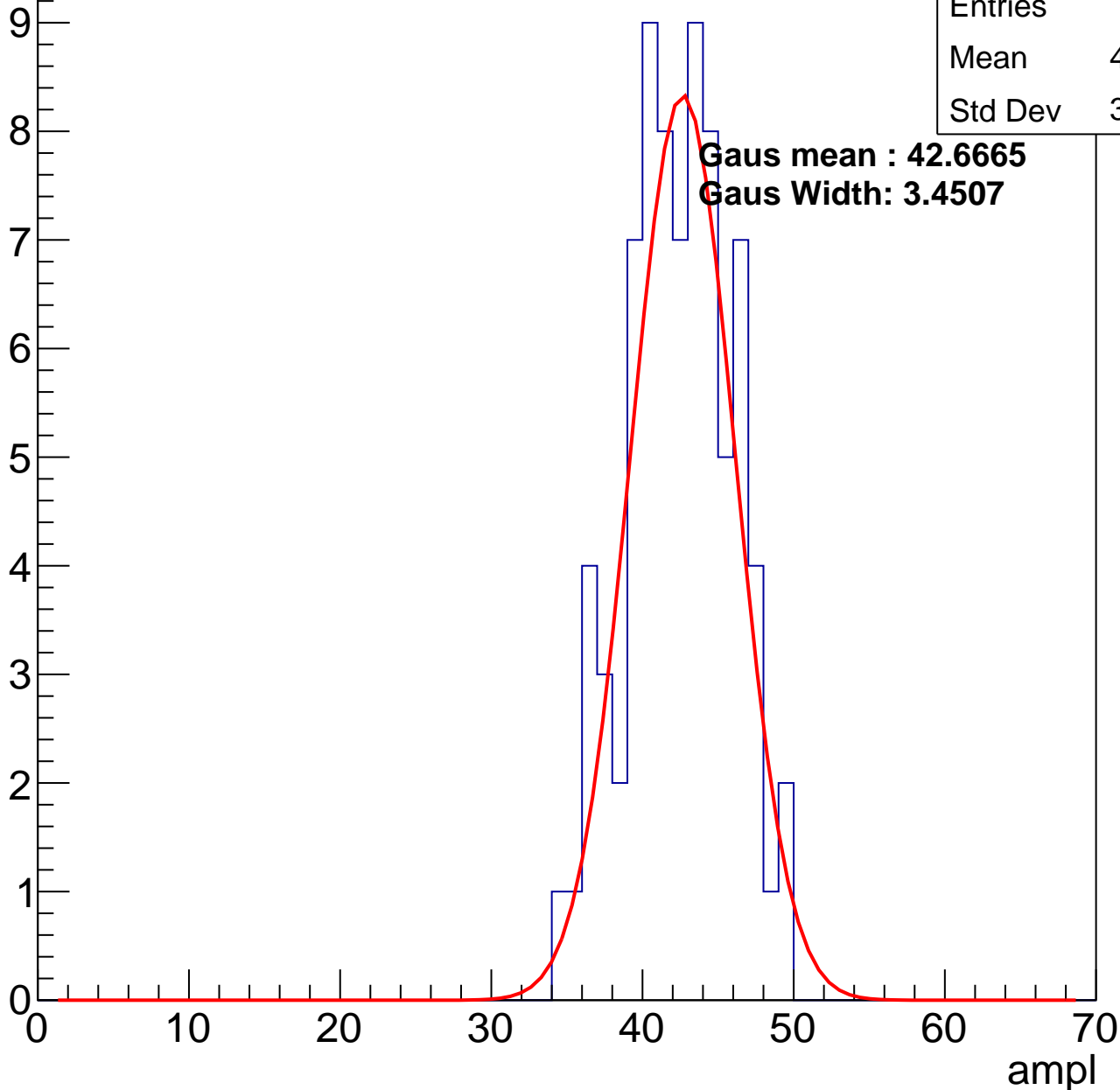
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.99
Std Dev	3.402

Gaus mean : 42.6665

Gaus Width: 3.4507

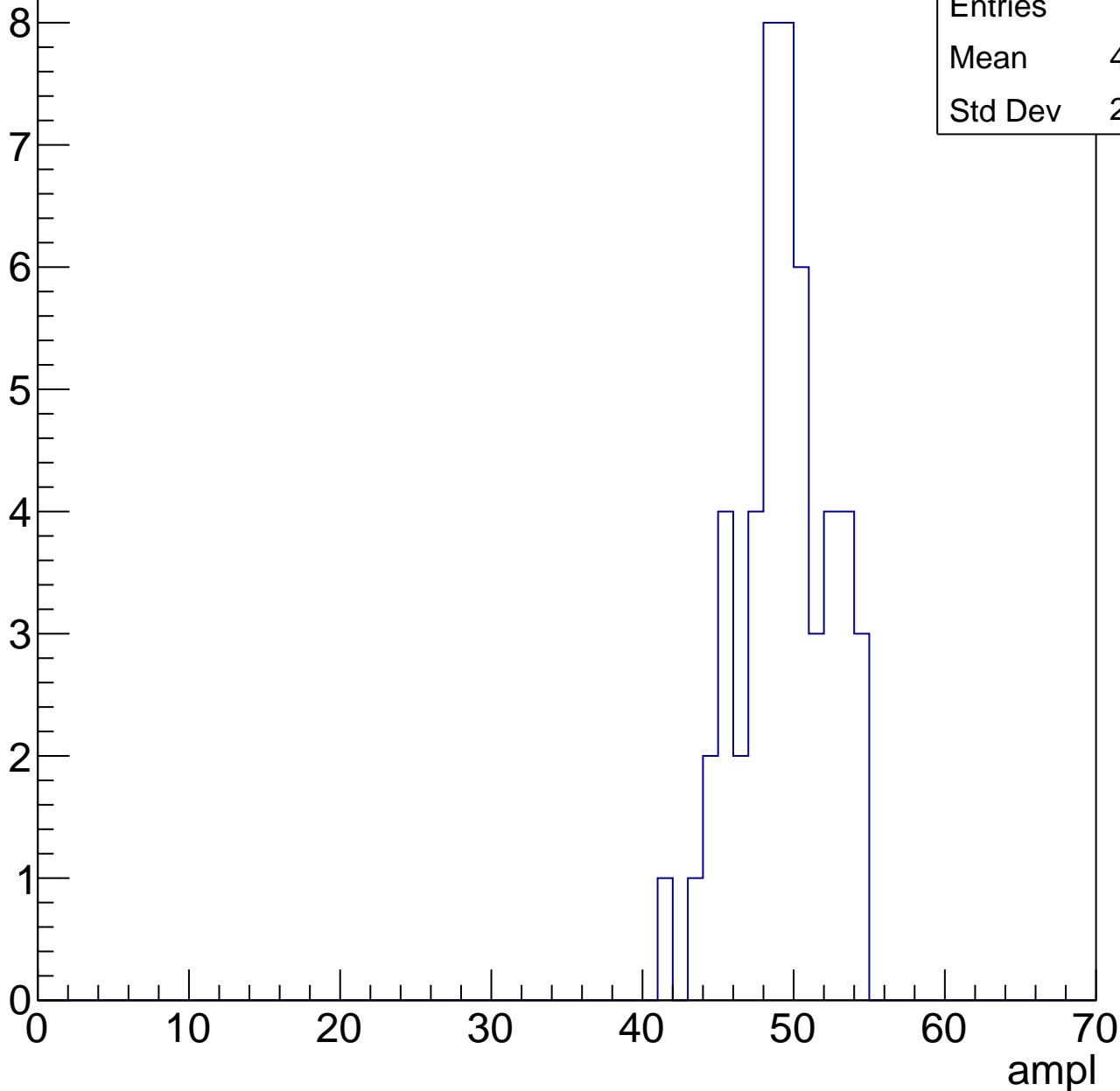


B1L102S, U10-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.86
Std Dev	2.993

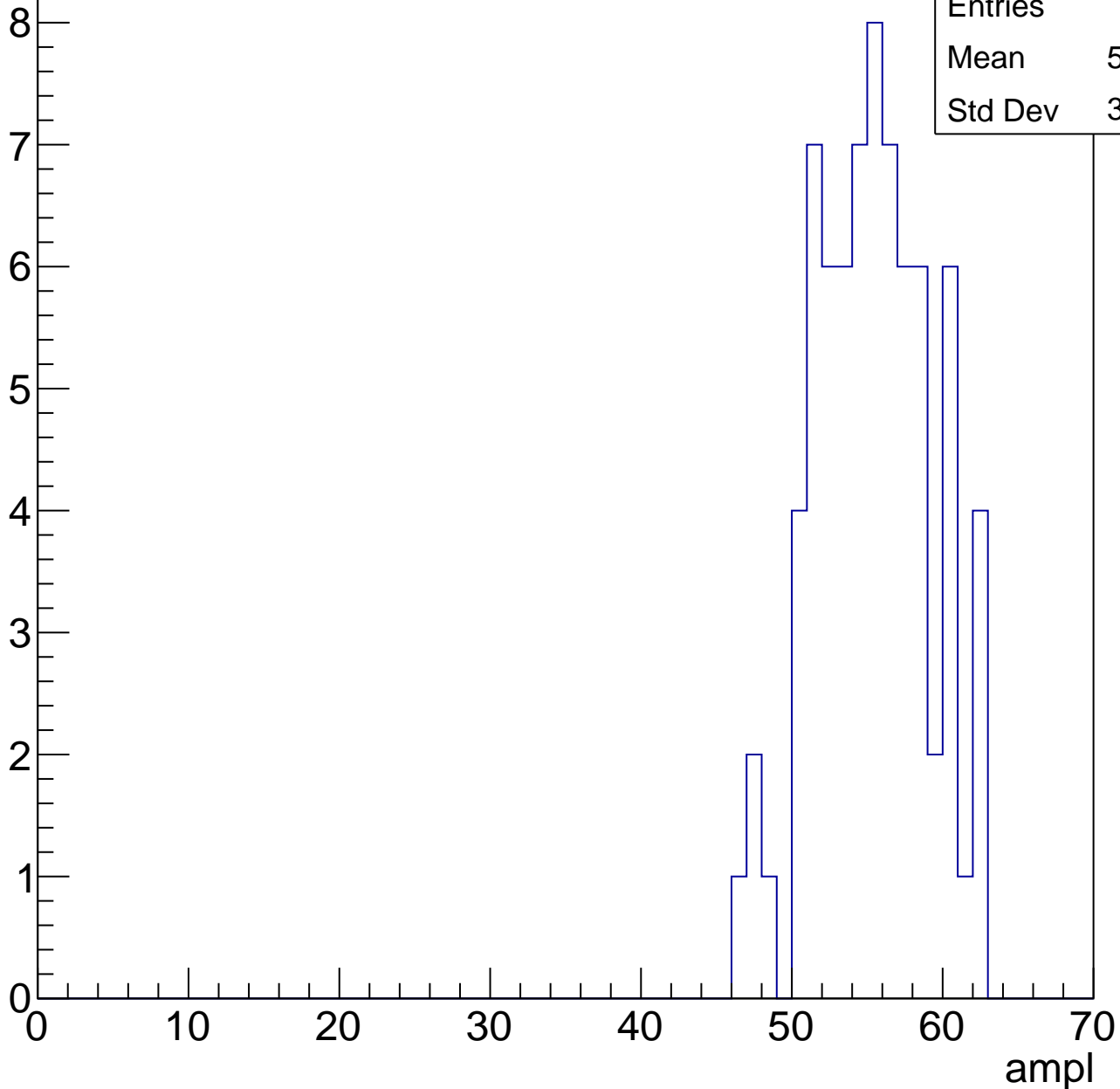


B1L102S, U10-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	54.89
Std Dev	3.772



B1L102S, U10-ch16, adc5

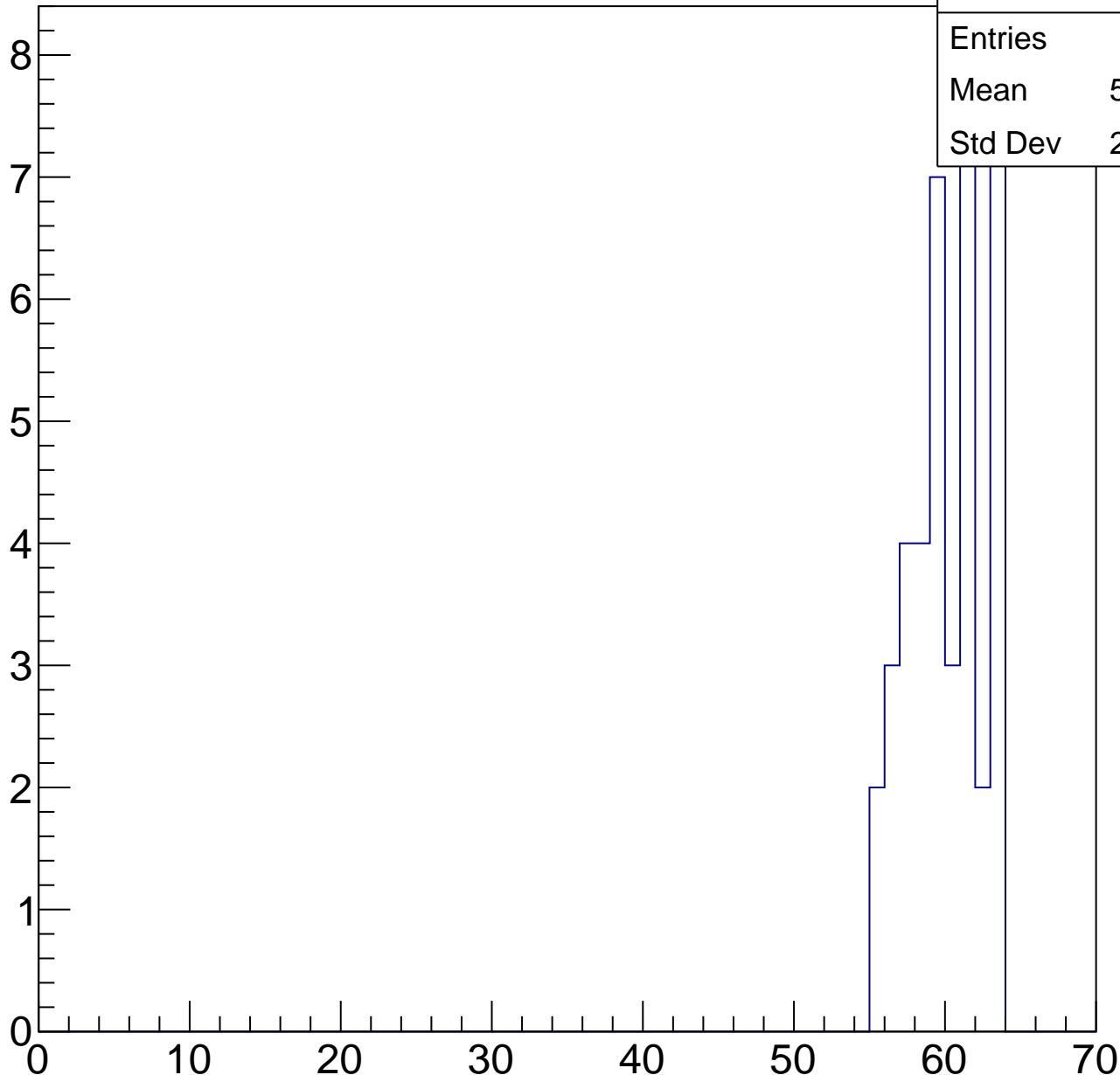
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.68
Std Dev	2.424

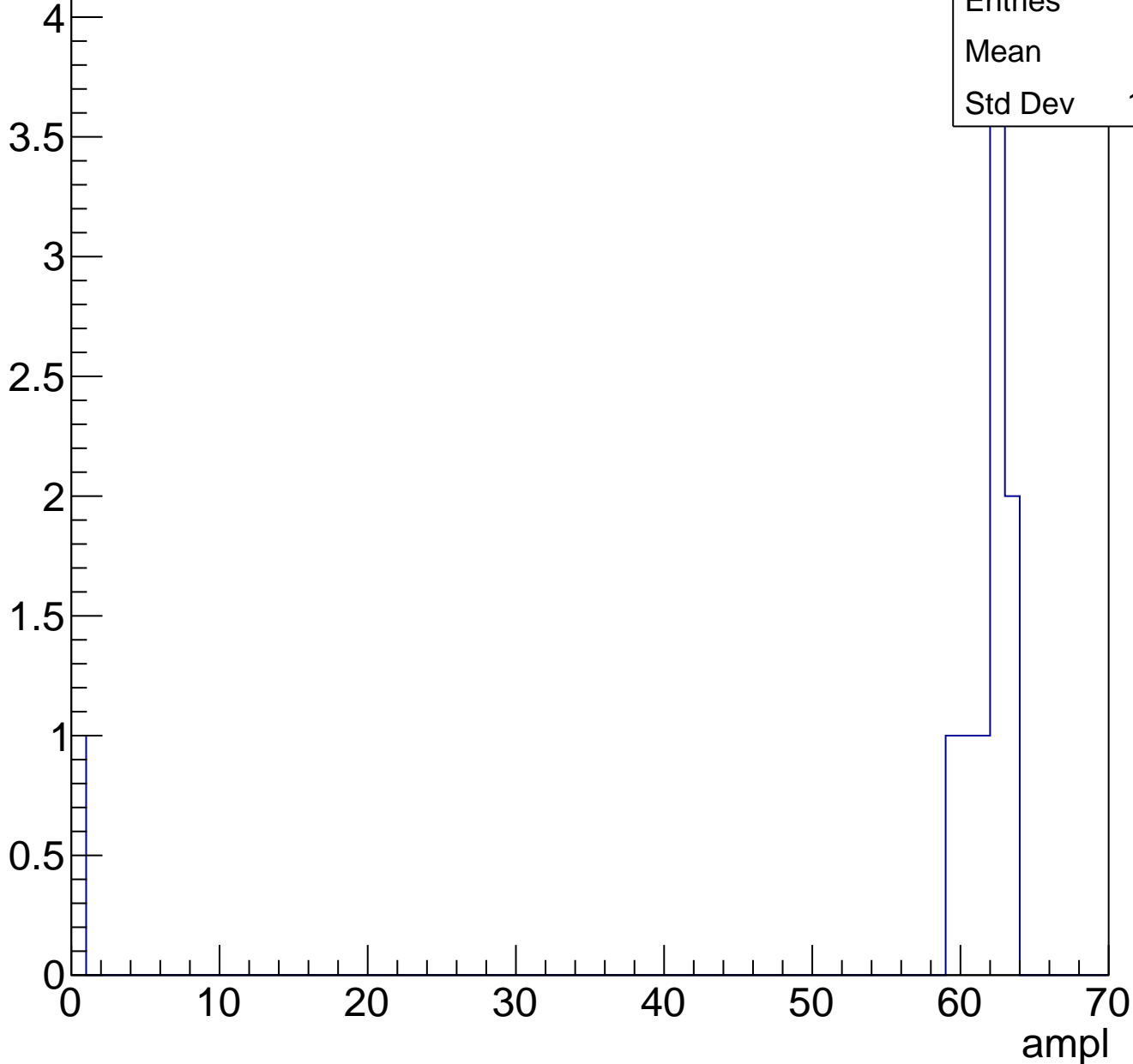
ampl



B1L102S, U10-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U10-ch17, adc0

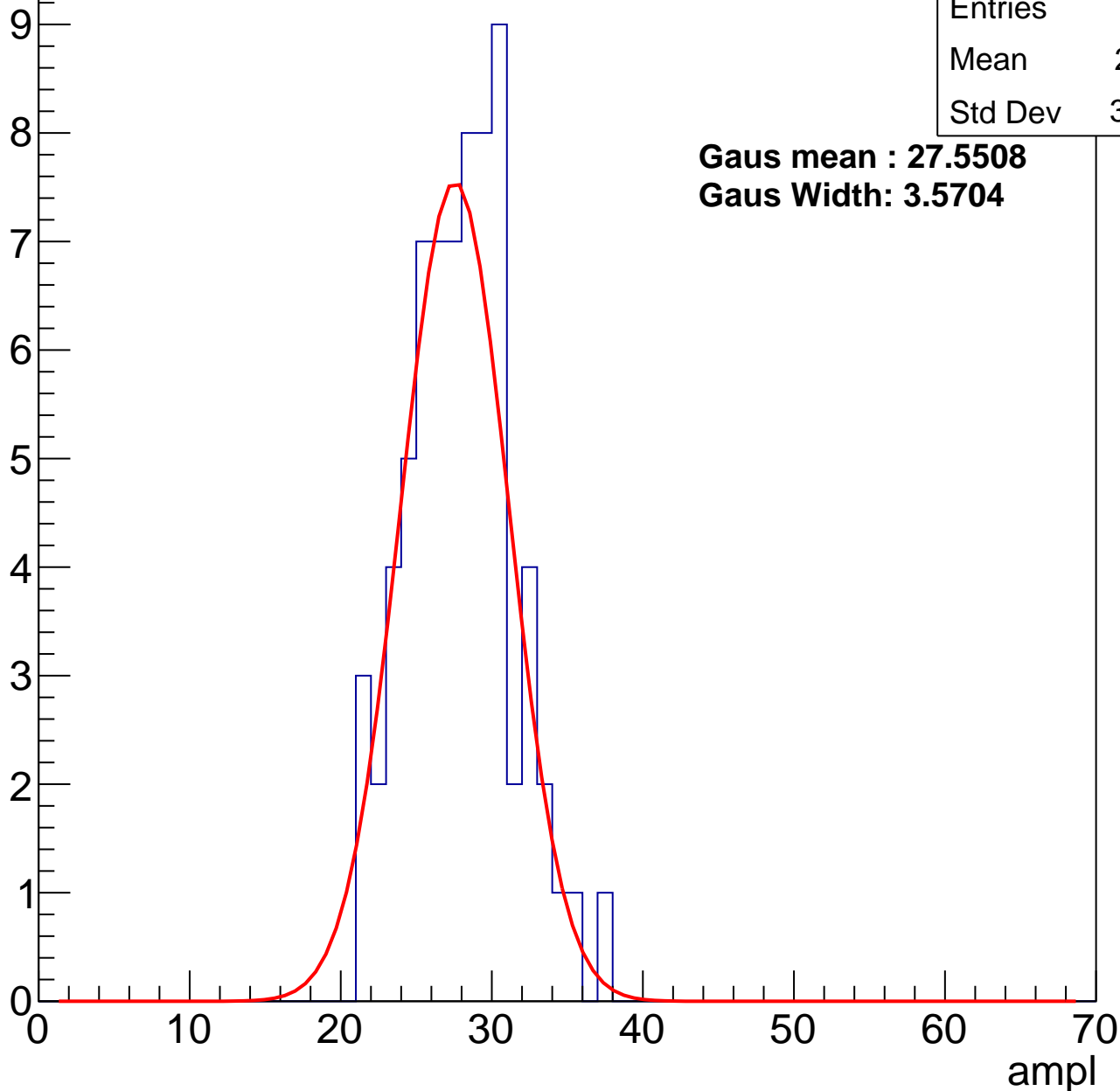
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.51
Std Dev	3.402

Gaus mean : 27.5508

Gaus Width: 3.5704



B1L102S, U10-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	34.9
Std Dev	3.473

Gaus mean : 35.7328

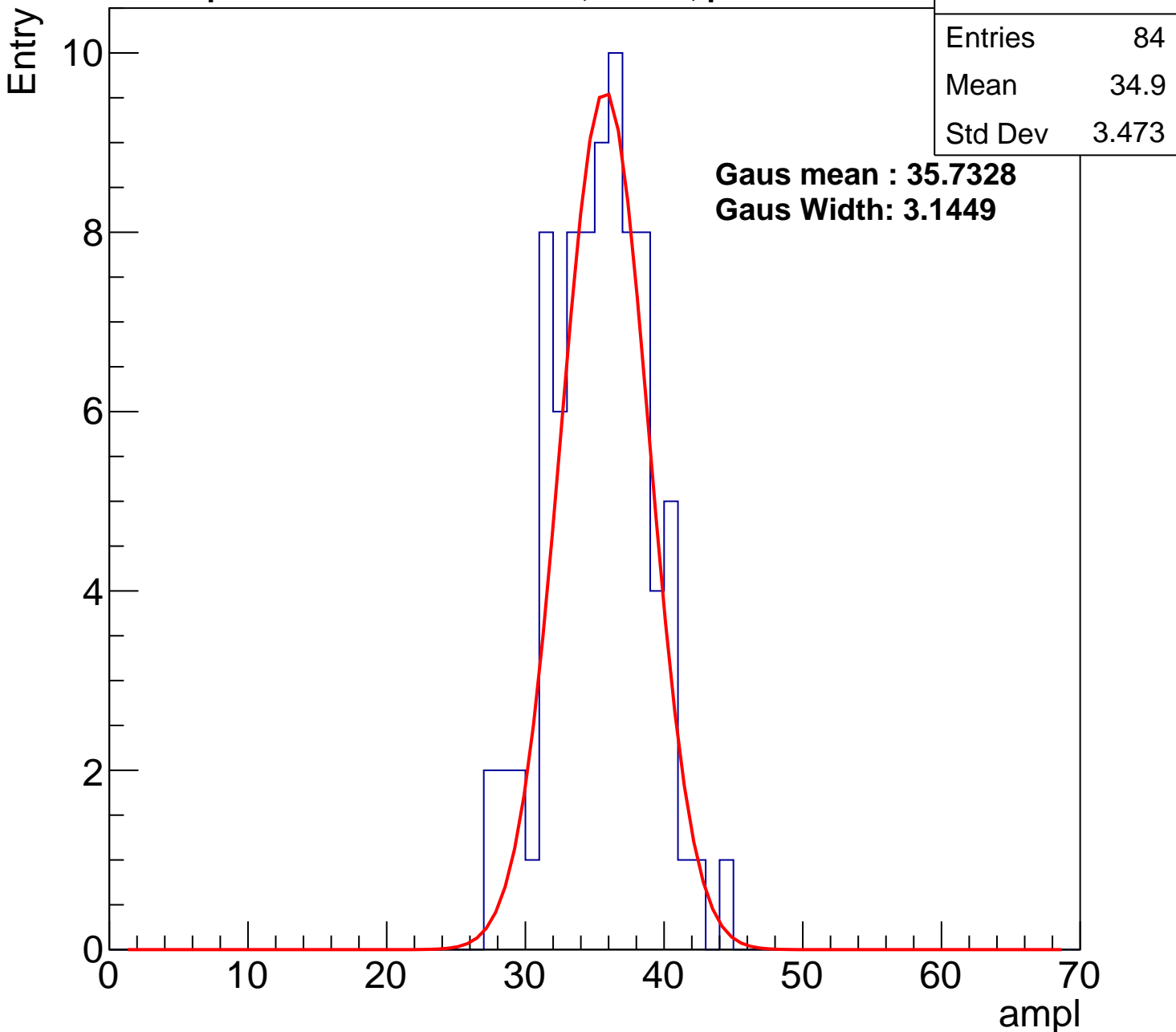
Gaus Width: 3.1449

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch17, adc2

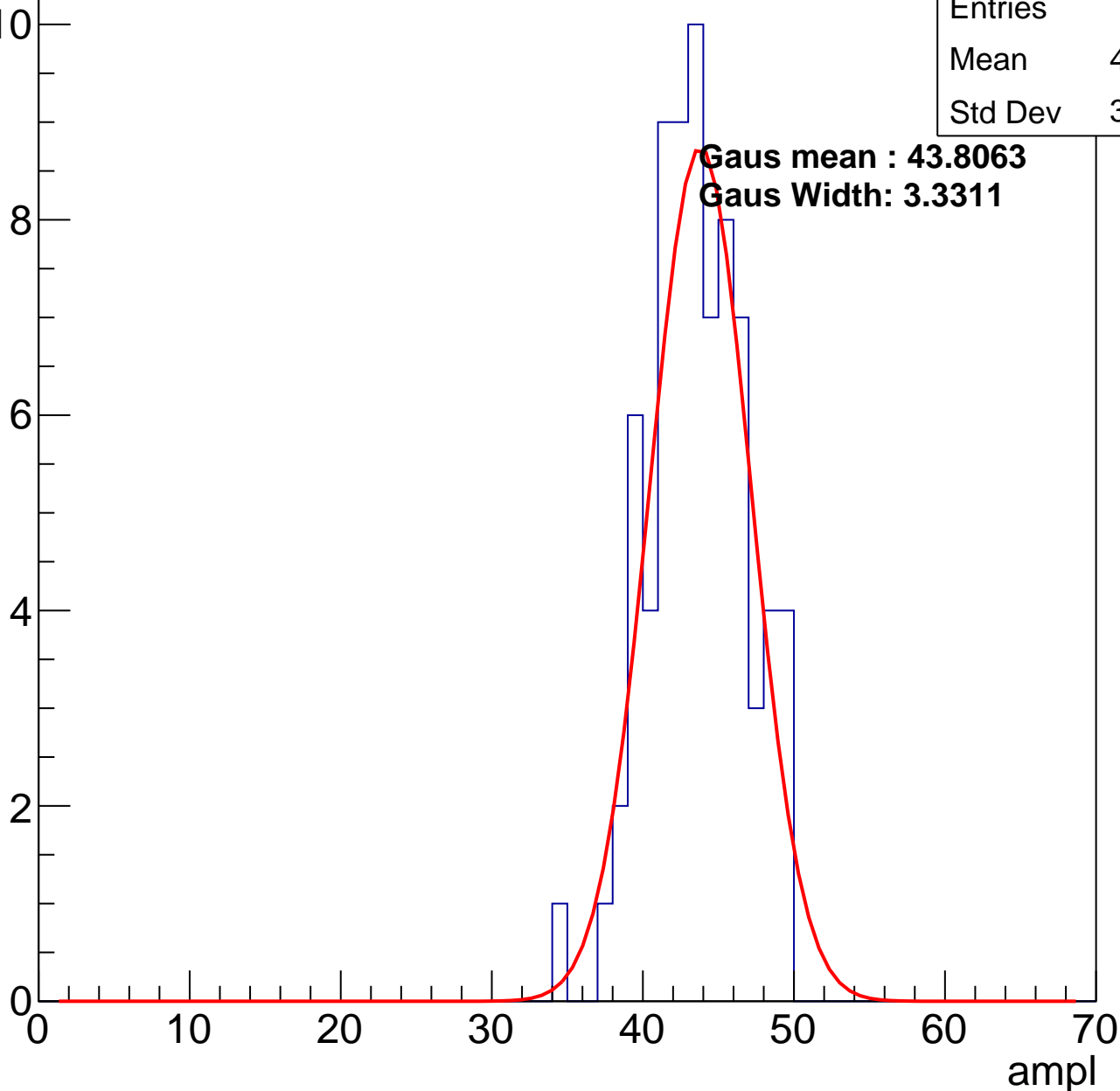
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.16
Std Dev	3.133

Gaus mean : 43.8063

Gaus Width: 3.3311

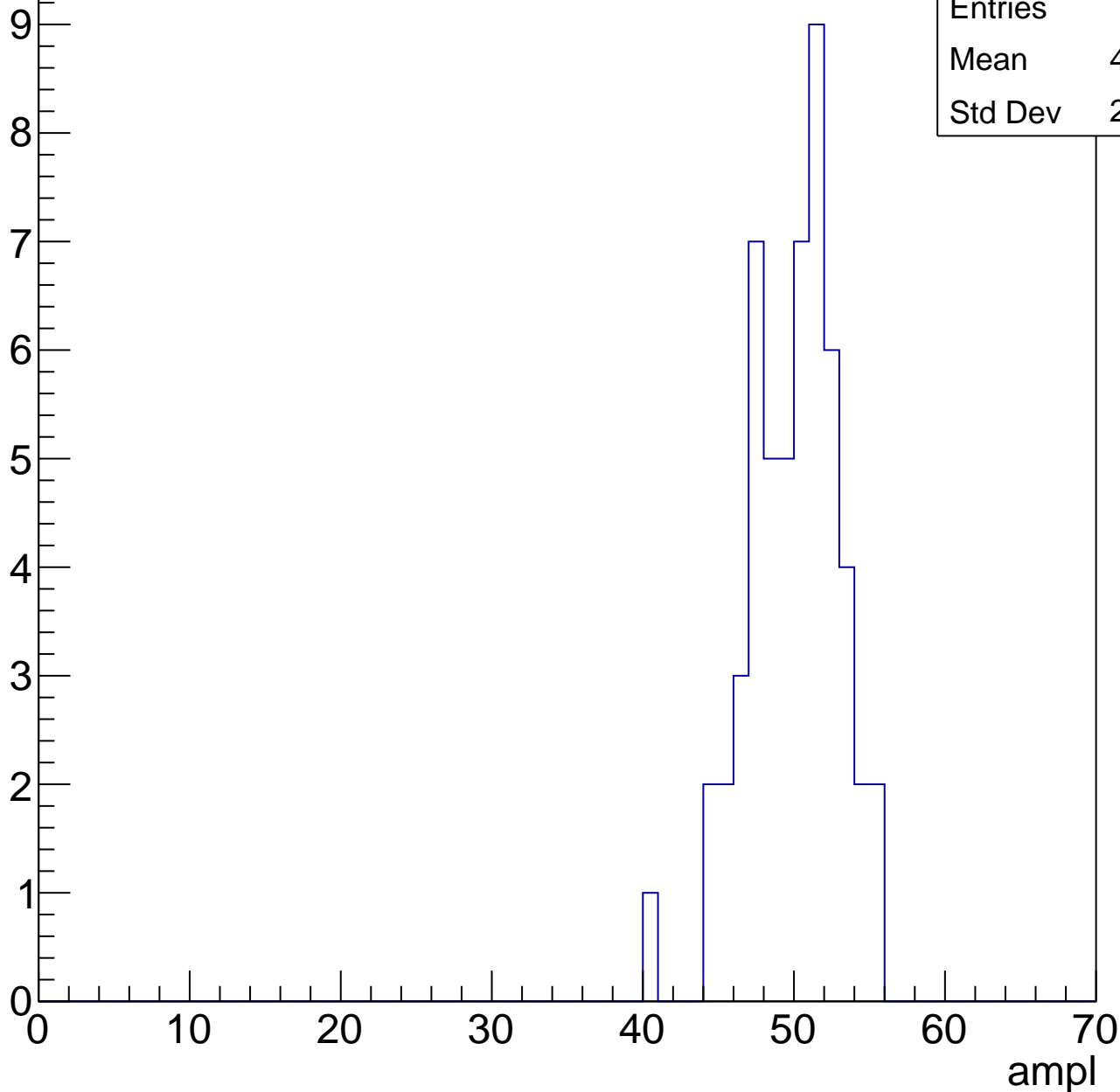


B1L102S, U10-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.47
Std Dev	2.996

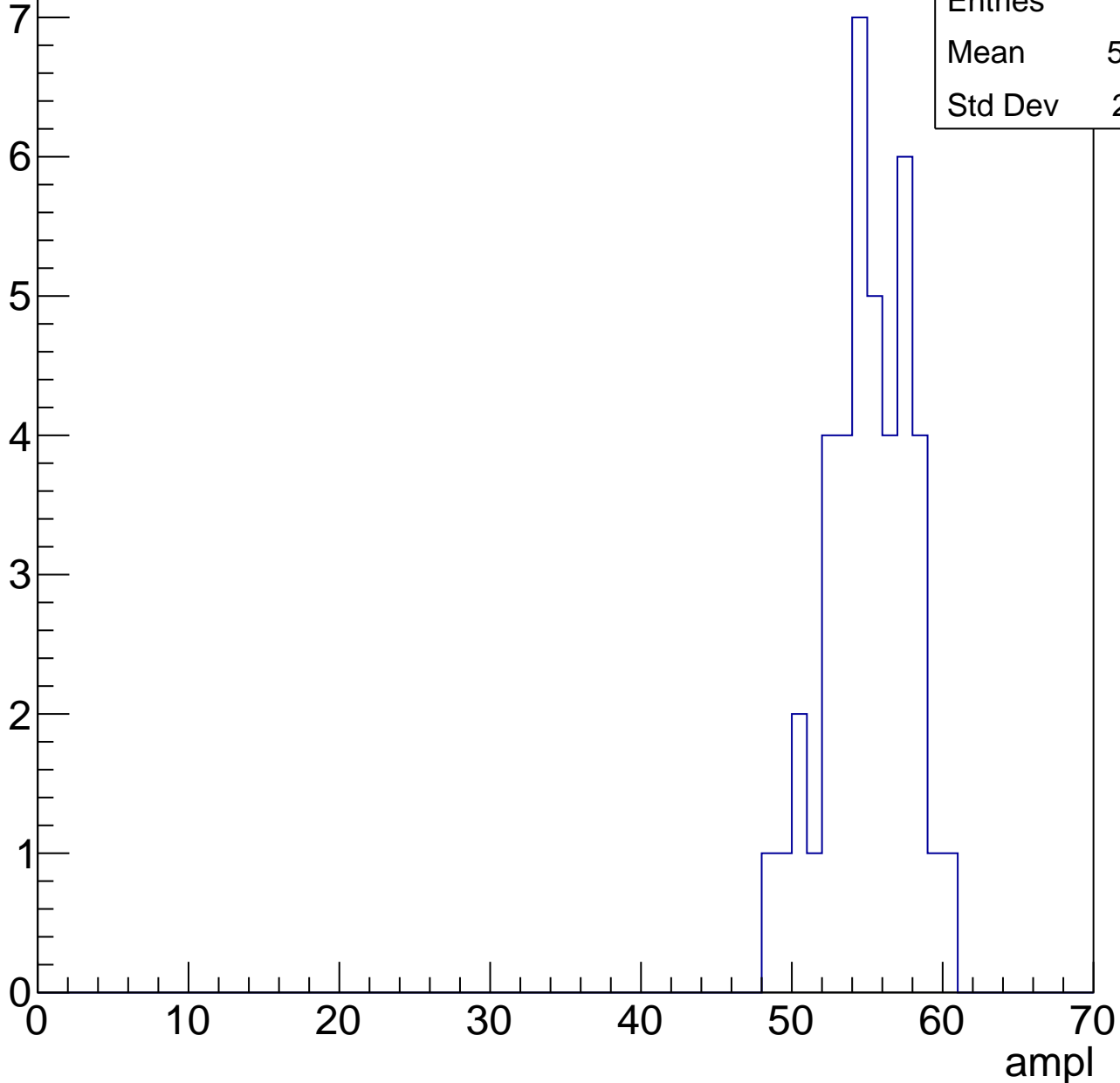


B1L102S, U10-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	54.59
Std Dev	2.741

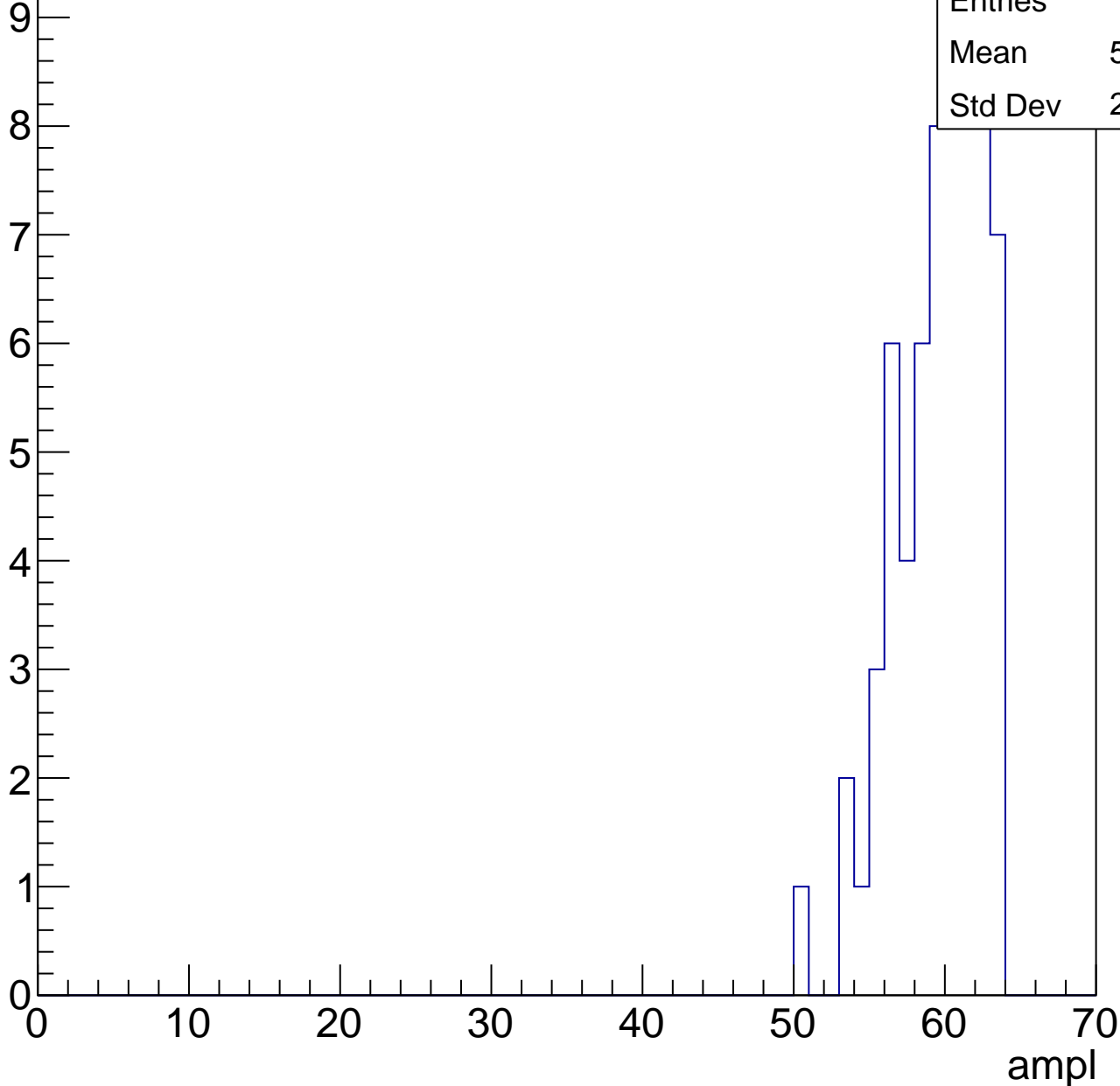


B1L102S, U10-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

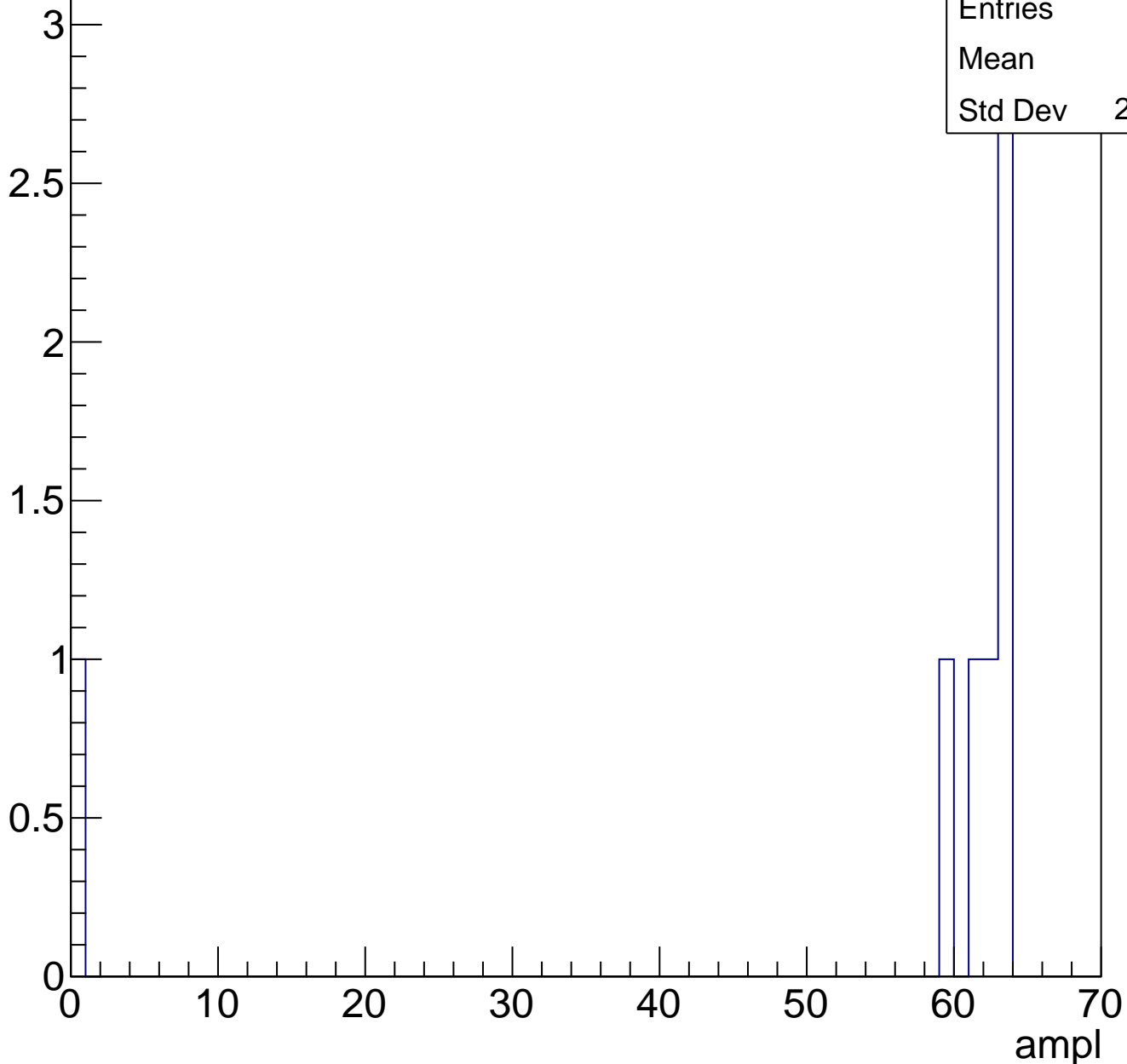
Entries	65
Mean	59.18
Std Dev	2.876



B1L102S, U10-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

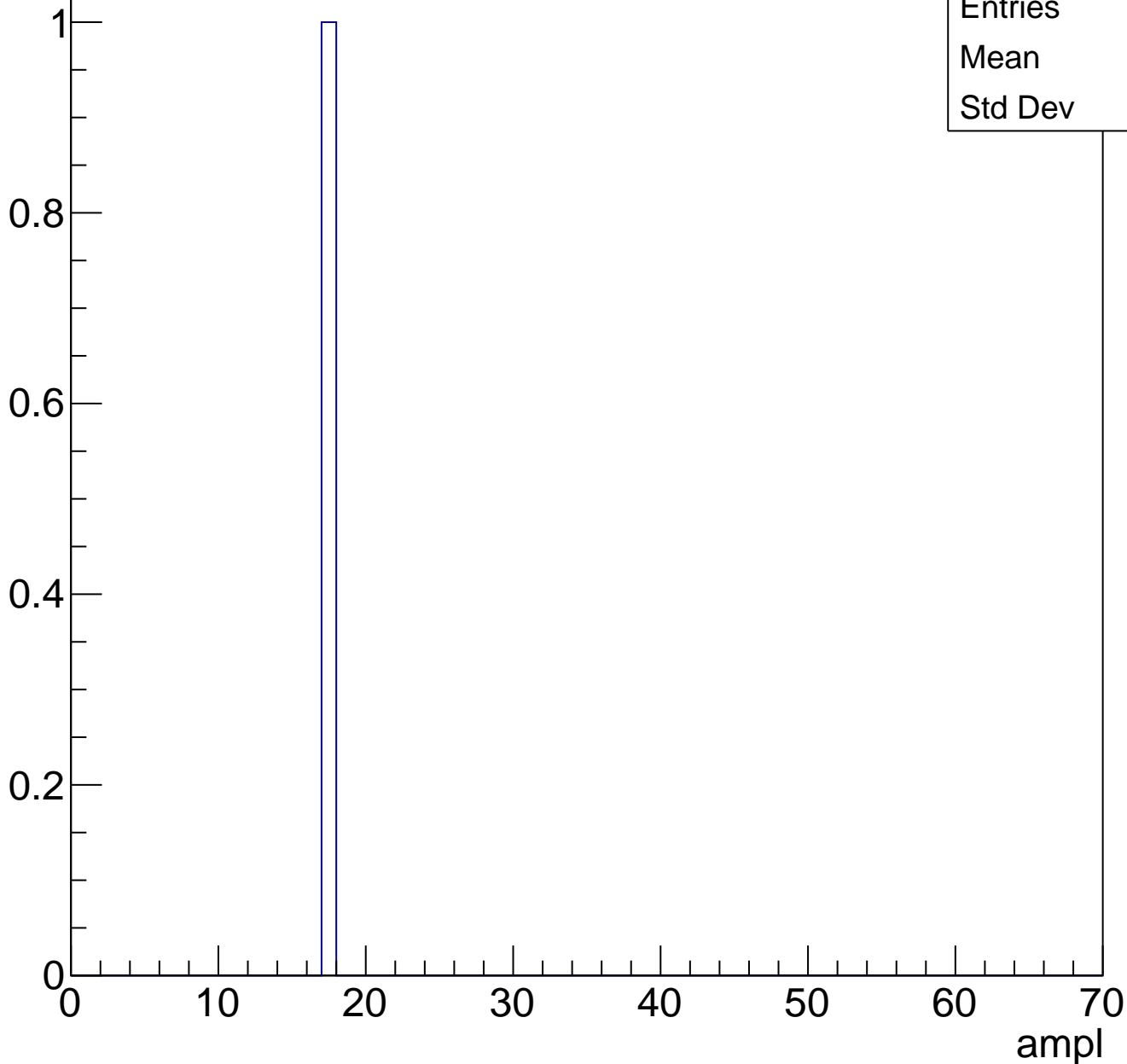
Entry



B1L102S, U10-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch18, adc0

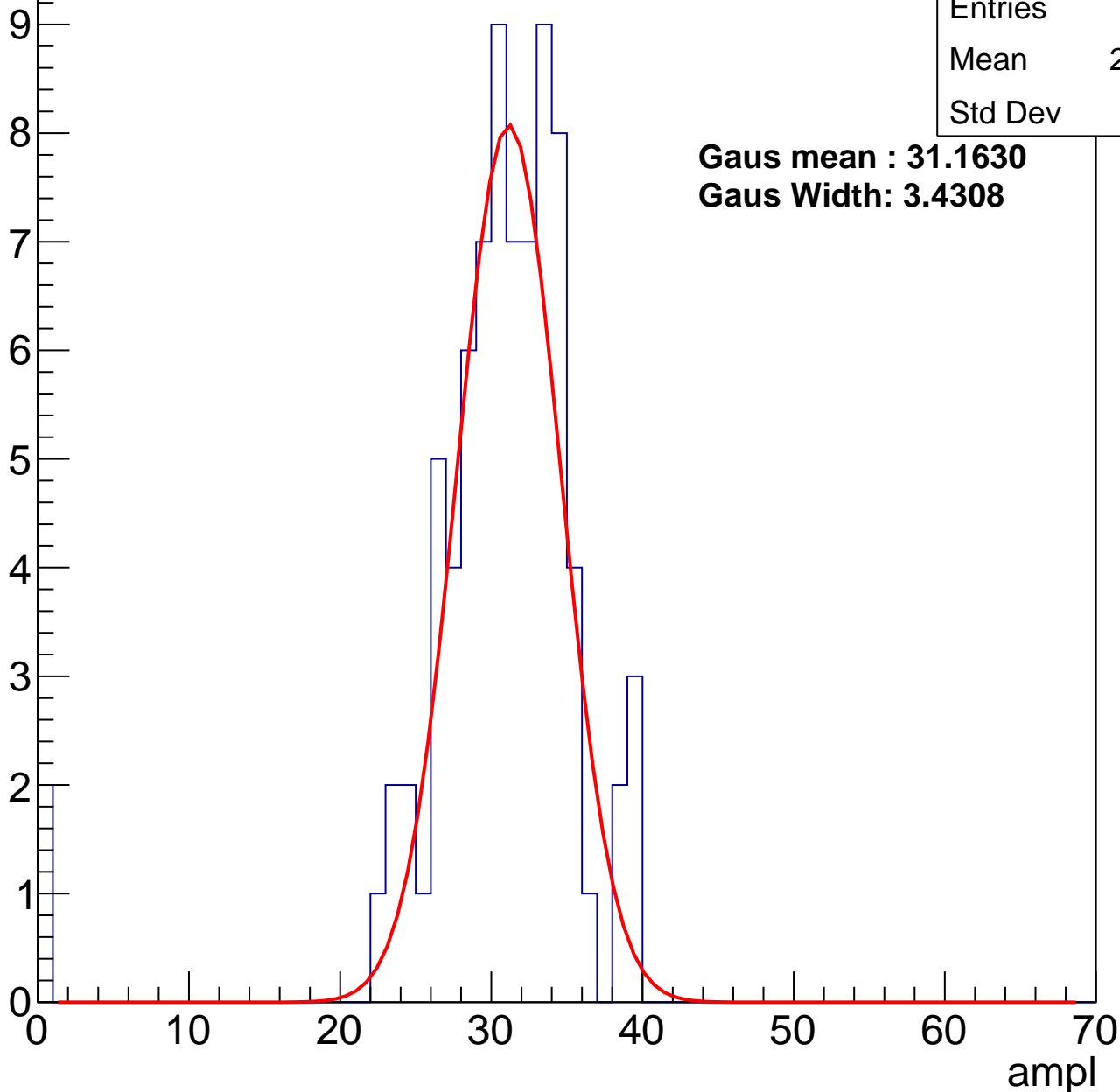
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.99
Std Dev	6.08

Gaus mean : 31.1630

Gaus Width: 3.4308



B1L102S, U10-ch18, adc1

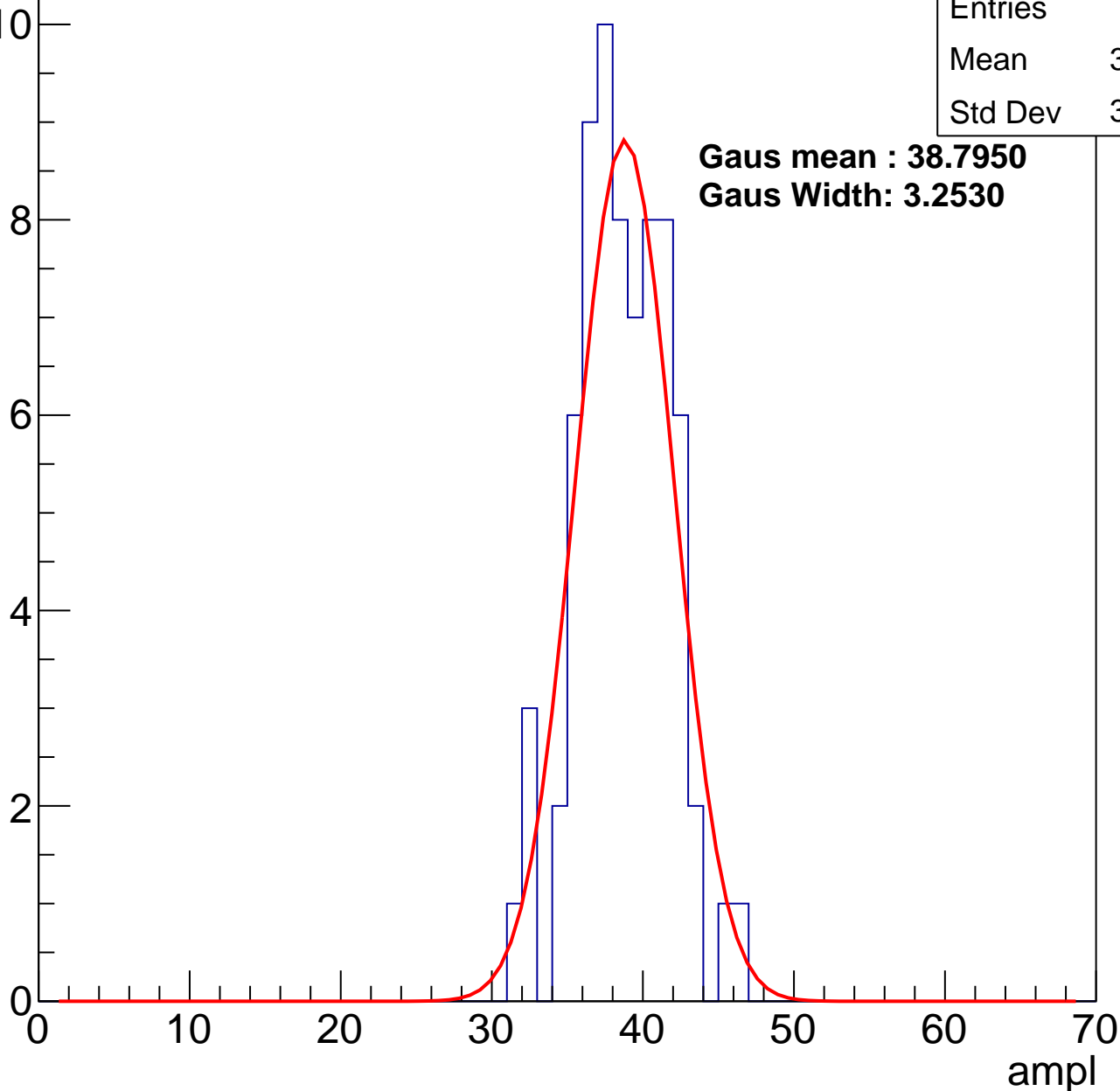
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	38.24
Std Dev	3.016

Gaus mean : 38.7950

Gaus Width: 3.2530



B1L102S, U10-ch18, adc2

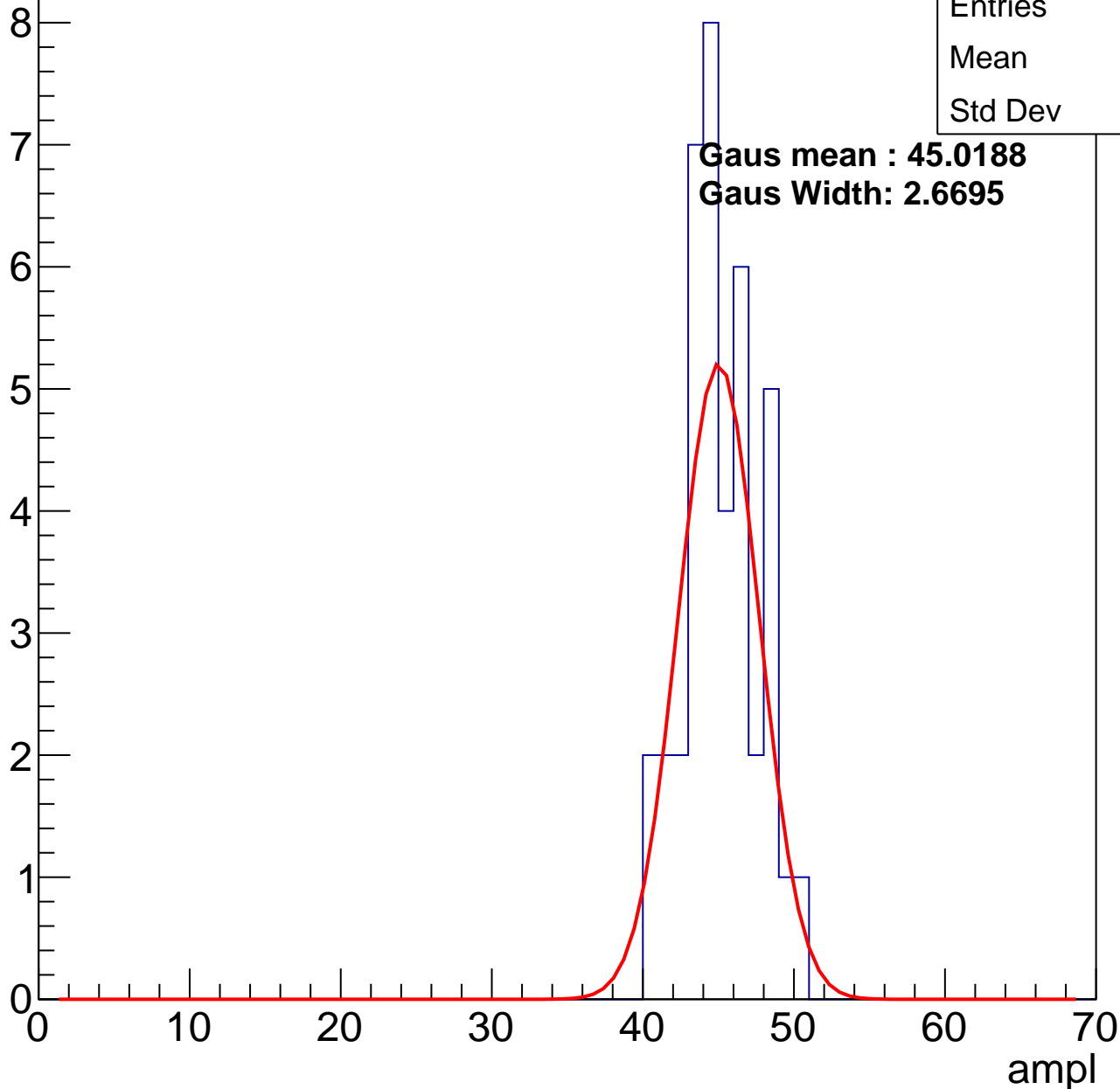
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	44.7
Std Dev	2.41

Gaus mean : 45.0188

Gaus Width: 2.6695

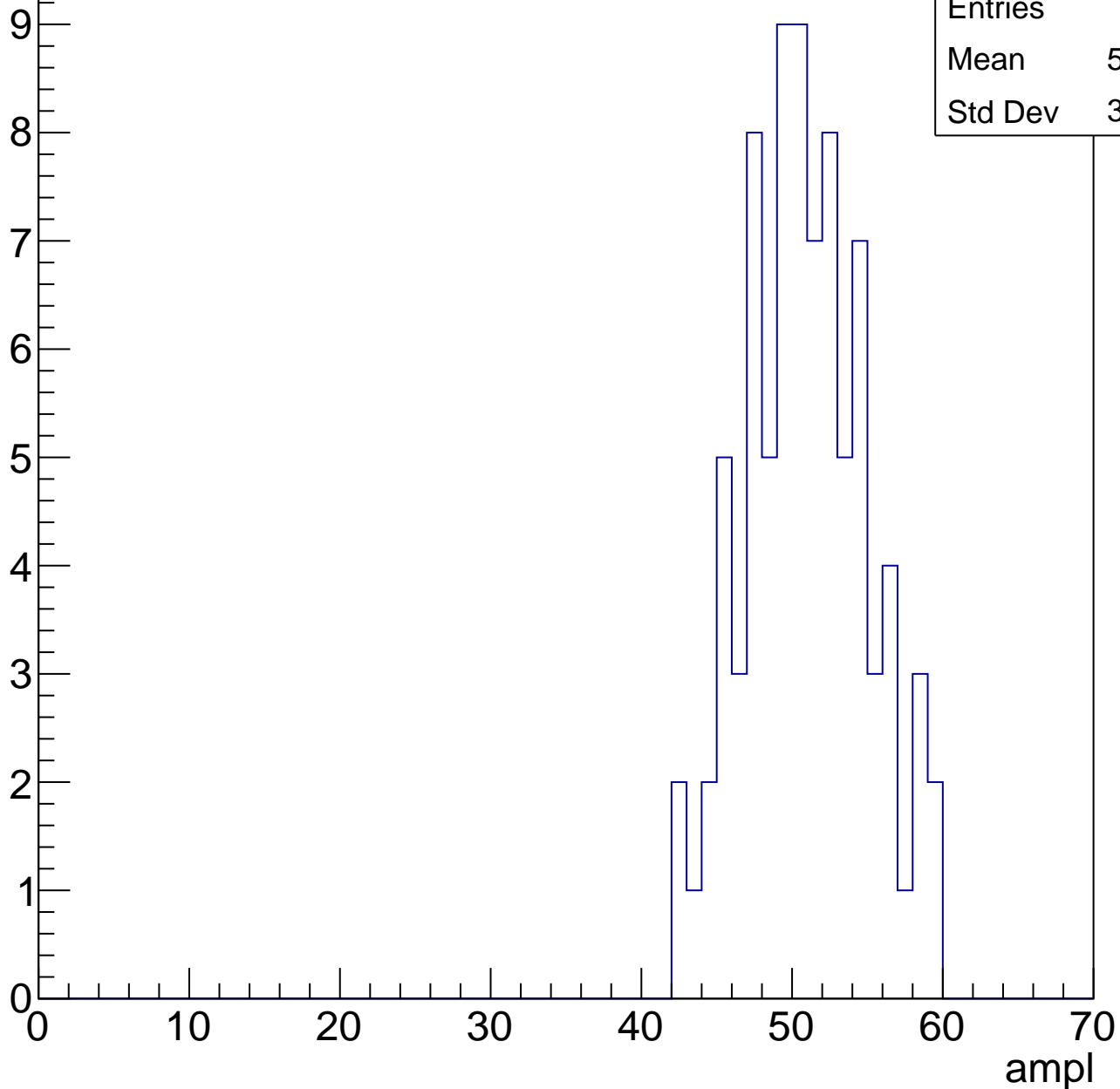


B1L102S, U10-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

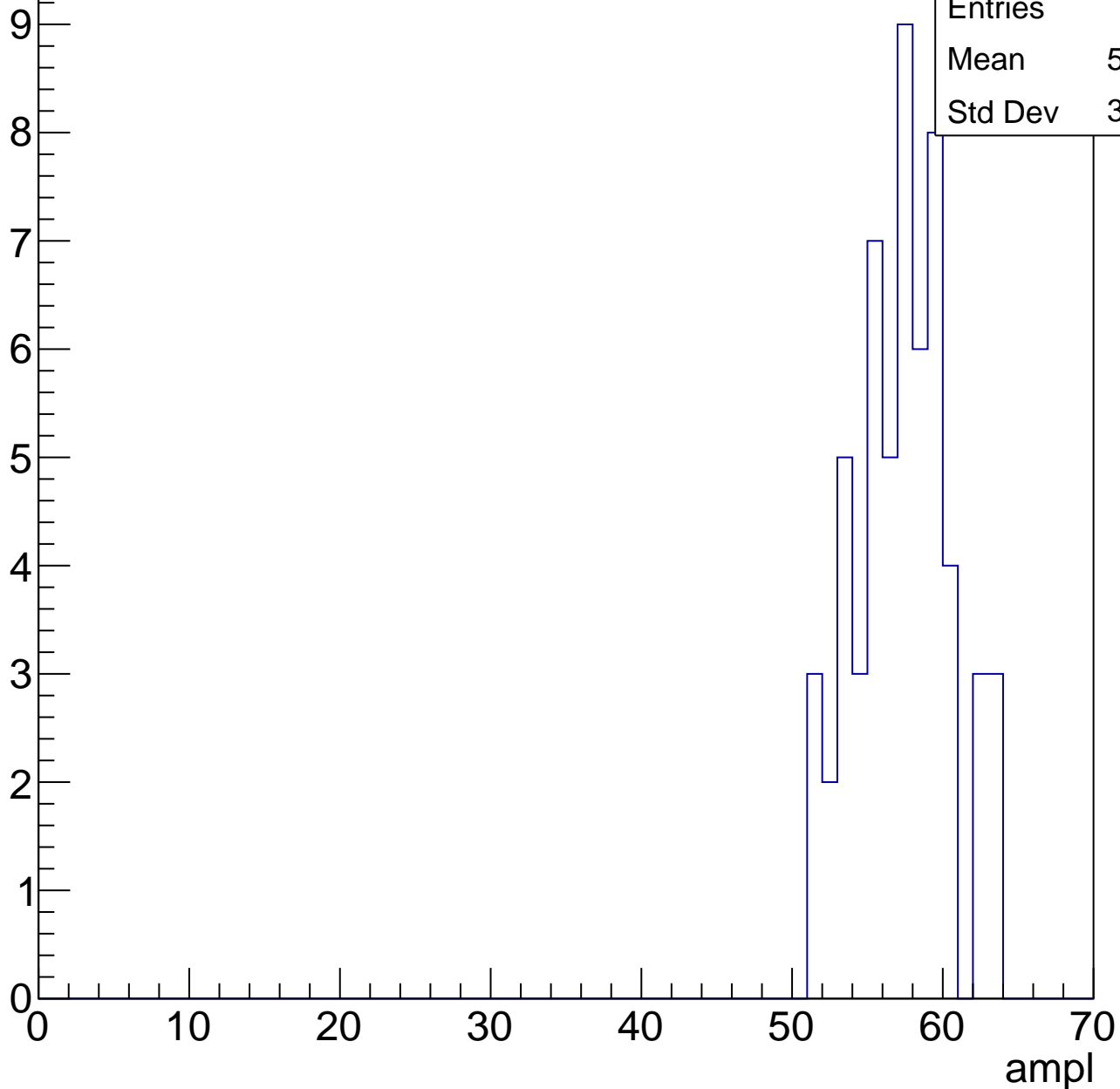
Entries	84
Mean	50.46
Std Dev	3.968



B1L102S, U10-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



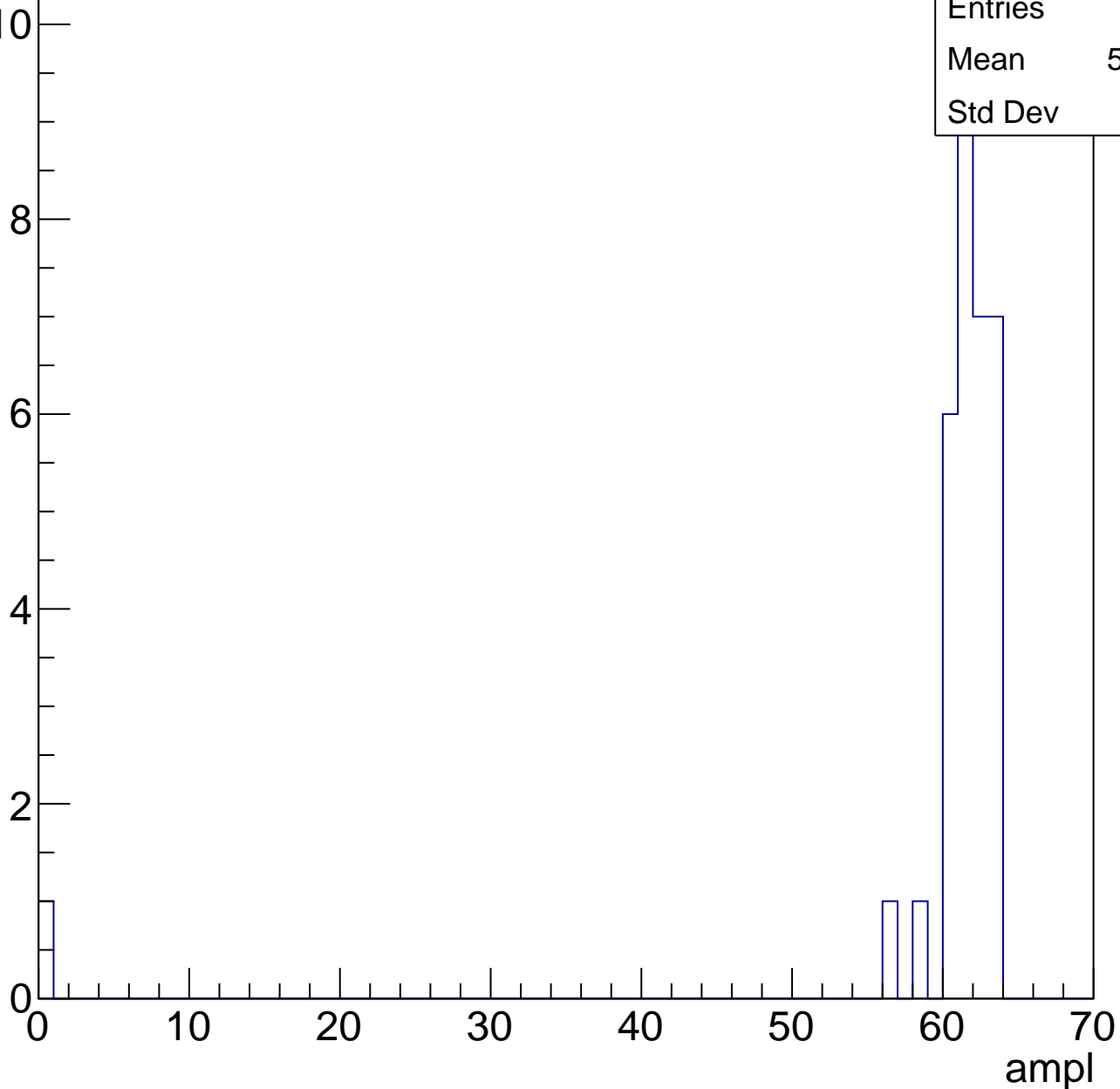
Entries	58
Mean	56.84
Std Dev	3.089

B1L102S, U10-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	59.36
Std Dev	10.6



B1L102S, U10-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch19, adc0

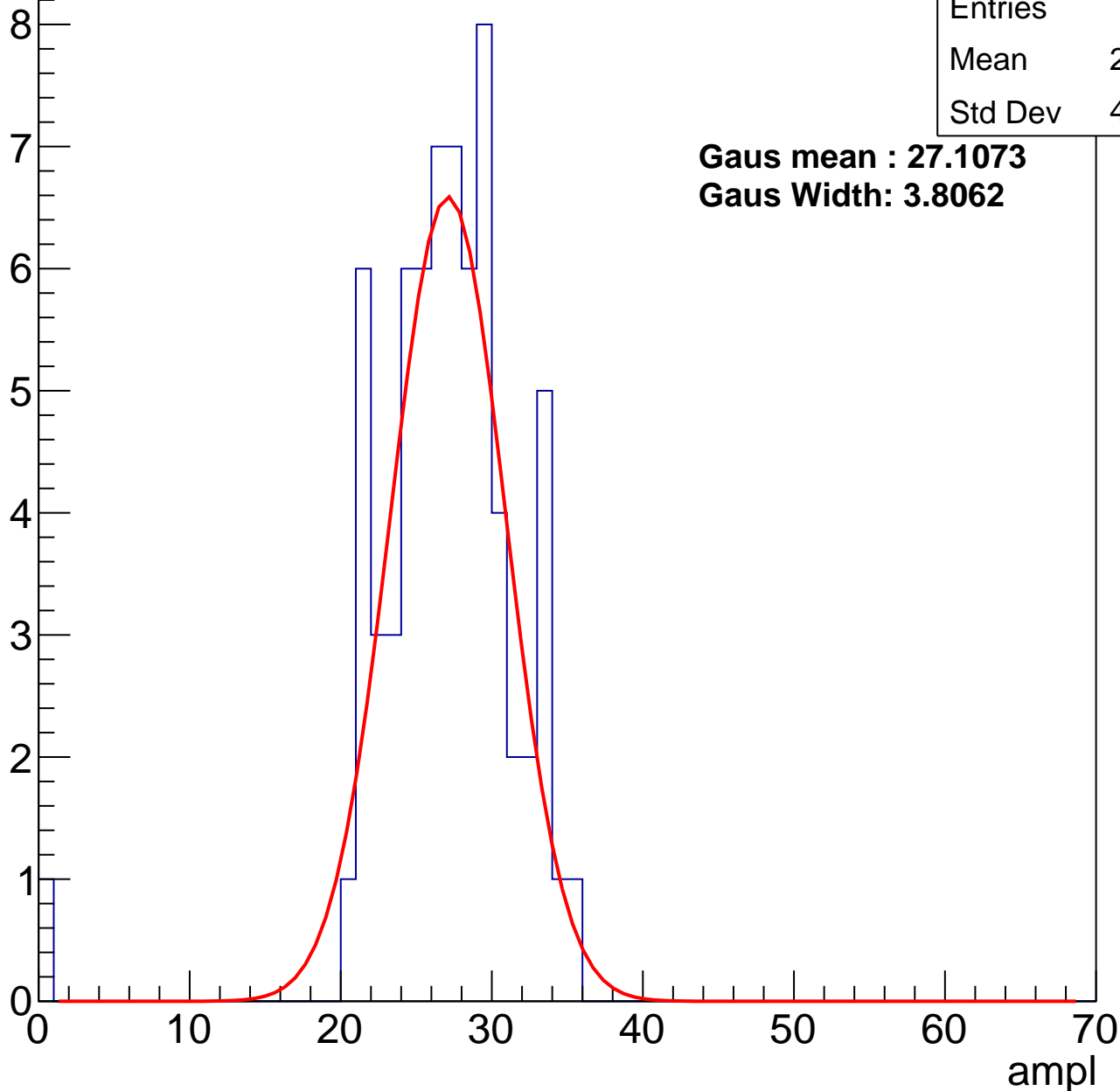
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	26.46
Std Dev	4.862

Gaus mean : 27.1073

Gaus Width: 3.8062



B1L102S, U10-ch19, adc1

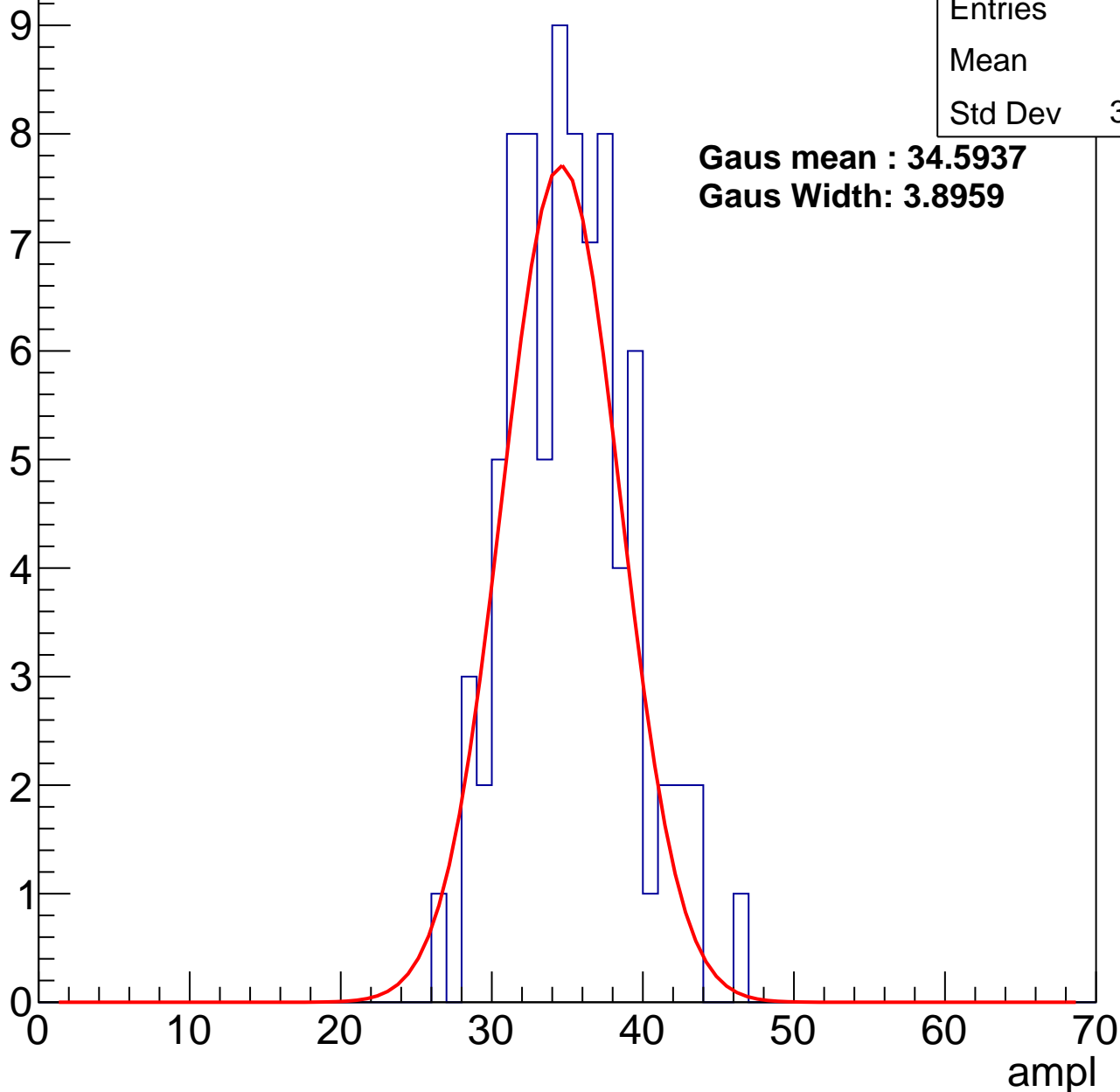
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.7
Std Dev	3.922

Gaus mean : 34.5937

Gaus Width: 3.8959



B1L102S, U10-ch19, adc2

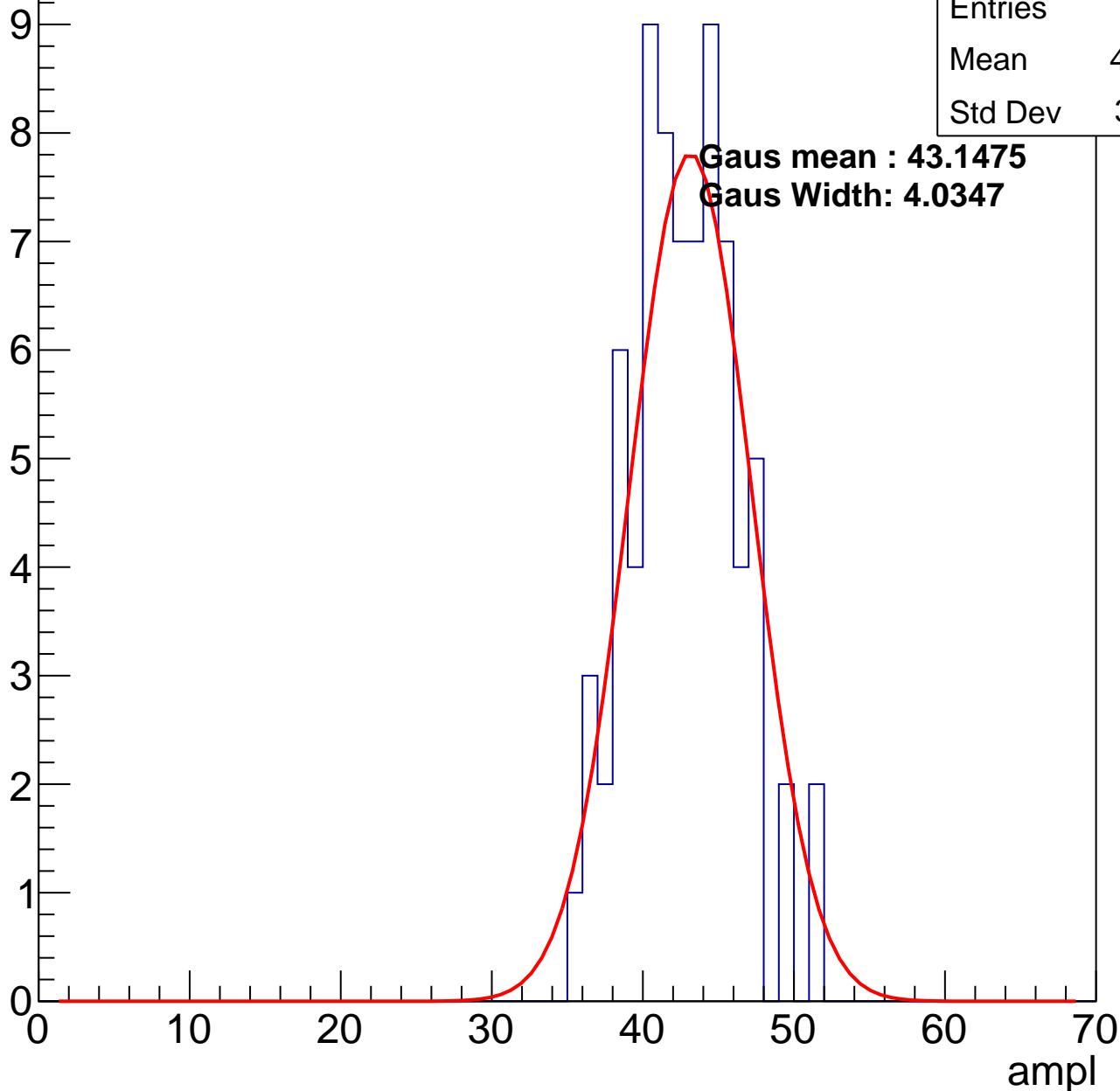
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.29
Std Dev	3.501

Gaus mean : 43.1475

Gaus Width: 4.0347

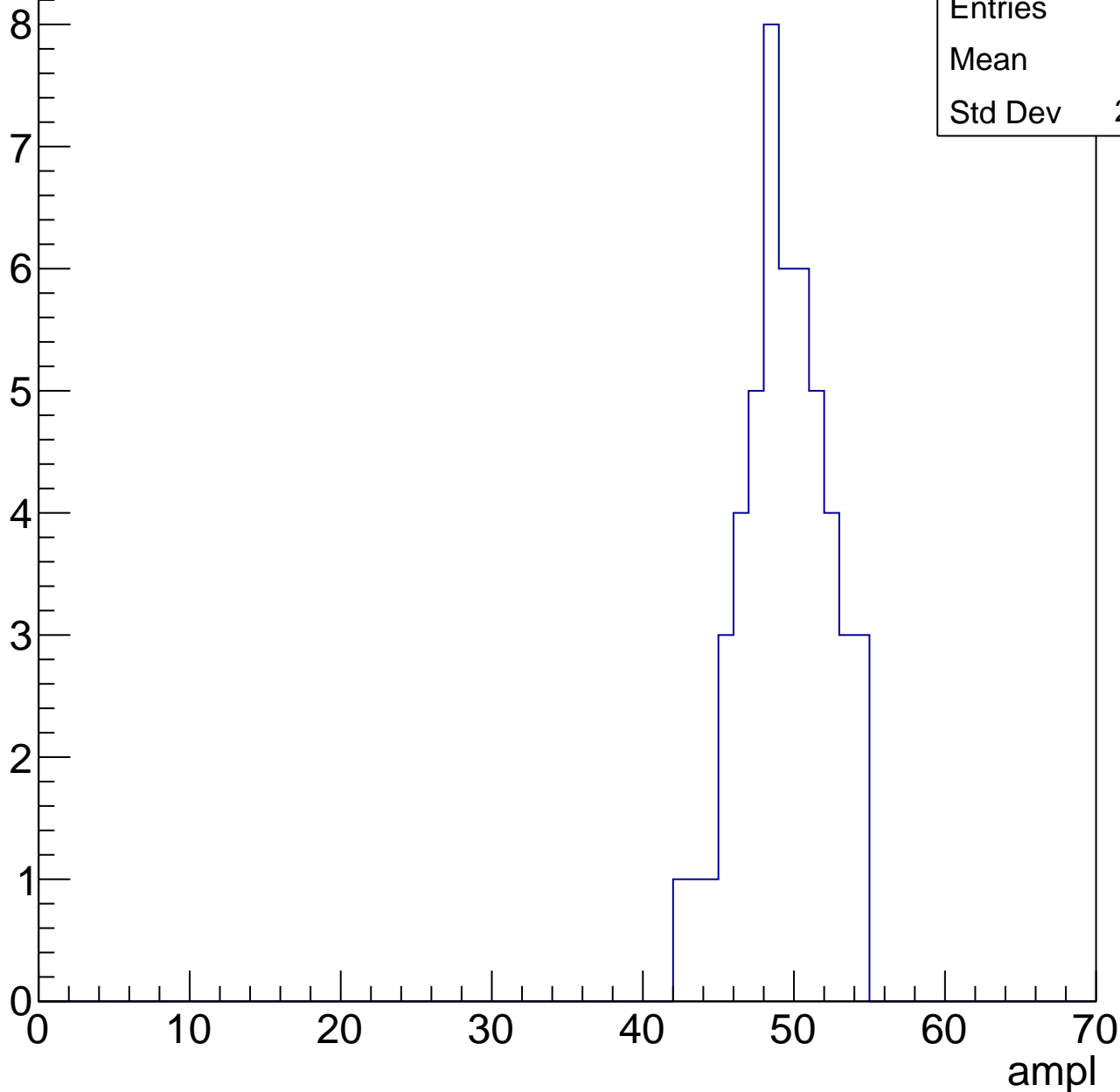


B1L102S, U10-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.9
Std Dev	2.851

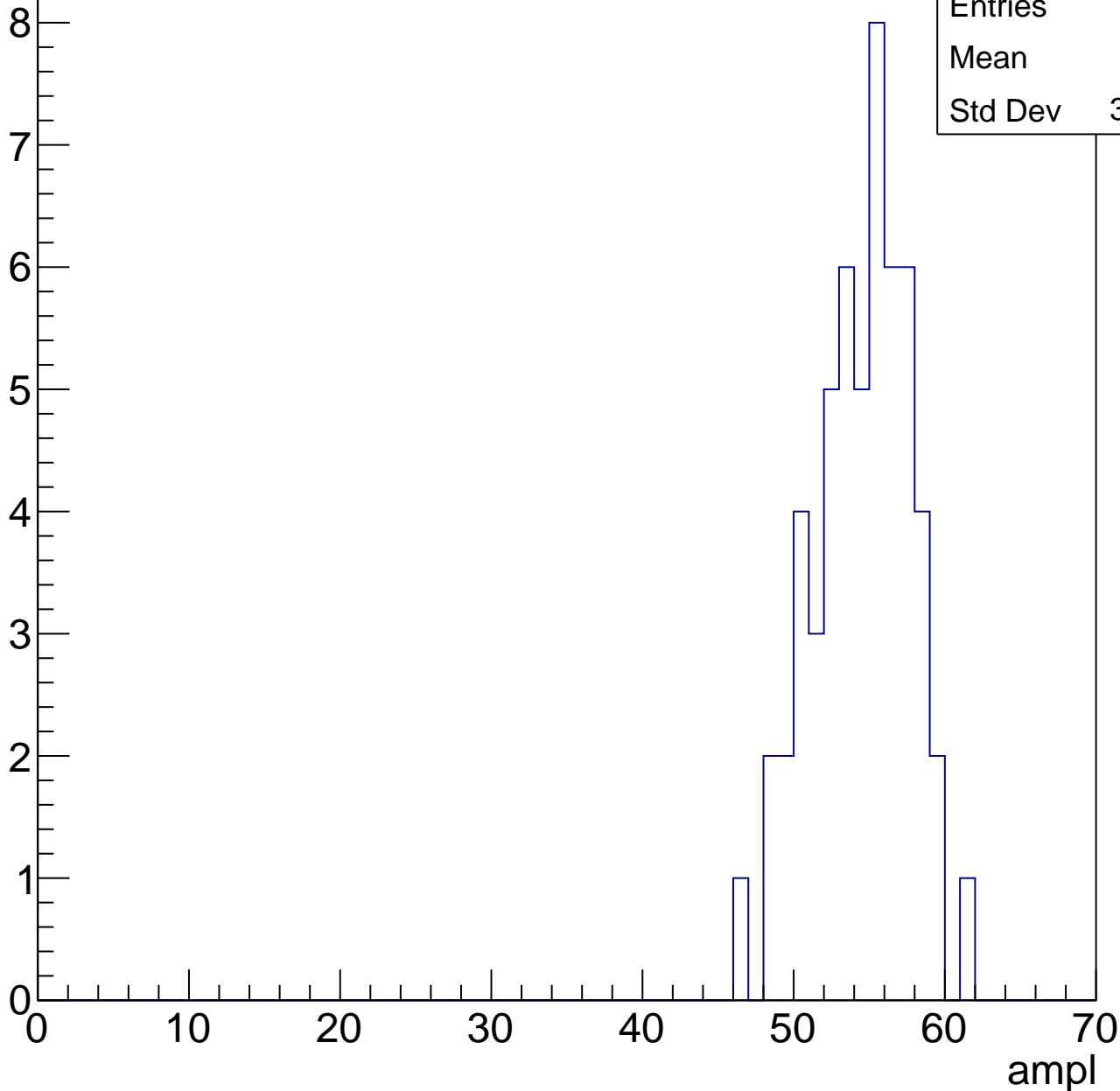


B1L102S, U10-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54
Std Dev	3.168

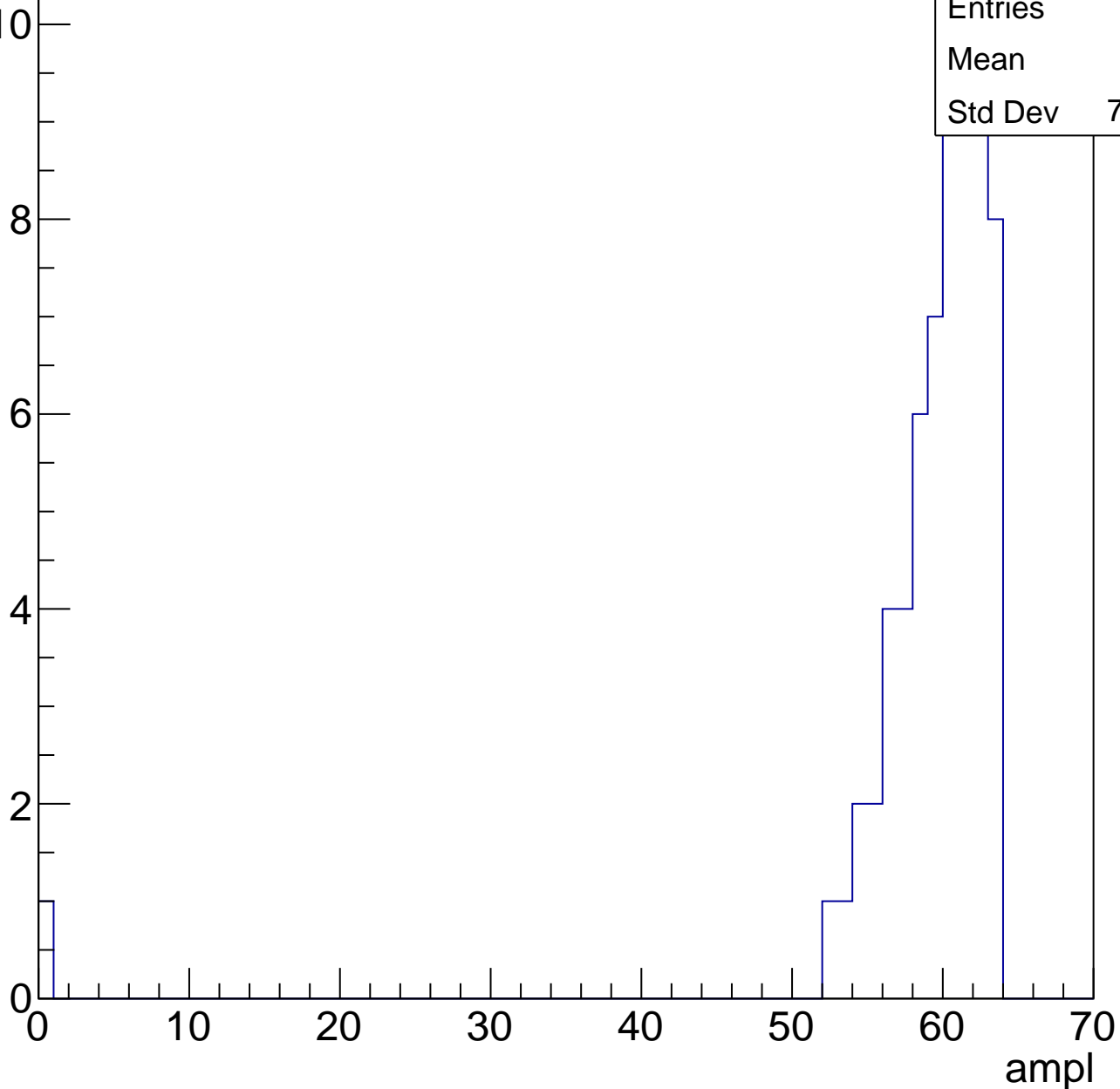


B1L102S, U10-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	58.6
Std Dev	7.805



B1L102S, U10-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch20, adc0

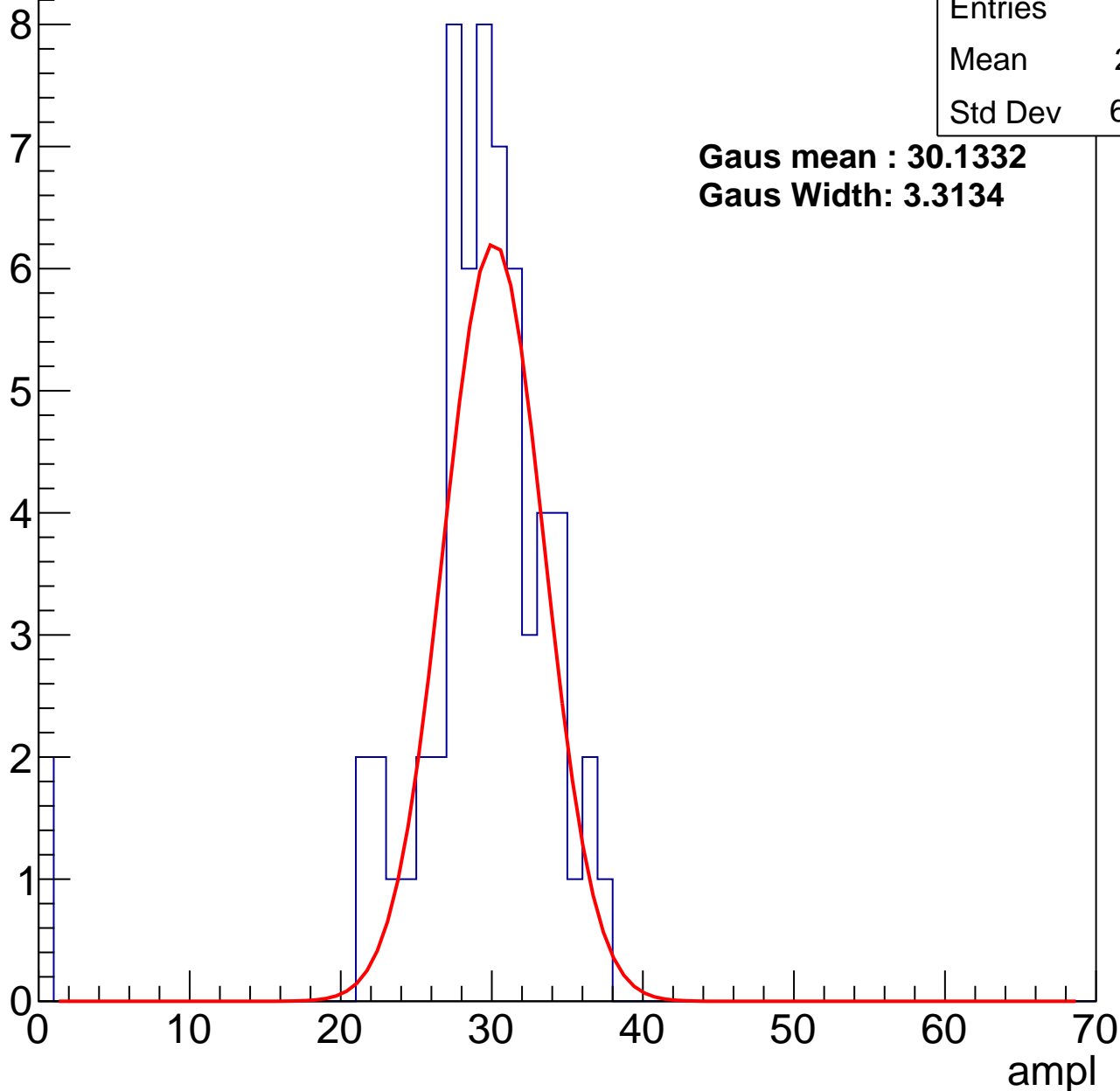
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.31
Std Dev	6.283

Gaus mean : 30.1332

Gaus Width: 3.3134



B1L102S, U10-ch20, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	36.05
Std Dev	2.964

Gaus mean : 36.5319

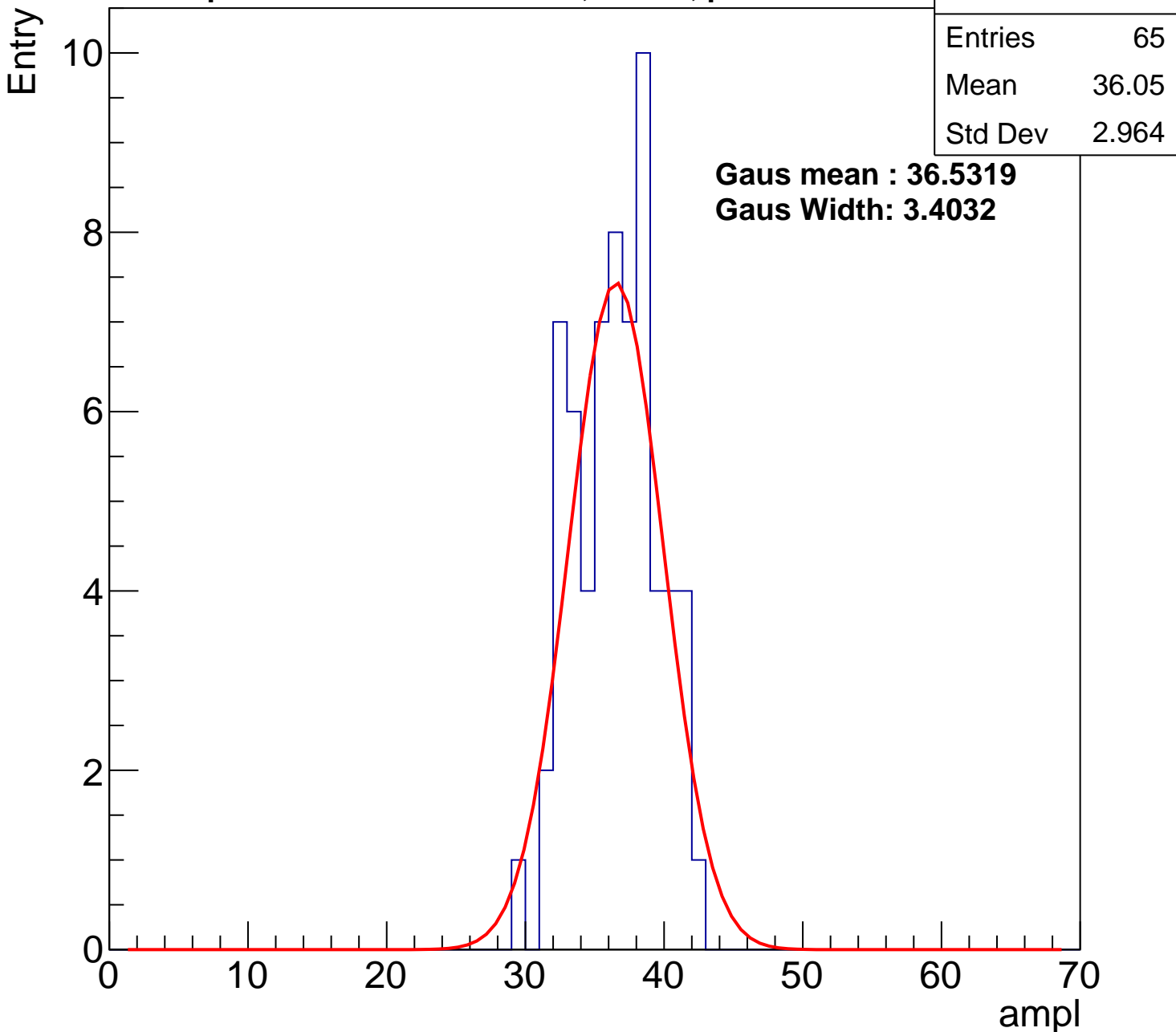
Gaus Width: 3.4032

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch20, adc2

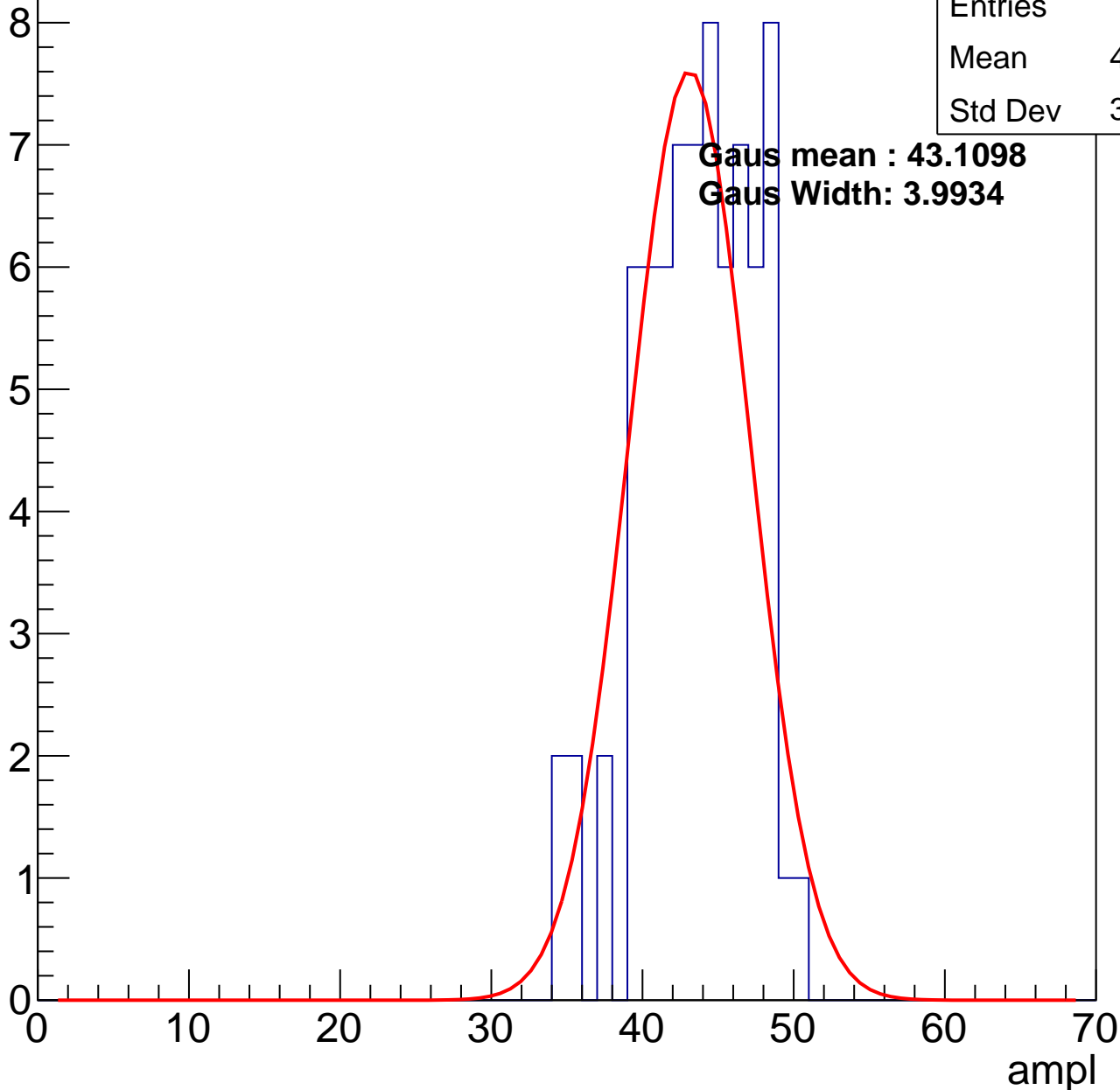
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.15
Std Dev	3.683

Gaus mean : 43.1098

Gaus Width: 3.9934

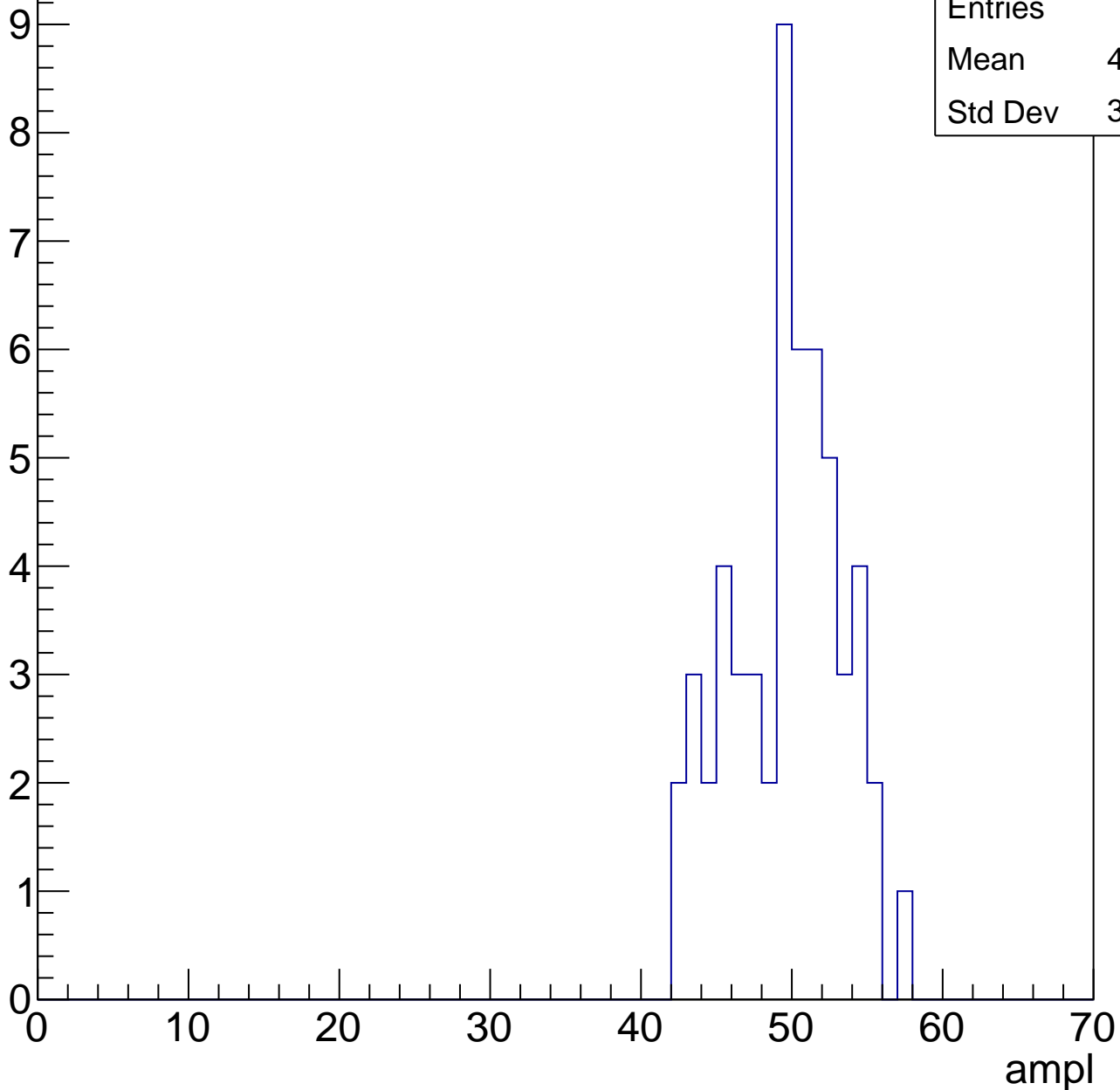


B1L102S, U10-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

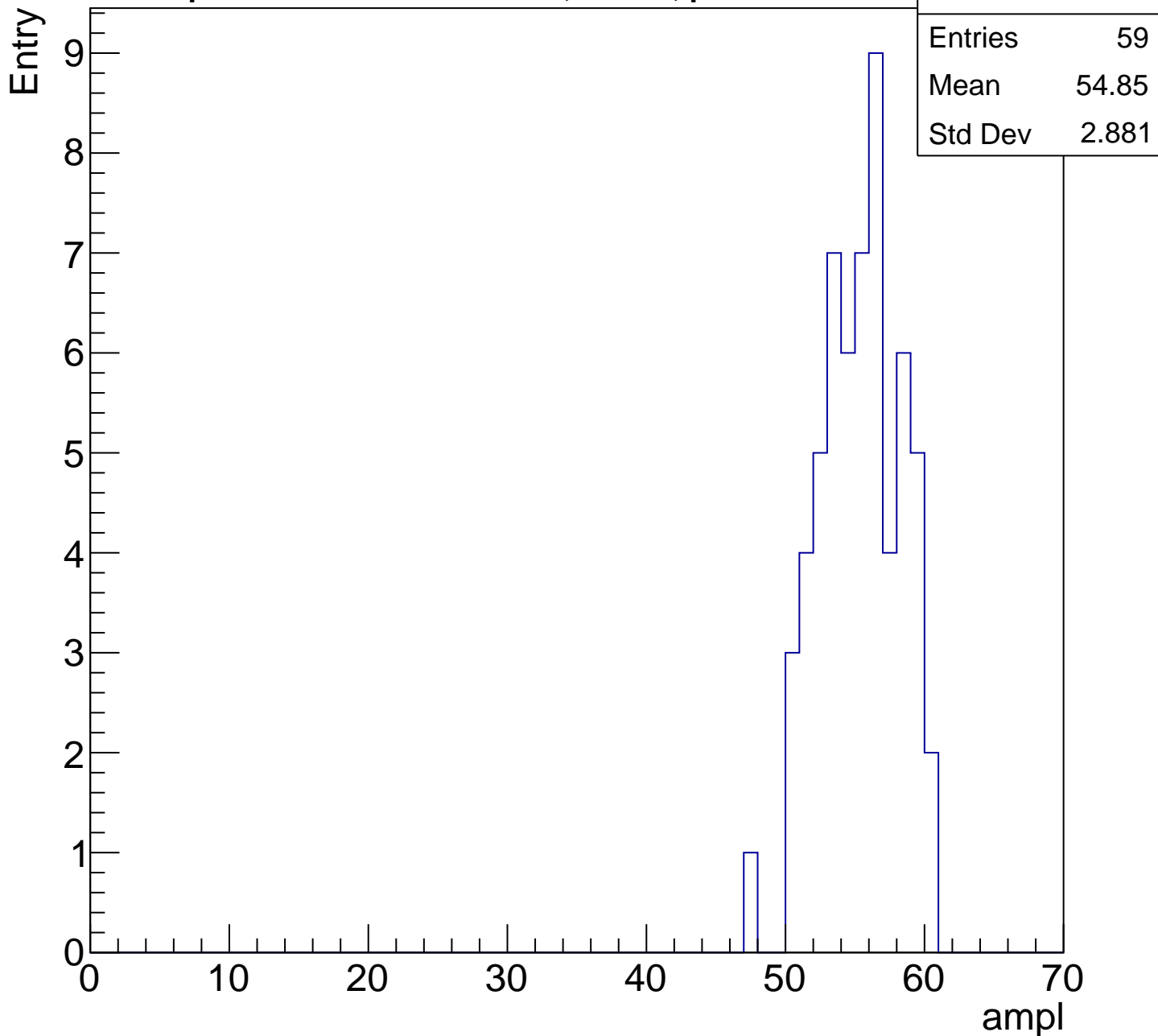
Entry

Entries	55
Mean	49.18
Std Dev	3.614



B1L102S, U10-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

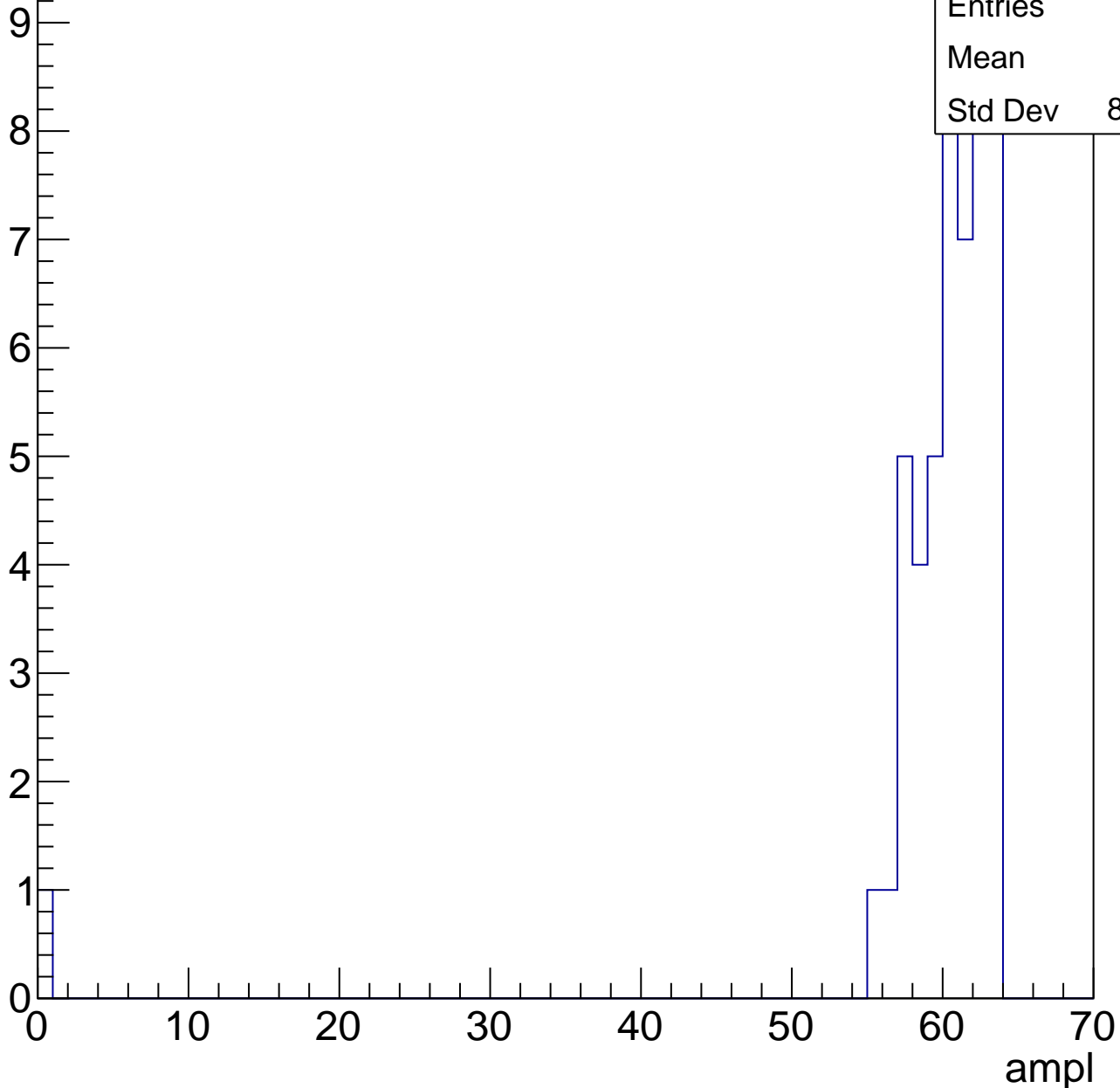


B1L102S, U10-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

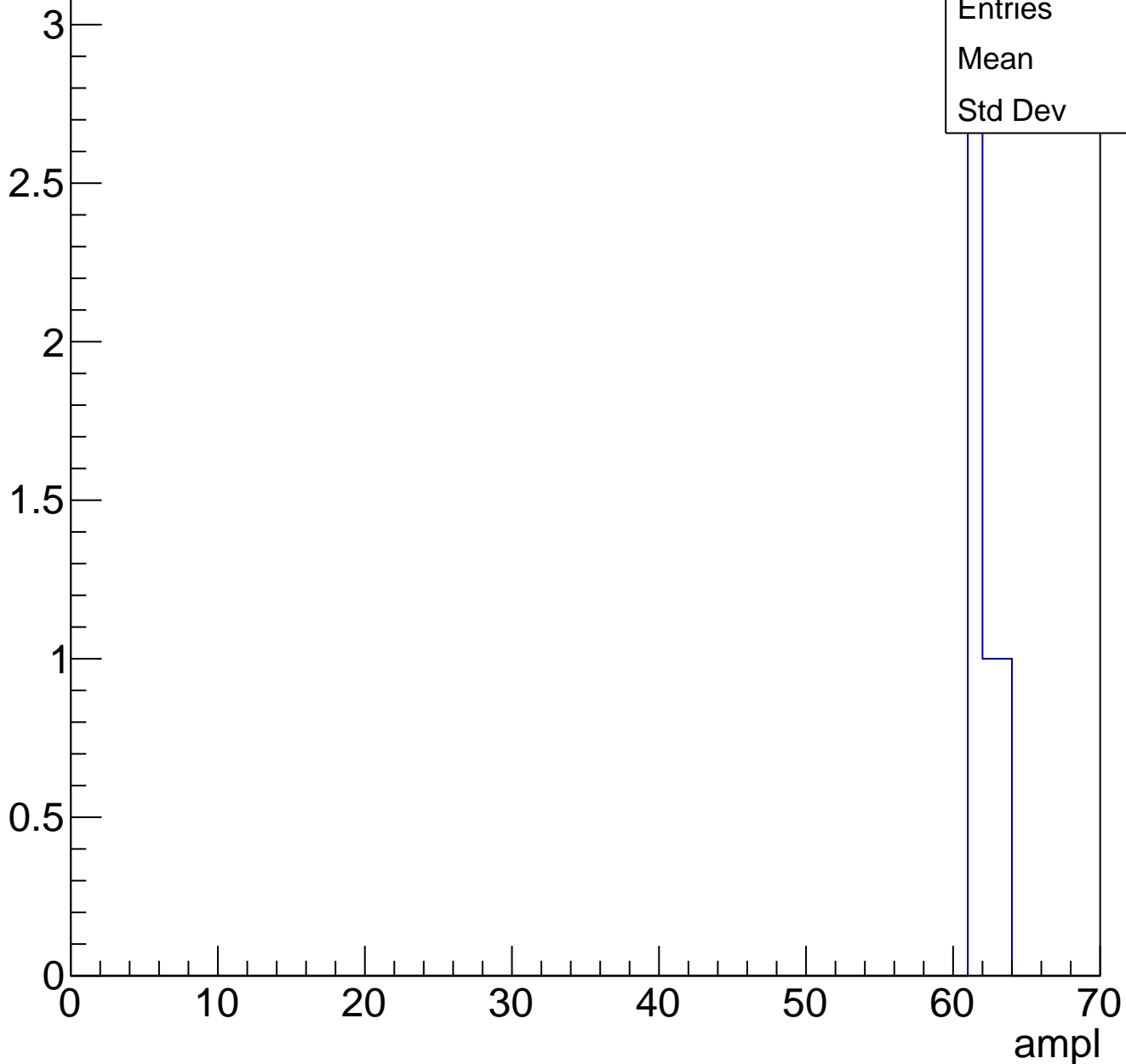
Entries	50
Mean	59.1
Std Dev	8.707



B1L102S, U10-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch21, adc0

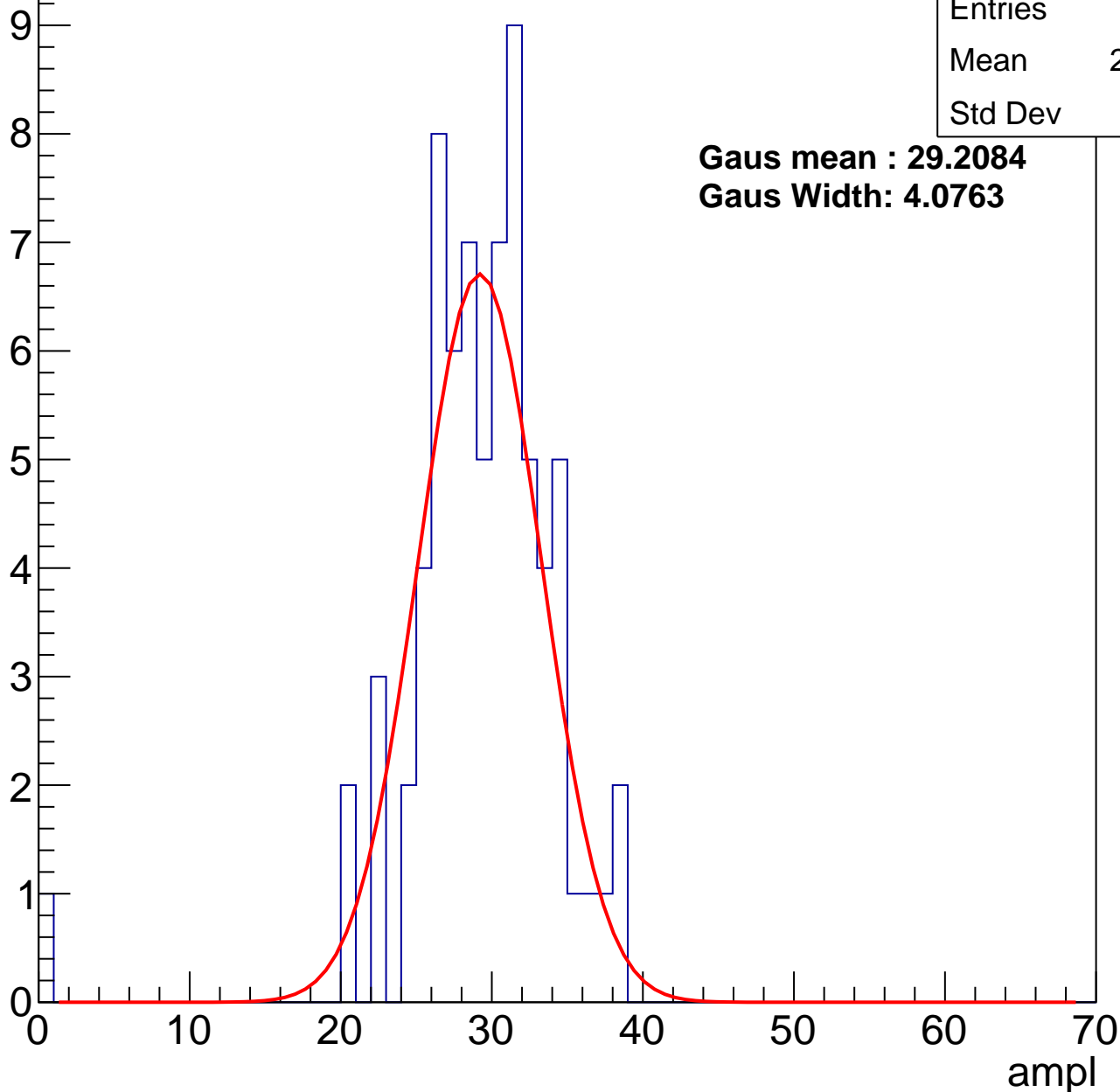
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.77
Std Dev	5.17

Gaus mean : 29.2084

Gaus Width: 4.0763



B1L102S, U10-ch21, adc1

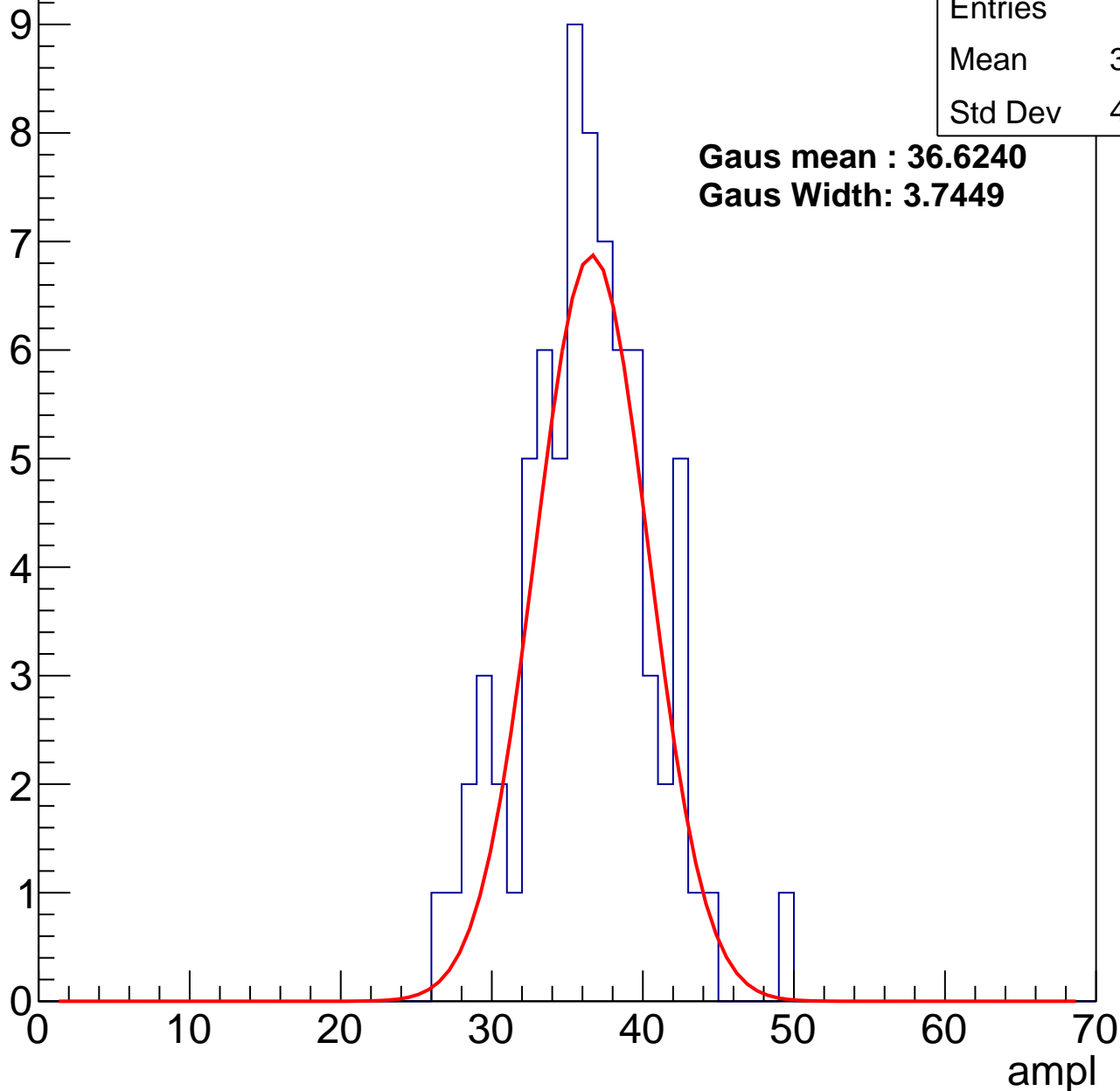
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.83
Std Dev	4.256

Gaus mean : 36.6240

Gaus Width: 3.7449



B1L102S, U10-ch21, adc2

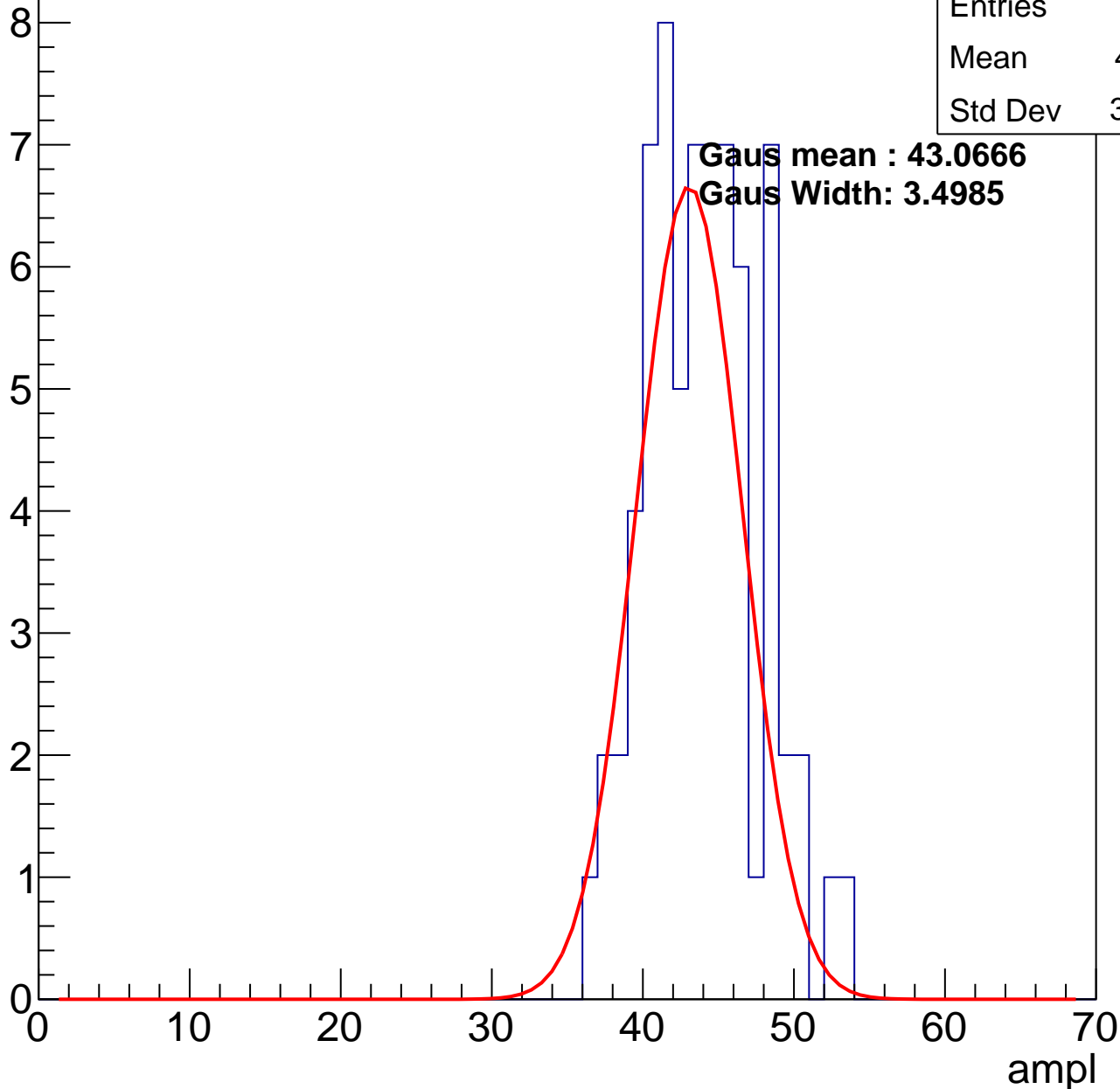
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.51
Std Dev	3.679

Gaus mean : 43.0666

Gaus Width: 3.4985

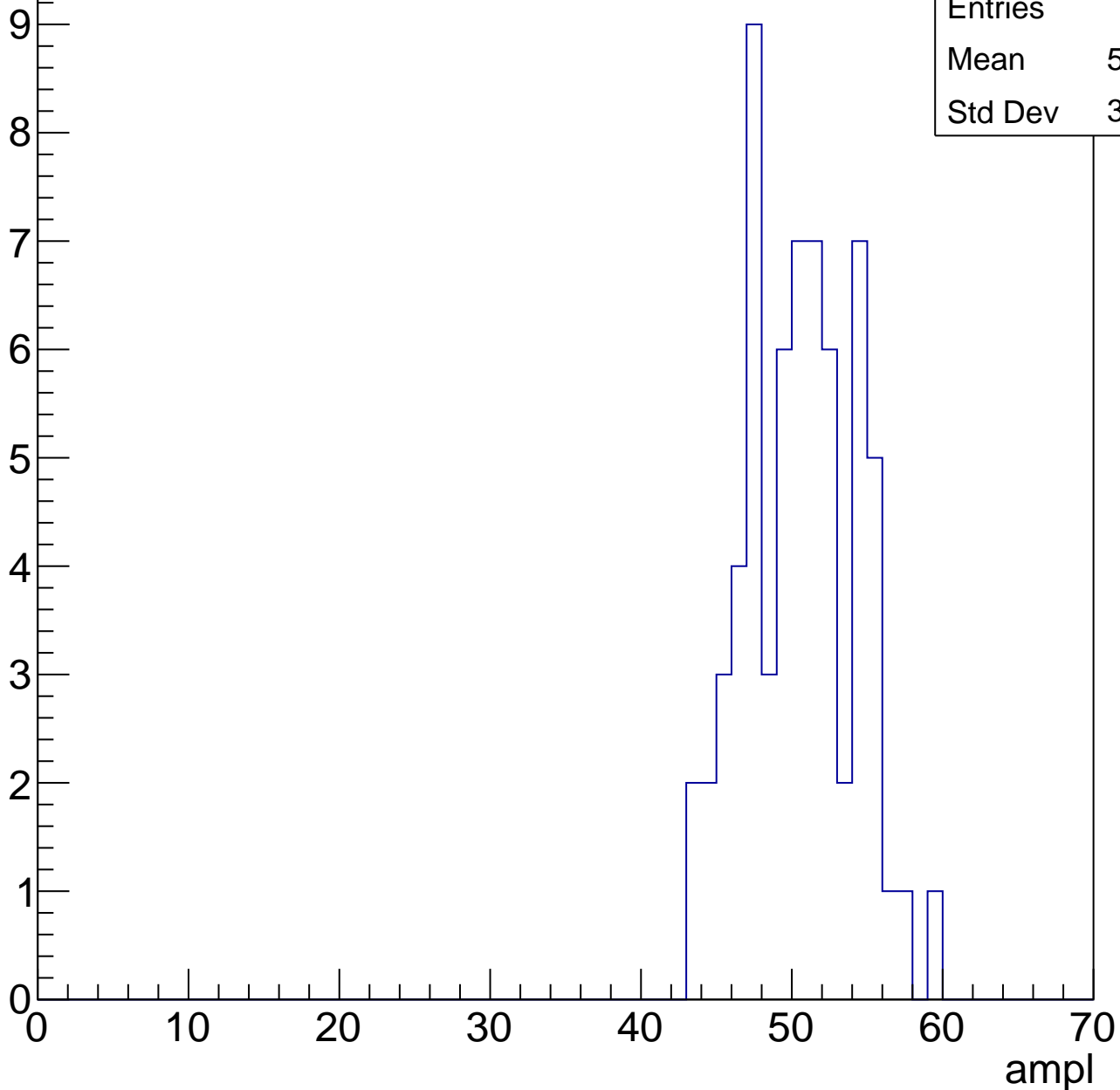


B1L102S, U10-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.06
Std Dev	3.609

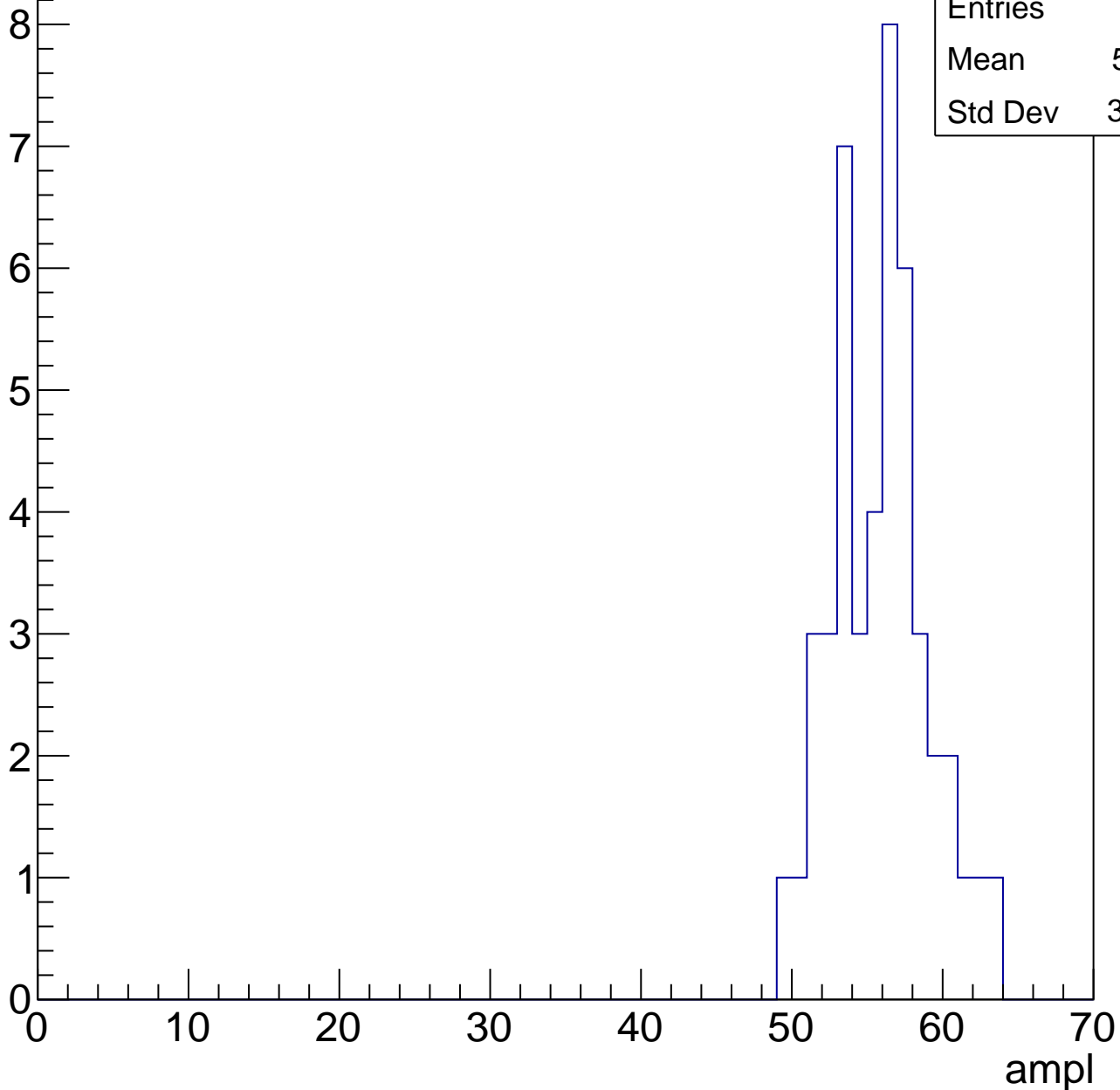


B1L102S, U10-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.41
Std Dev	3.125

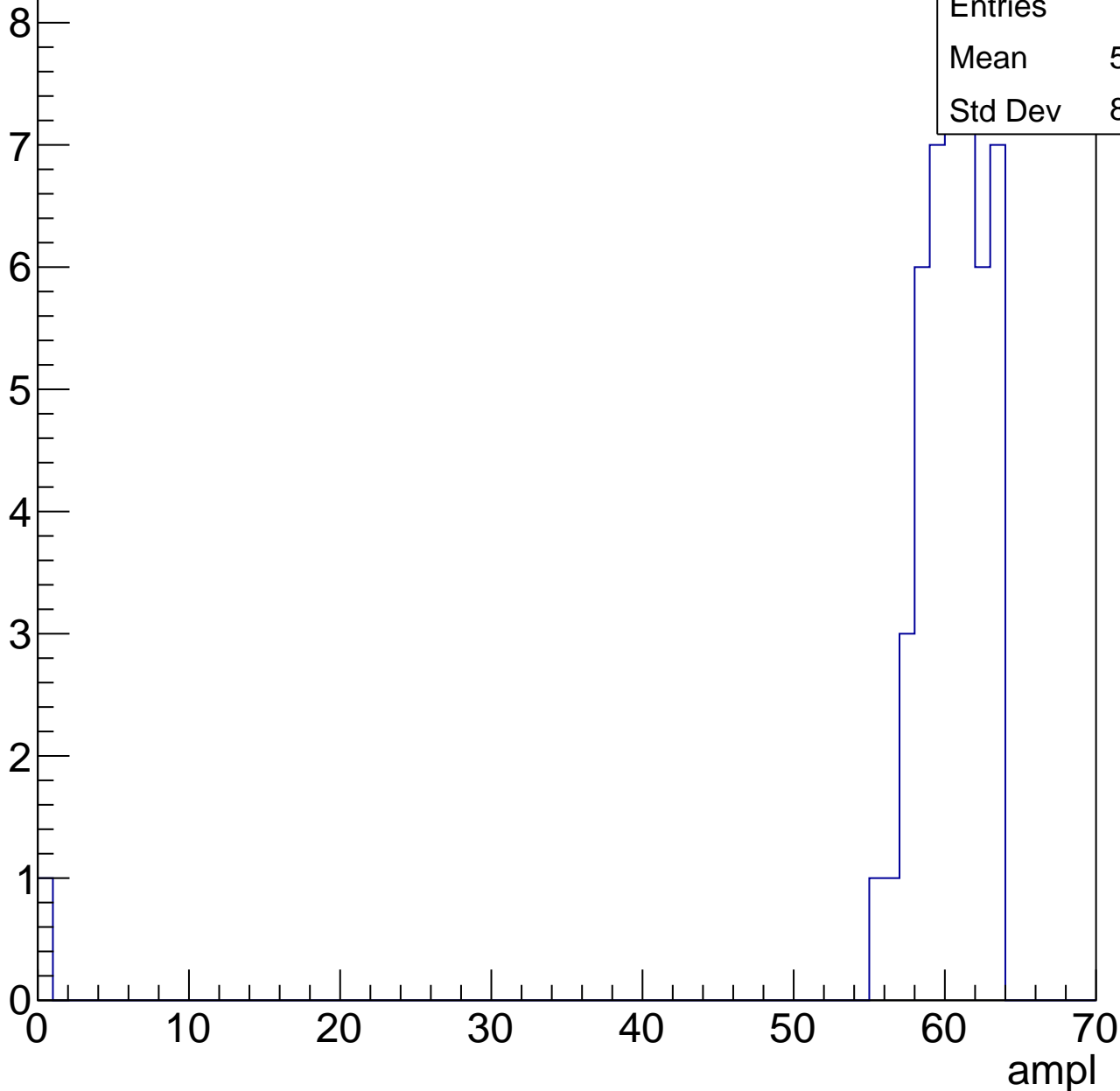


B1L102S, U10-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

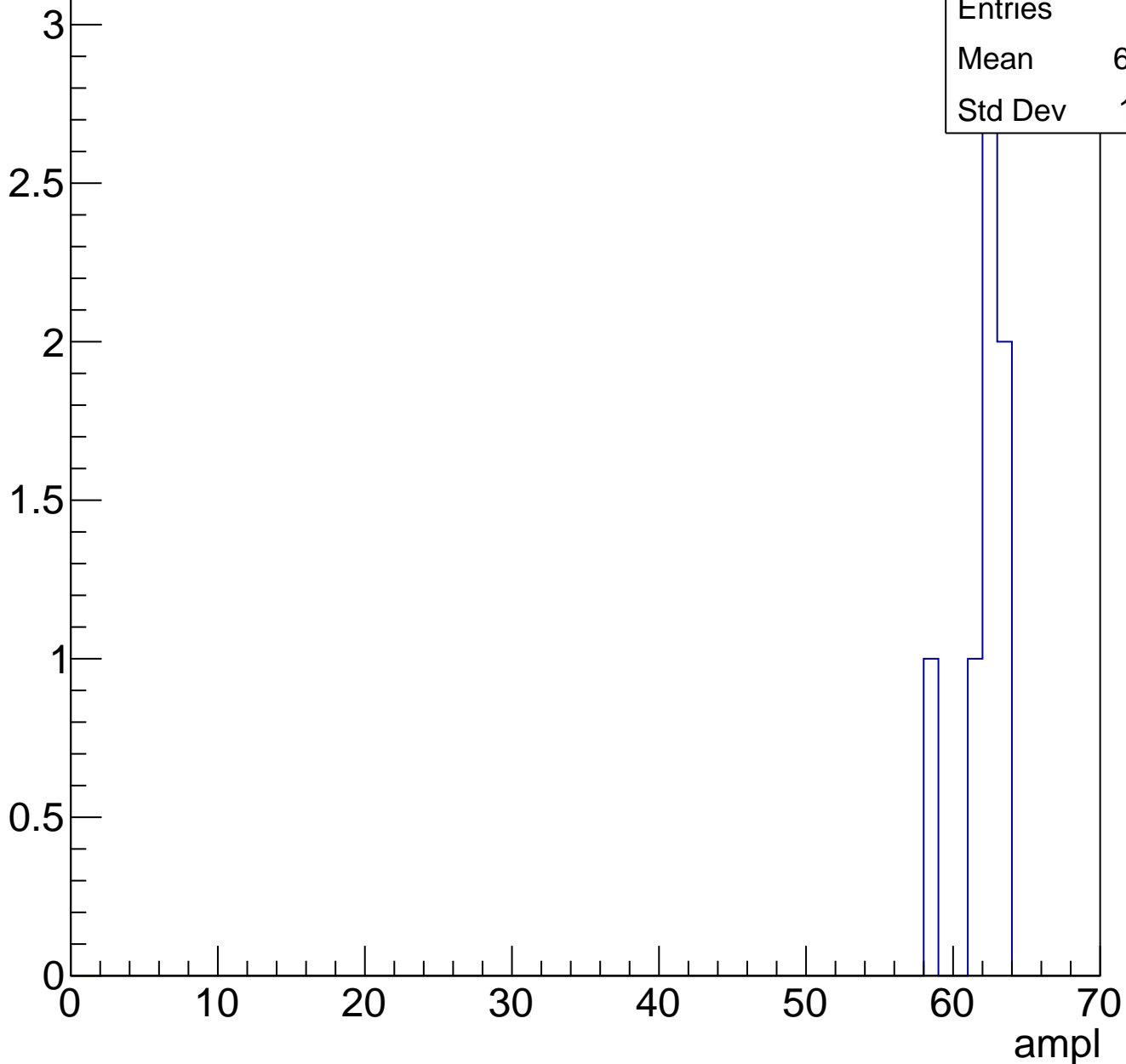
Entries	48
Mean	58.83
Std Dev	8.814



B1L102S, U10-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch22, adc0

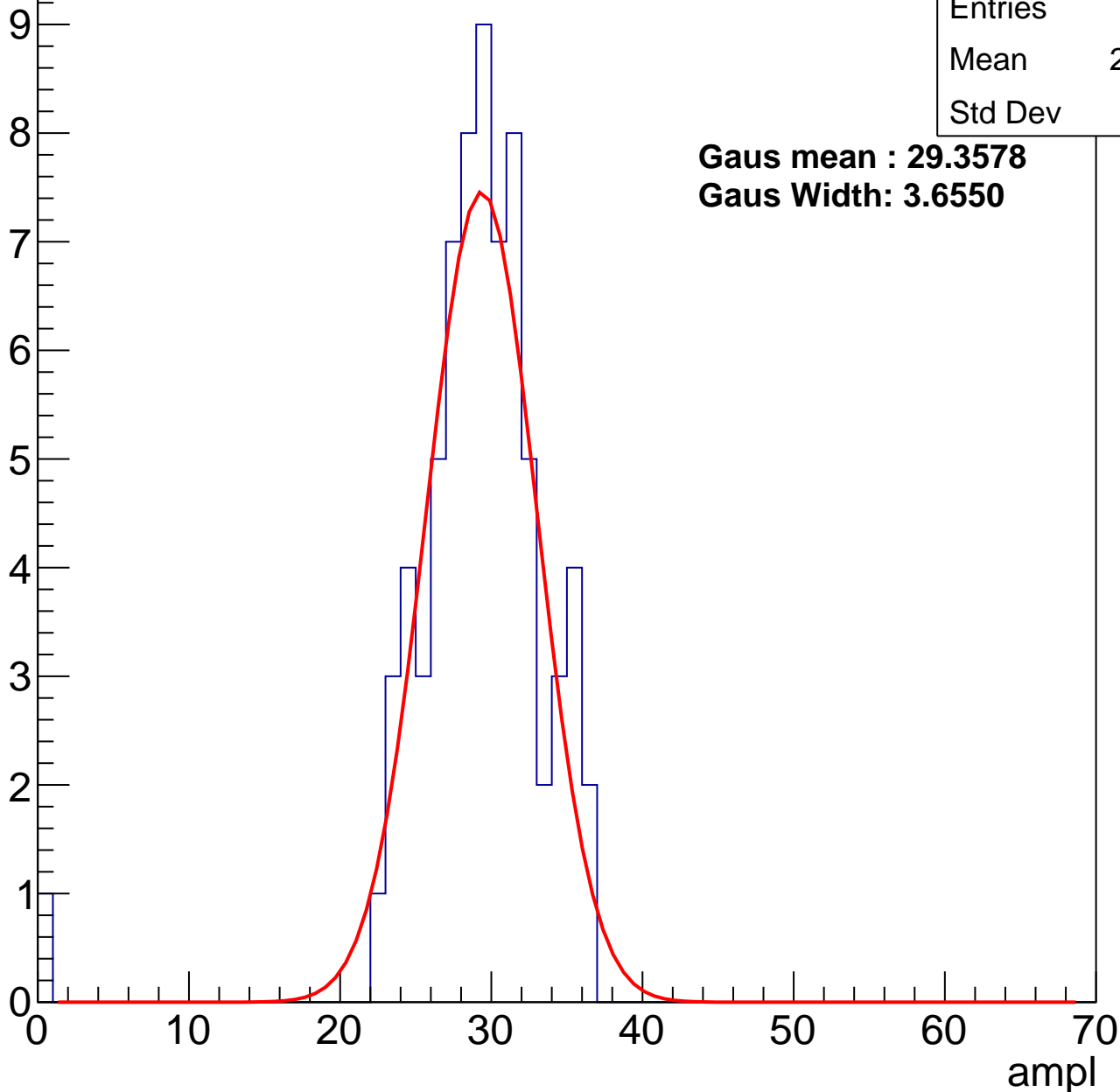
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.67
Std Dev	4.79

Gaus mean : 29.3578

Gaus Width: 3.6550



B1L102S, U10-ch22, adc1

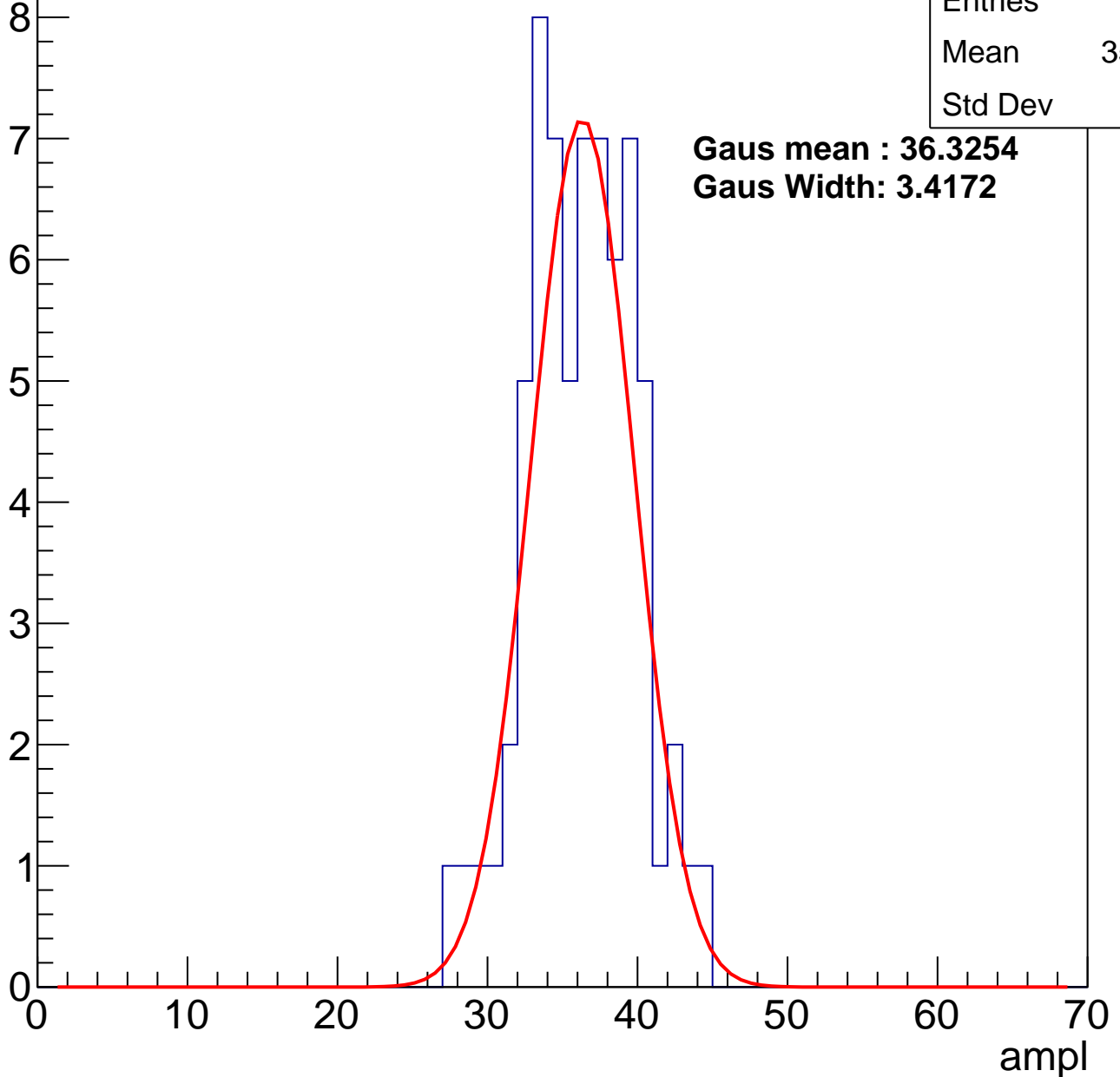
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.84
Std Dev	3.53

Gaus mean : 36.3254

Gaus Width: 3.4172



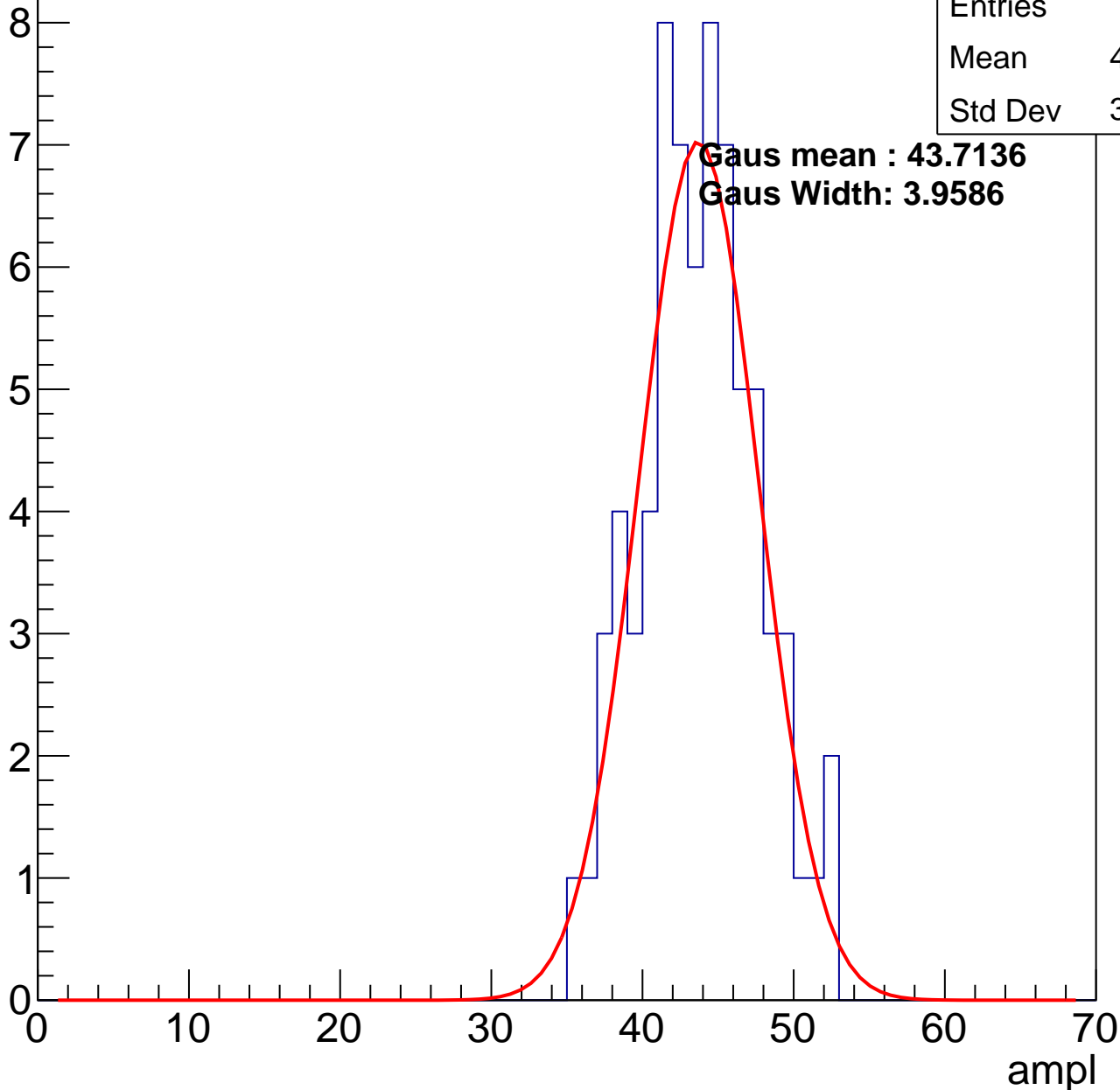
B1L102S, U10-ch22, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.32
Std Dev	3.829

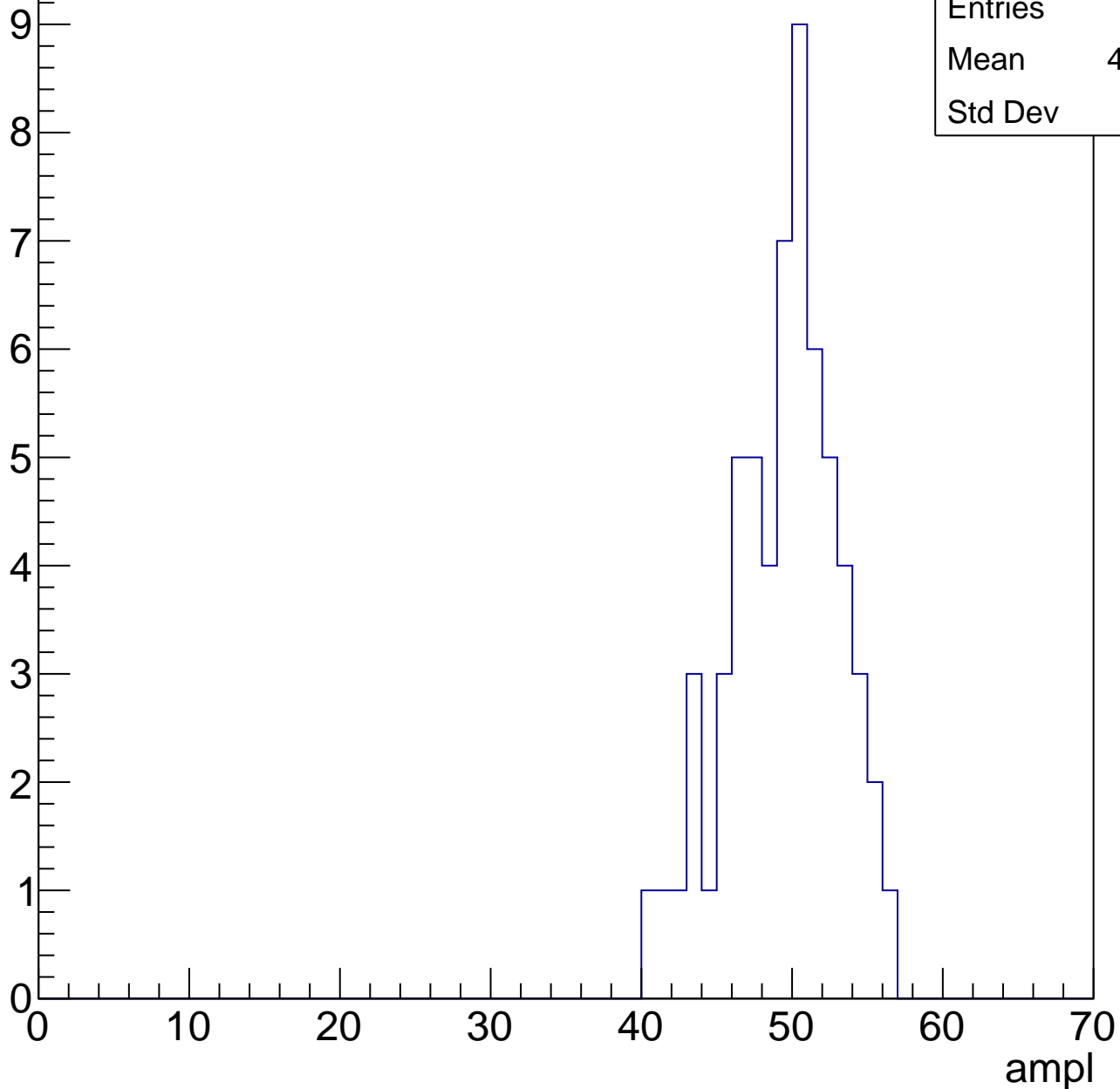
Gaus mean : 43.7136
Gaus Width: 3.9586



B1L102S, U10-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

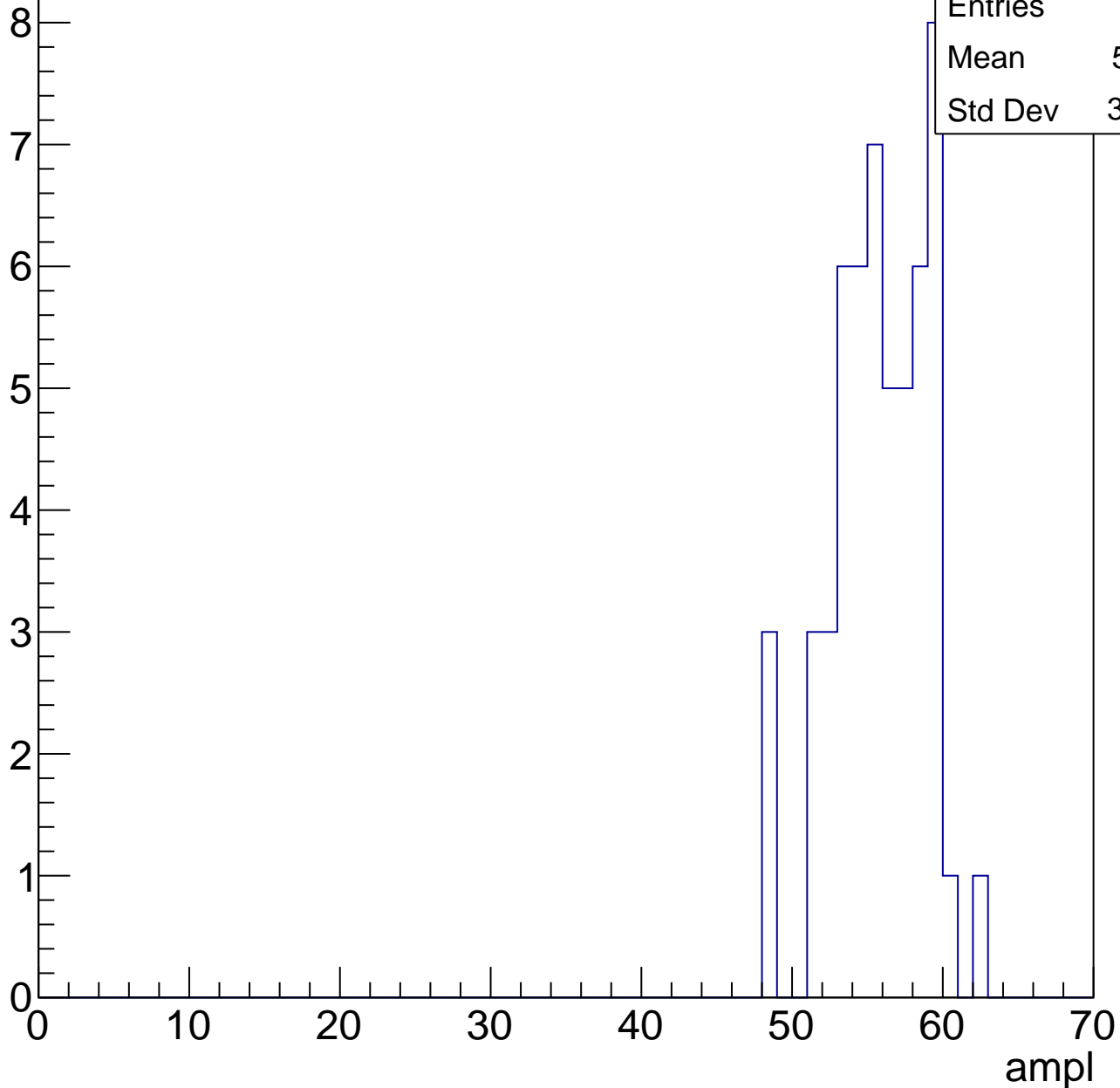


Entries	61
Mean	48.97
Std Dev	3.58

B1L102S, U10-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.78
Std Dev	8.667

ampl

0

10

20

30

40

50

60

70

B1L102S, U10-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch23, adc0

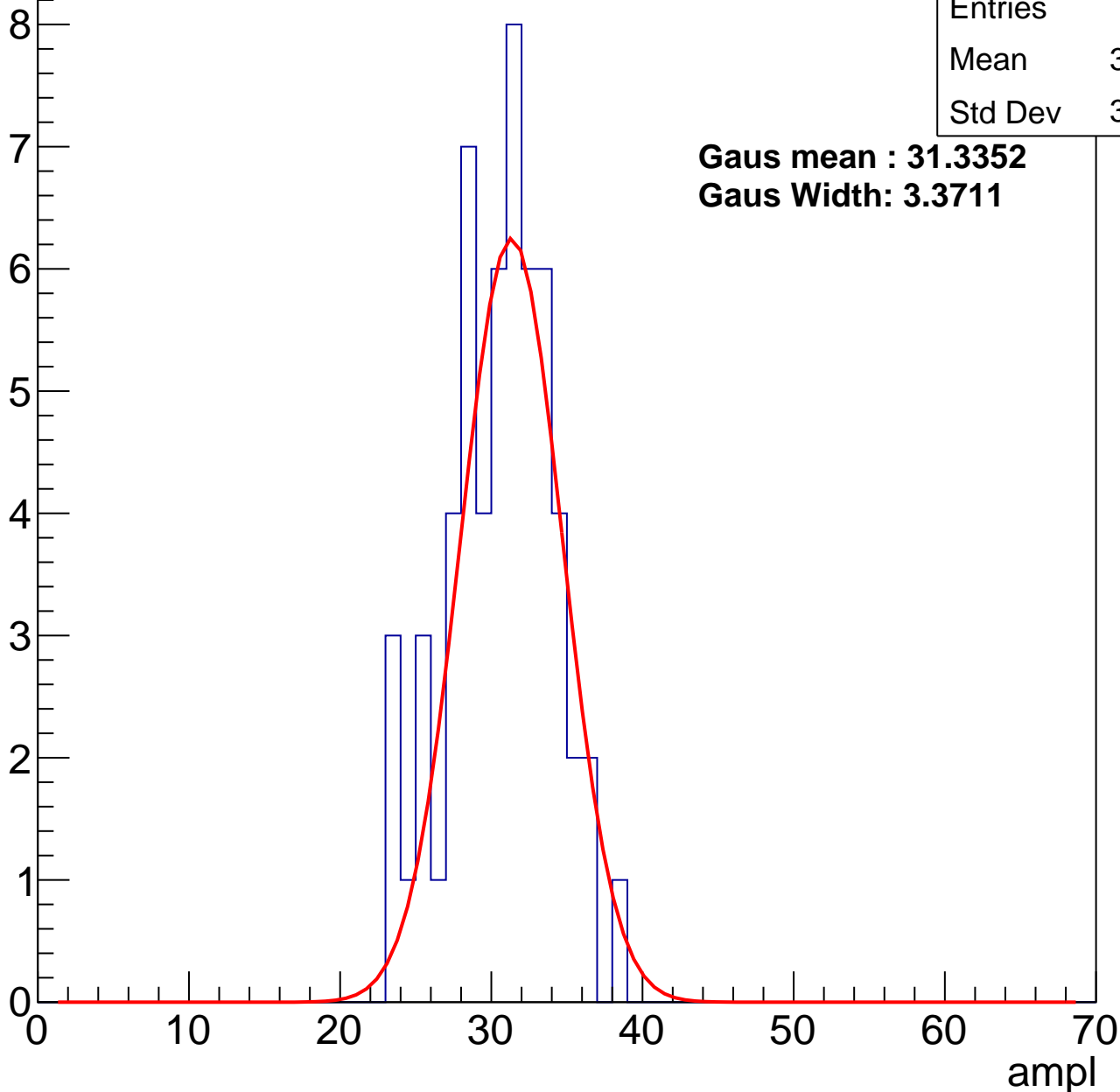
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	30.14
Std Dev	3.416

Gaus mean : 31.3352

Gaus Width: 3.3711



B1L102S, U10-ch23, adc1

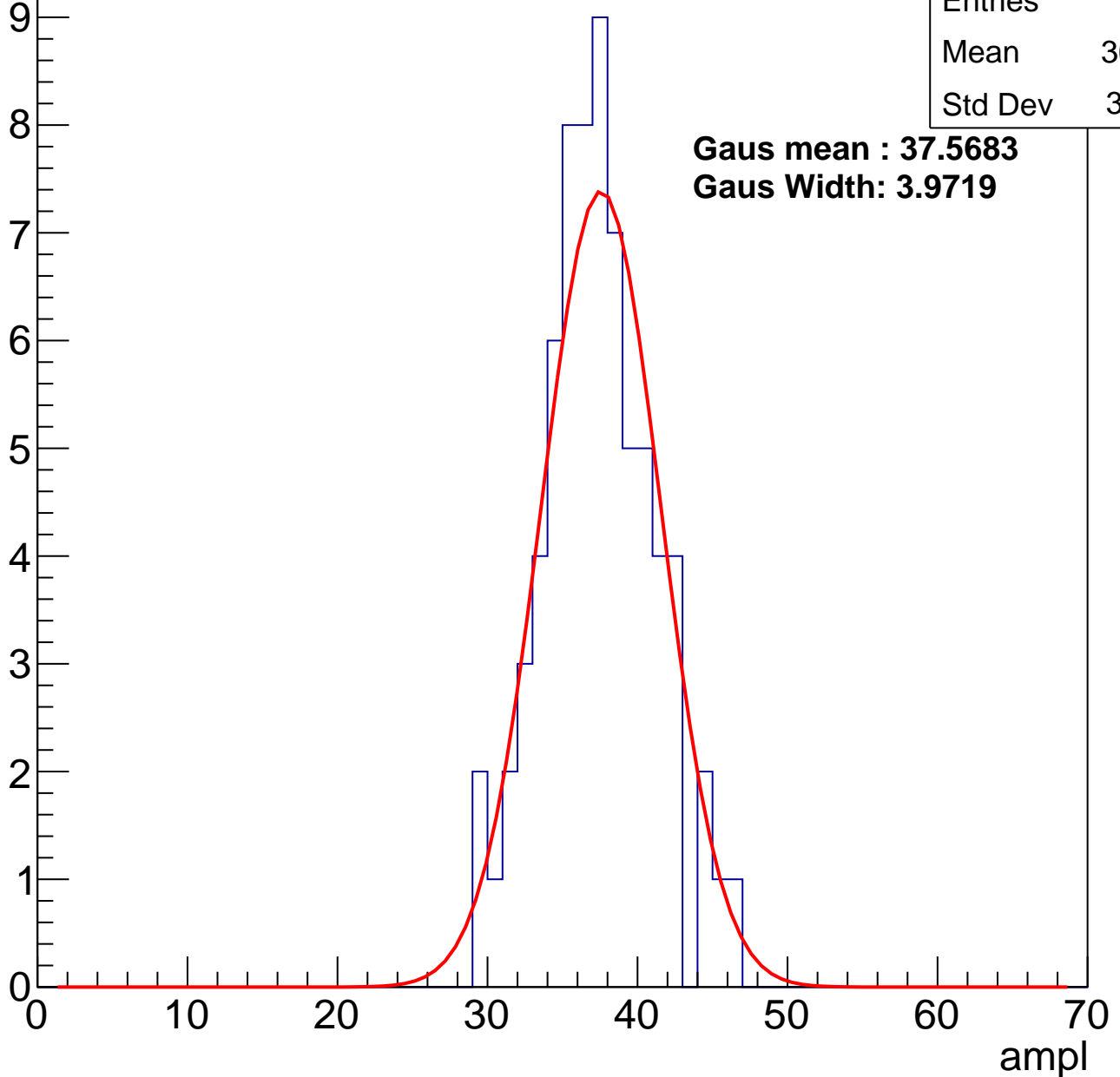
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.88
Std Dev	3.651

Gaus mean : 37.5683

Gaus Width: 3.9719



B1L102S, U10-ch23, adc2

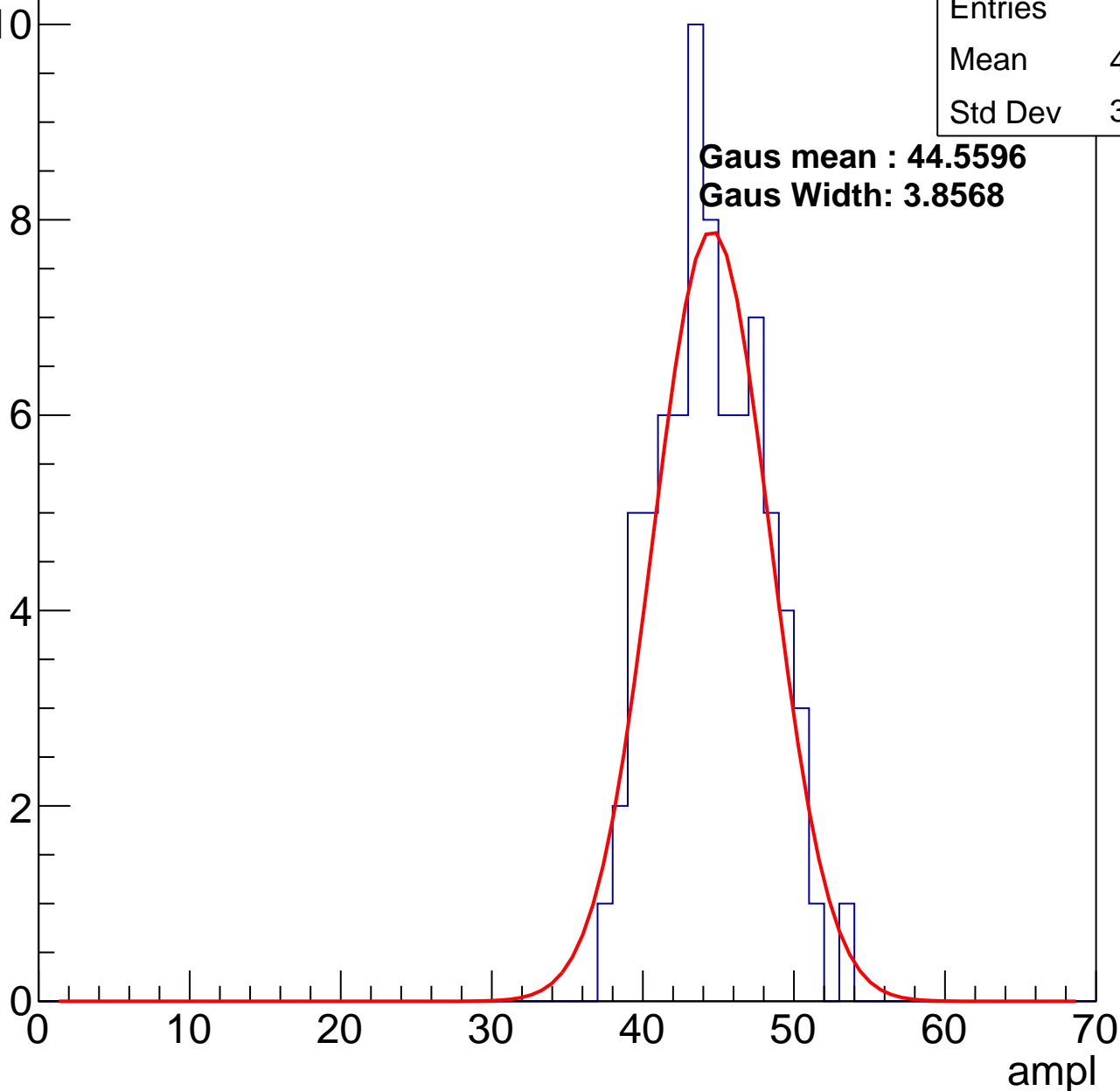
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	44.12
Std Dev	3.487

Gaus mean : 44.5596

Gaus Width: 3.8568

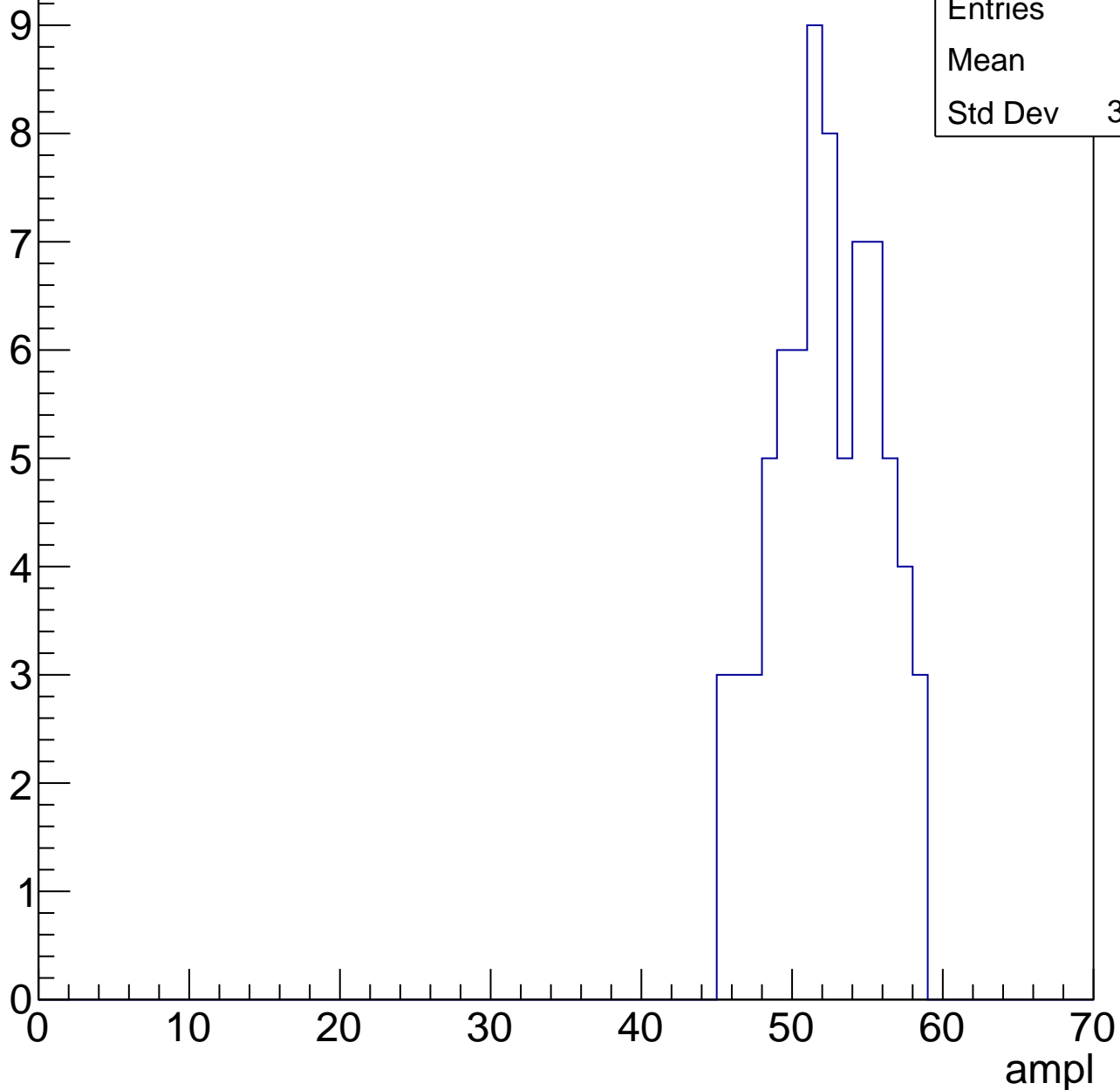


B1L102S, U10-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	51.8
Std Dev	3.444

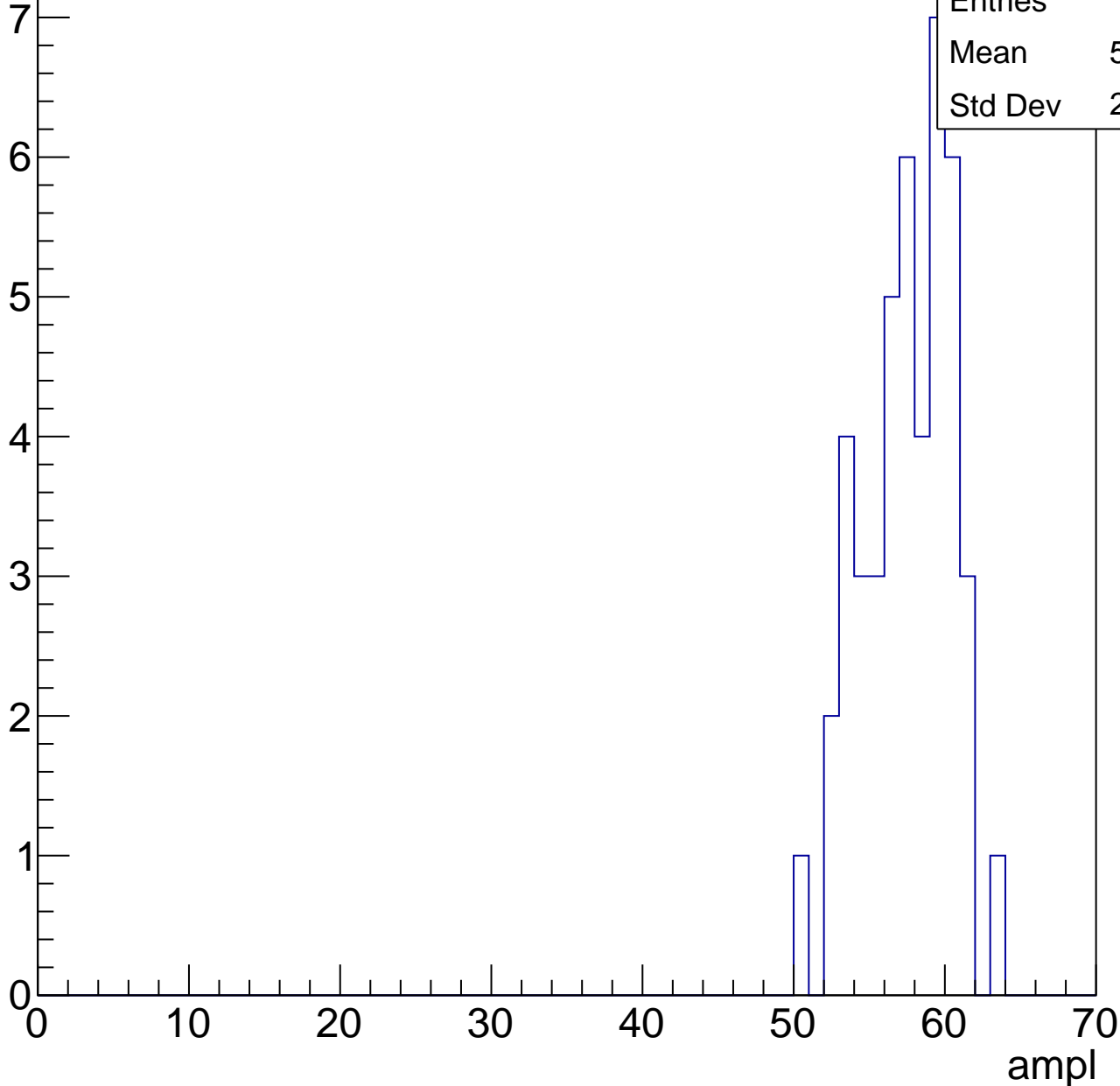


B1L102S, U10-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	57.02
Std Dev	2.894

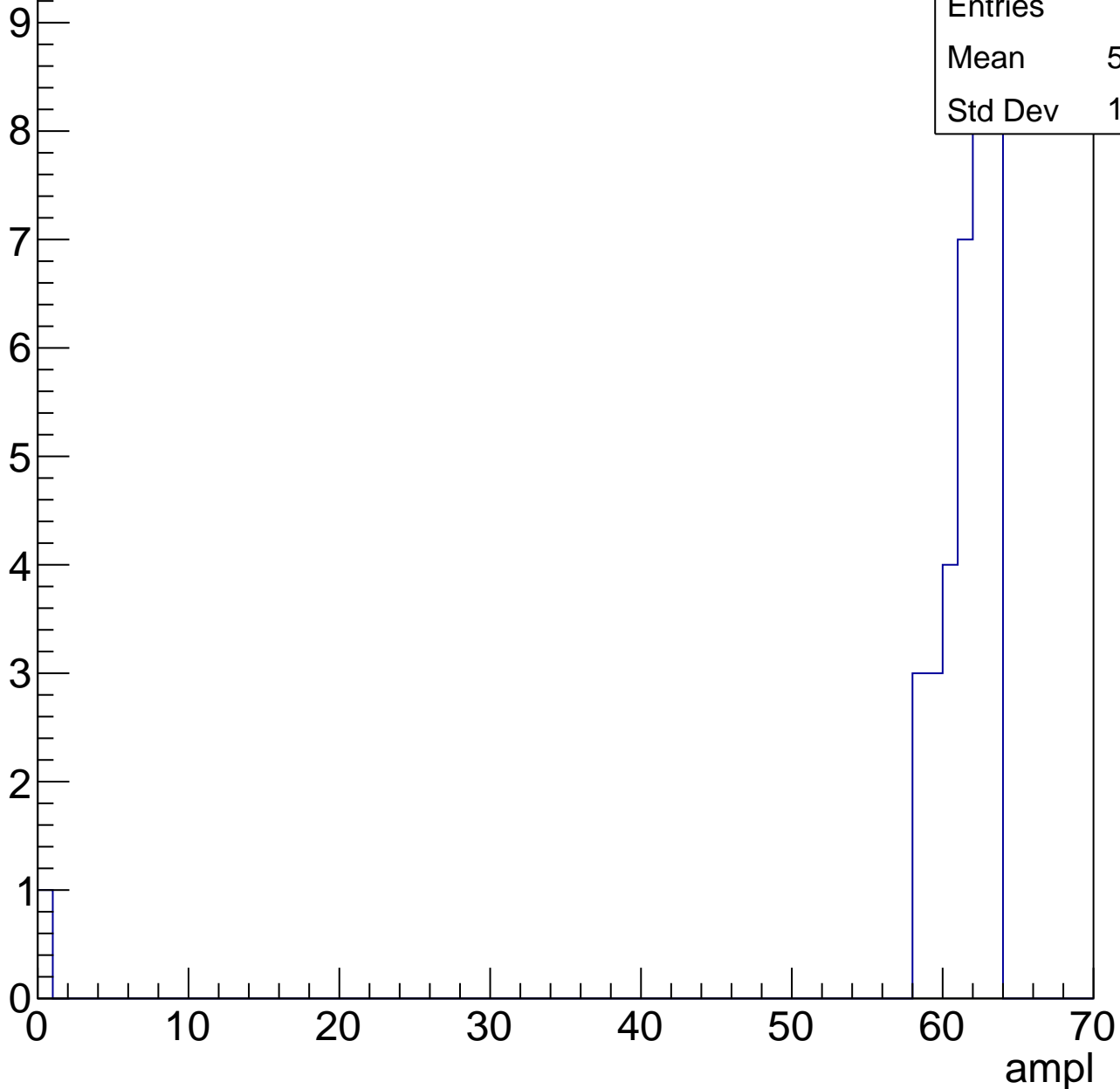


B1L102S, U10-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.46
Std Dev	10.32



B1L102S, U10-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch24, adc0

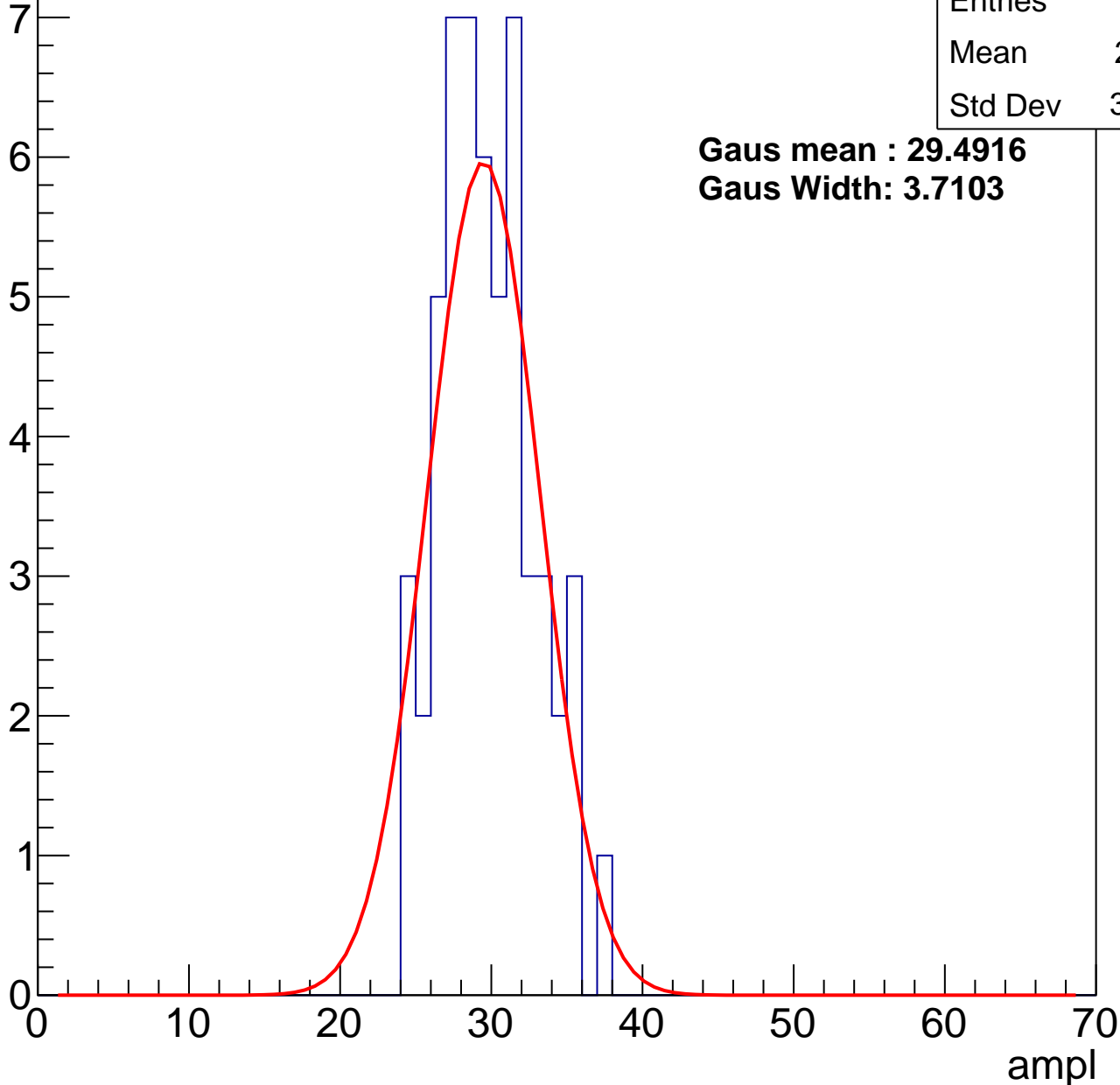
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	29.31
Std Dev	3.078

Gaus mean : 29.4916

Gaus Width: 3.7103



B1L102S, U10-ch24, adc1

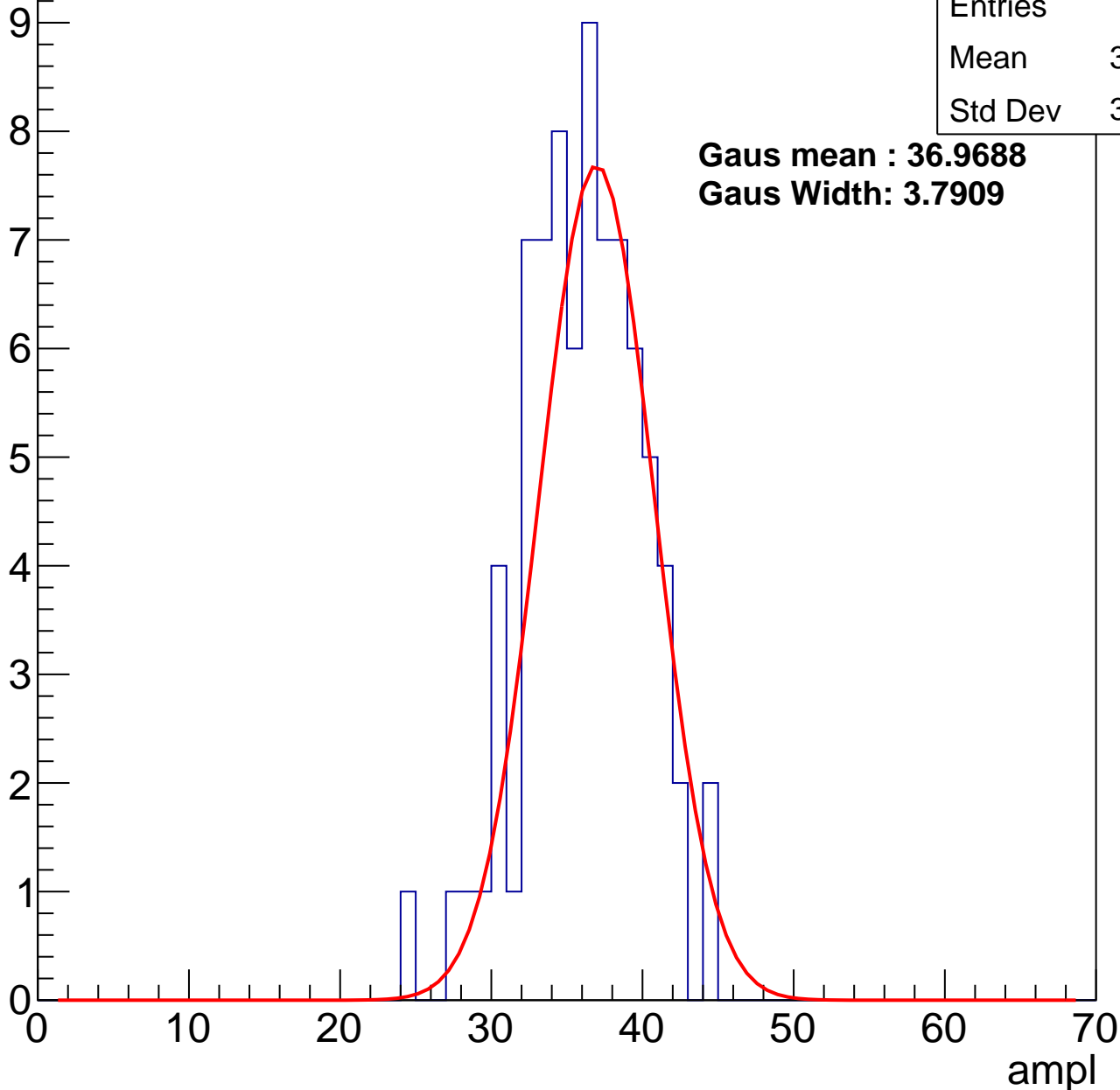
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.63
Std Dev	3.862

Gaus mean : 36.9688

Gaus Width: 3.7909



B1L102S, U10-ch24, adc2

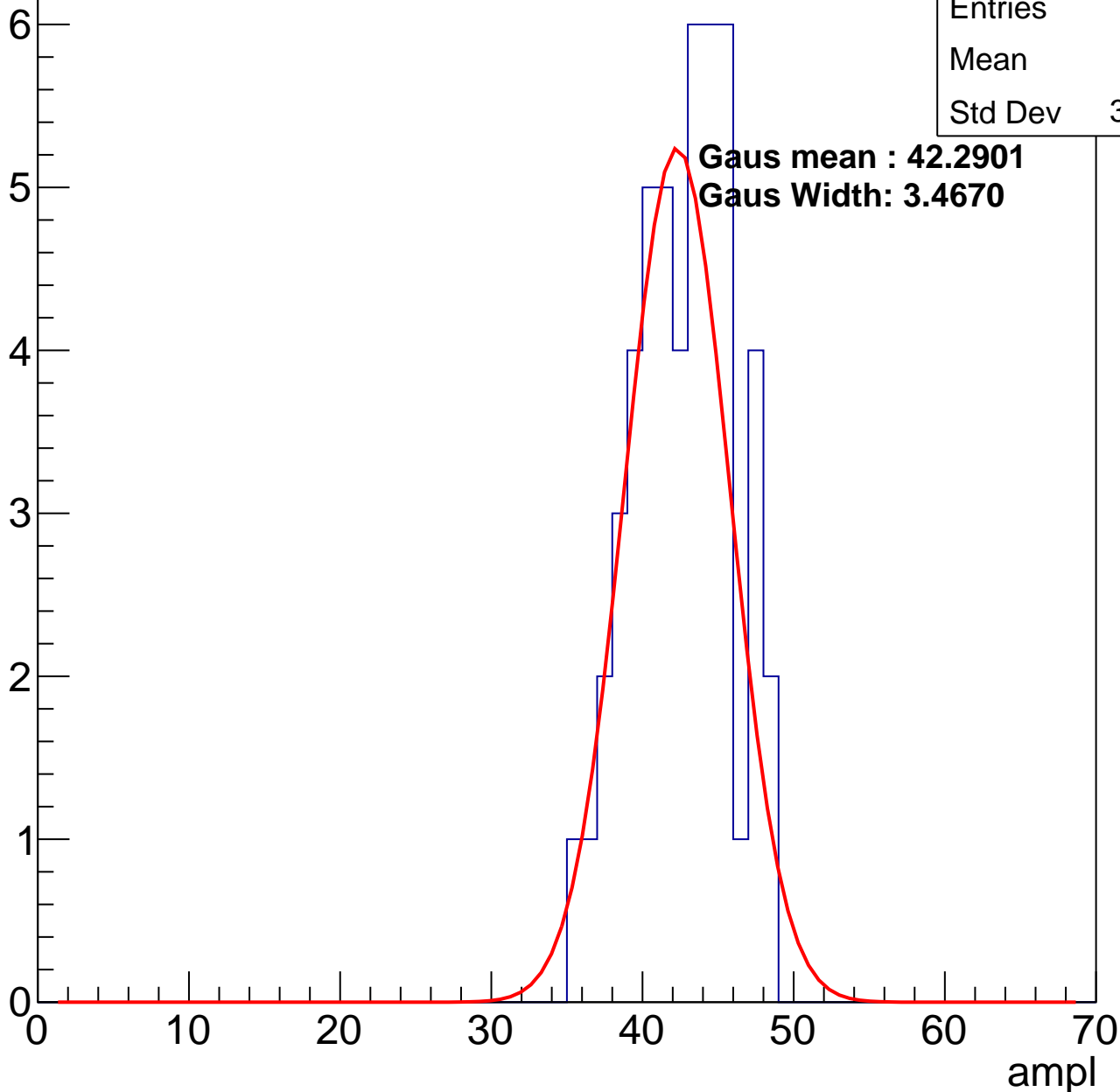
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	42.2
Std Dev	3.206

Gaus mean : 42.2901

Gaus Width: 3.4670

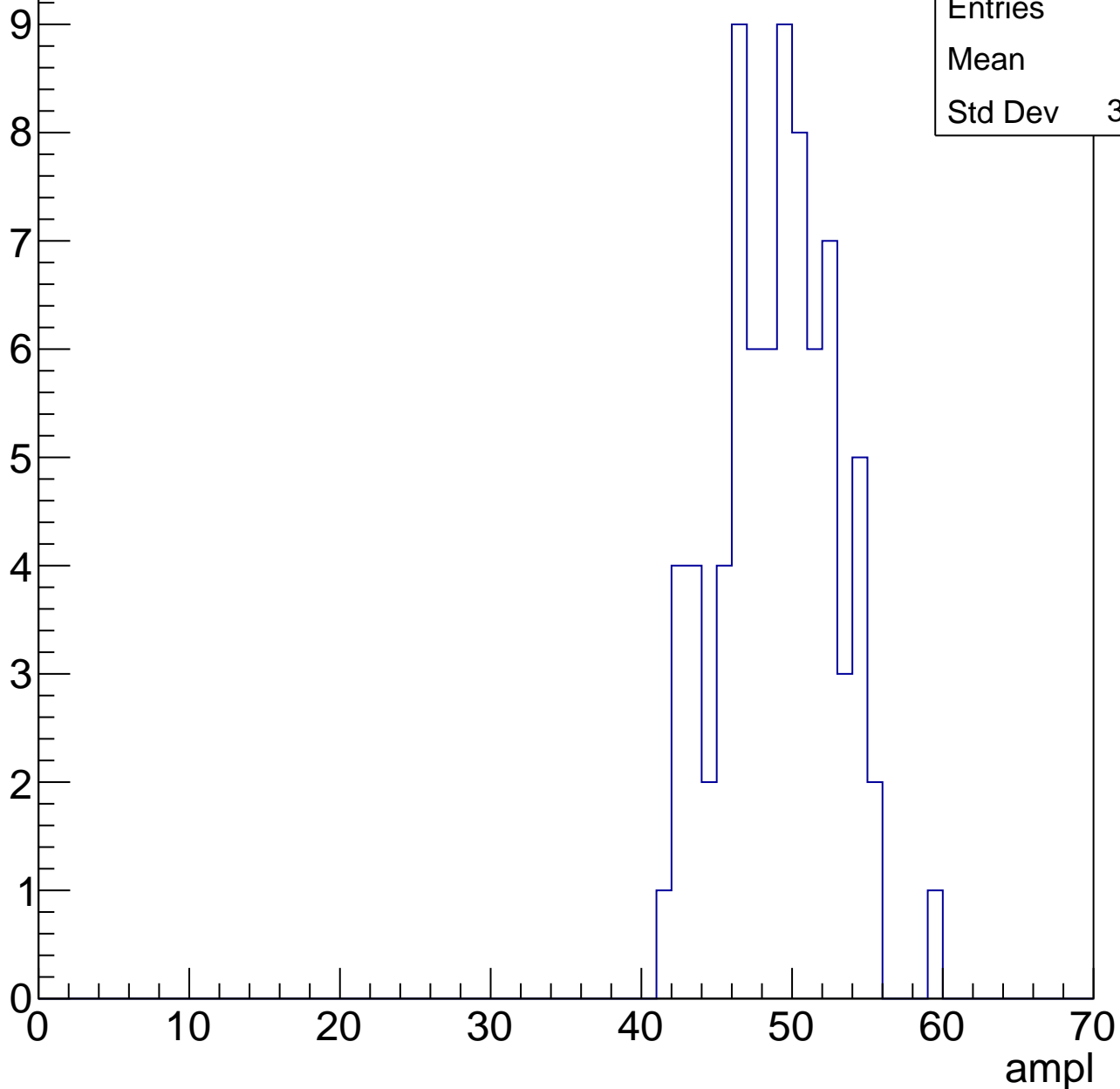


B1L102S, U10-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.6
Std Dev	3.704

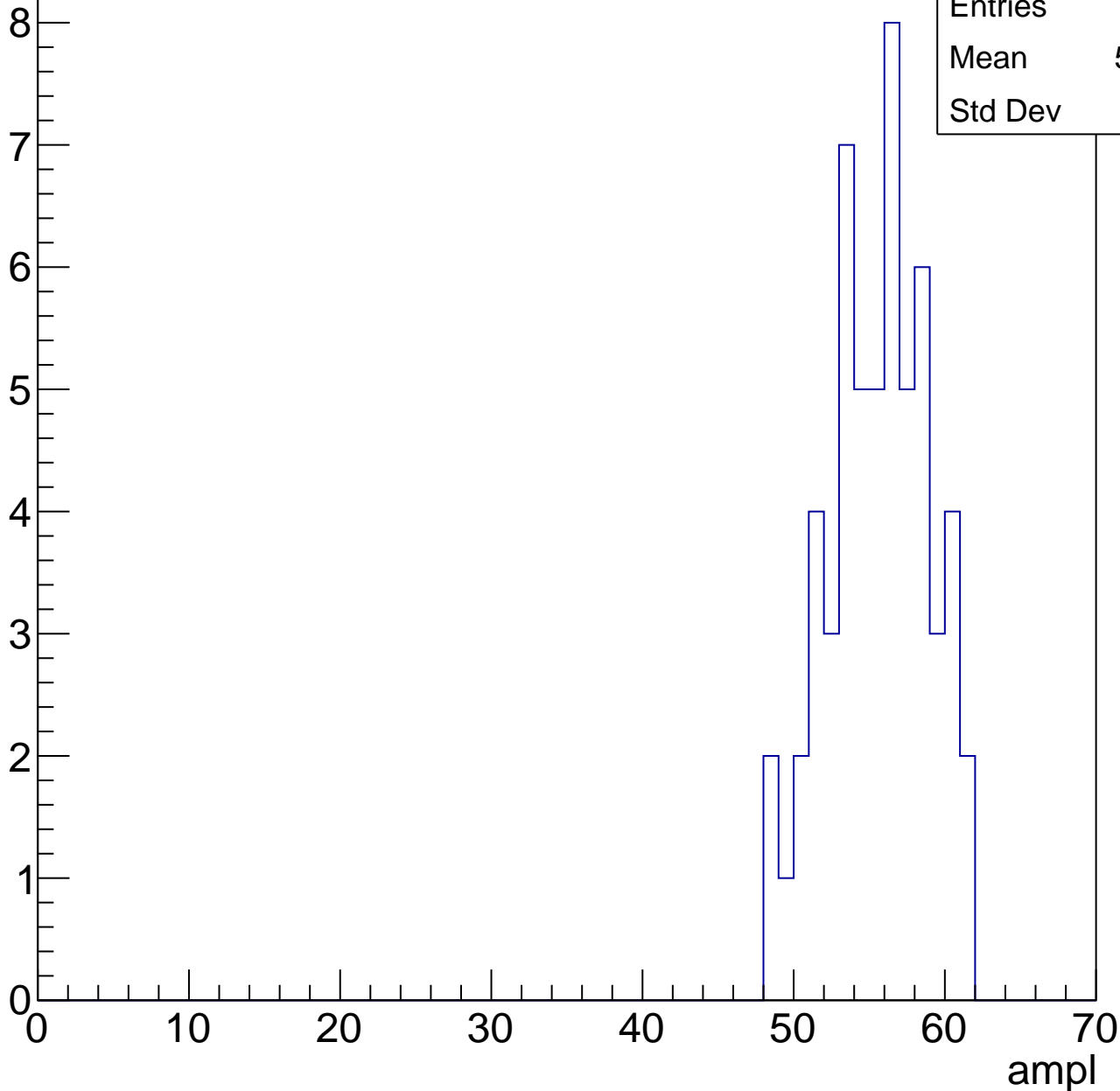


B1L102S, U10-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.11
Std Dev	3.27

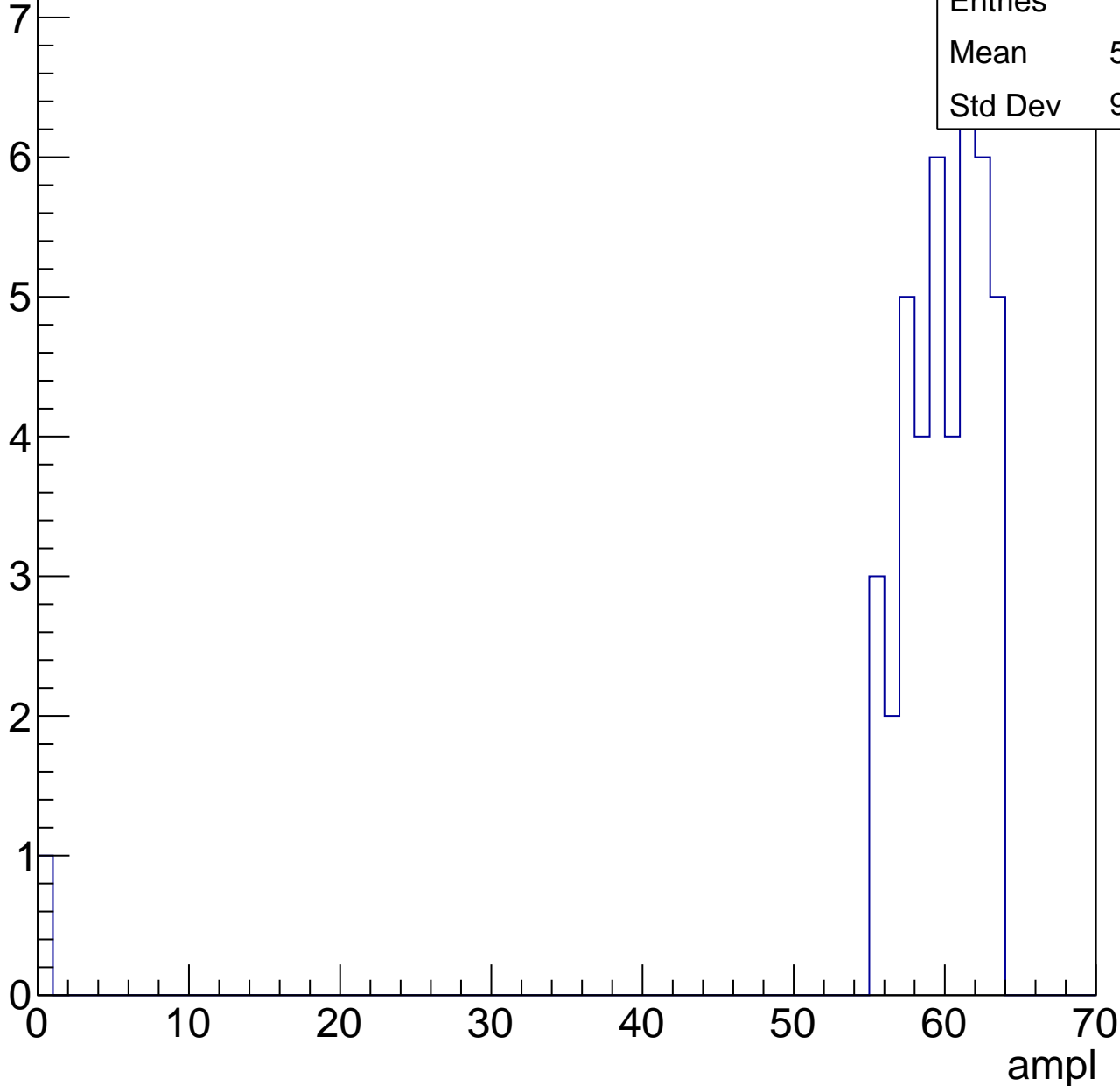


B1L102S, U10-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.19
Std Dev	9.287

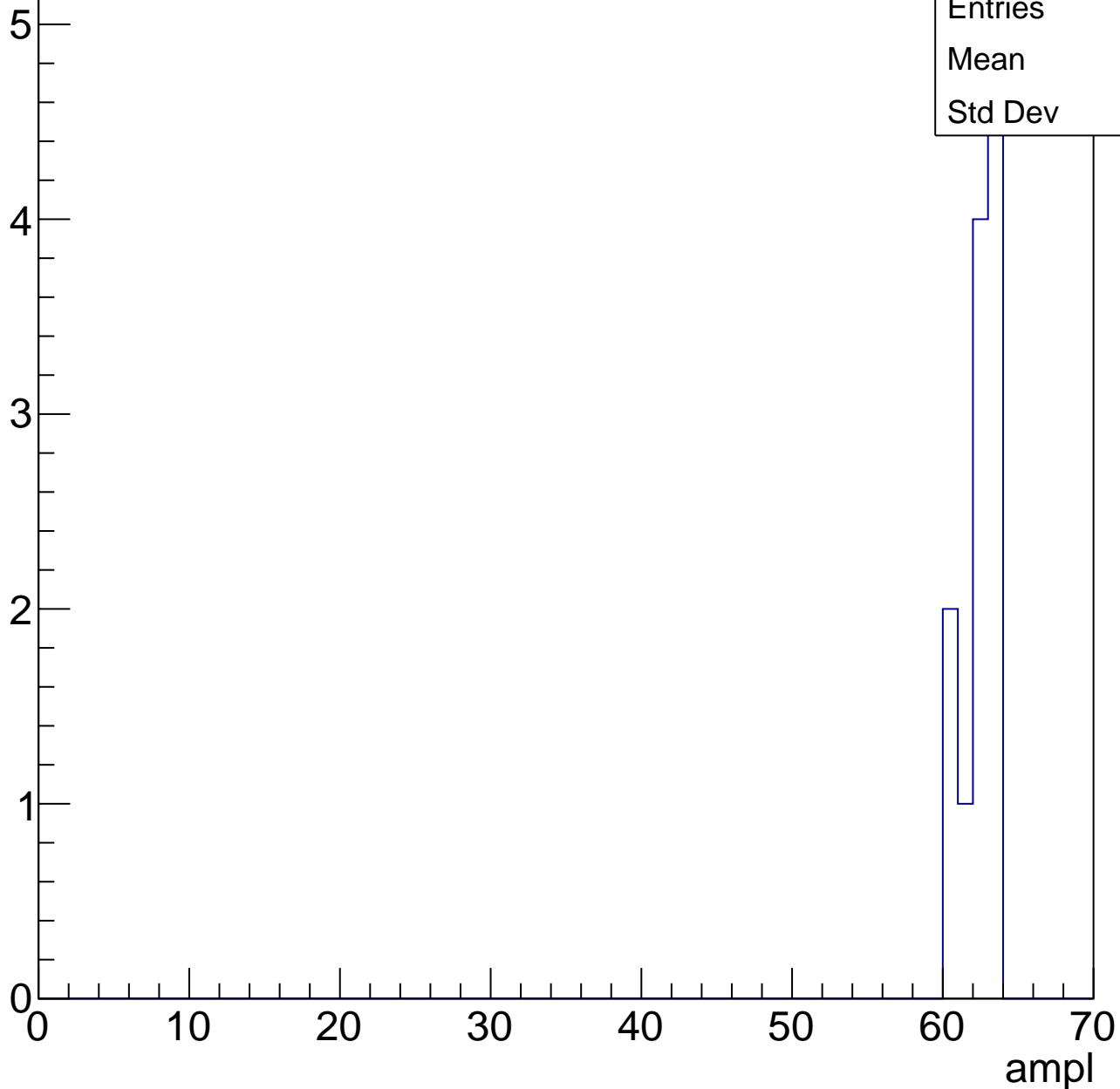


B1L102S, U10-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62
Std Dev	1.08



B1L102S, U10-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch25, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	52
Mean	28.73
Std Dev	3.052

Gaus mean : 29.1683

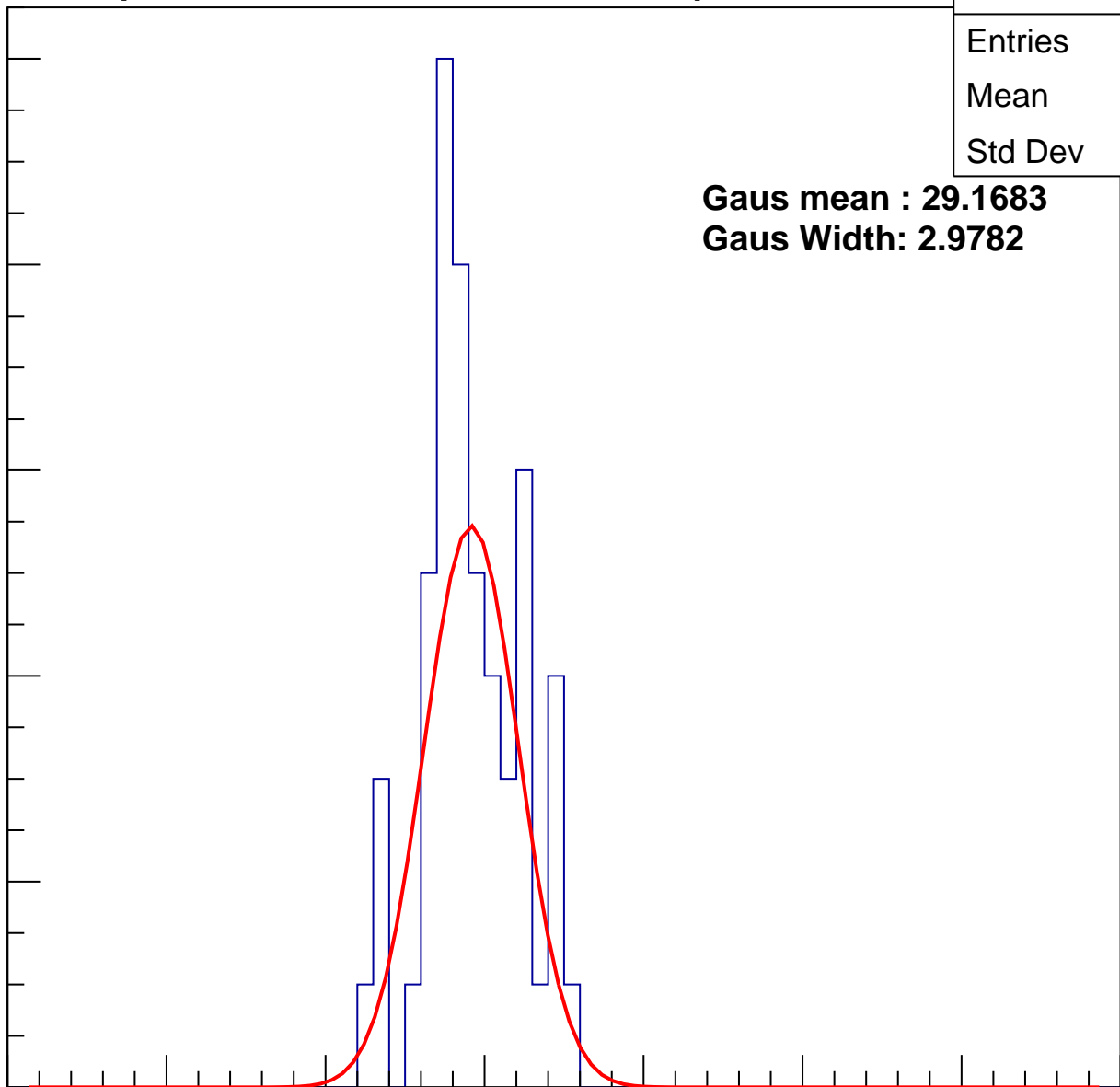
Gaus Width: 2.9782

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch25, adc1

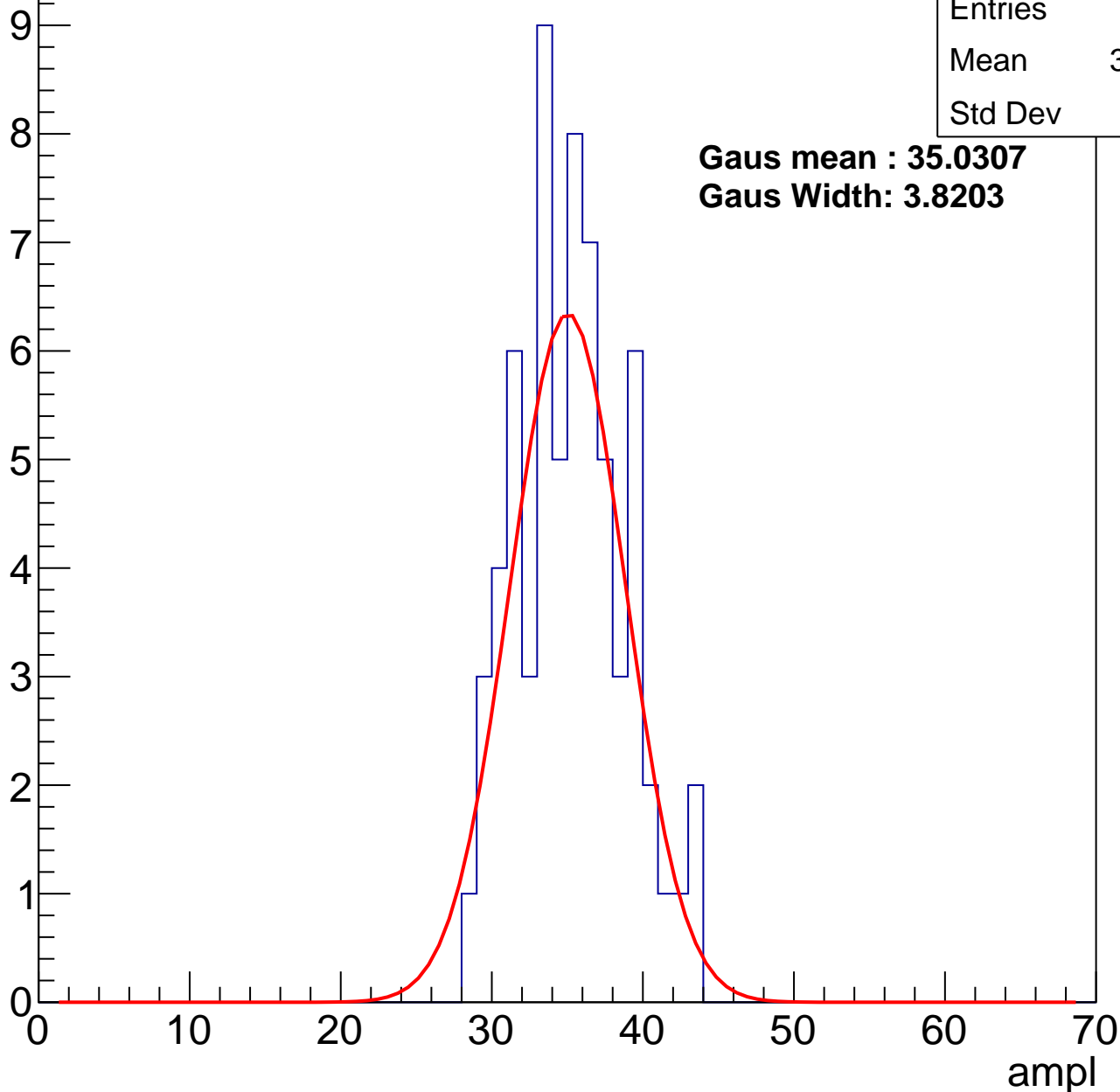
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.82
Std Dev	3.55

Gaus mean : 35.0307

Gaus Width: 3.8203



B1L102S, U10-ch25, adc2

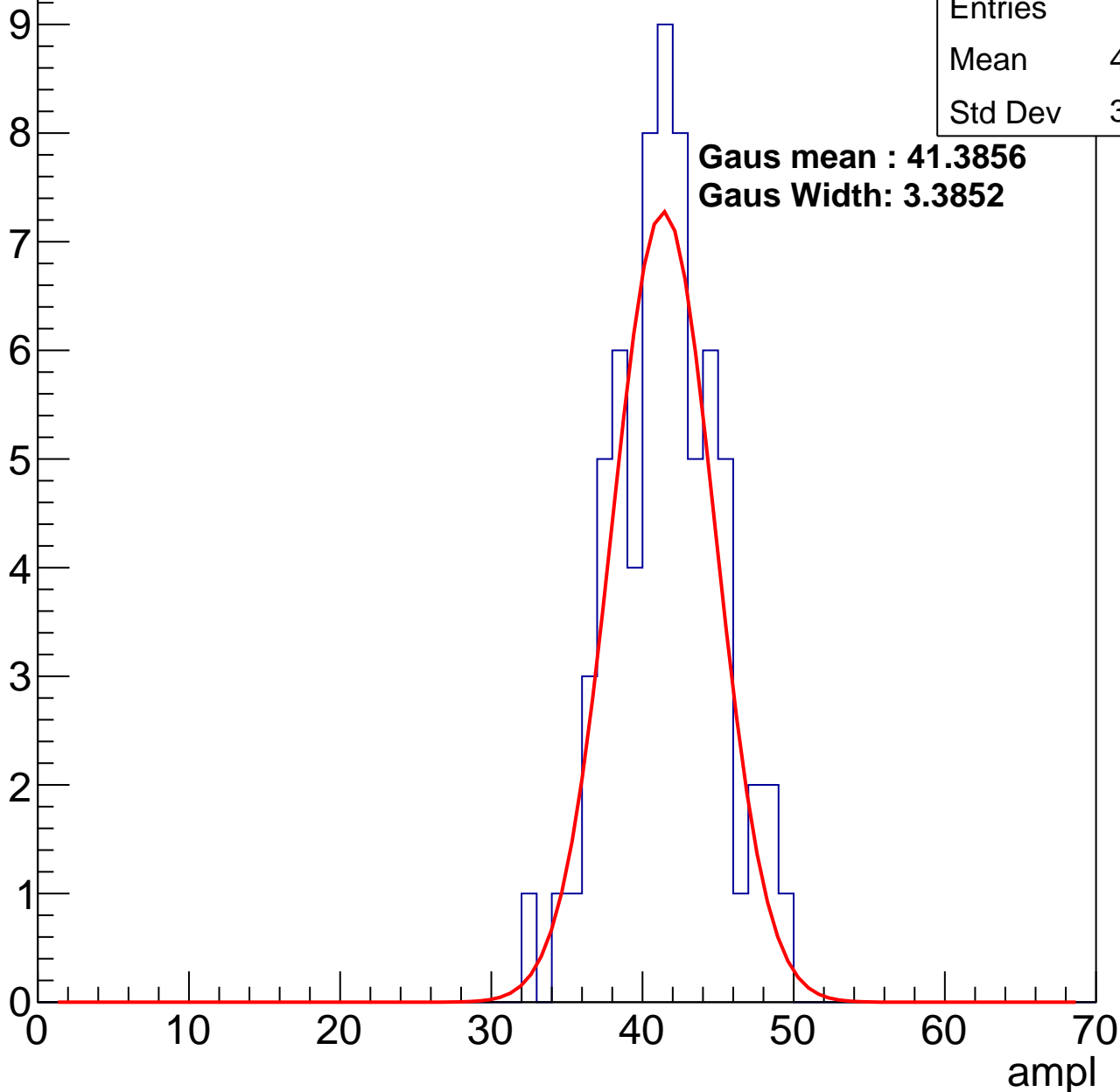
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.06
Std Dev	3.472

Gaus mean : 41.3856

Gaus Width: 3.3852

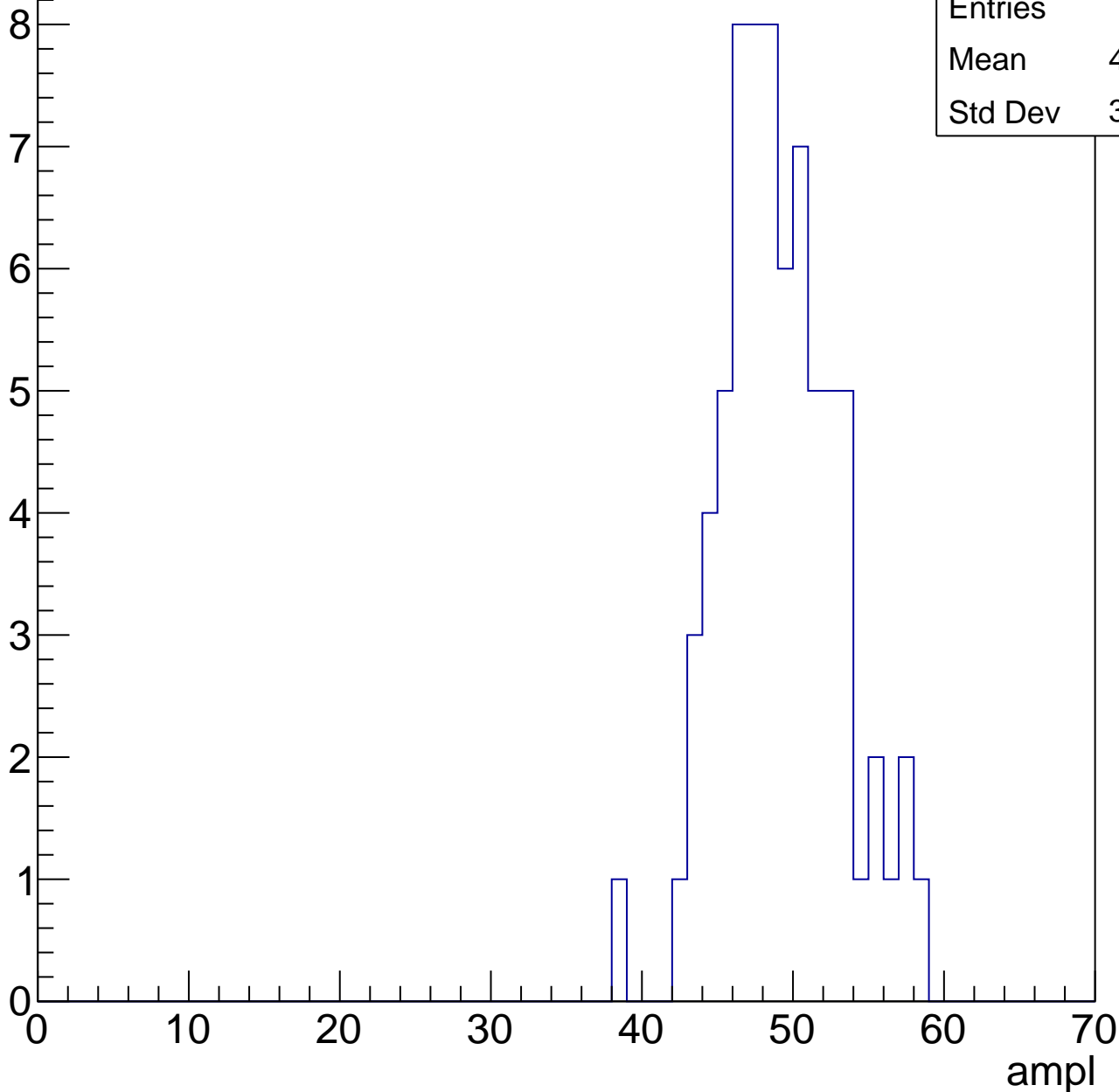


B1L102S, U10-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.68
Std Dev	3.828

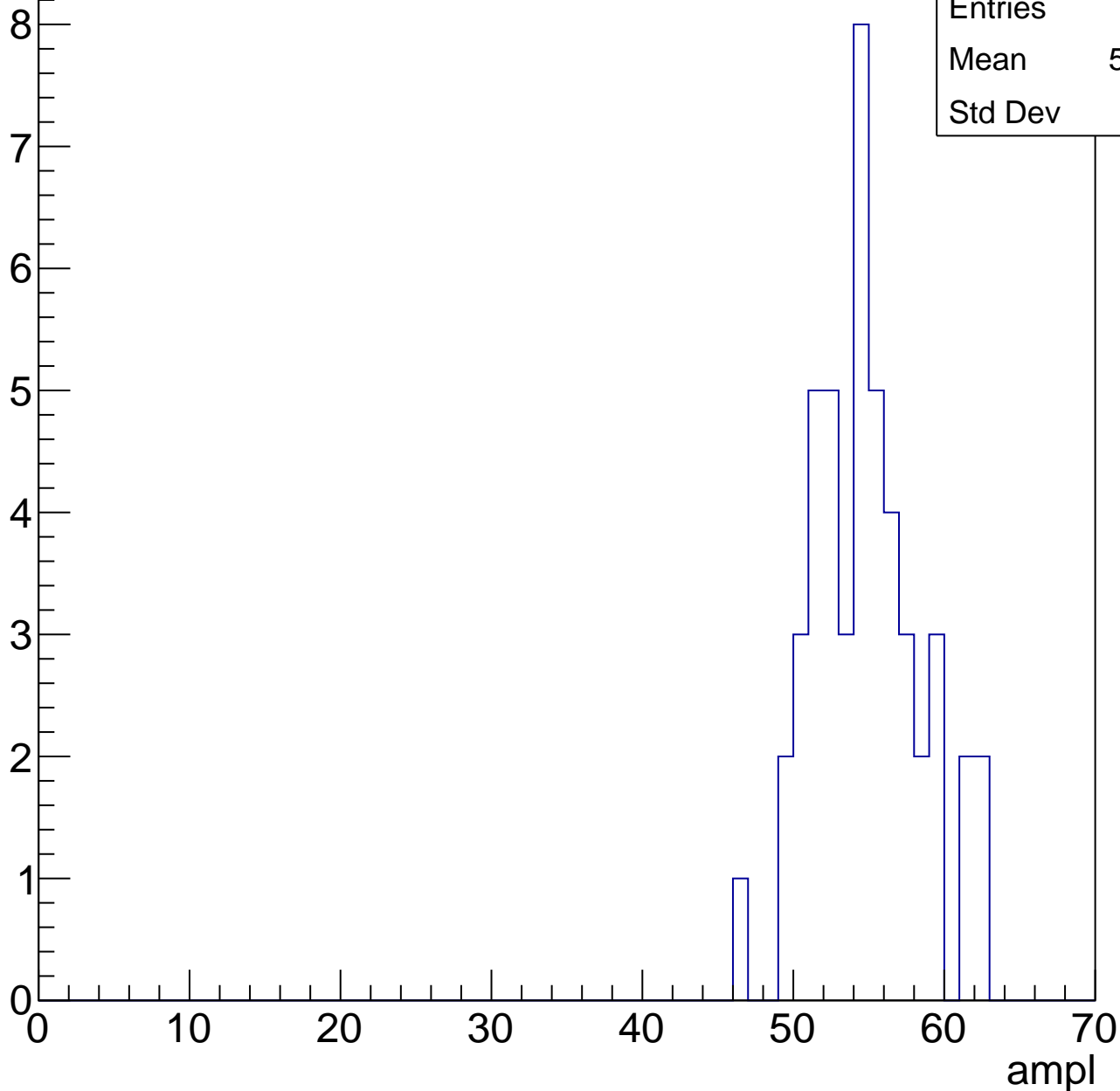


B1L102S, U10-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.35
Std Dev	3.55

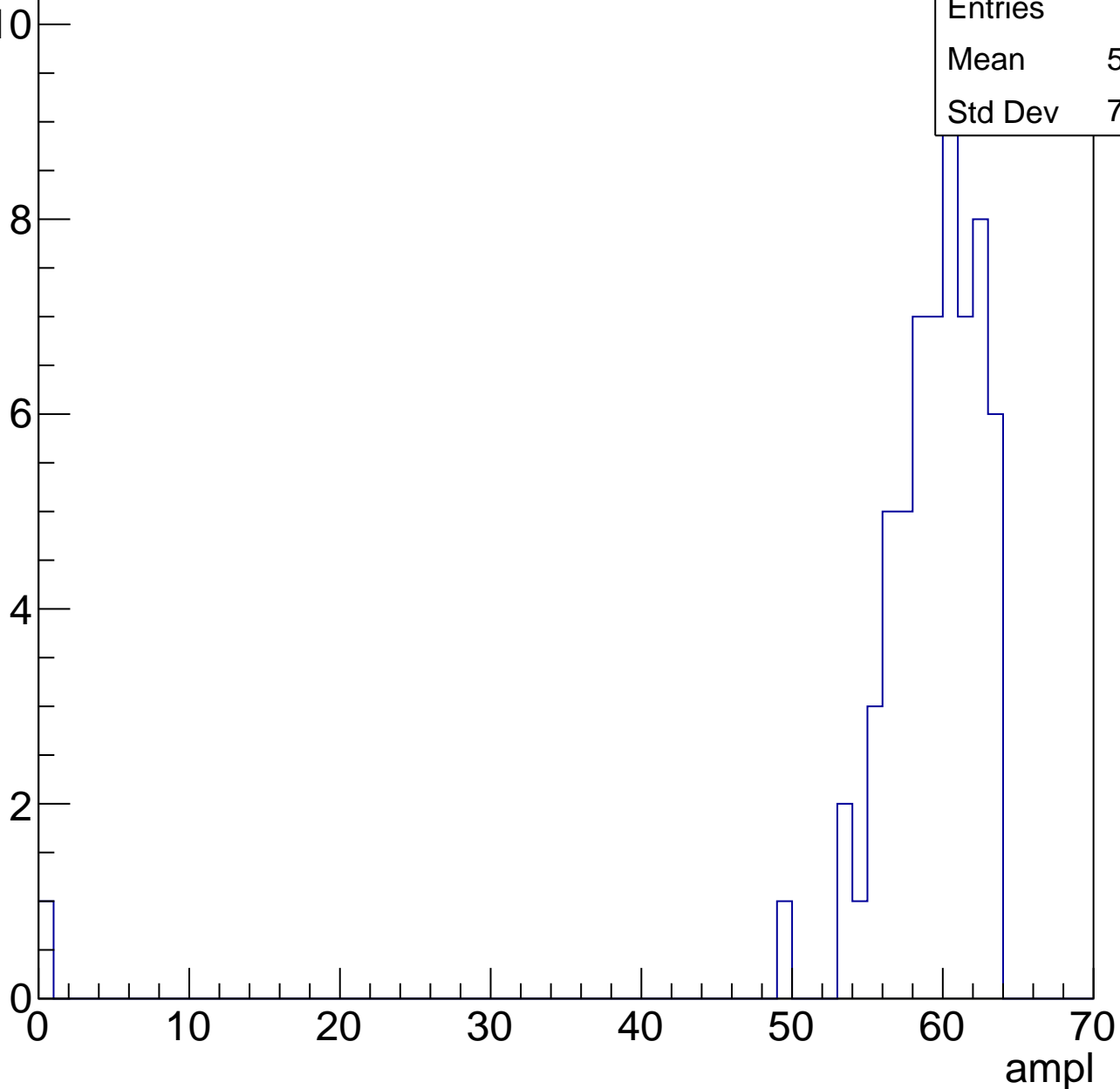


B1L102S, U10-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

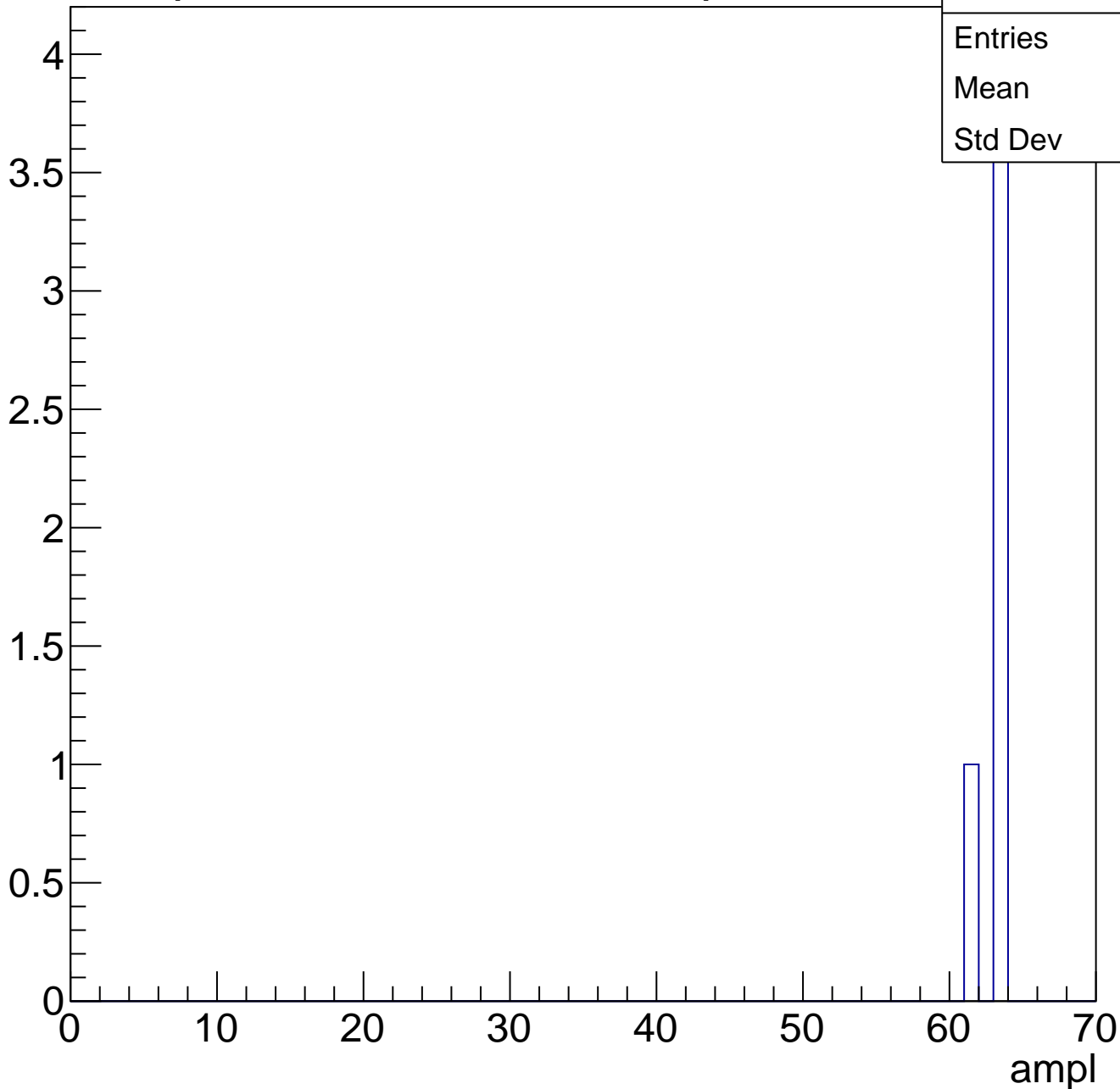
Entries	63
Mean	58.08
Std Dev	7.919



B1L102S, U10-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch26, adc0

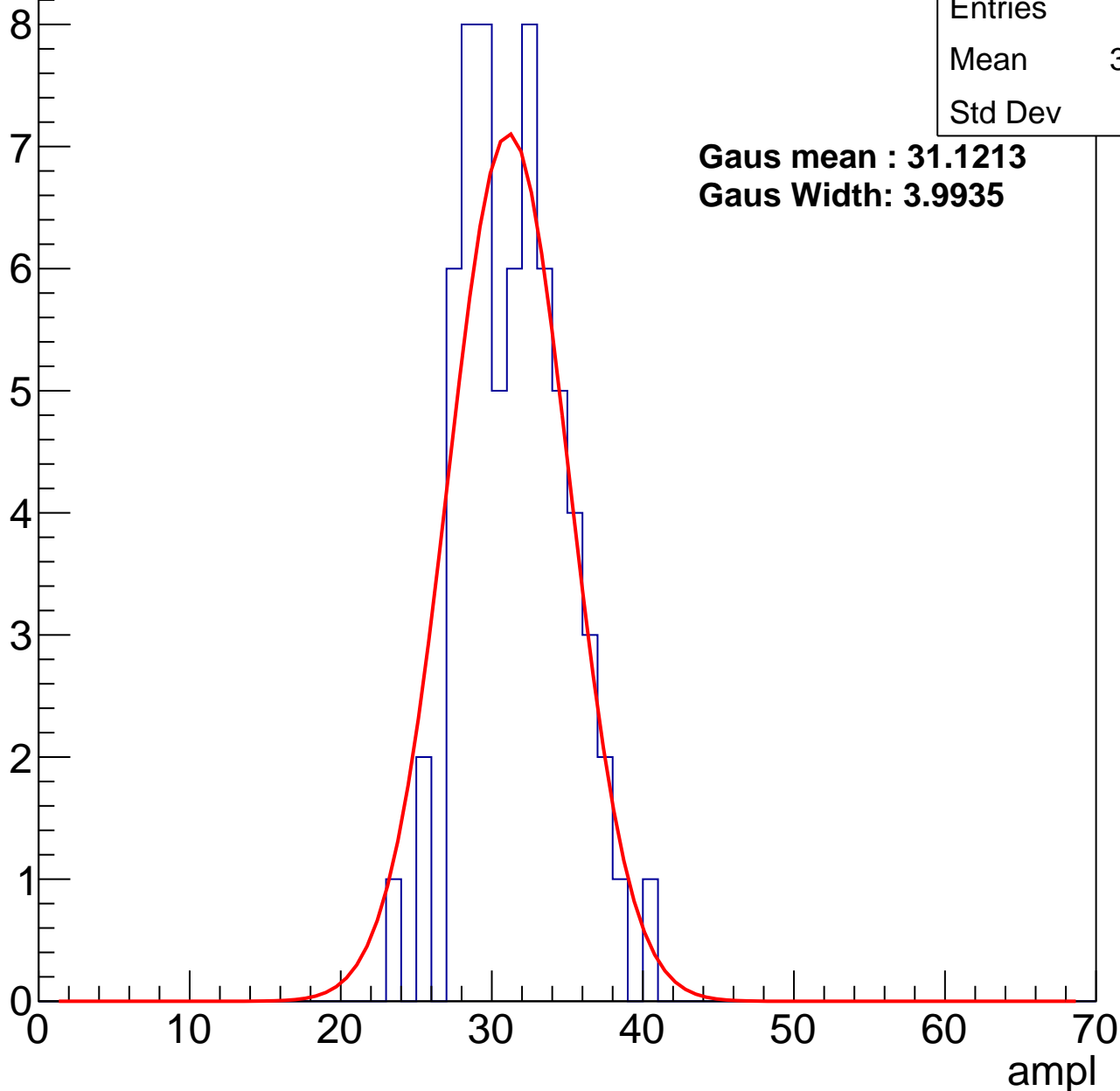
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	31.08
Std Dev	3.39

Gaus mean : 31.1213

Gaus Width: 3.9935

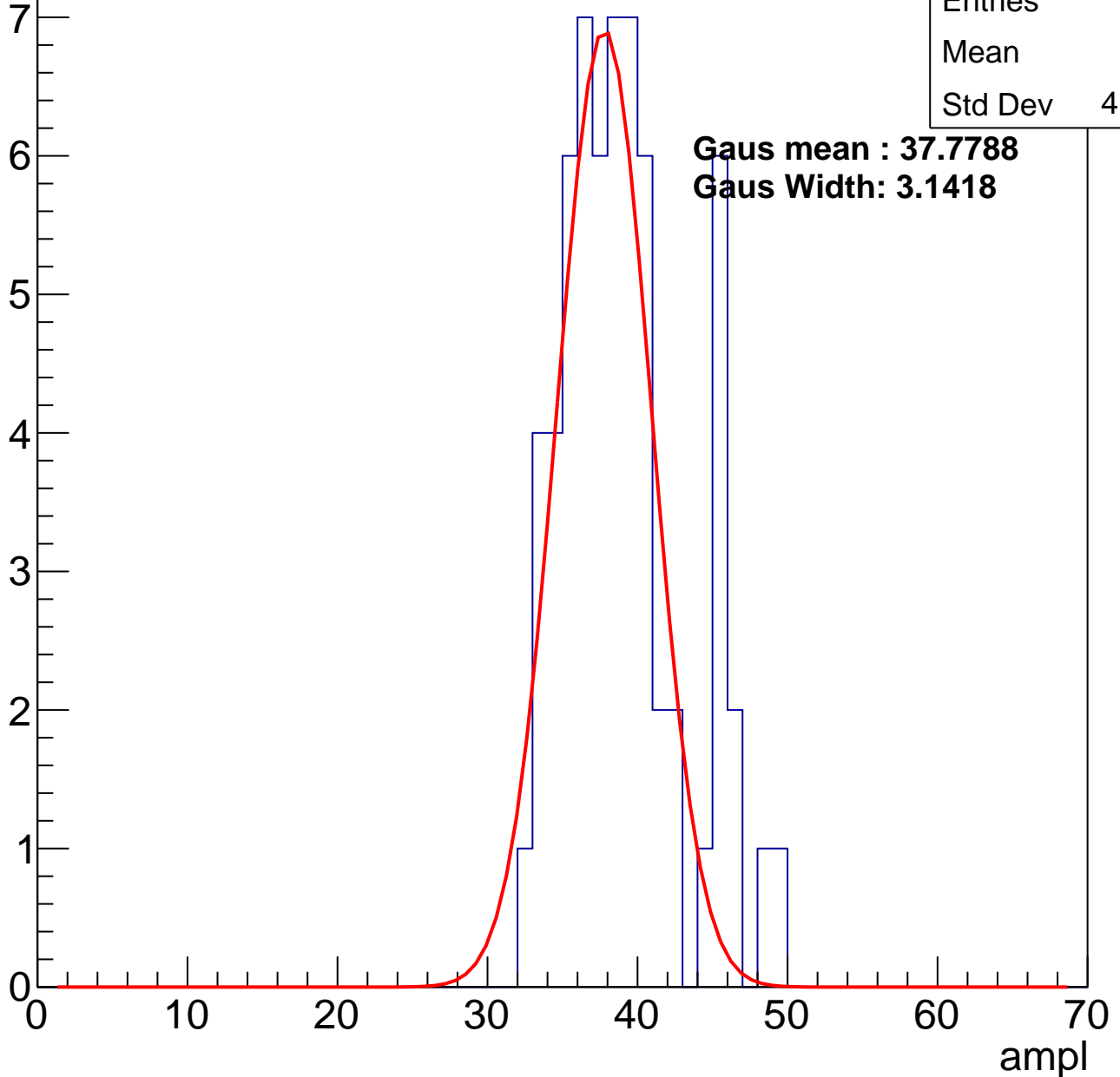


B1L102S, U10-ch26, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	38.6
Std Dev	4.034



B1L102S, U10-ch26, adc2

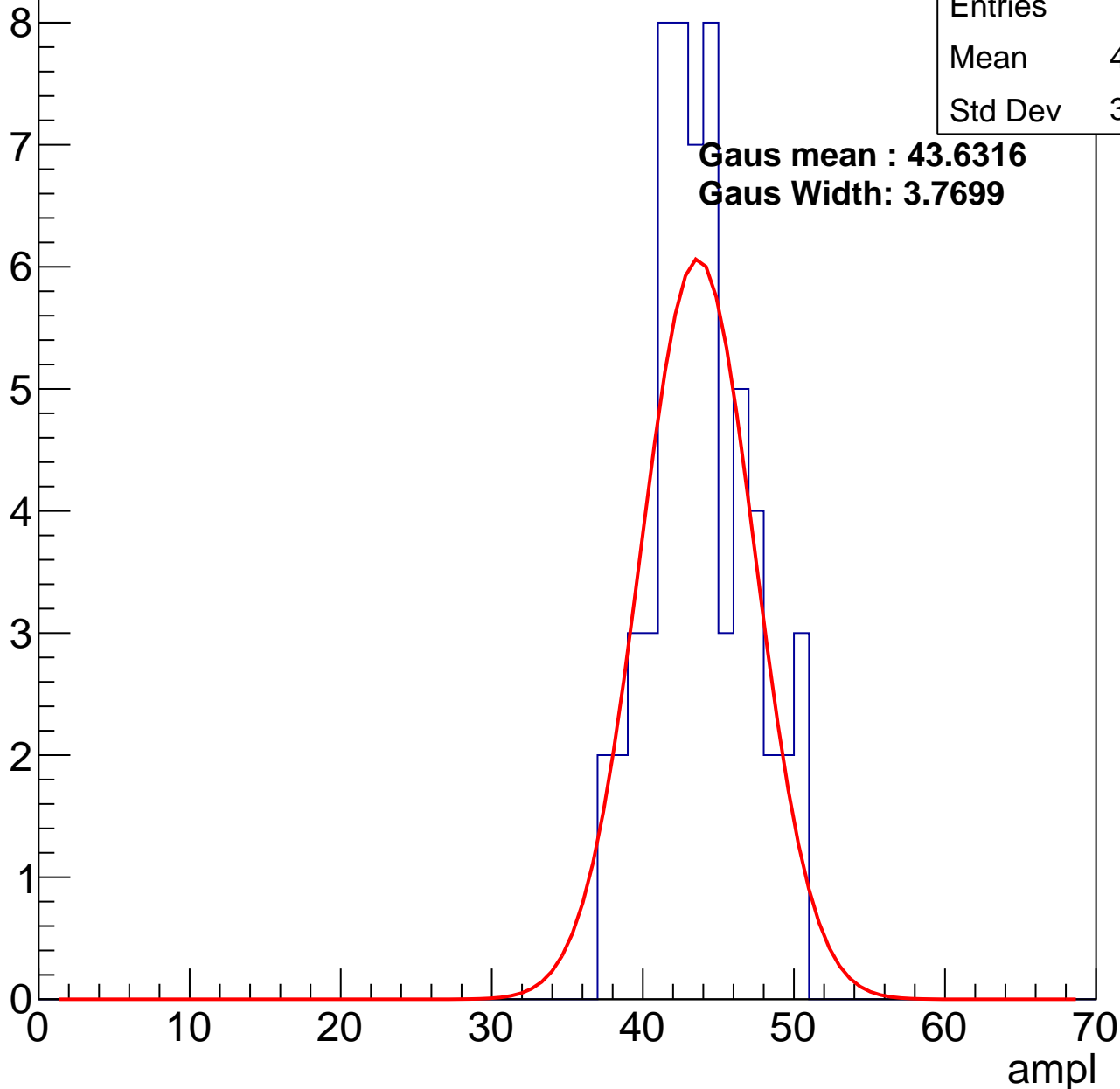
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.35
Std Dev	3.234

Gaus mean : 43.6316

Gaus Width: 3.7699

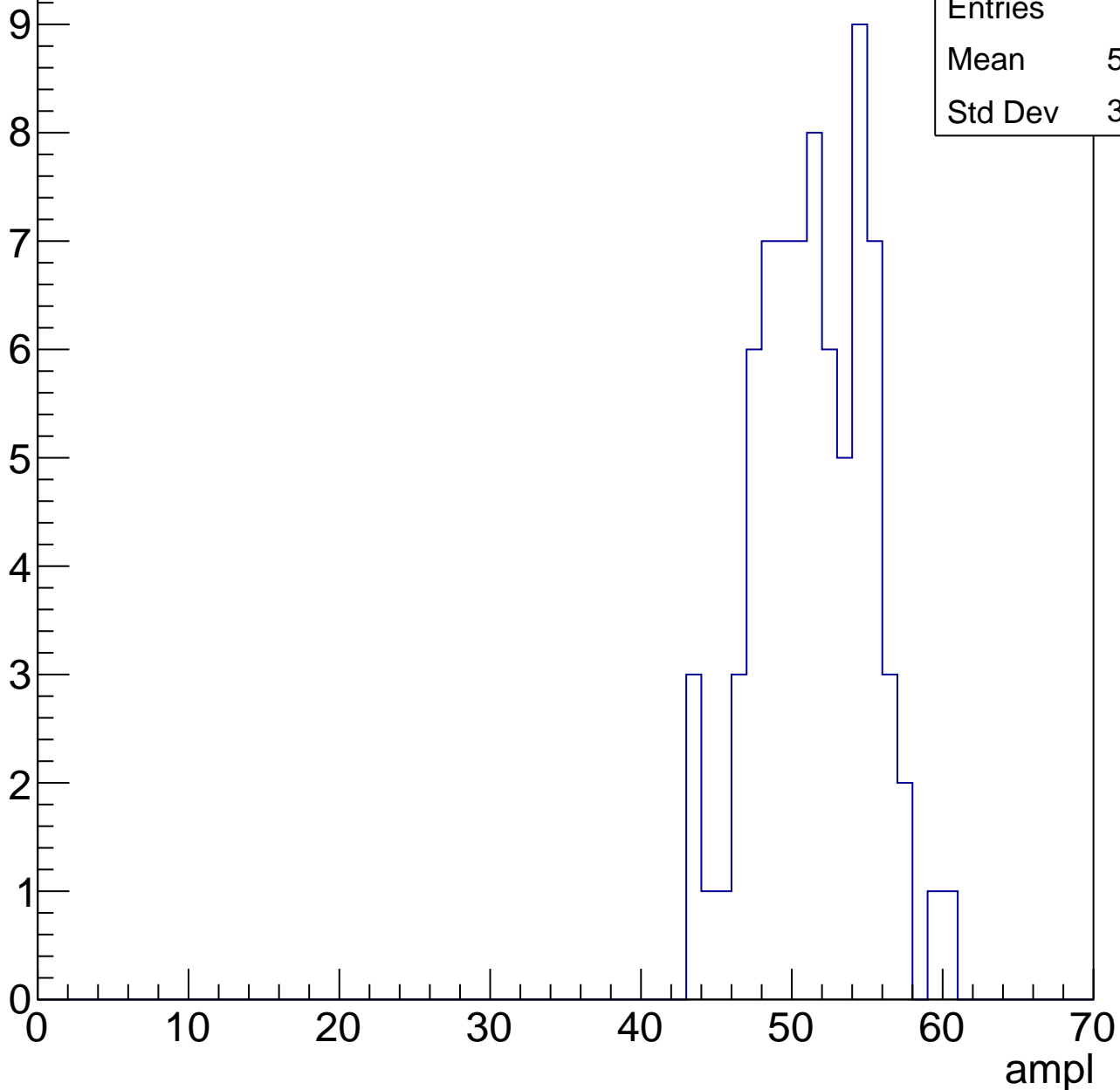


B1L102S, U10-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	50.96
Std Dev	3.705

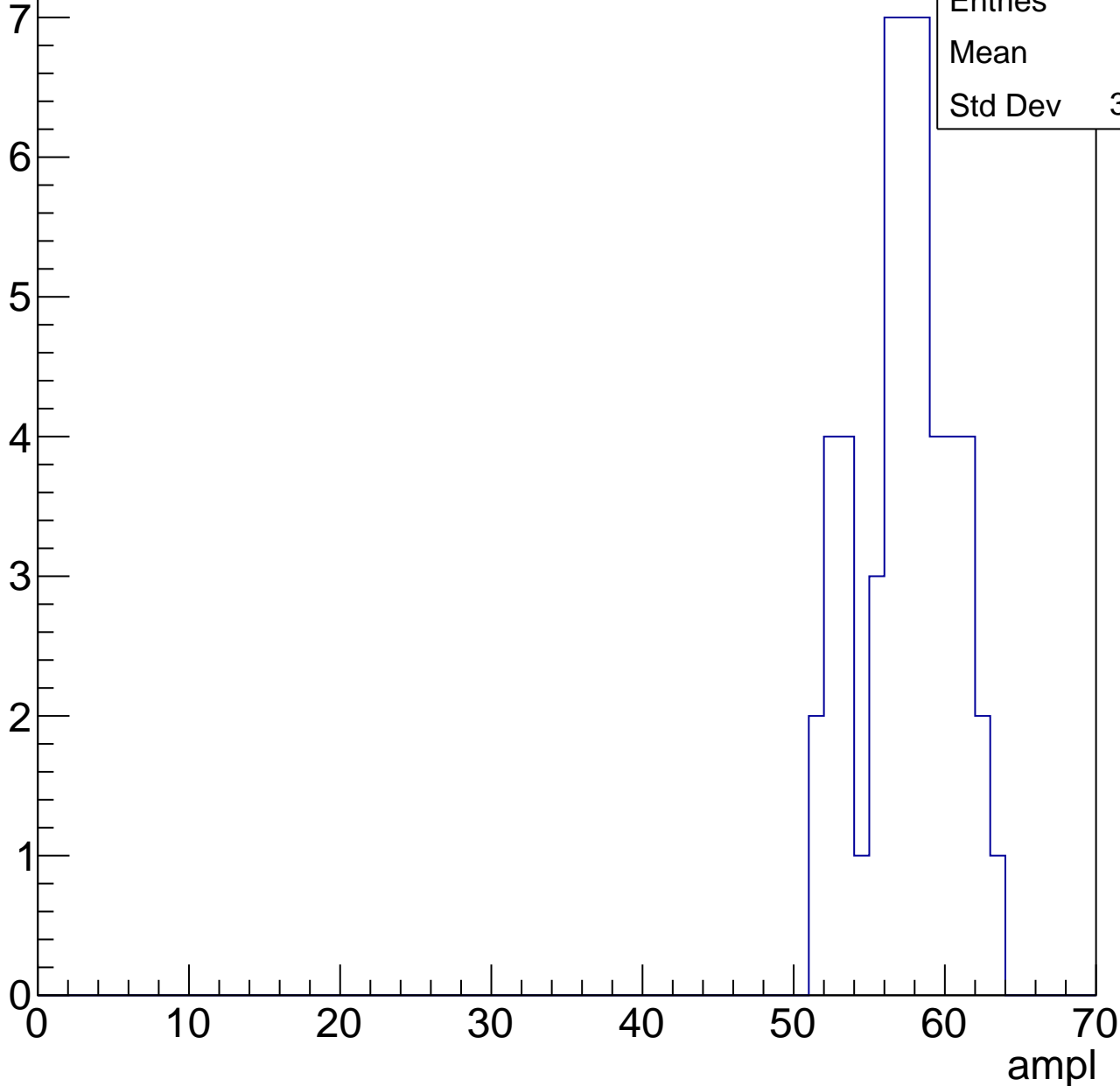


B1L102S, U10-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.9
Std Dev	3.074

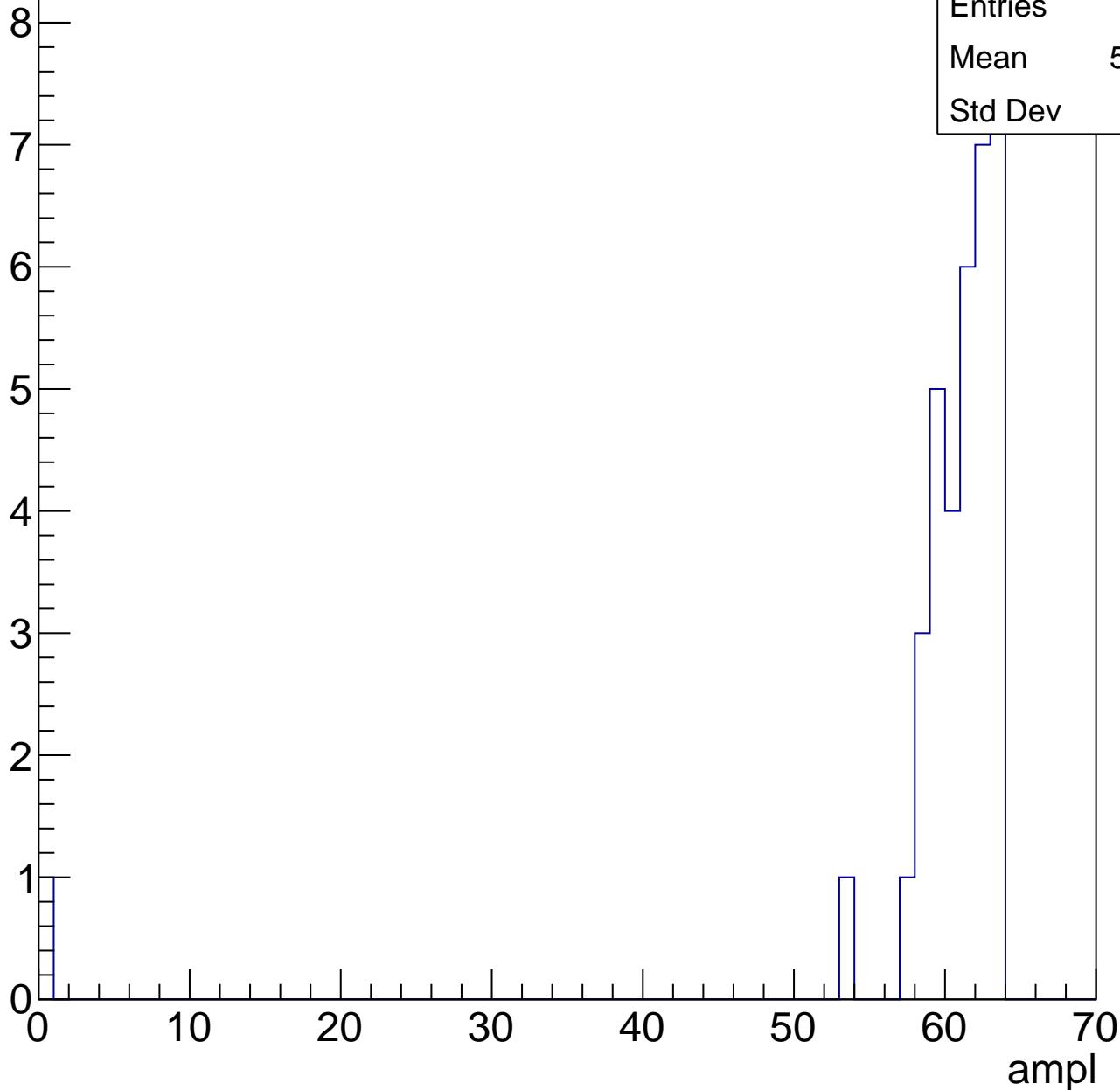


B1L102S, U10-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

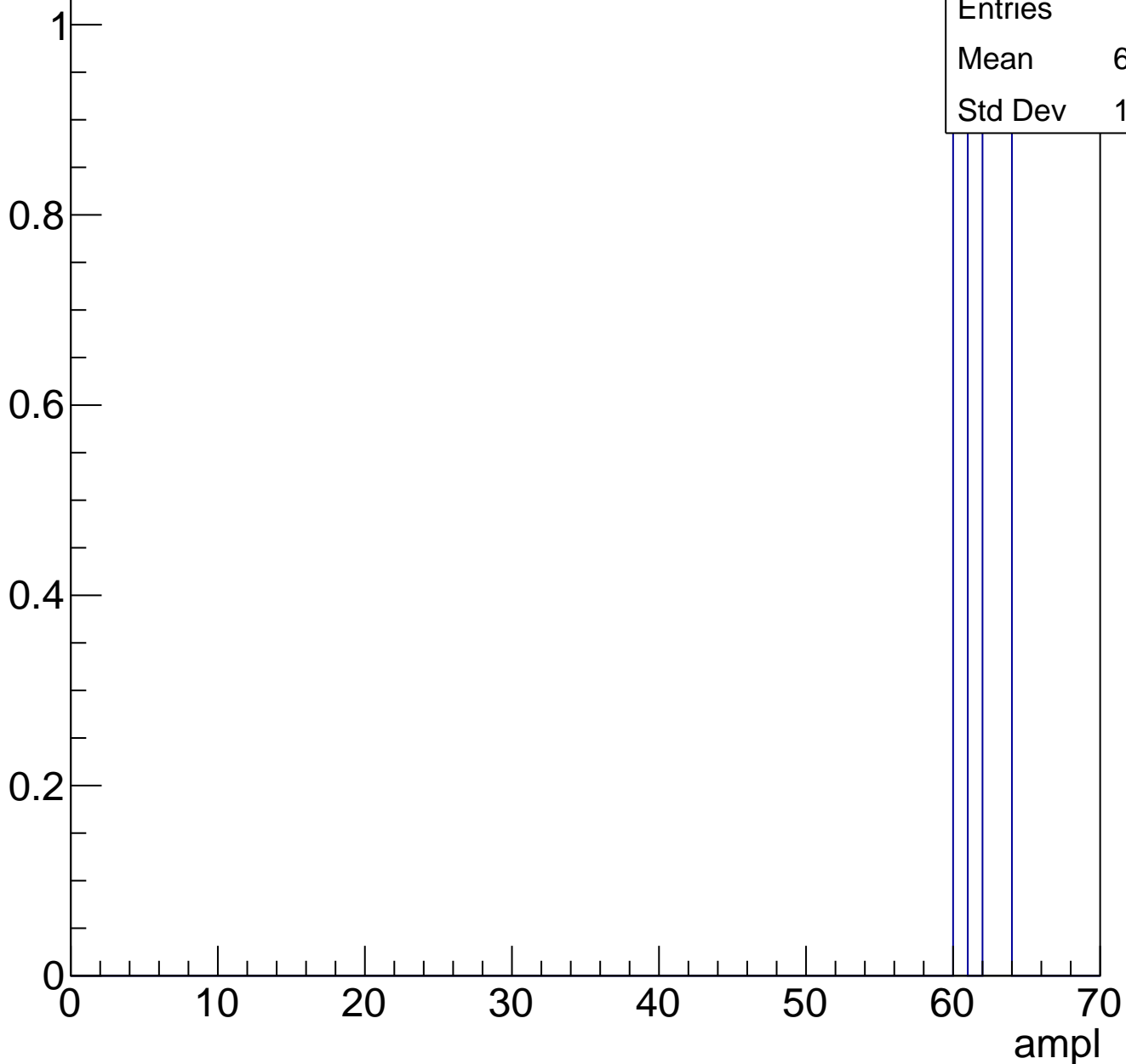
Entries	36
Mean	58.97
Std Dev	10.2



B1L102S, U10-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch27, adc0

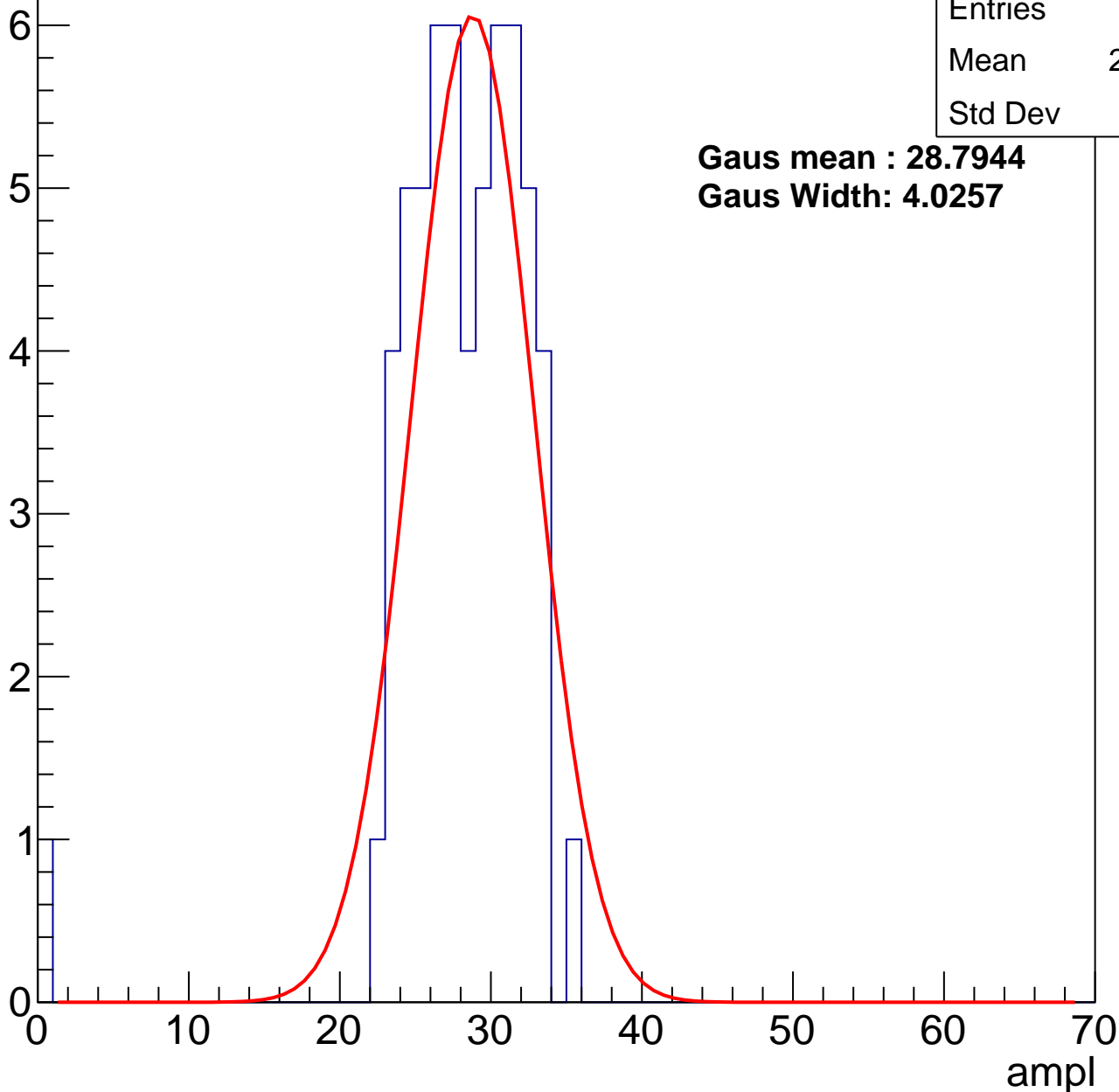
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.58
Std Dev	4.83

Gaus mean : 28.7944

Gaus Width: 4.0257



B1L102S, U10-ch27, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	90
Mean	35.44
Std Dev	3.83

Gaus mean : 36.3122

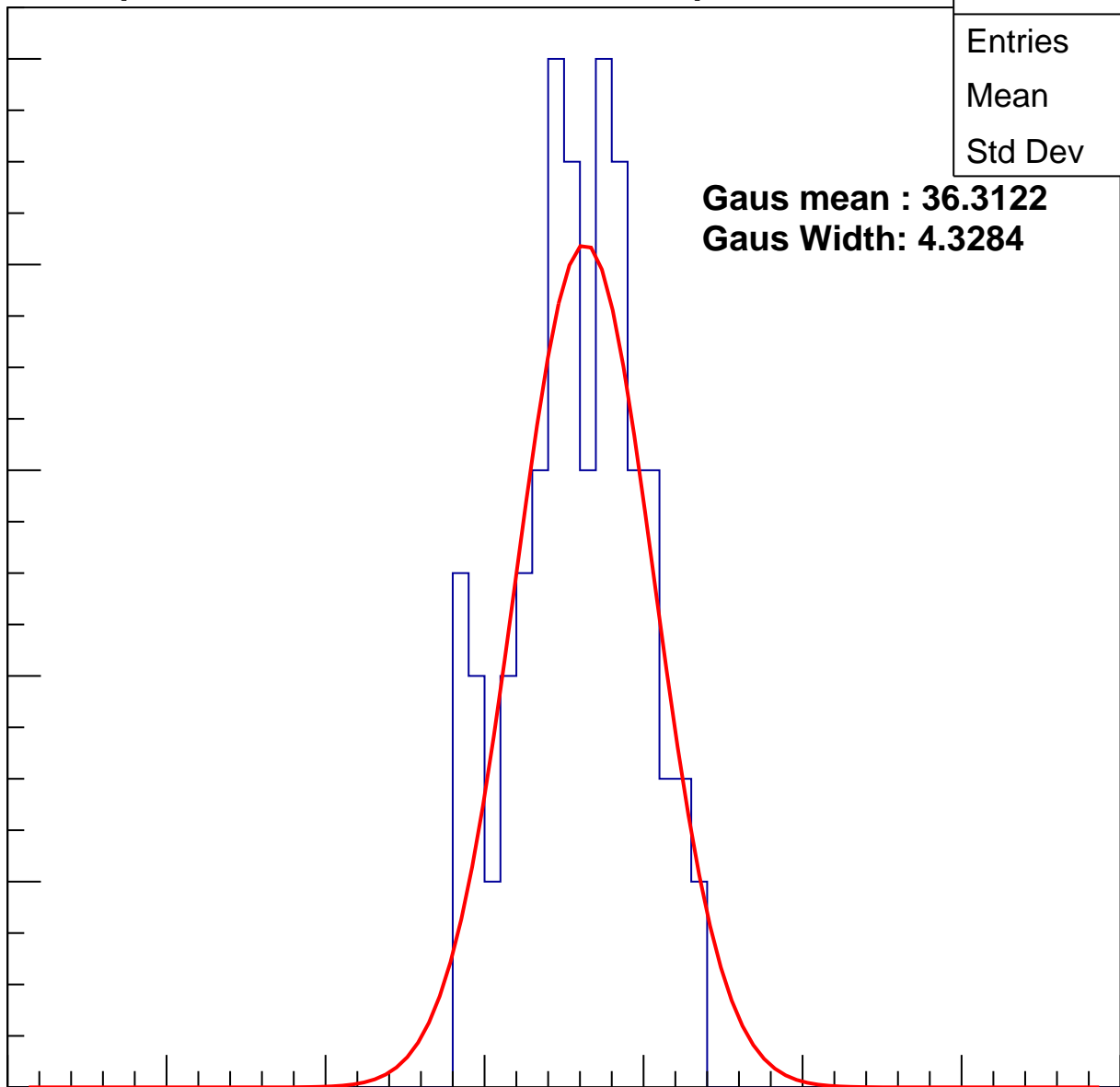
Gaus Width: 4.3284

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch27, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.88
Std Dev	3.867

Gaus mean : 44.0913

Gaus Width: 3.6448

10

8

6

4

2

0

0

10

20

30

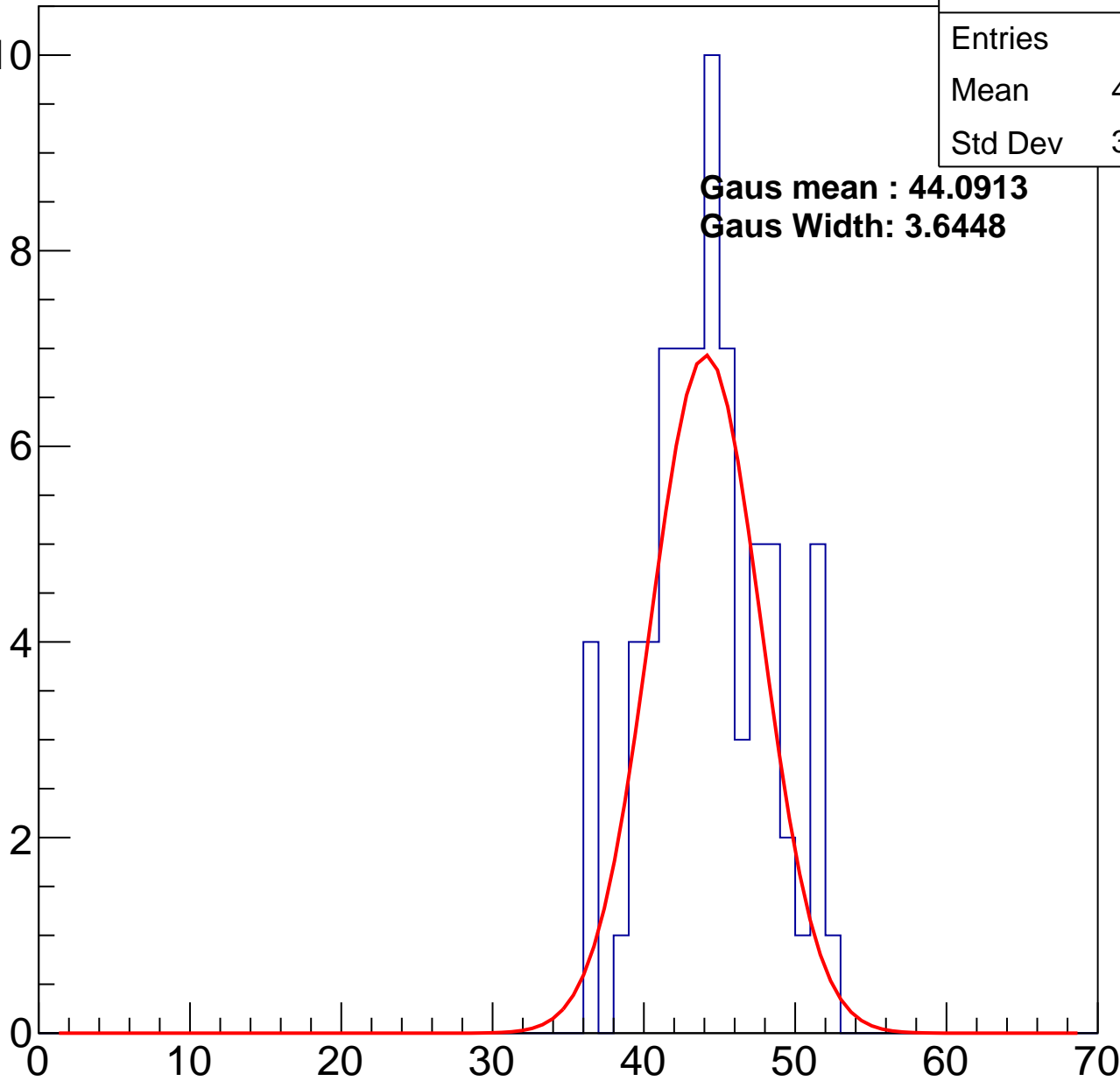
40

50

60

70

ampl

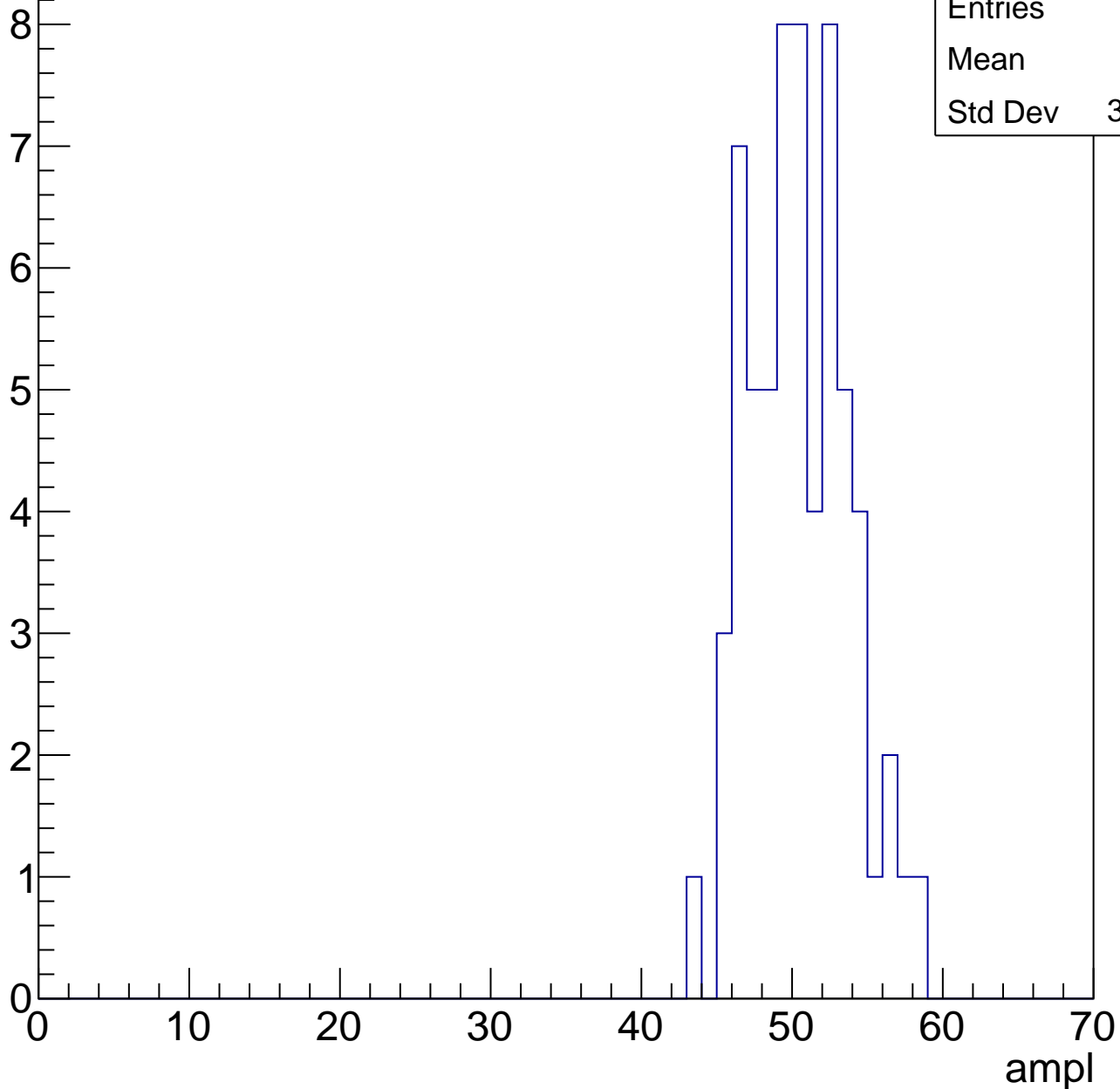


B1L102S, U10-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

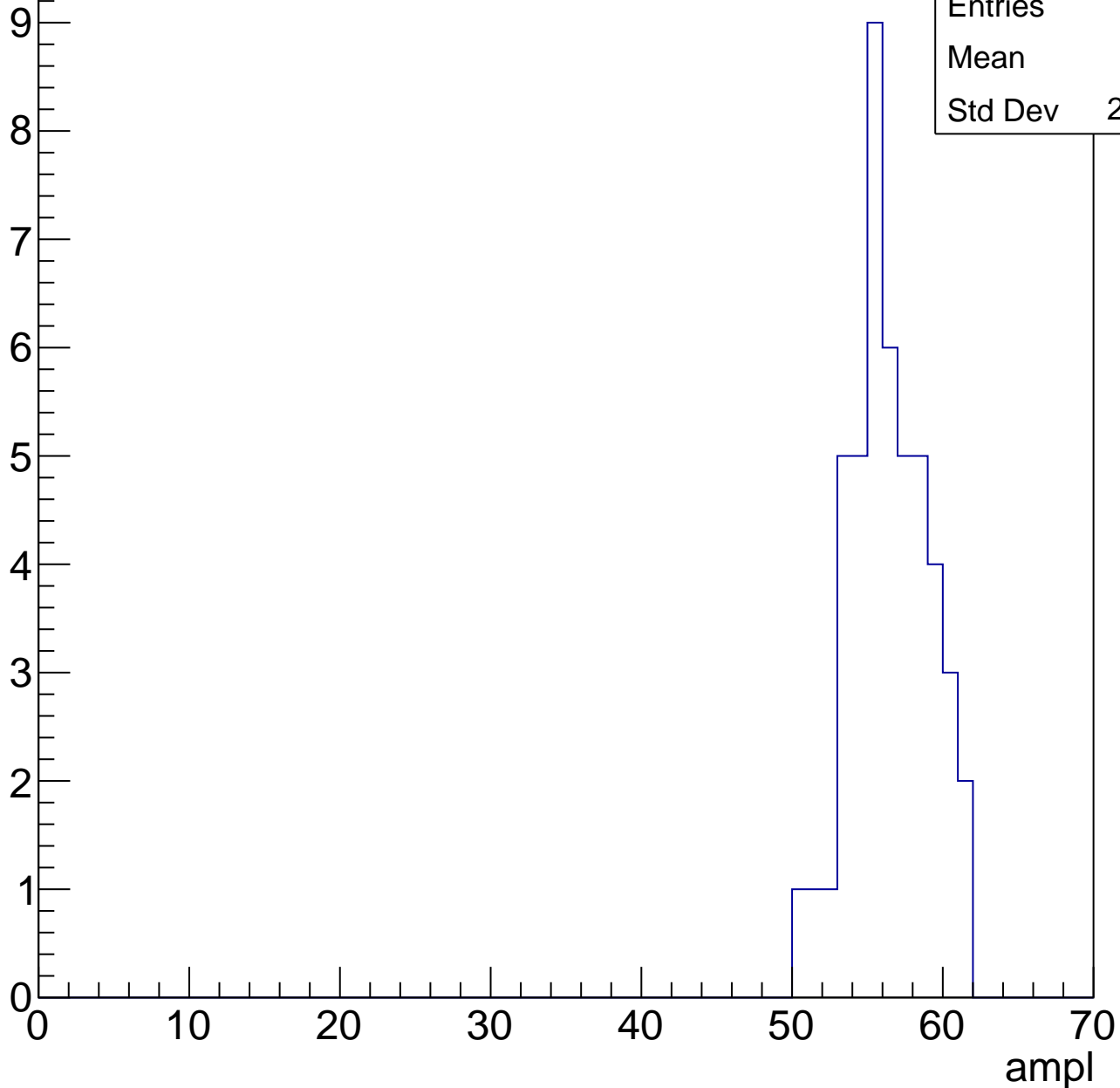
Entries	63
Mean	50
Std Dev	3.246



B1L102S, U10-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



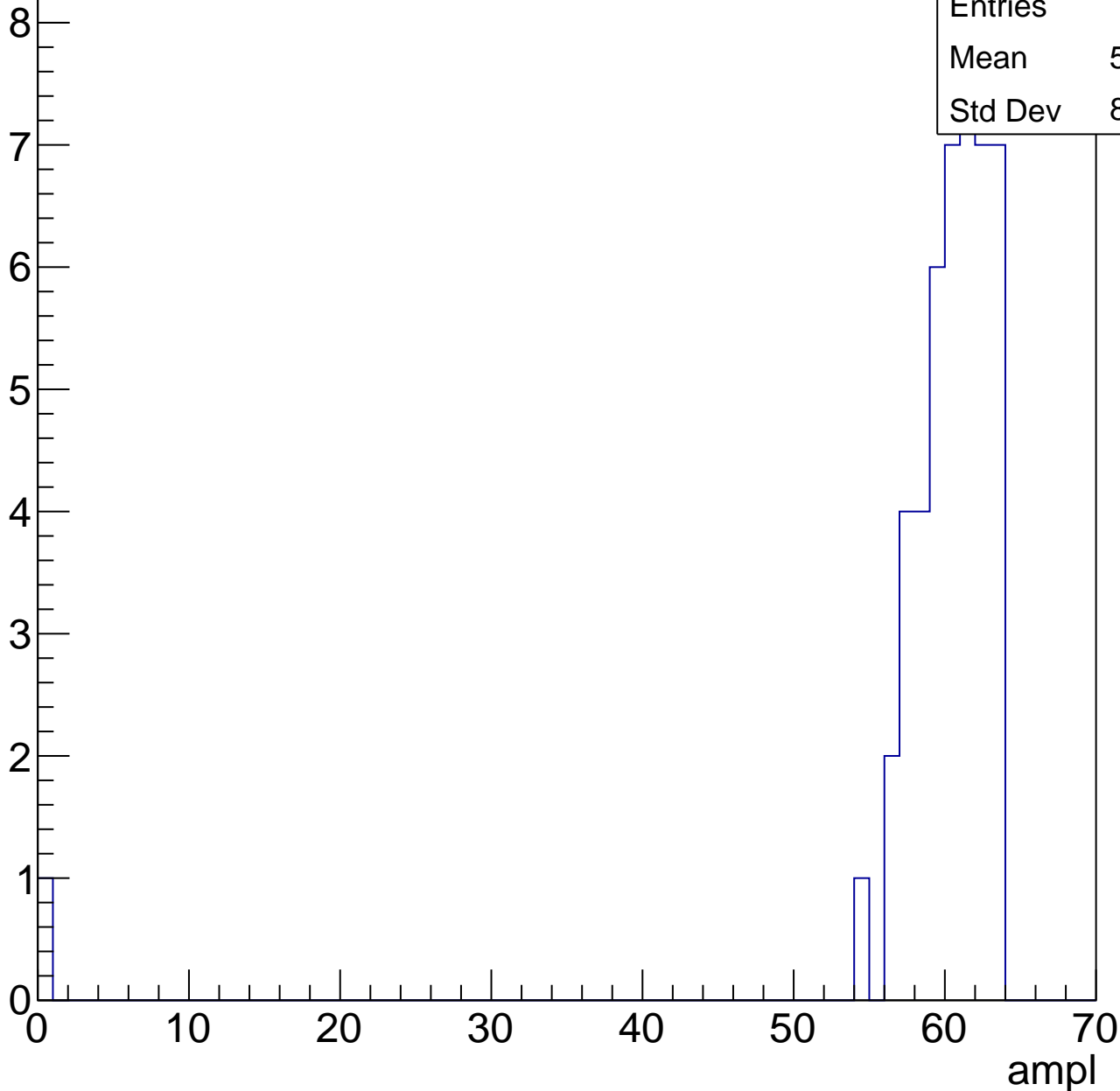
Entries	47
Mean	56
Std Dev	2.568

B1L102S, U10-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.79
Std Dev	8.939



B1L102S, U10-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L102S, U10-ch28, adc0

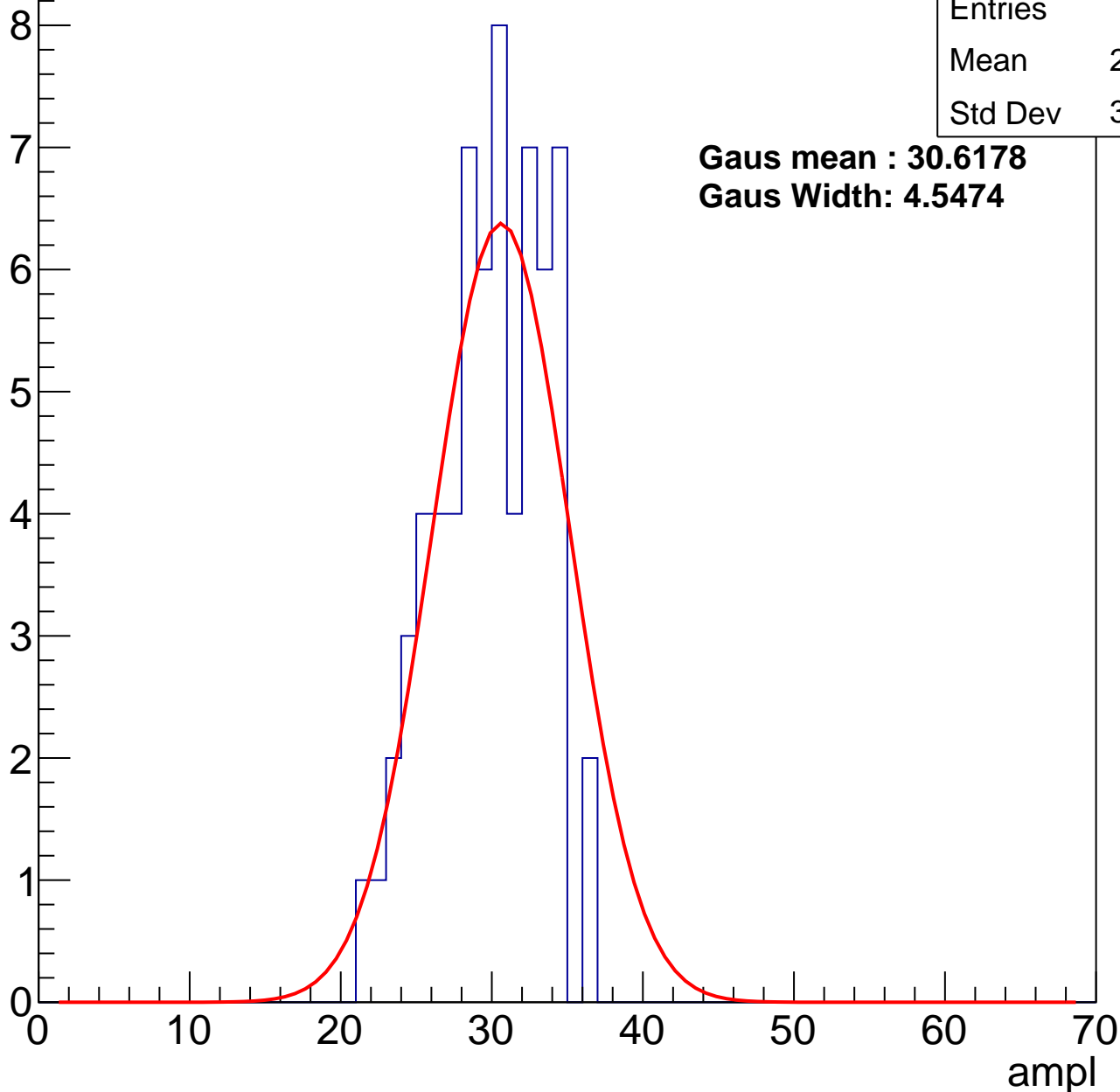
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.38
Std Dev	3.545

Gaus mean : 30.6178

Gaus Width: 4.5474



B1L102S, U10-ch28, adc1

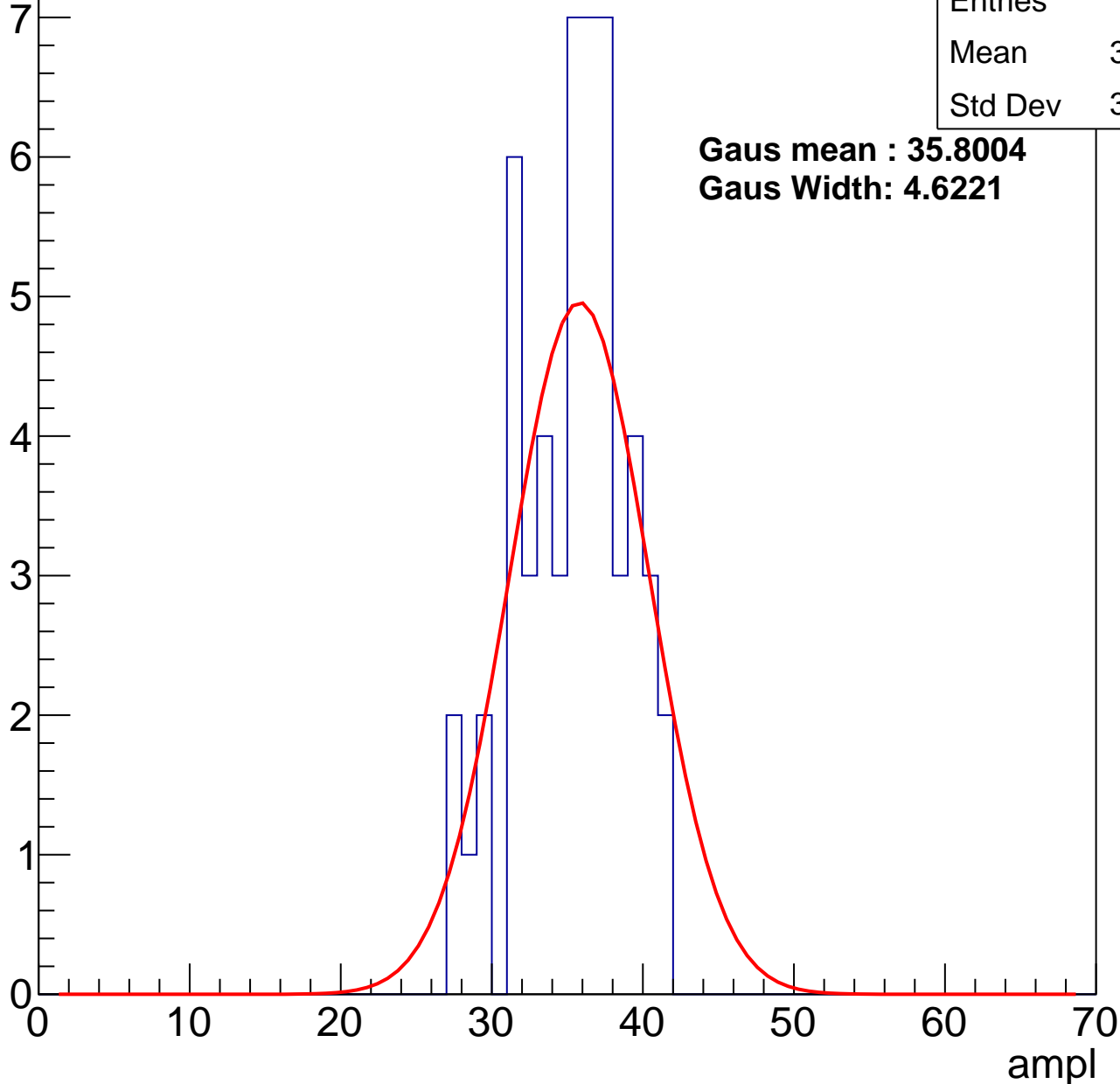
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.89
Std Dev	3.505

Gaus mean : 35.8004

Gaus Width: 4.6221



B1L102S, U10-ch28, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	42.85
Std Dev	3.713

Gaus mean : 43.3014

Gaus Width: 3.7529

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

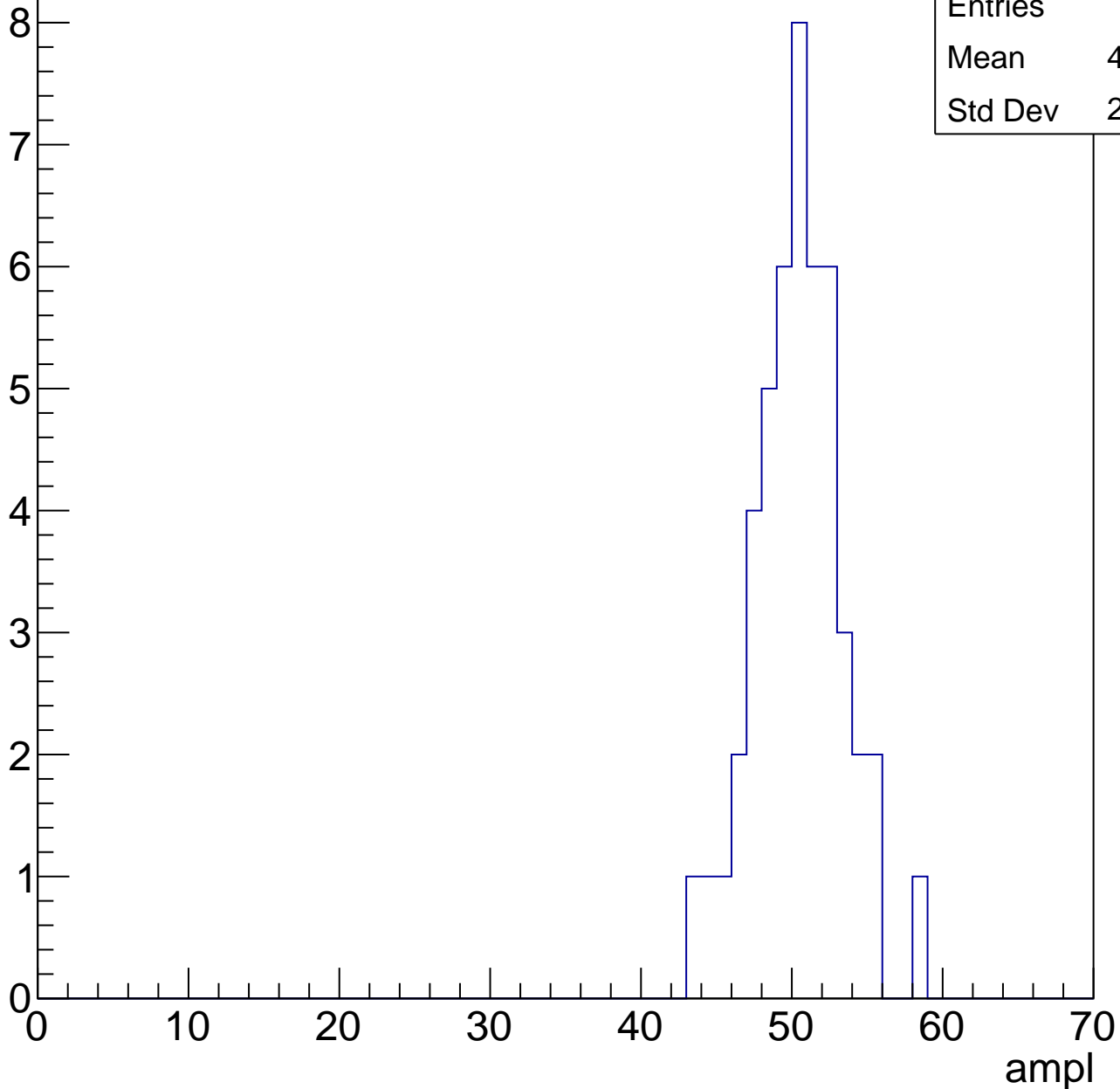
70

B1L102S, U10-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	49.98
Std Dev	2.912

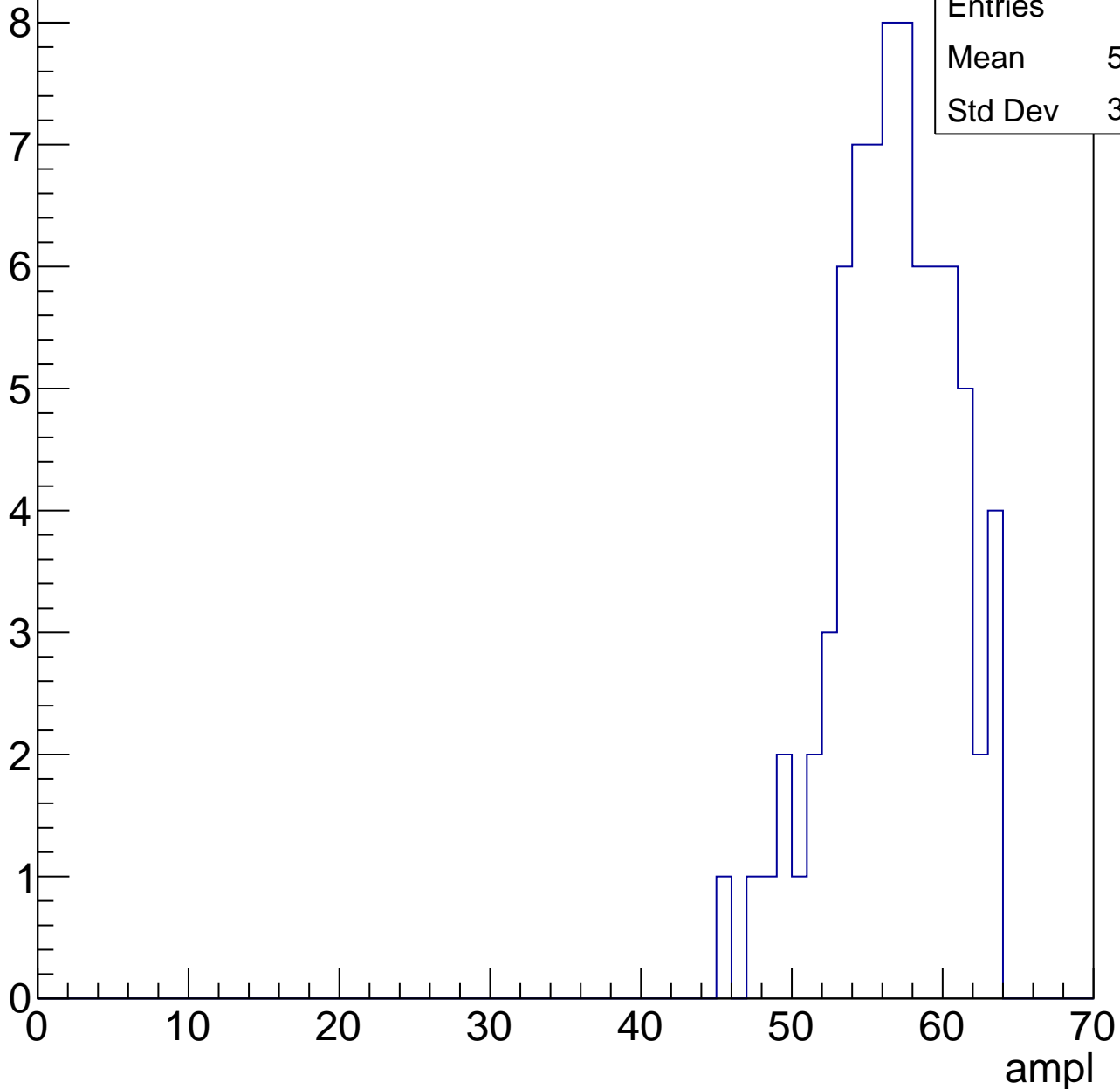


B1L102S, U10-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

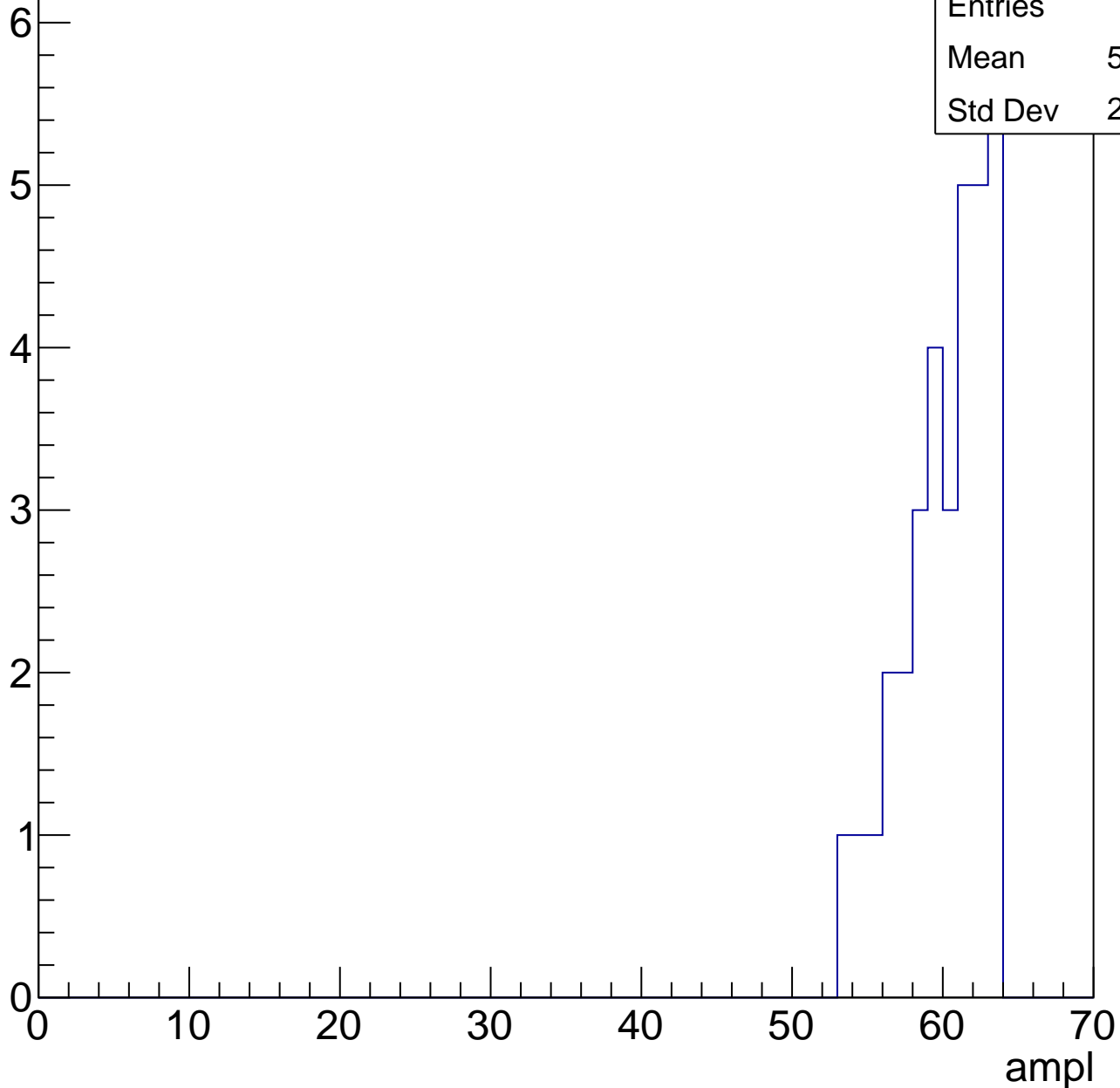
Entries	76
Mean	56.24
Std Dev	3.903



B1L102S, U10-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

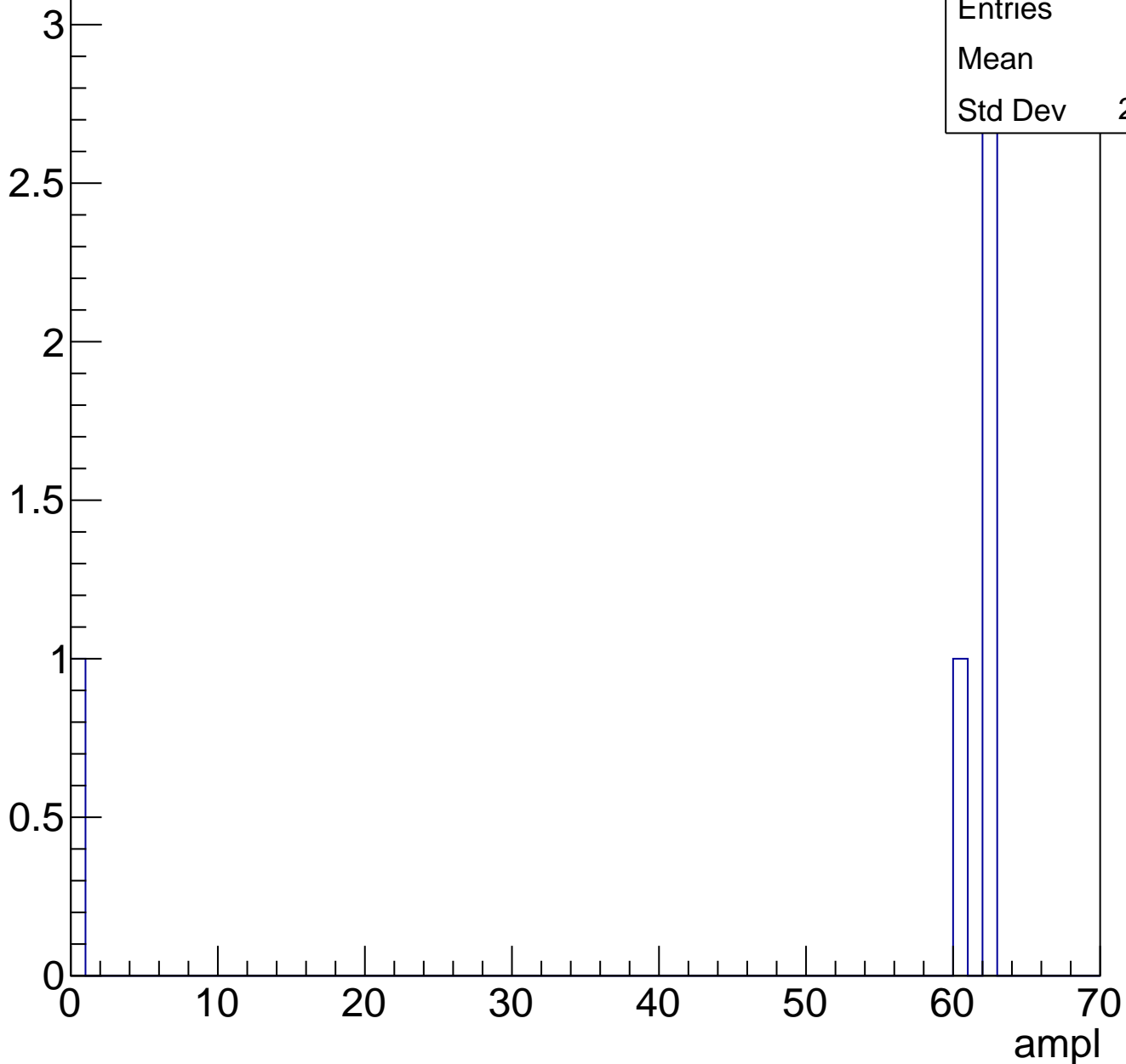
Entry



B1L102S, U10-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U10-ch29, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	27.82
Std Dev	3.282

Gaus mean : 27.7396

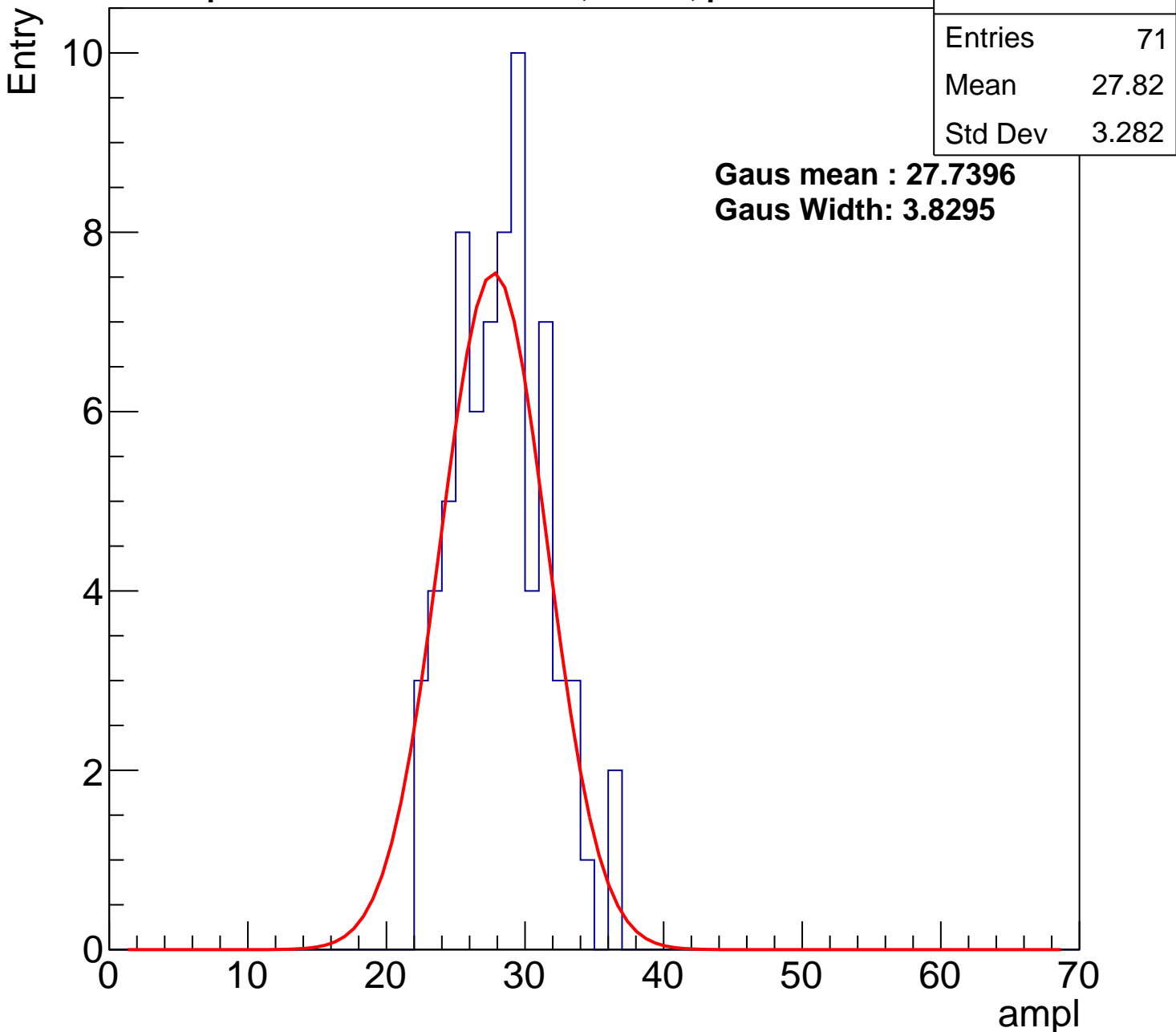
Gaus Width: 3.8295

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch29, adc1

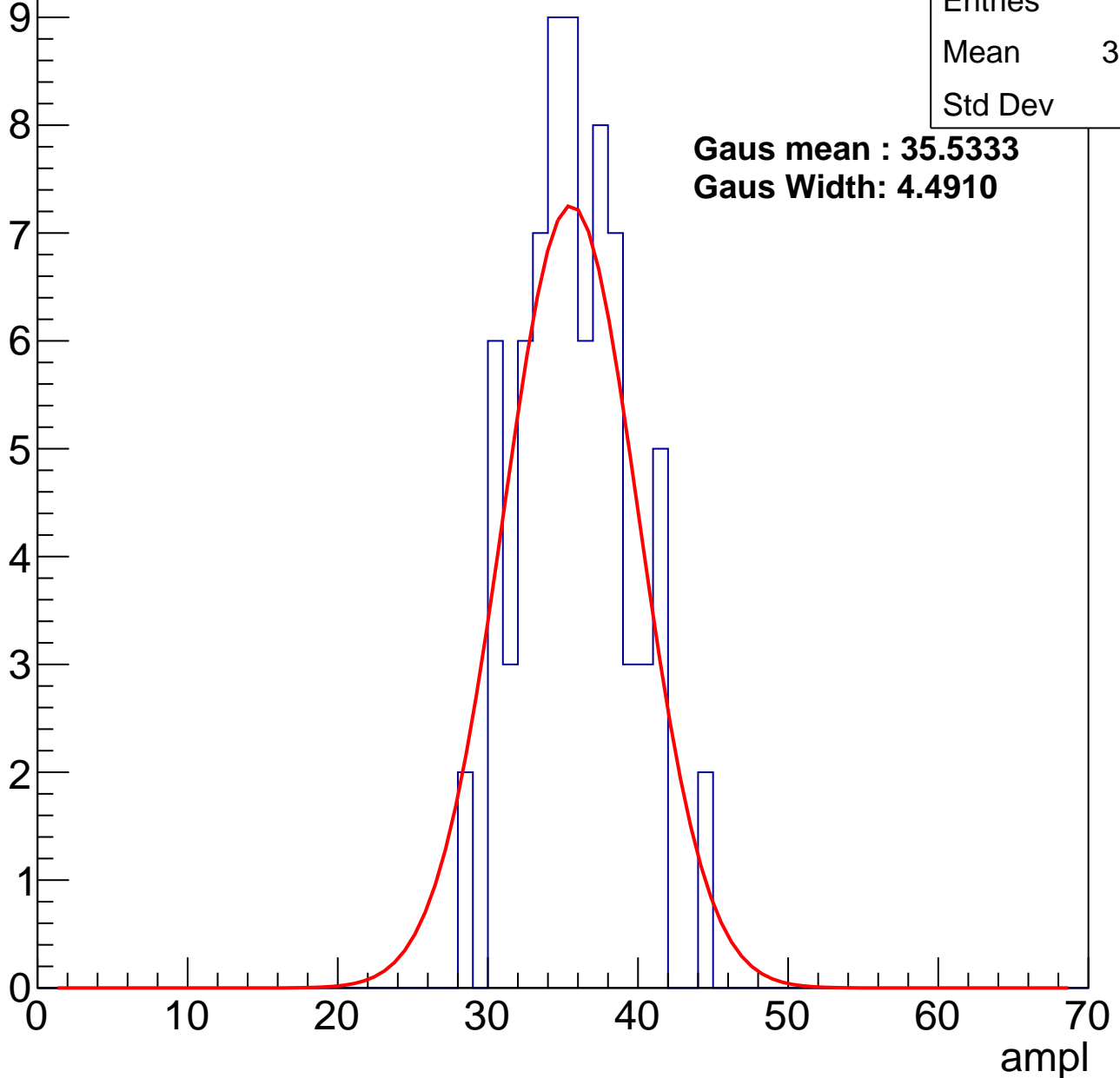
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.28
Std Dev	3.53

Gaus mean : 35.5333

Gaus Width: 4.4910



B1L102S, U10-ch29, adc2

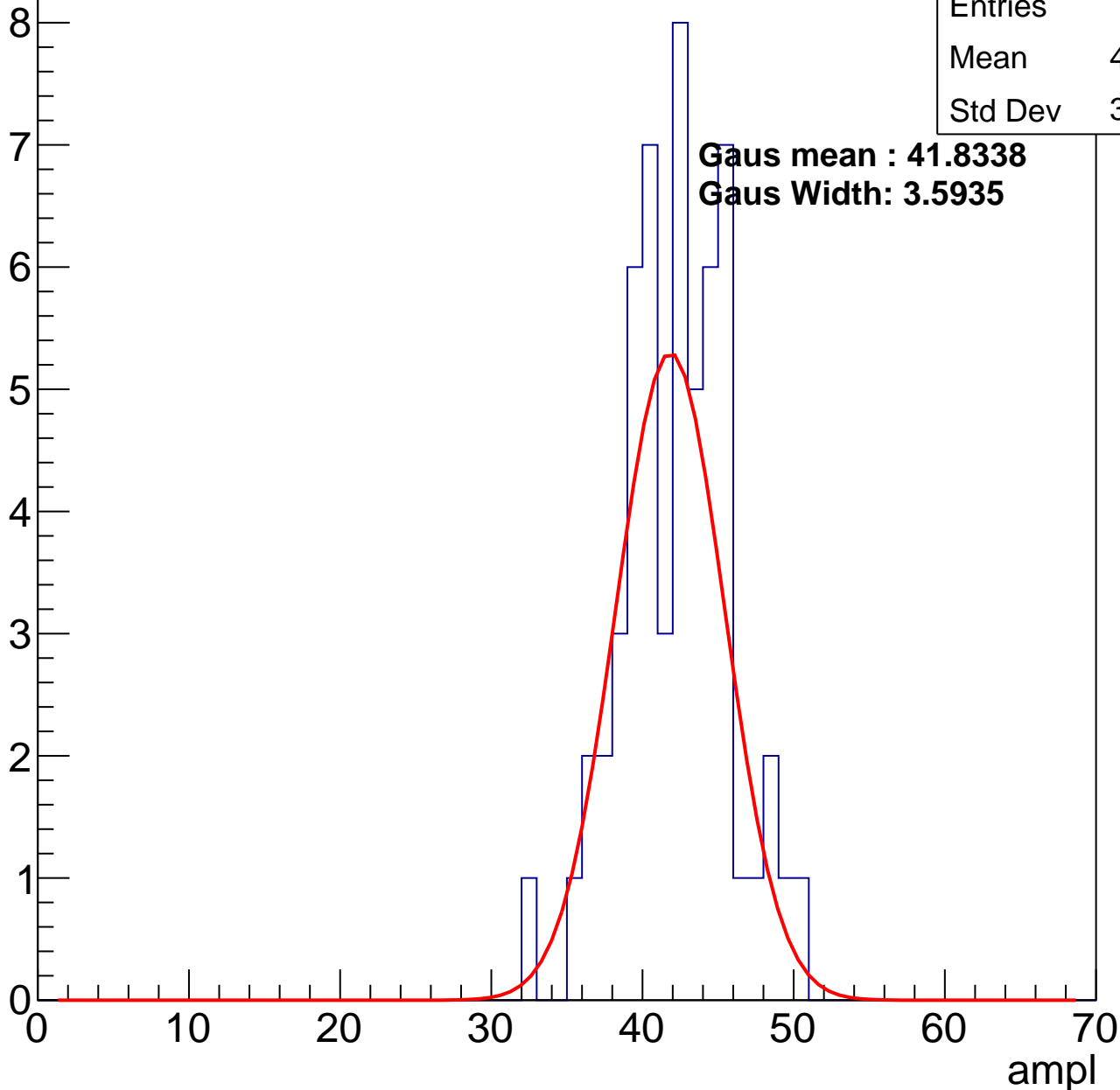
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.79
Std Dev	3.553

Gaus mean : 41.8338

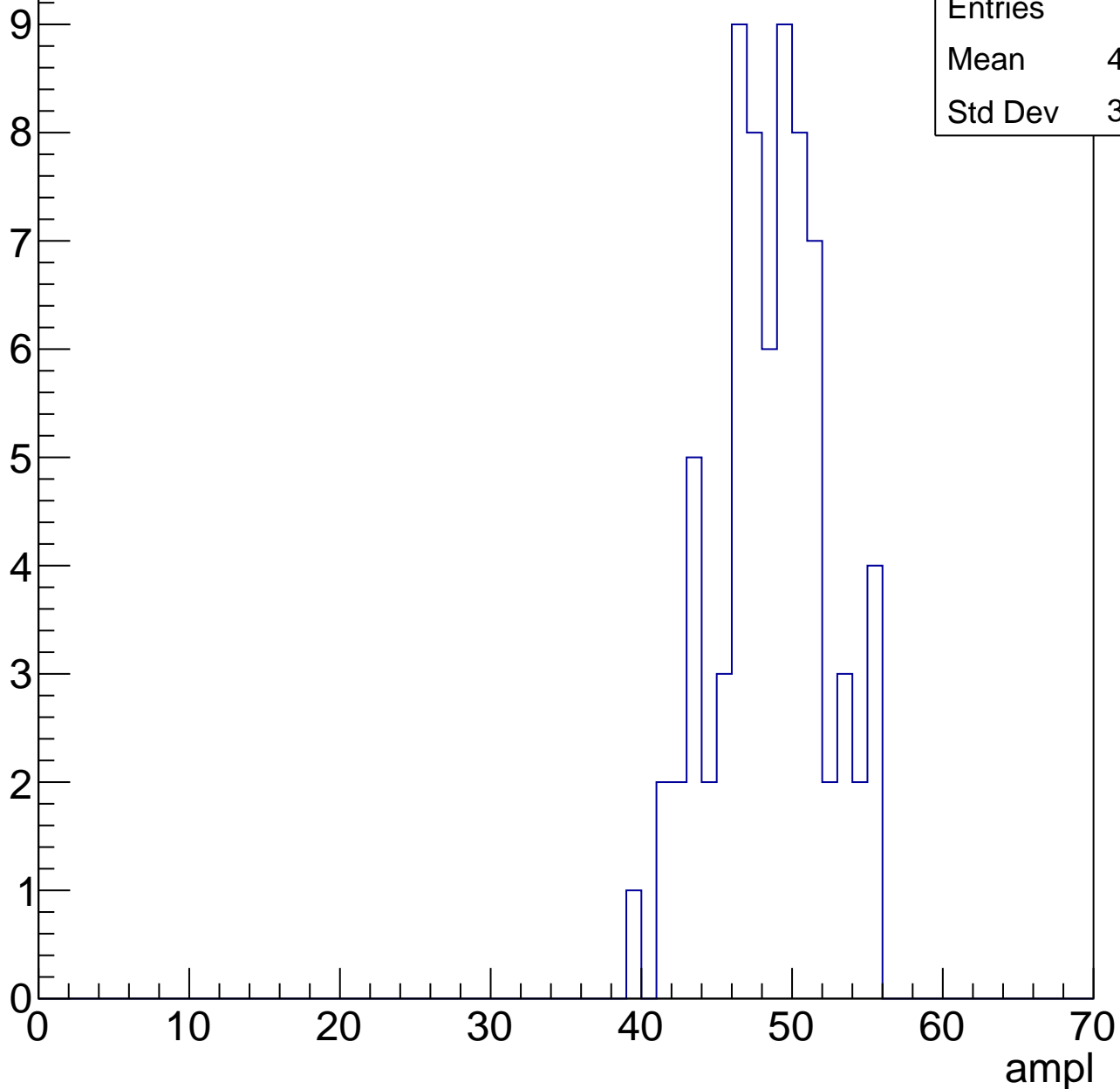
Gaus Width: 3.5935



B1L102S, U10-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



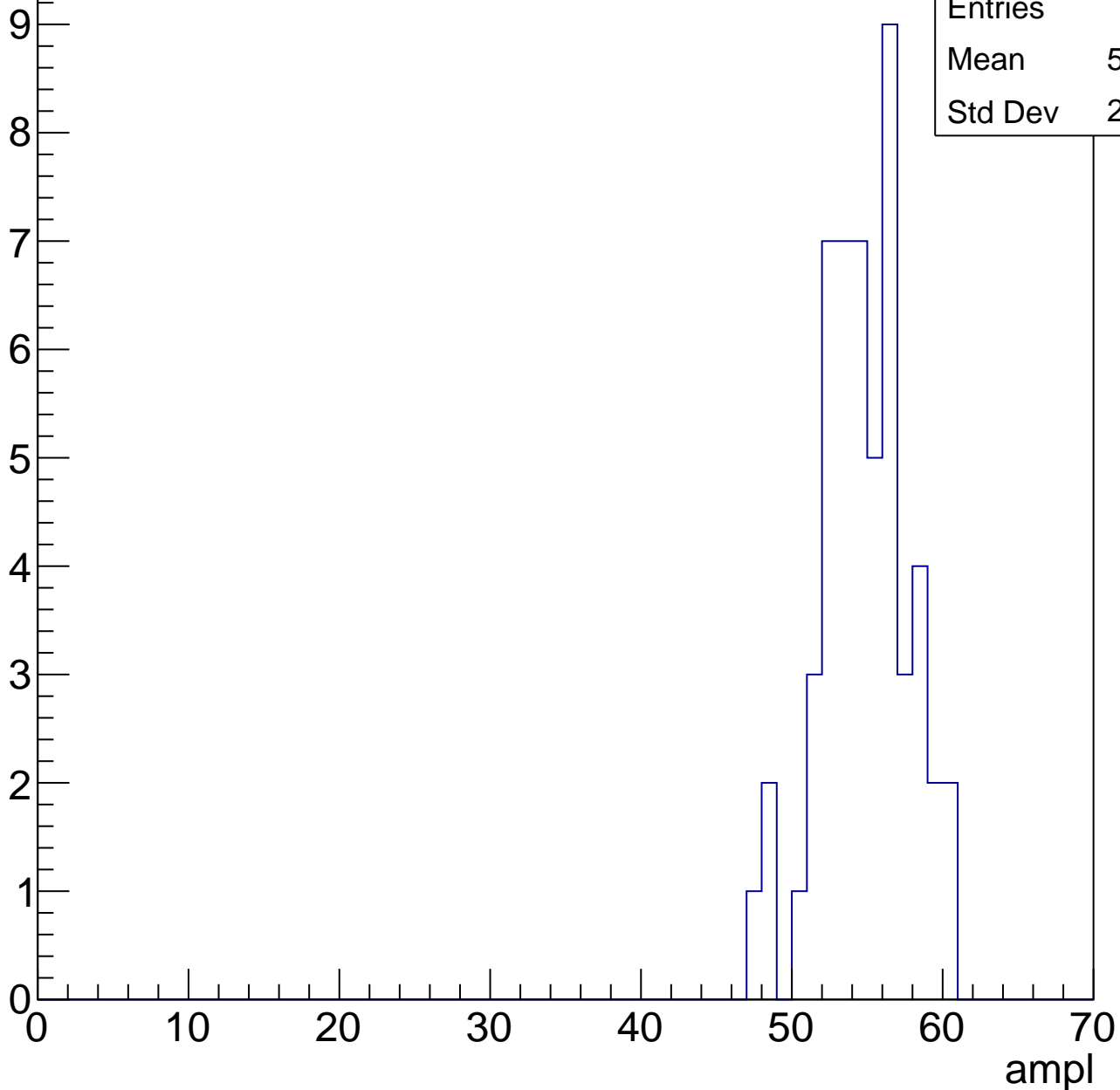
Entries	73
Mean	48.08
Std Dev	3.622

B1L102S, U10-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.32
Std Dev	2.906

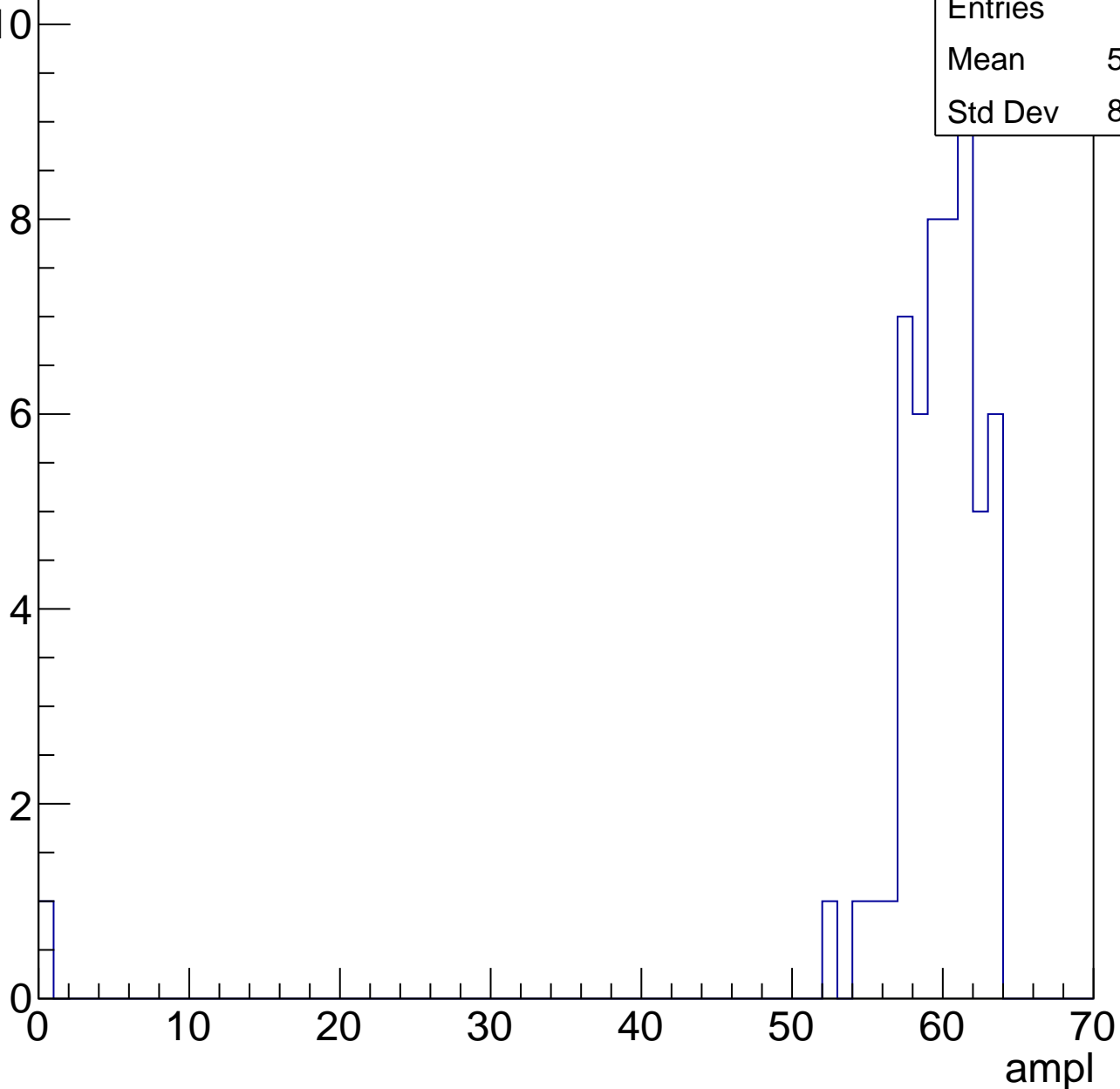


B1L102S, U10-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.44
Std Dev	8.296

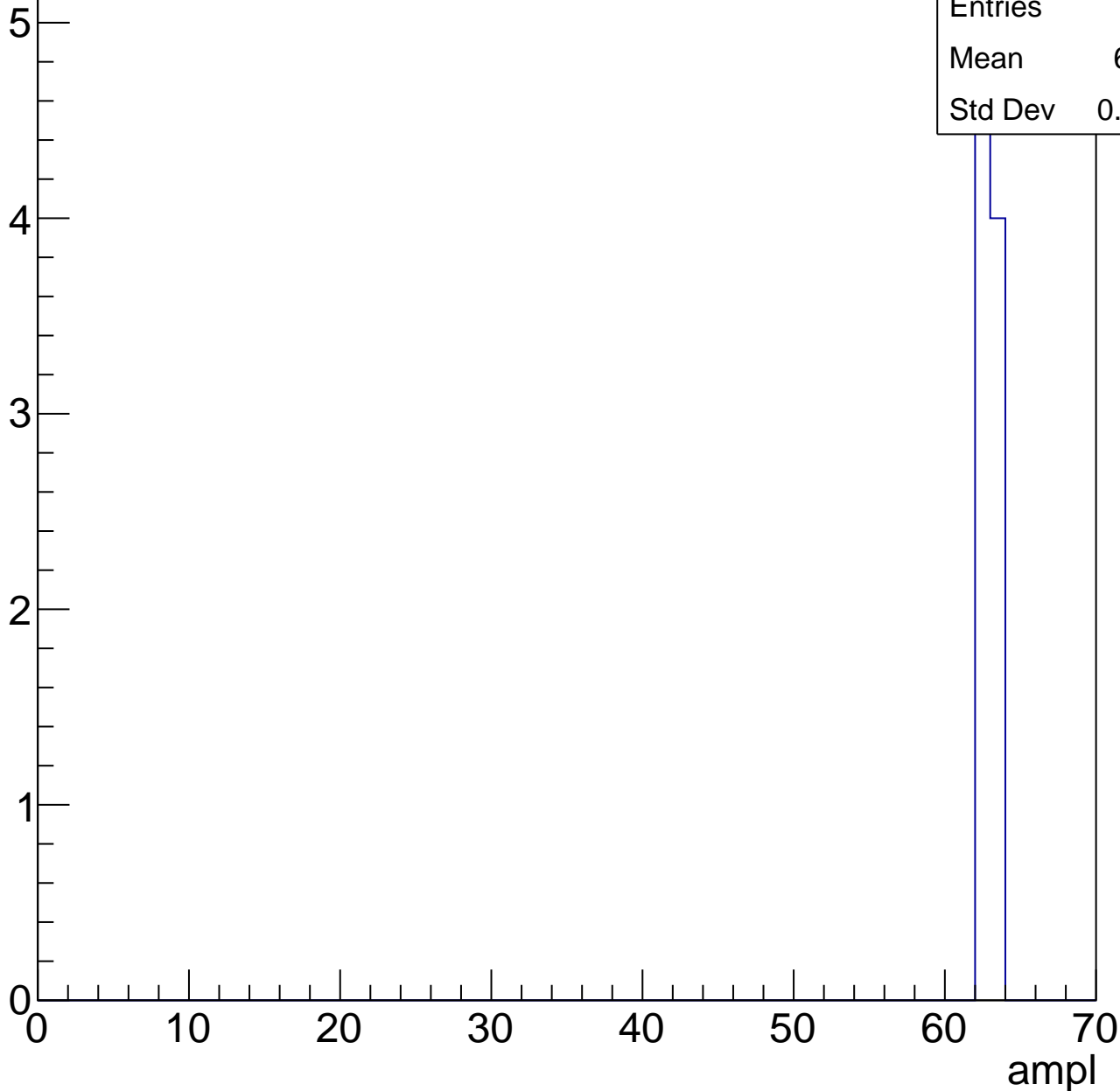


B1L102S, U10-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.44
Std Dev	0.4969



B1L102S, U10-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch30, adc0

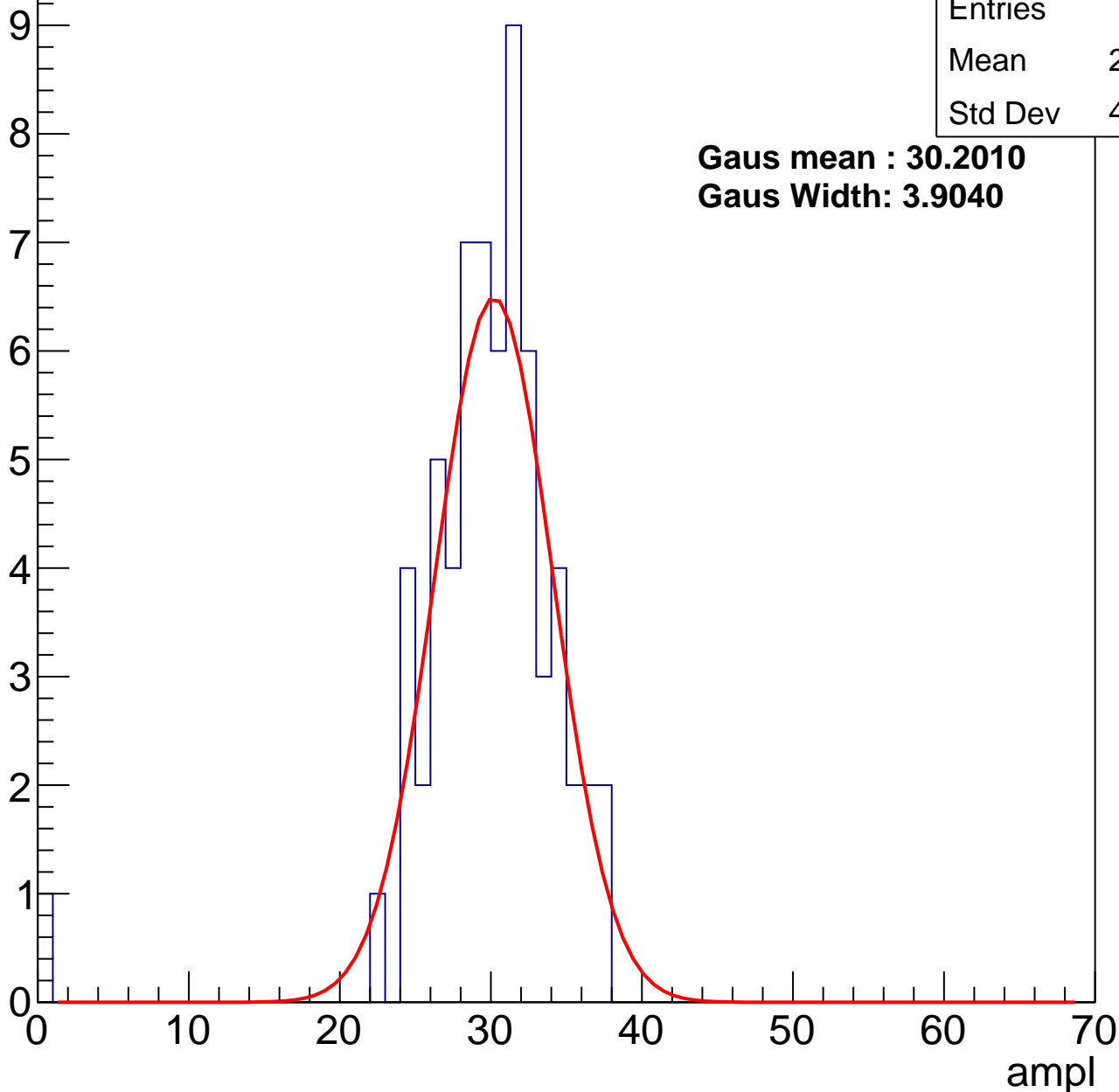
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.34
Std Dev	4.999

Gaus mean : 30.2010

Gaus Width: 3.9040



B1L102S, U10-ch30, adc1

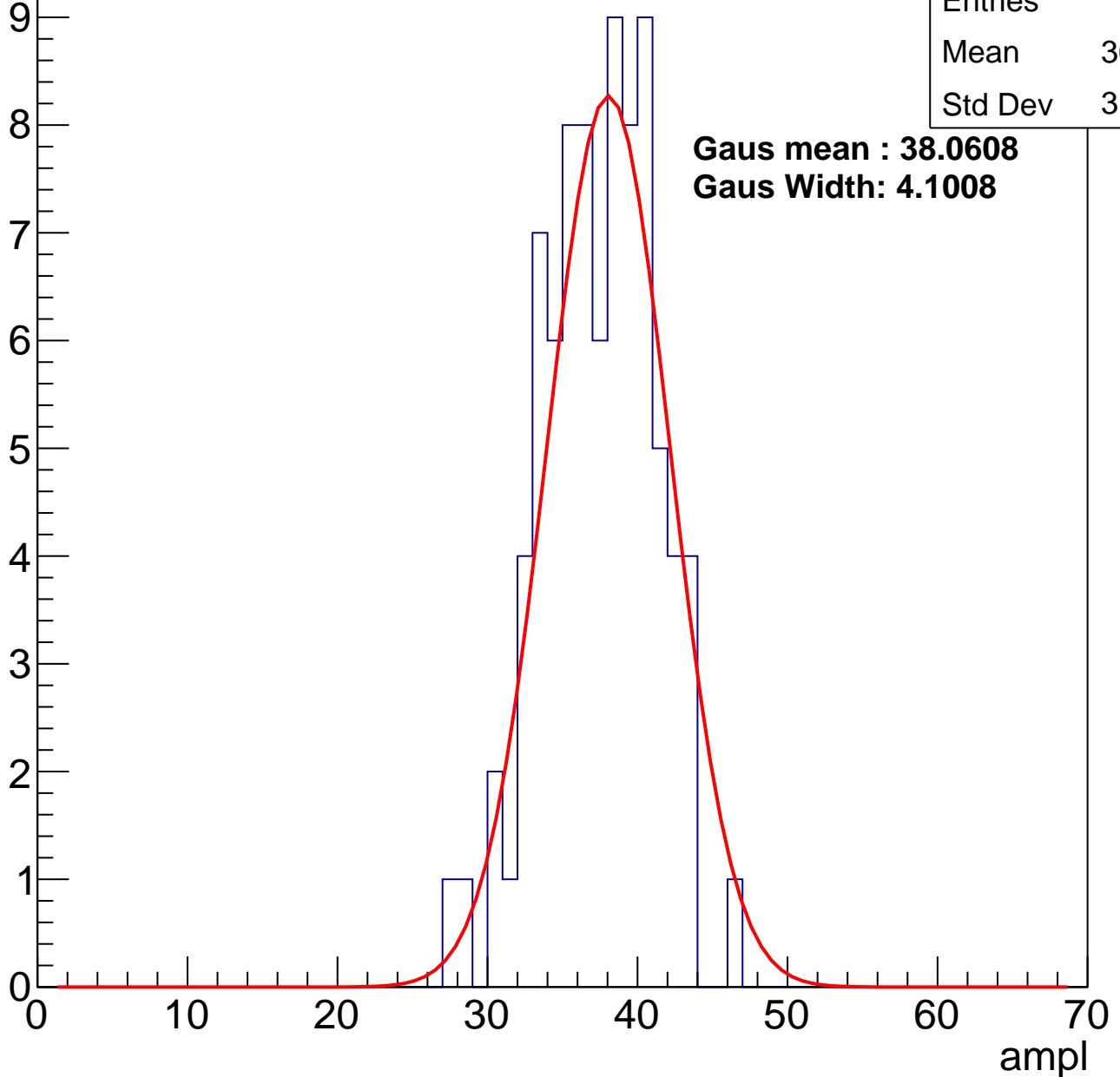
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	36.95
Std Dev	3.697

Gaus mean : 38.0608

Gaus Width: 4.1008



B1L102S, U10-ch30, adc2

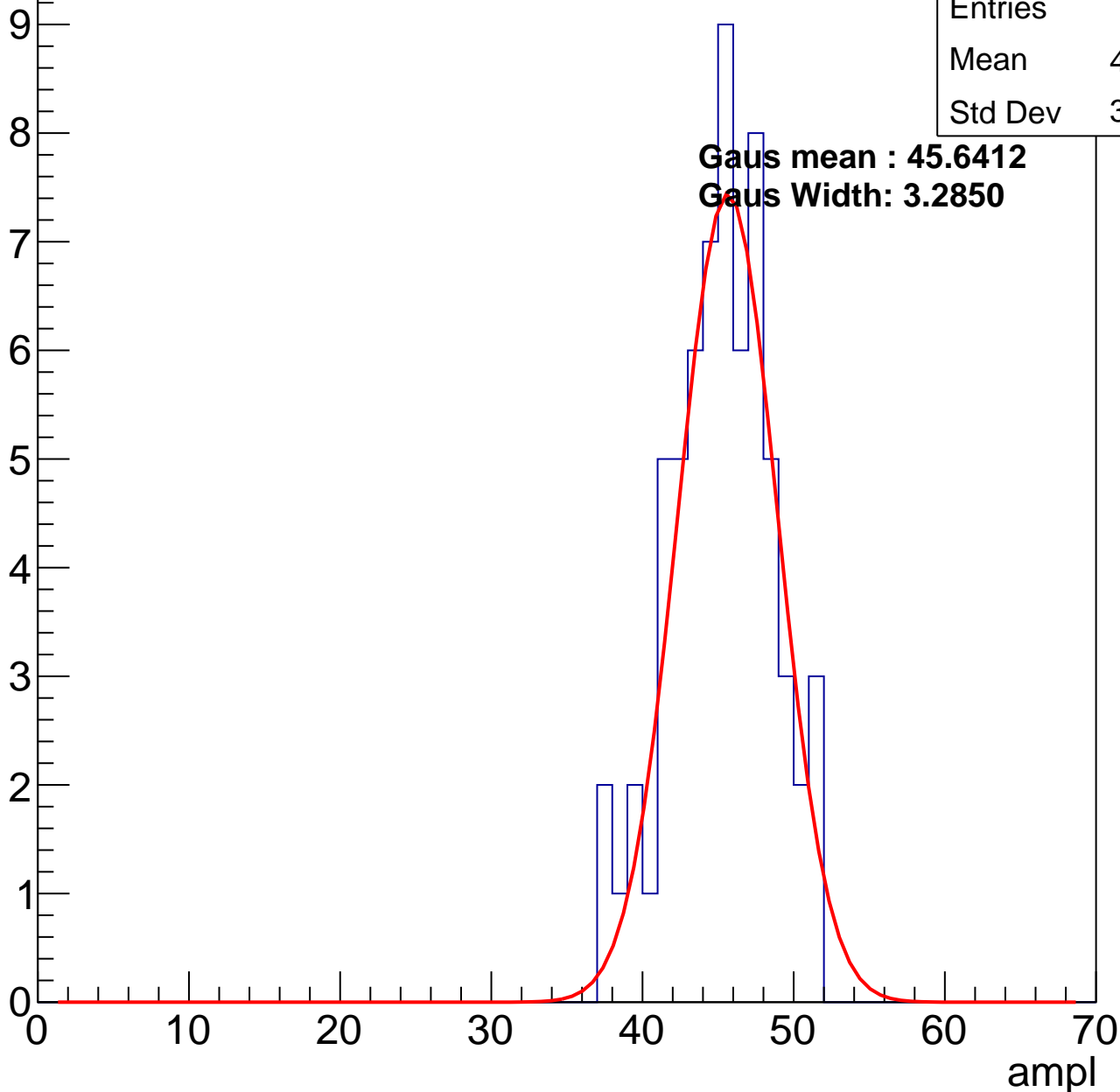
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.74
Std Dev	3.316

Gaus mean : 45.6412

Gaus Width: 3.2850

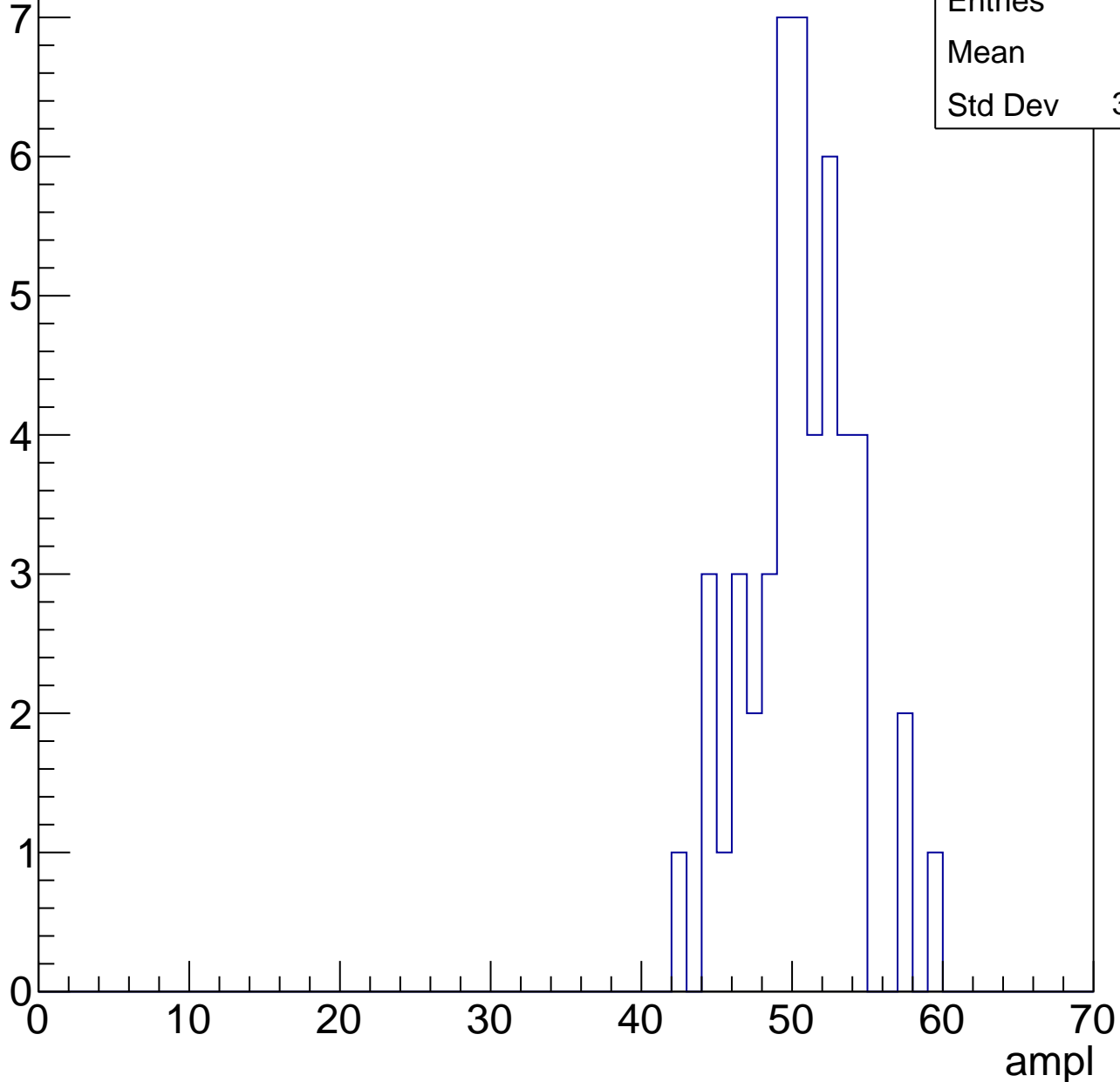


B1L102S, U10-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

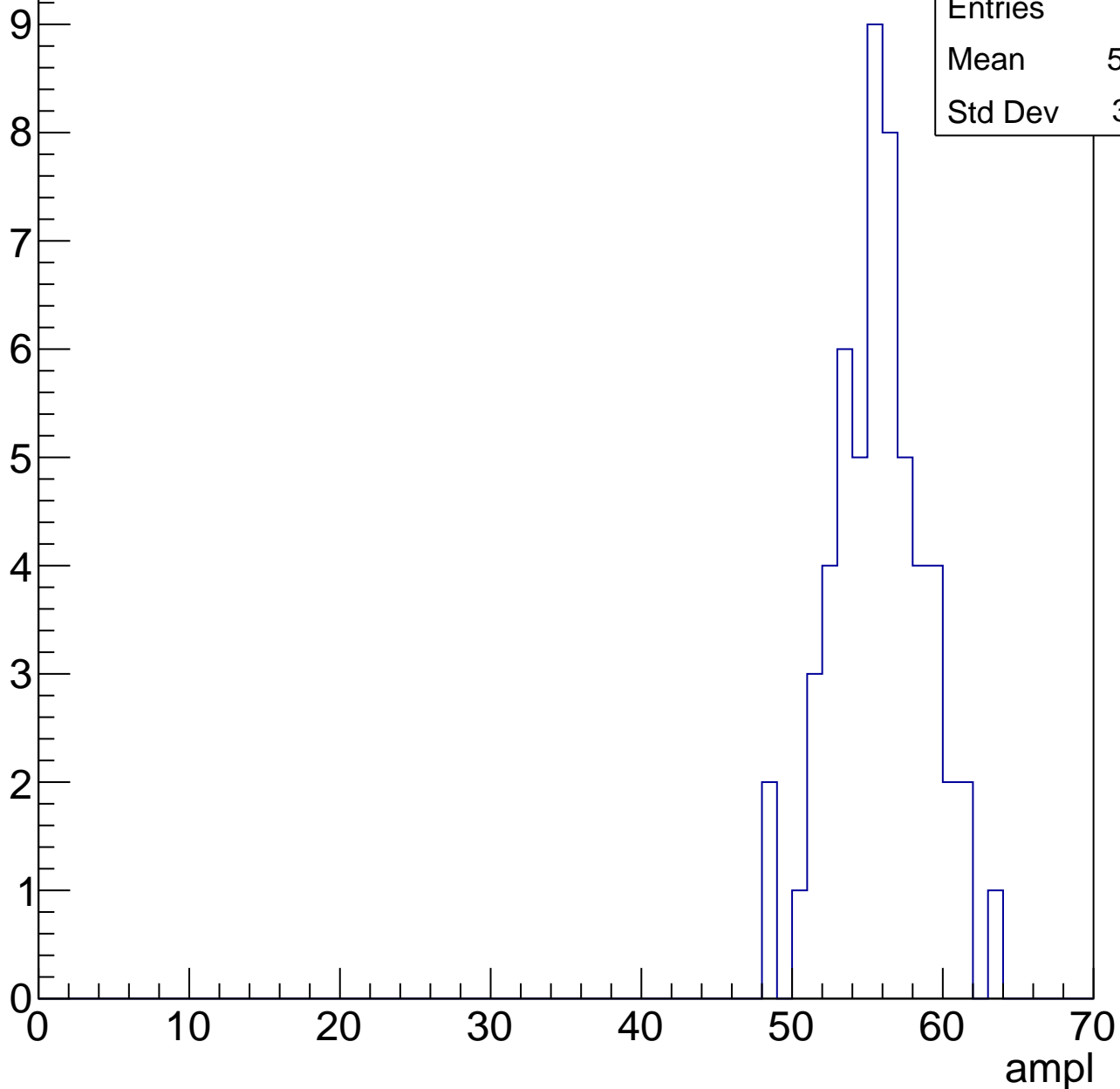
Entries	48
Mean	50.1
Std Dev	3.501



B1L102S, U10-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



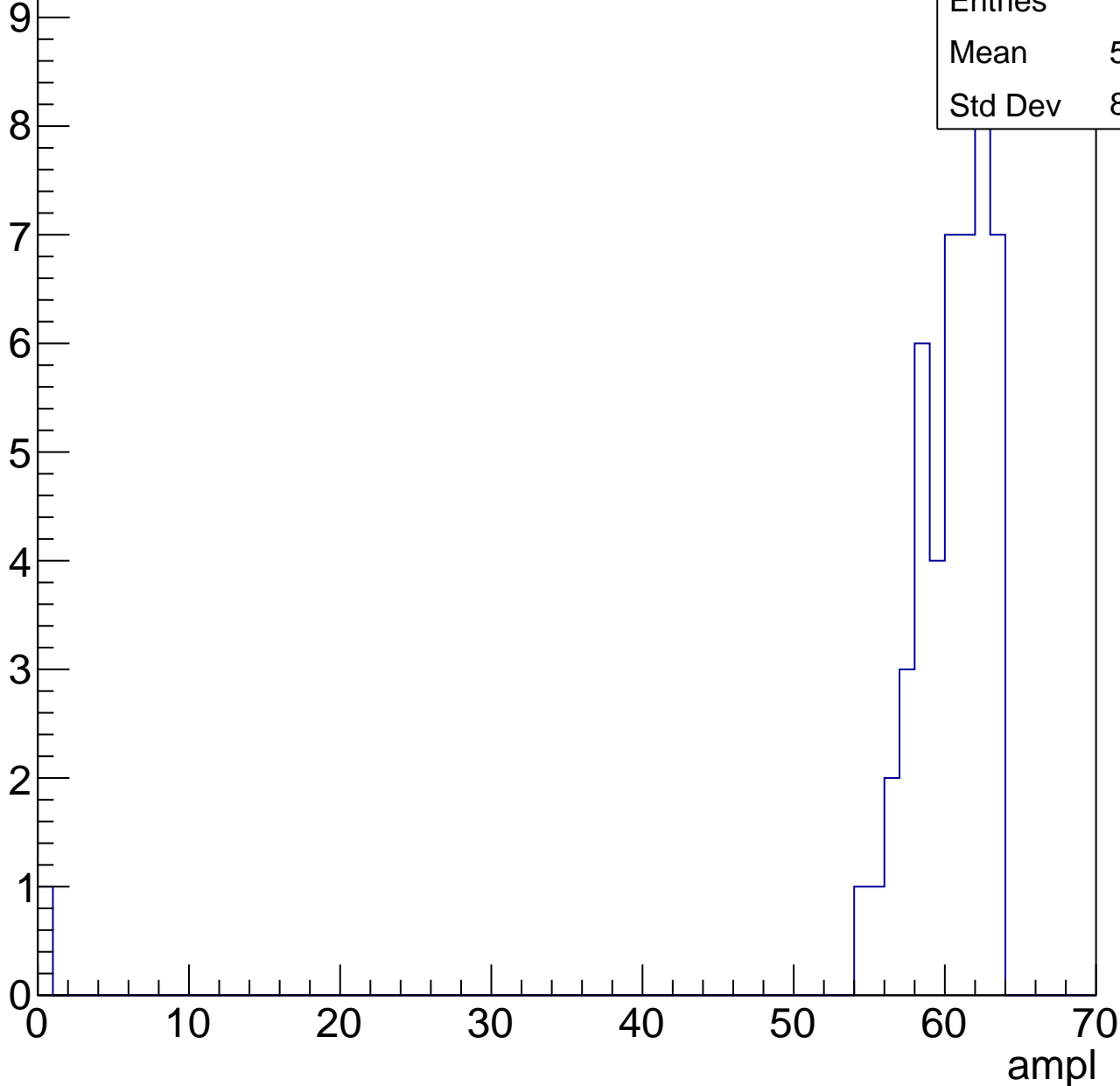
Entries	56
Mean	55.29
Std Dev	3.121

B1L102S, U10-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

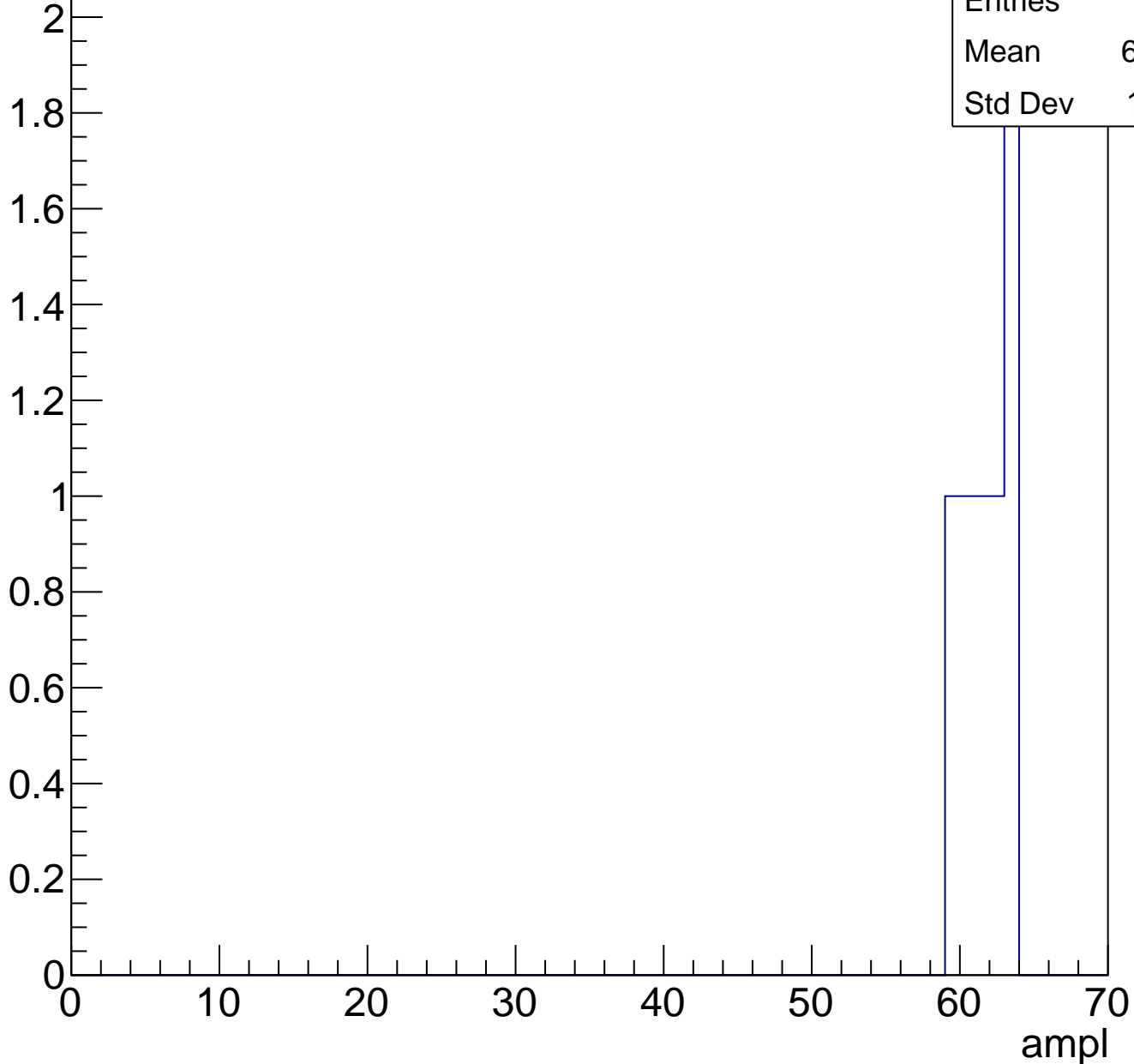
Entries	48
Mean	58.79
Std Dev	8.879



B1L102S, U10-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch31, adc0

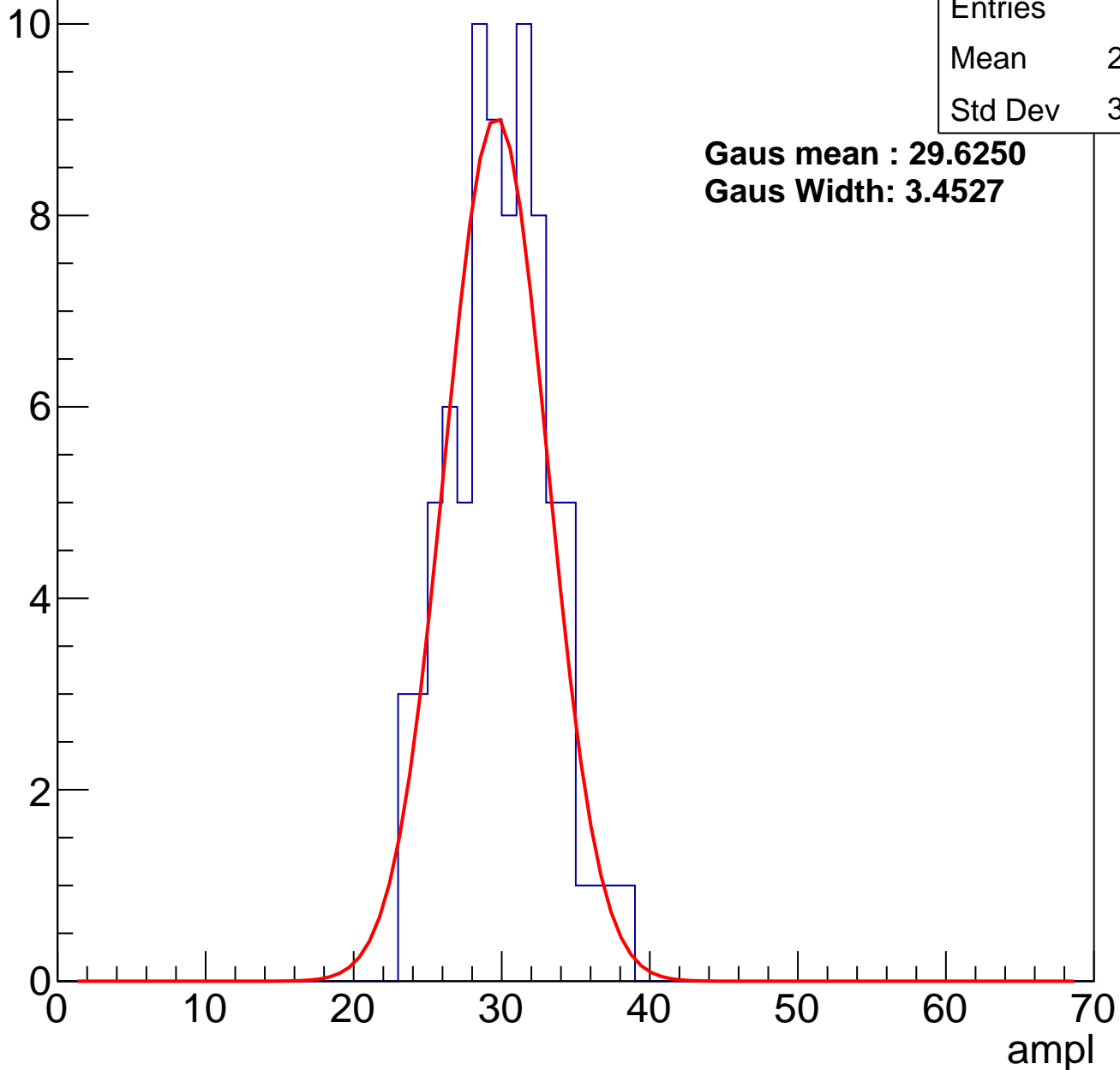
calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	29.44
Std Dev	3.307

Gaus mean : 29.6250

Gaus Width: 3.4527

Entry



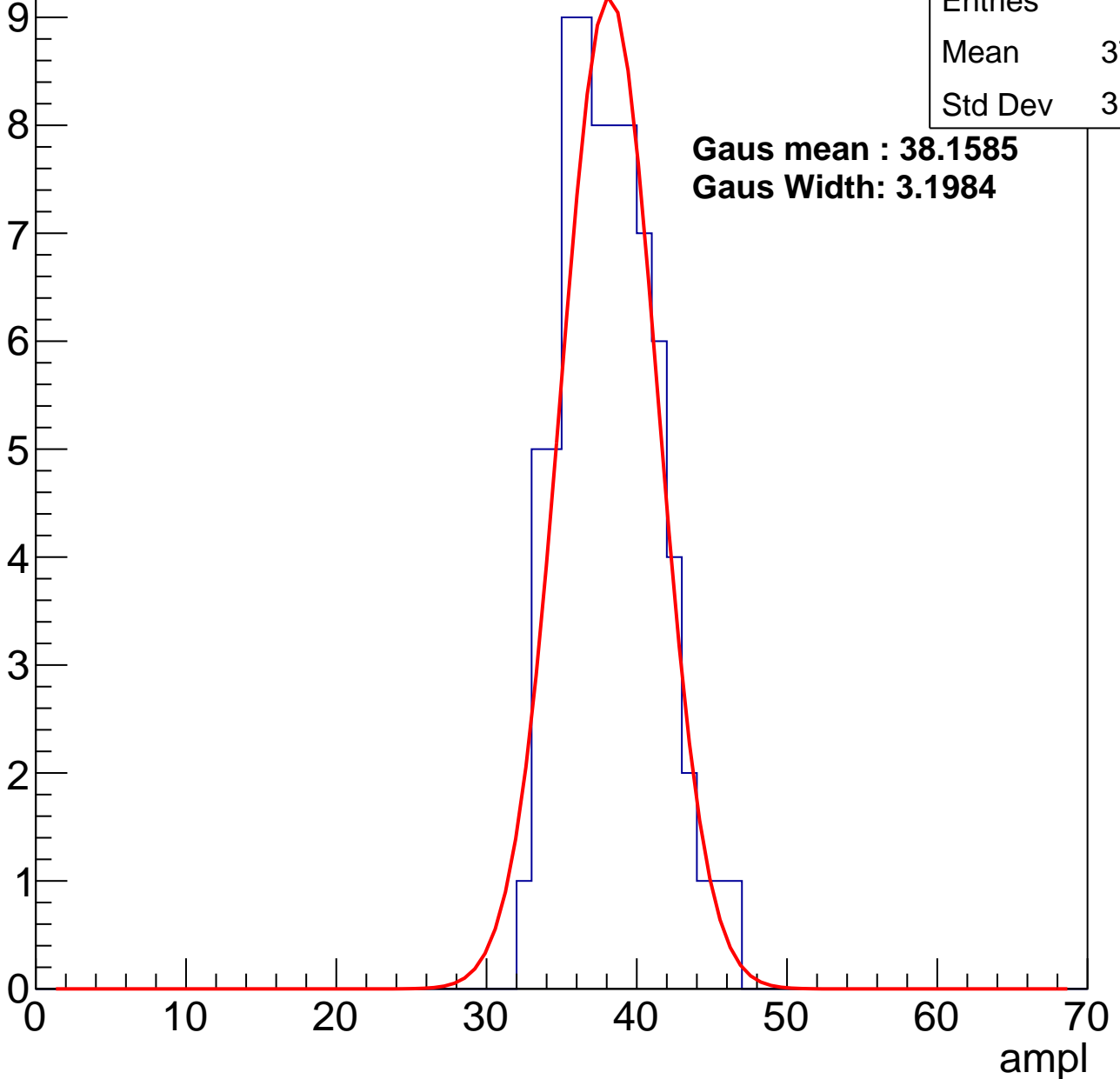
B1L102S, U10-ch31, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	37.77
Std Dev	3.088

Gaus mean : 38.1585
Gaus Width: 3.1984



B1L102S, U10-ch31, adc2

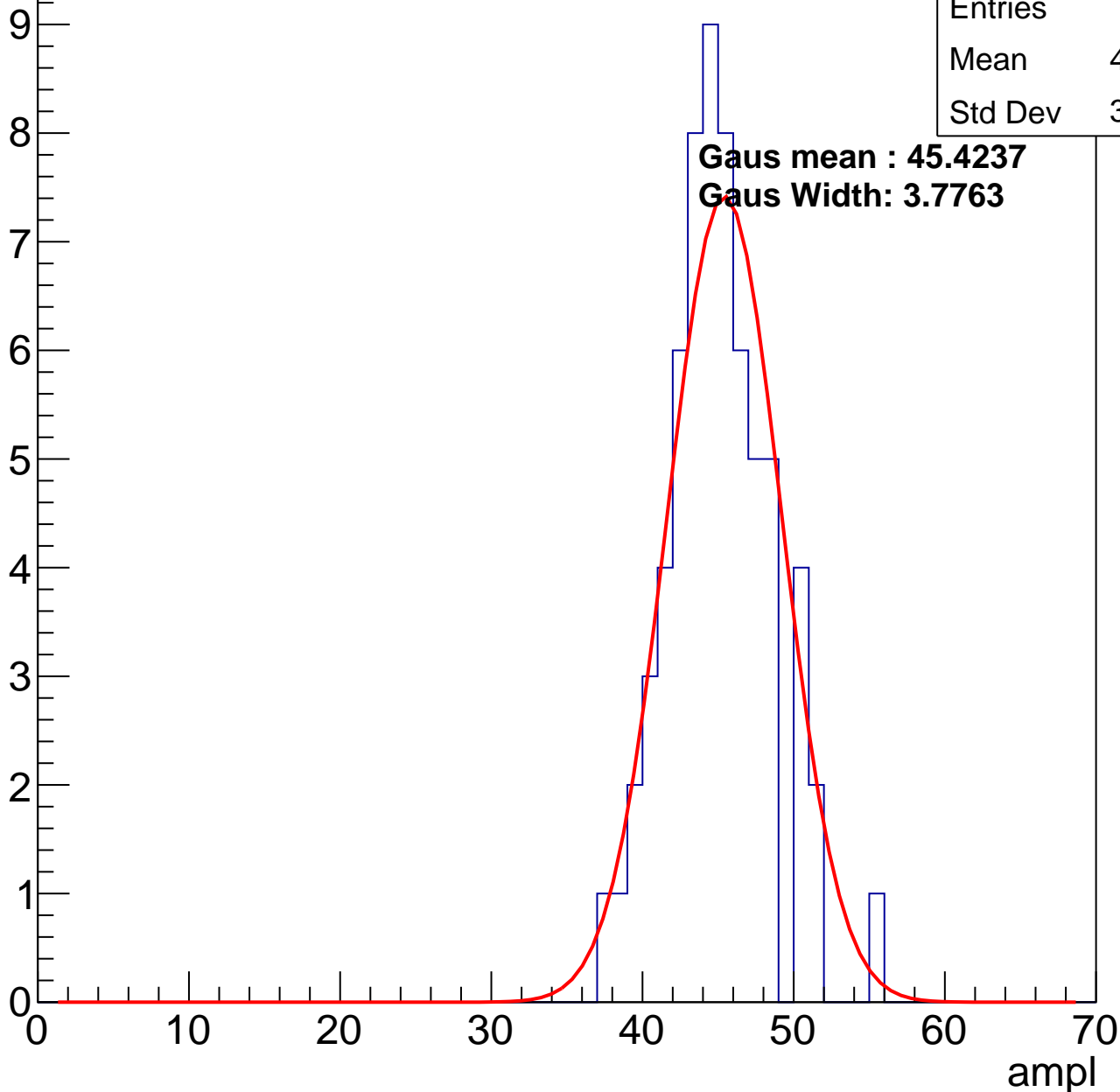
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.57
Std Dev	3.397

Gaus mean : 45.4237

Gaus Width: 3.7763



B1L102S, U10-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	50.75
Std Dev	2.873

Entry

10

8

6

4

2

0

0

10

20

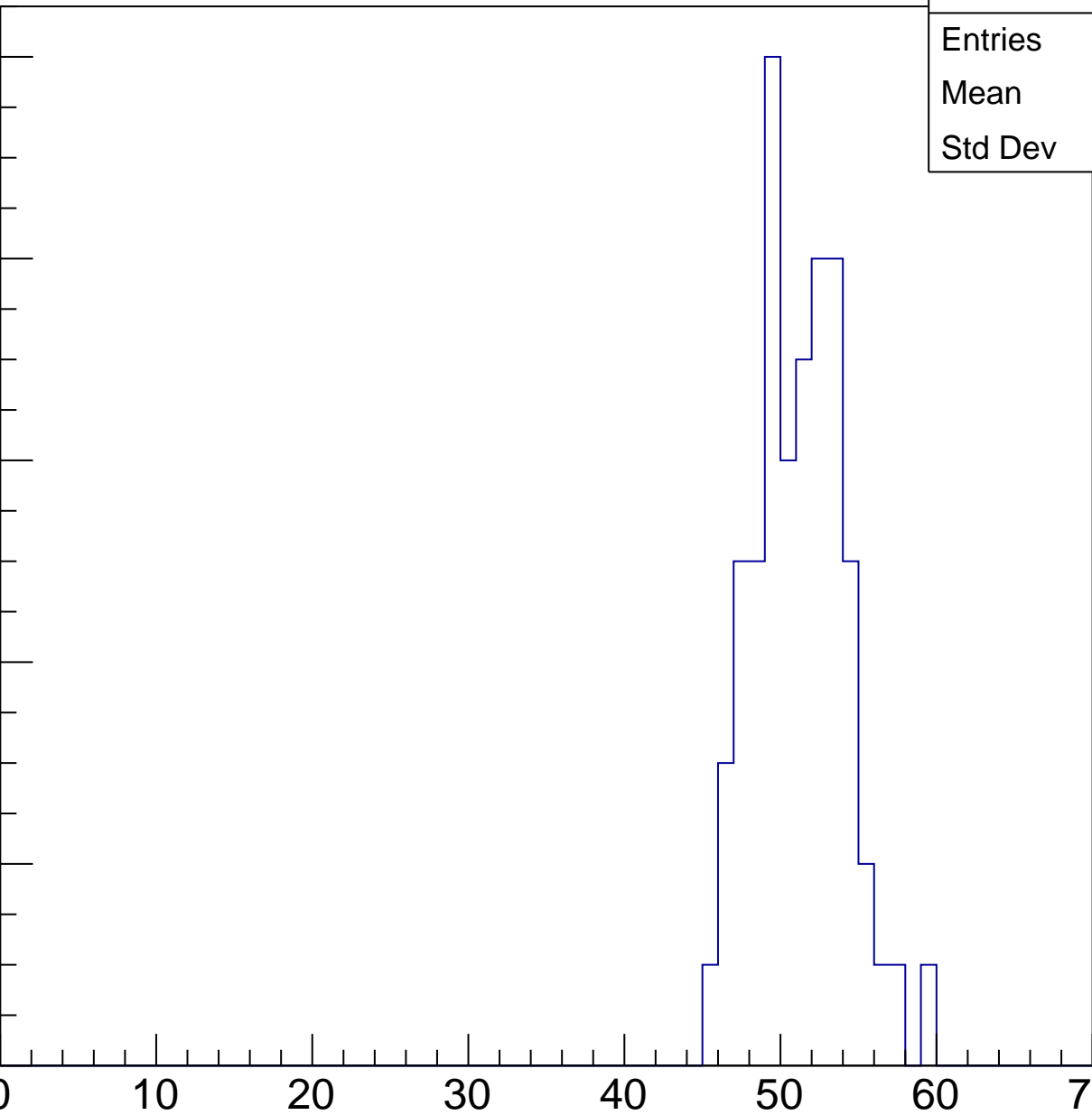
30

40

50

60

ampl

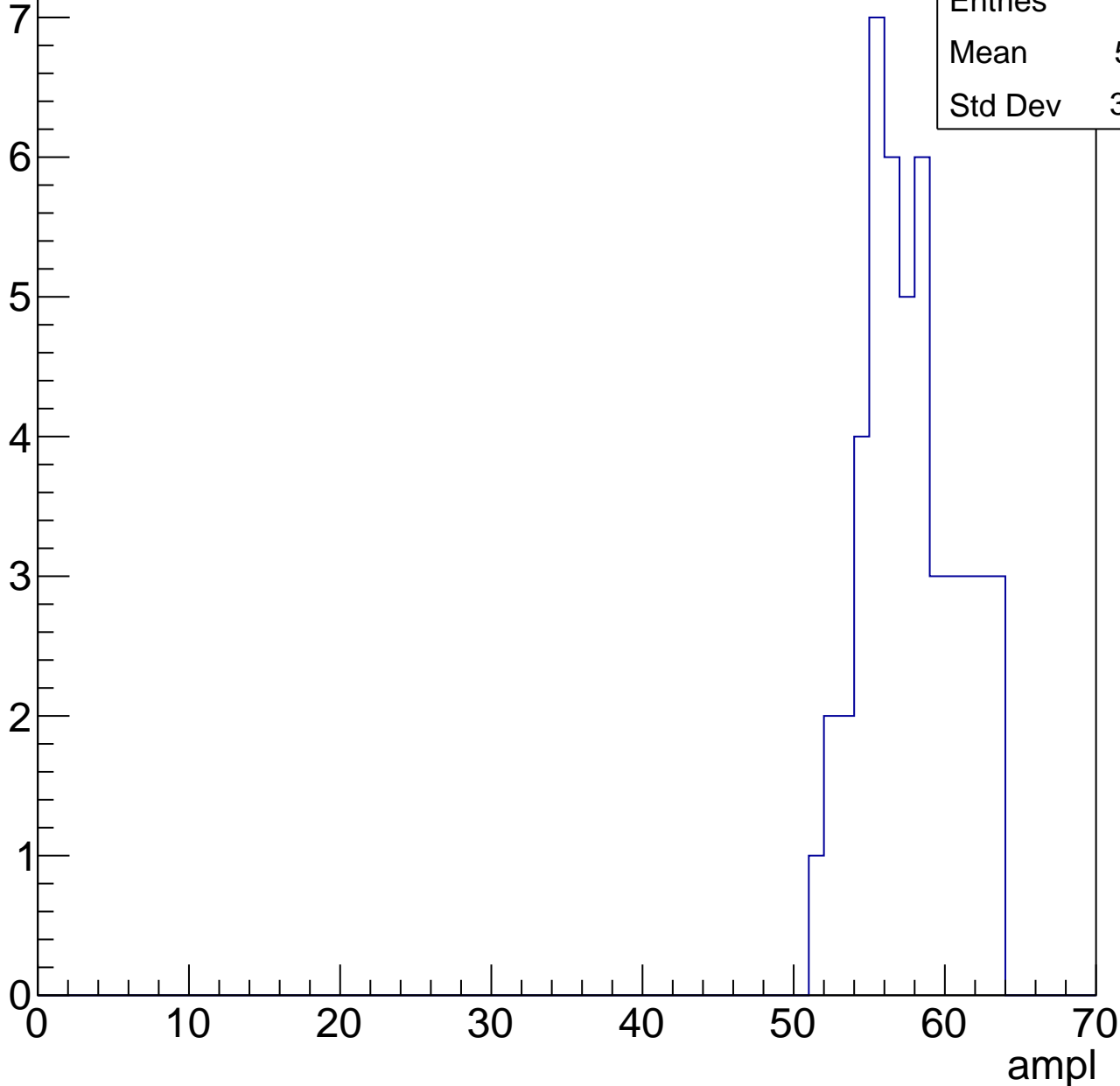


B1L102S, U10-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.21
Std Dev	3.102

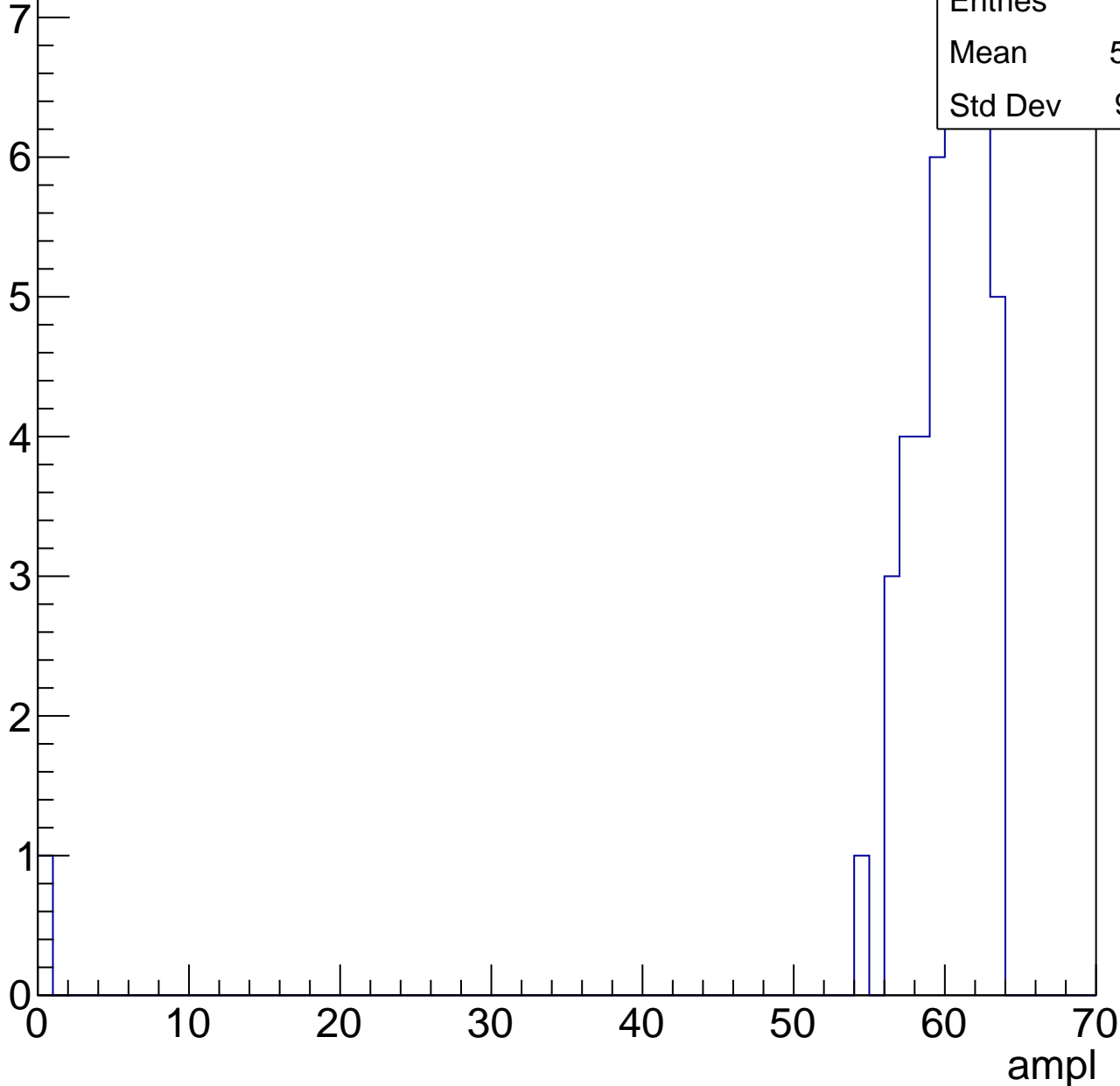


B1L102S, U10-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.49
Std Dev	9.091



B1L102S, U10-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L102S, U10-ch32, adc0

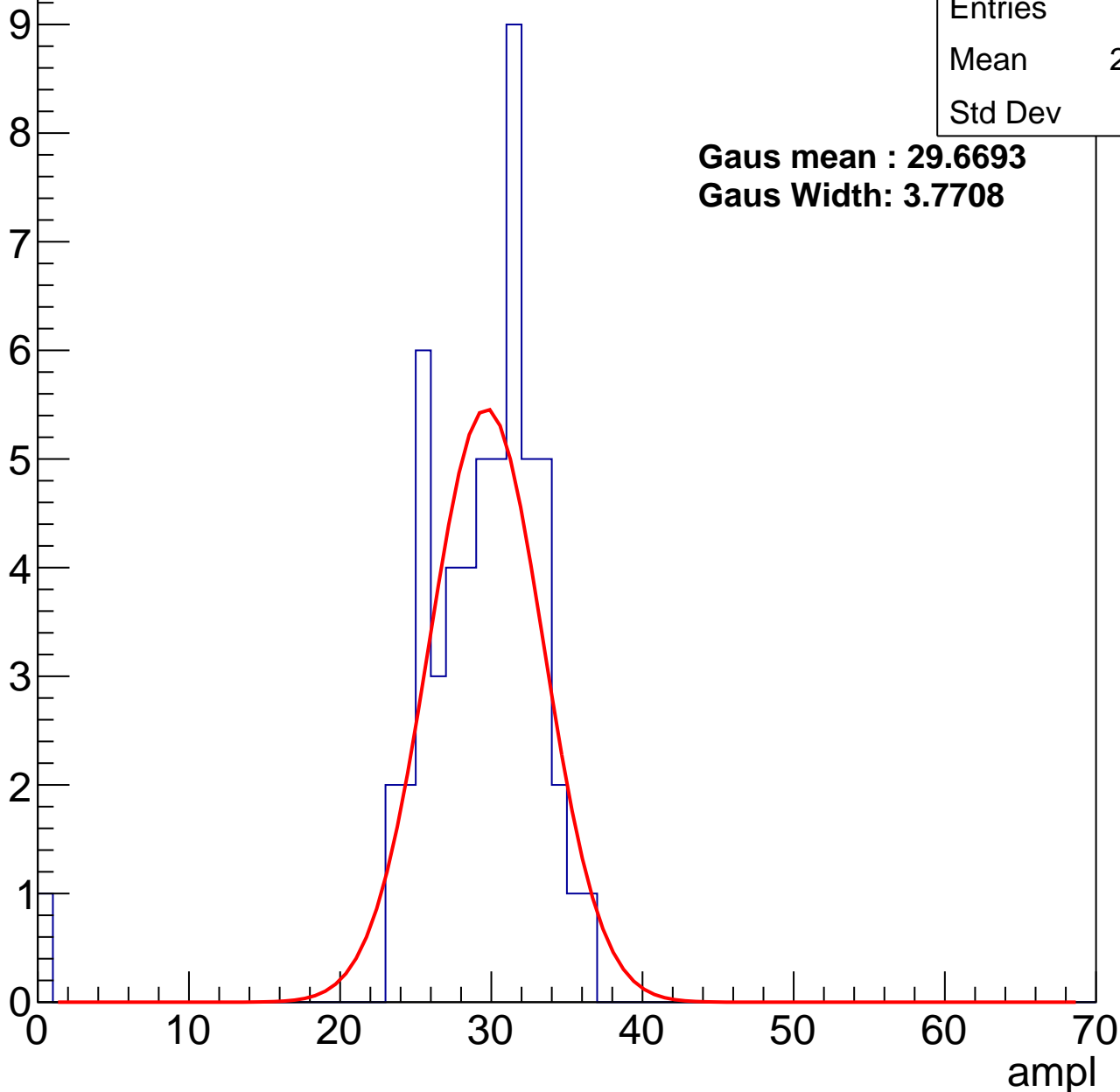
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.73
Std Dev	5.05

Gaus mean : 29.6693

Gaus Width: 3.7708



B1L102S, U10-ch32, adc1

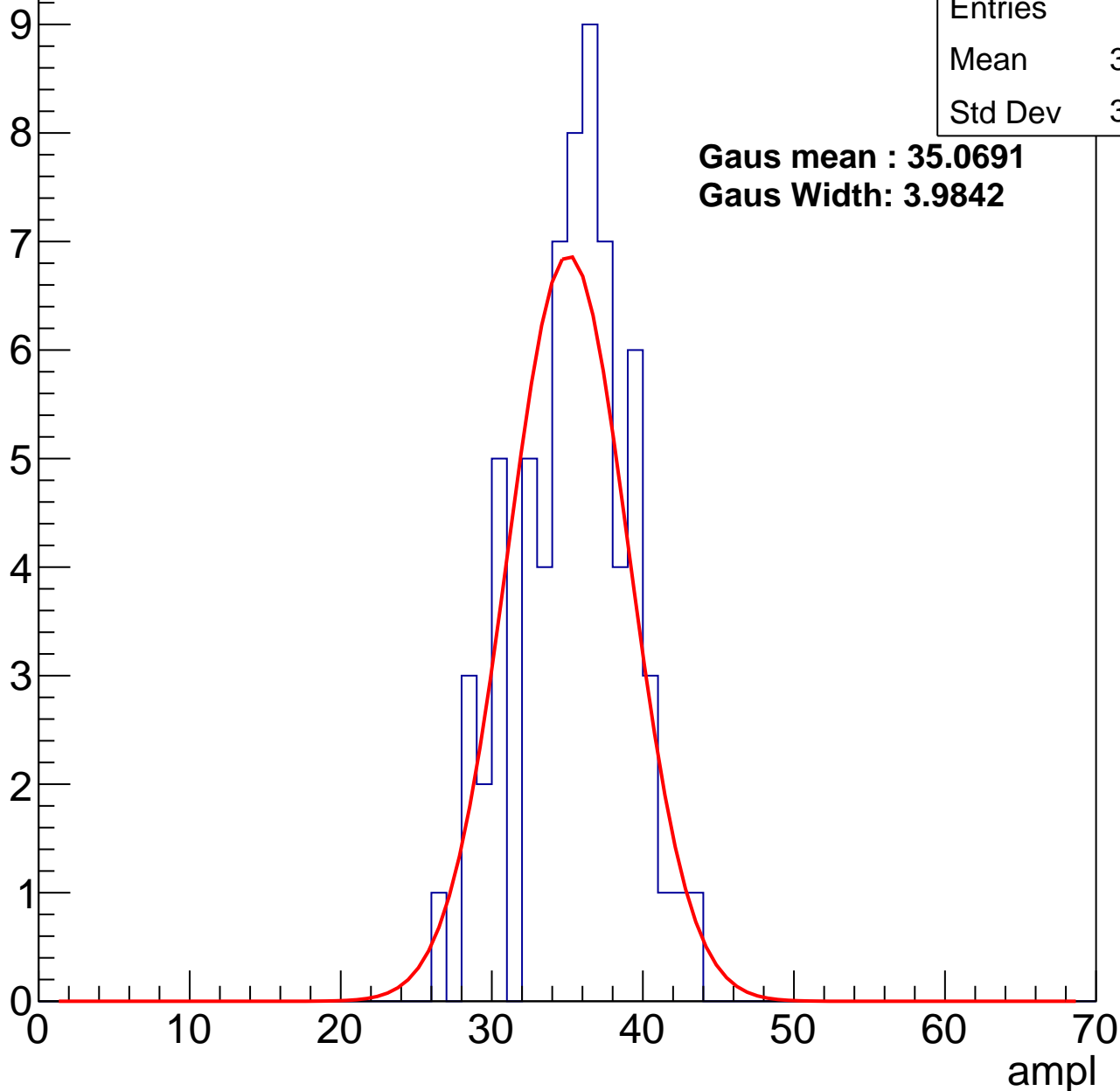
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.97
Std Dev	3.636

Gaus mean : 35.0691

Gaus Width: 3.9842



B1L102S, U10-ch32, adc2

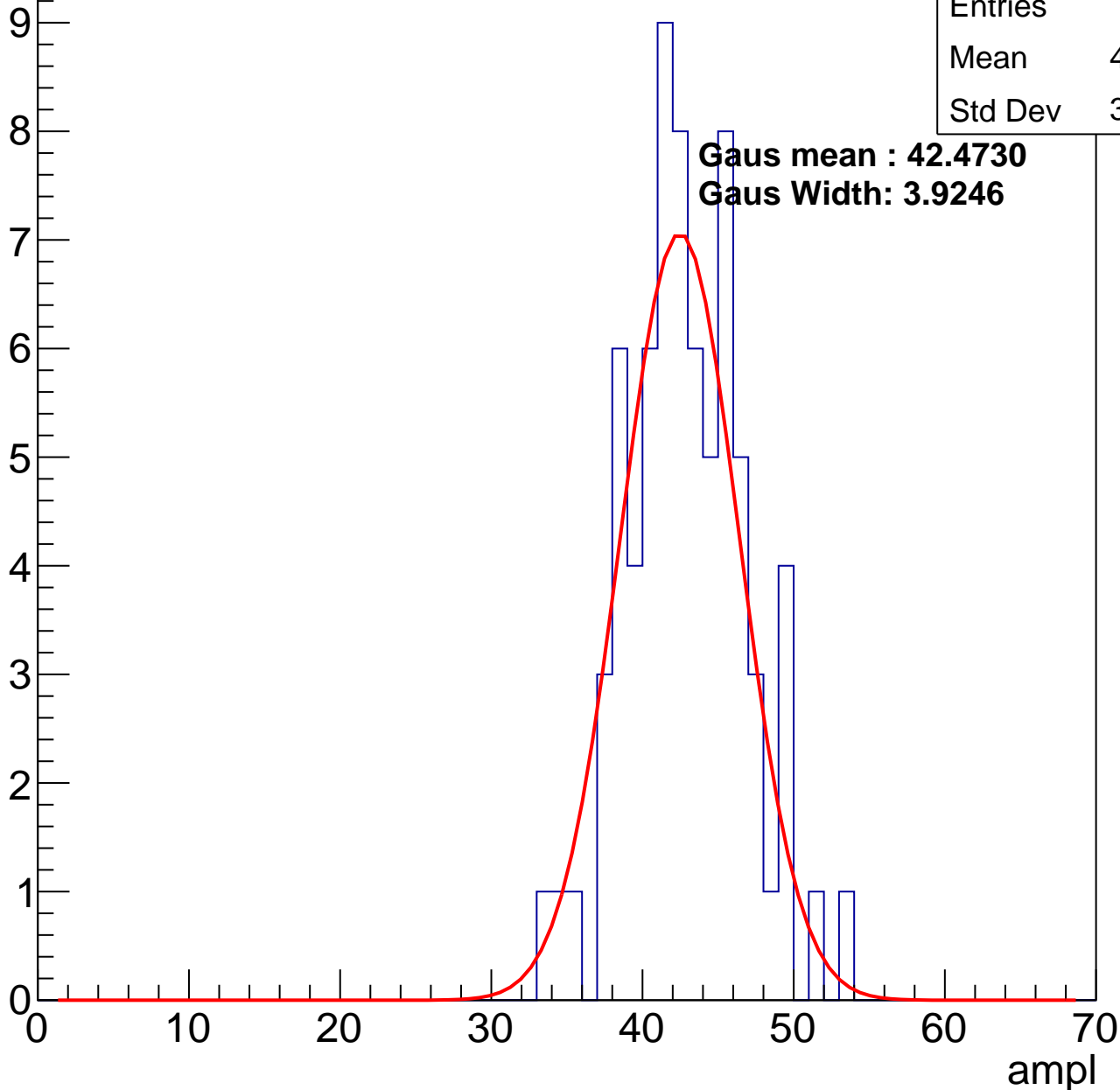
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.45
Std Dev	3.893

Gaus mean : 42.4730

Gaus Width: 3.9246

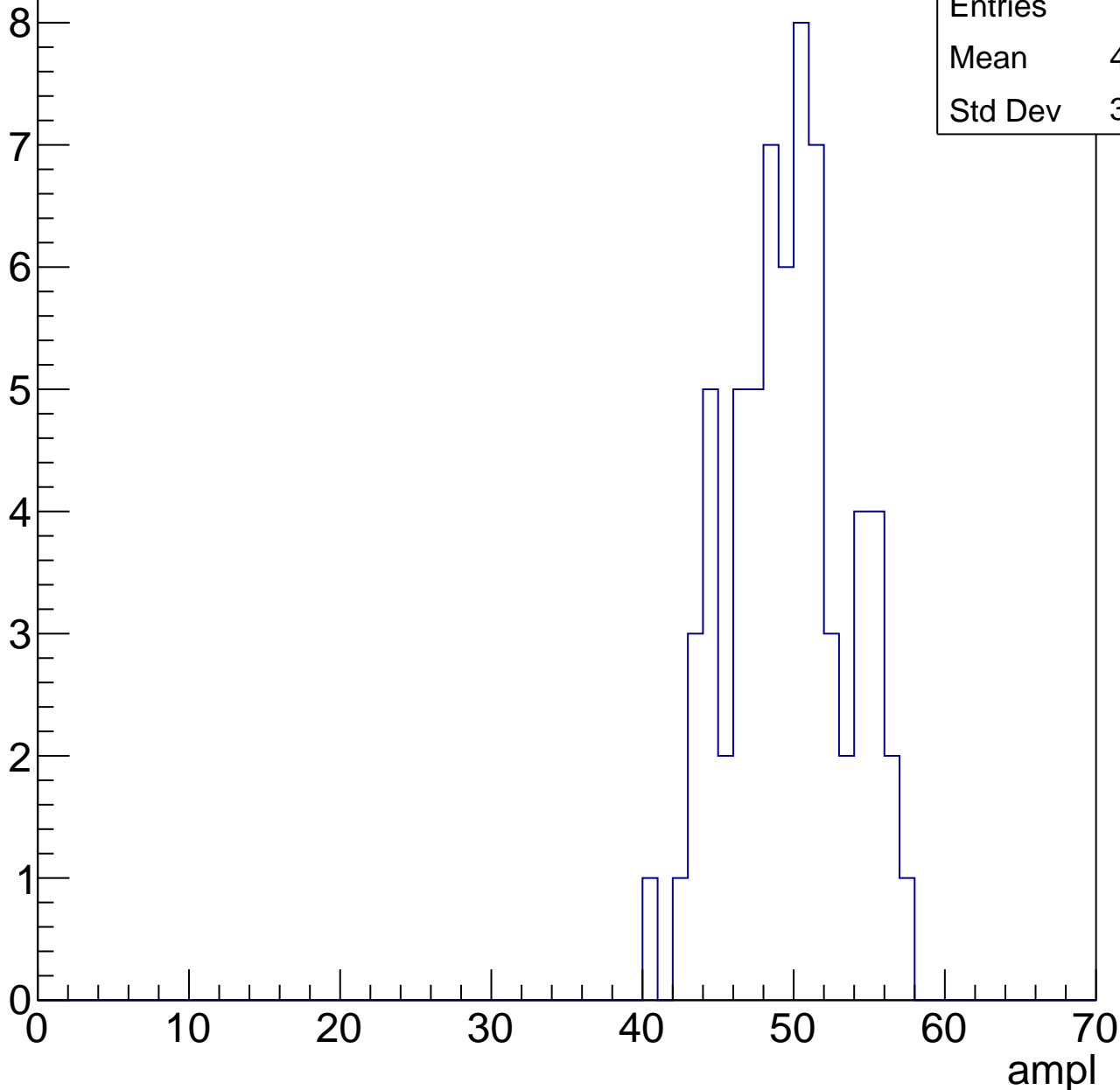


B1L102S, U10-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.09
Std Dev	3.829

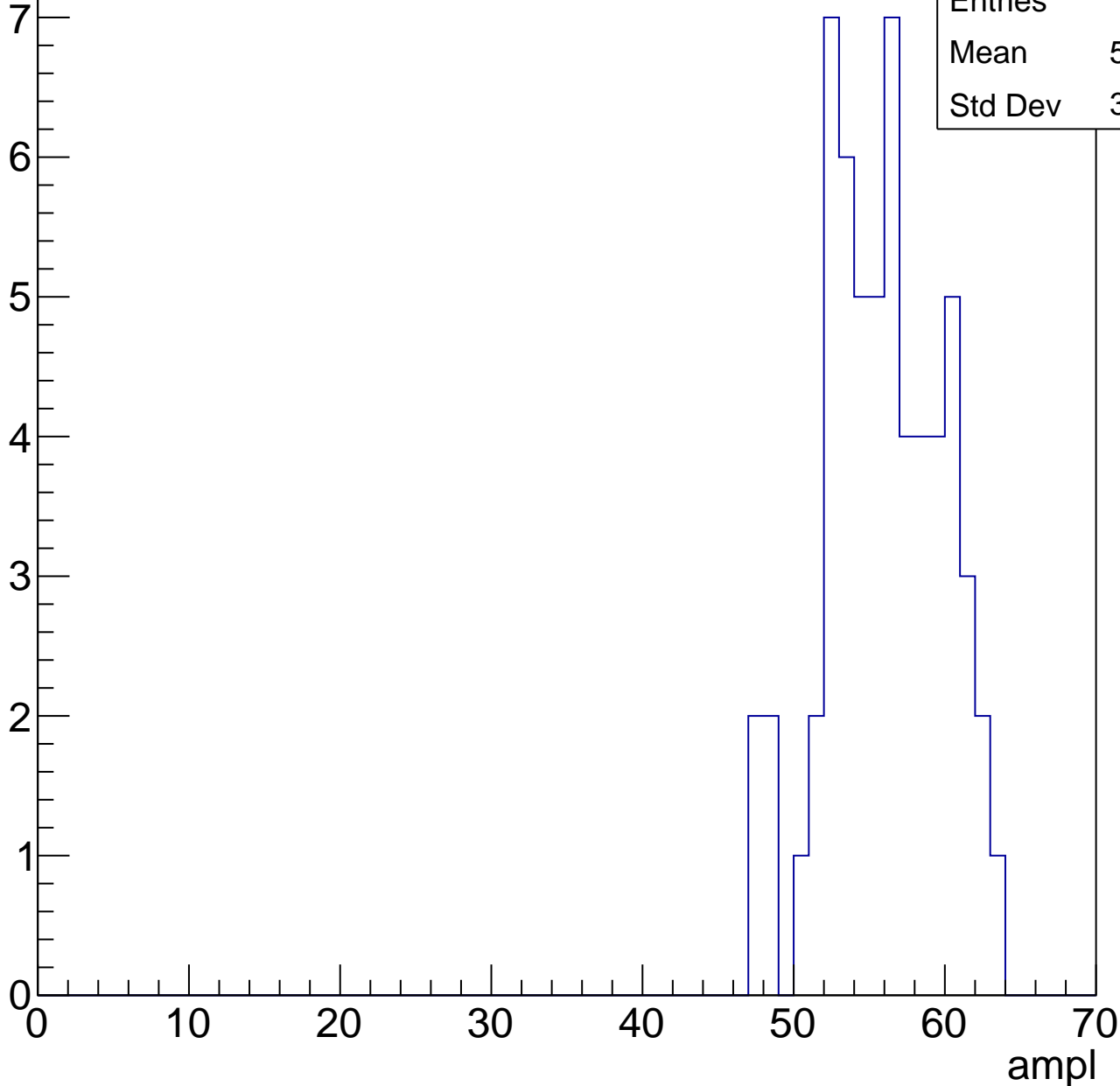


B1L102S, U10-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.45
Std Dev	3.832

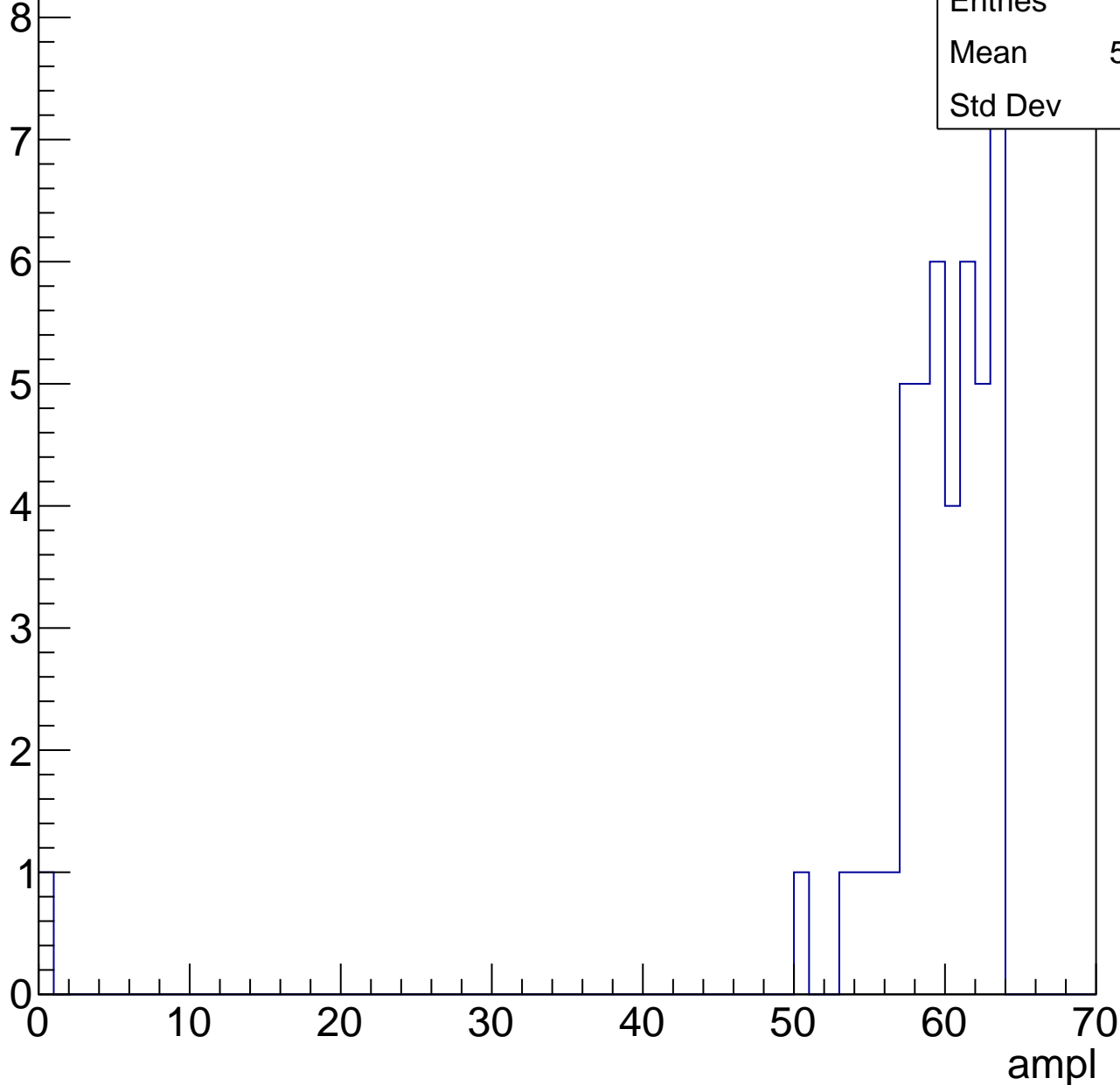


B1L102S, U10-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

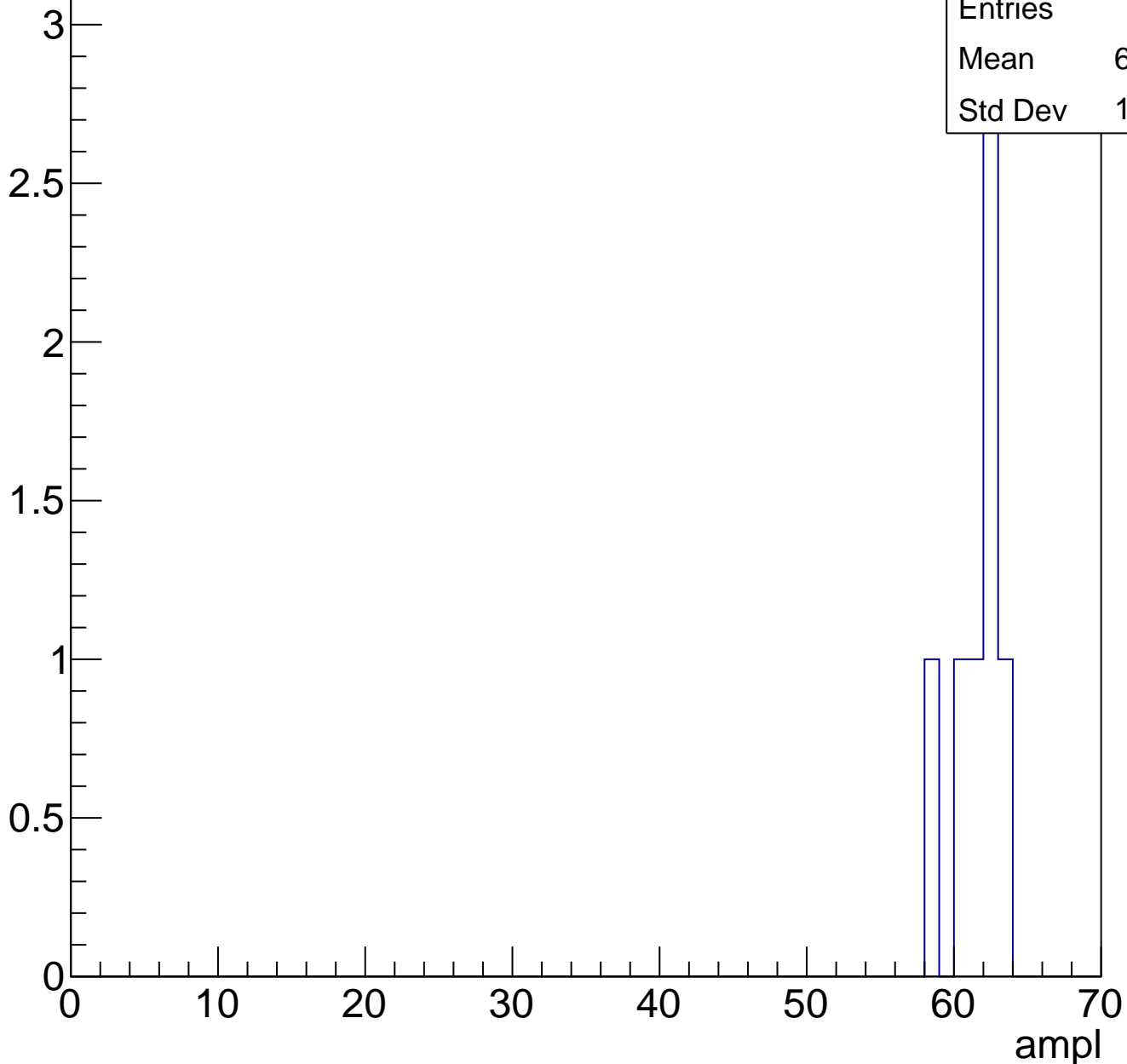
Entries	45
Mean	58.16
Std Dev	9.24



B1L102S, U10-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch33, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	28.73
Std Dev	6.109

Gaus mean : 29.4603

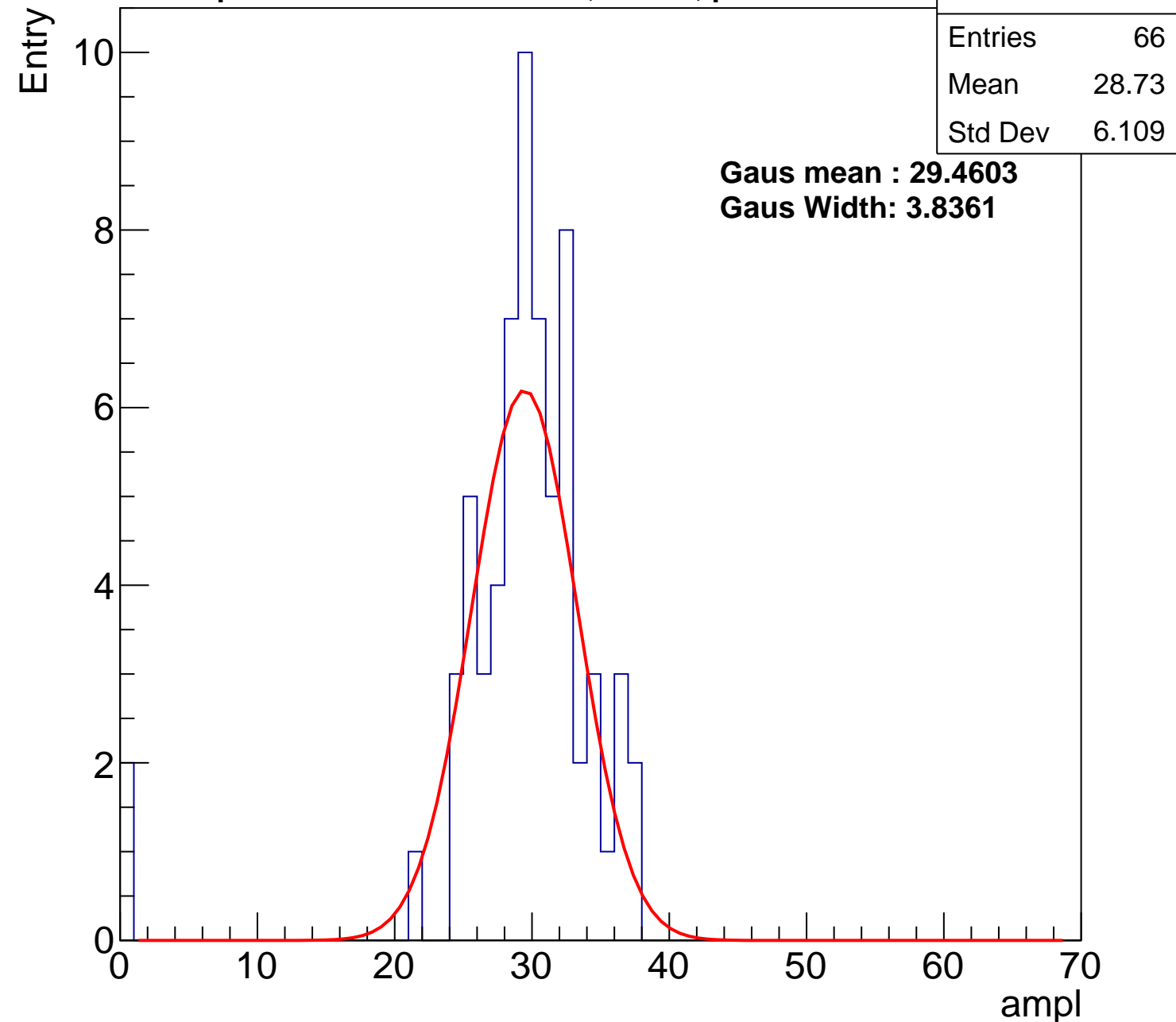
Gaus Width: 3.8361

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch33, adc1

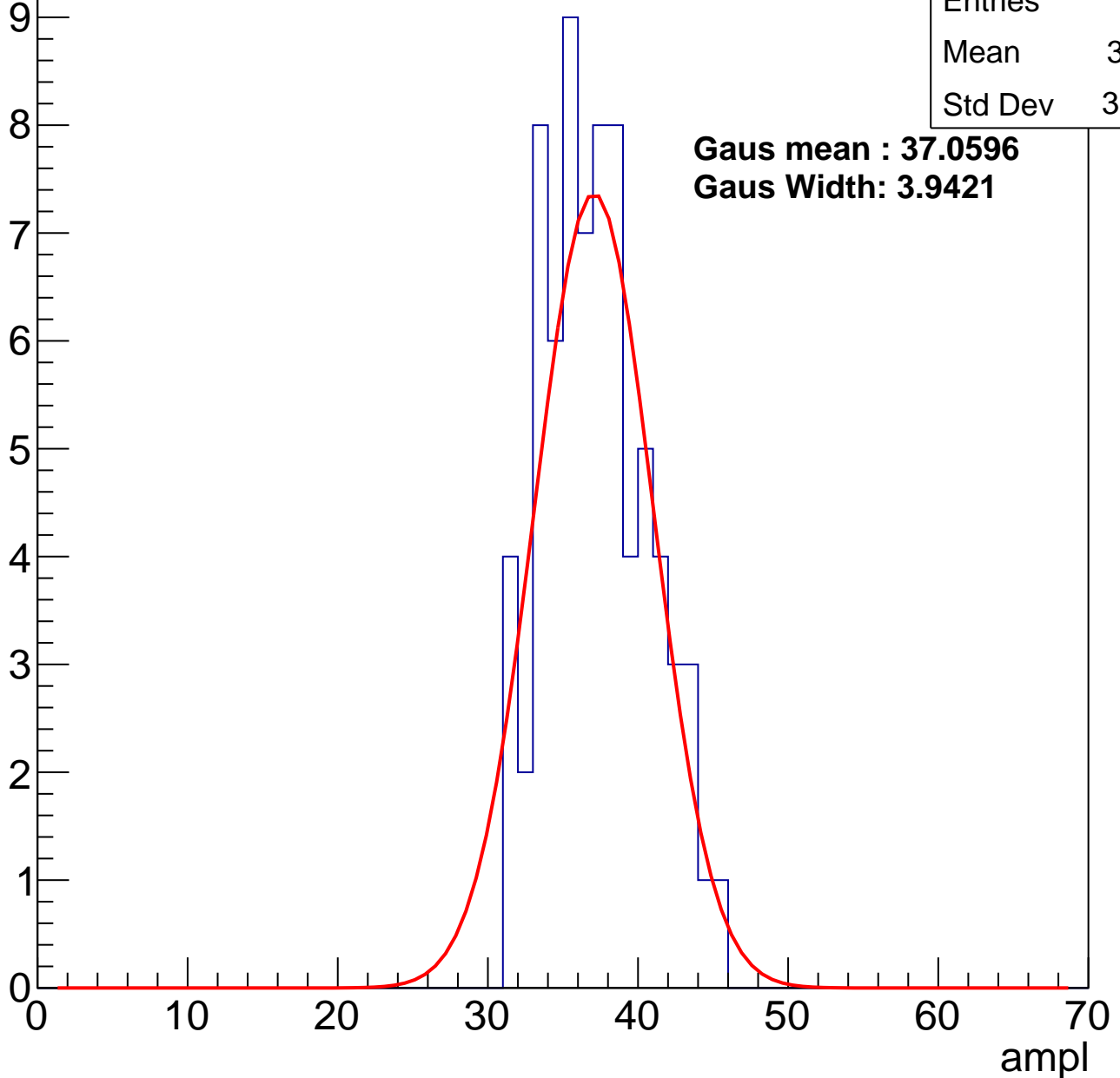
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.81
Std Dev	3.403

Gaus mean : 37.0596

Gaus Width: 3.9421



B1L102S, U10-ch33, adc2

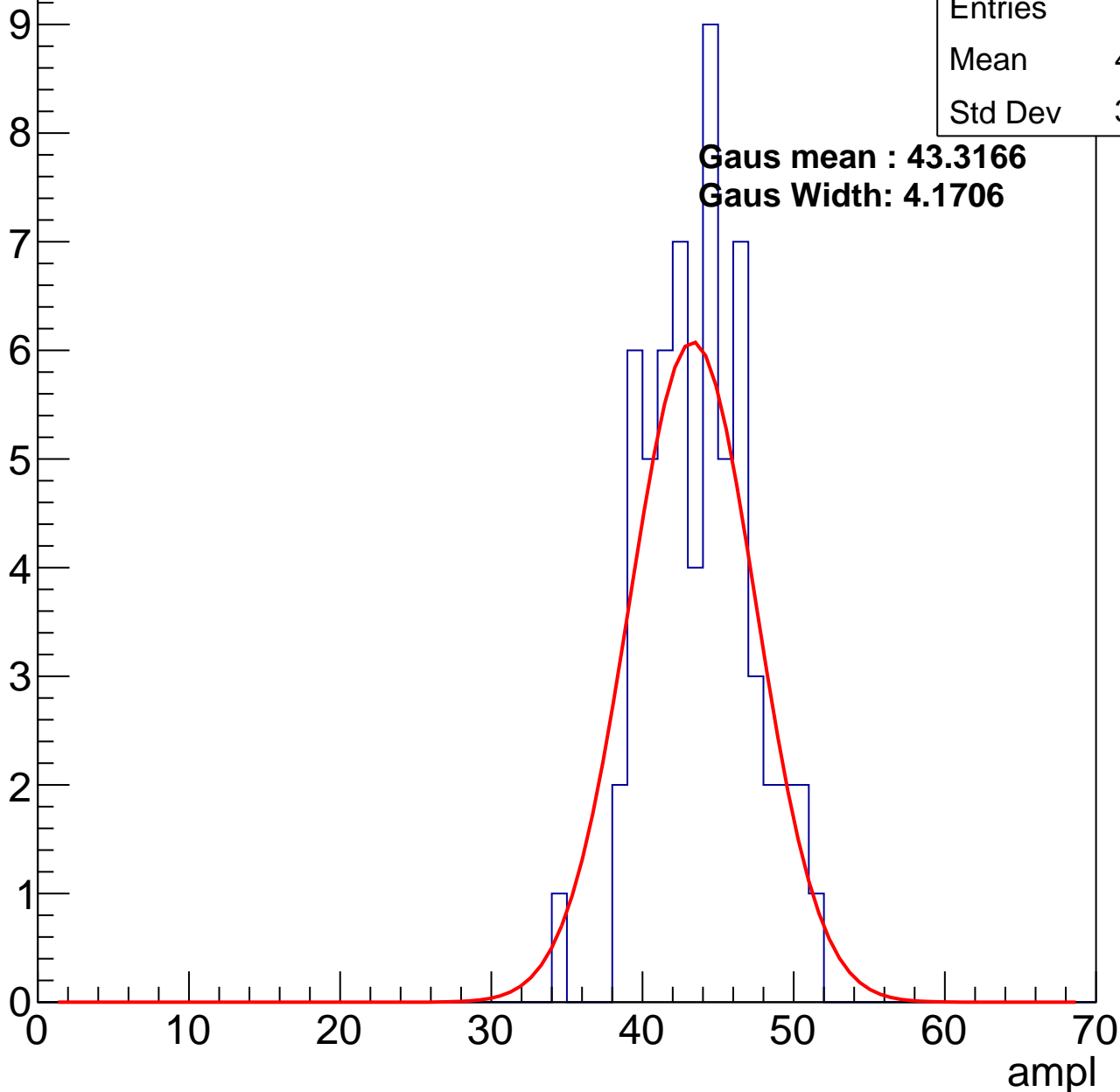
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.31
Std Dev	3.411

Gaus mean : 43.3166

Gaus Width: 4.1706

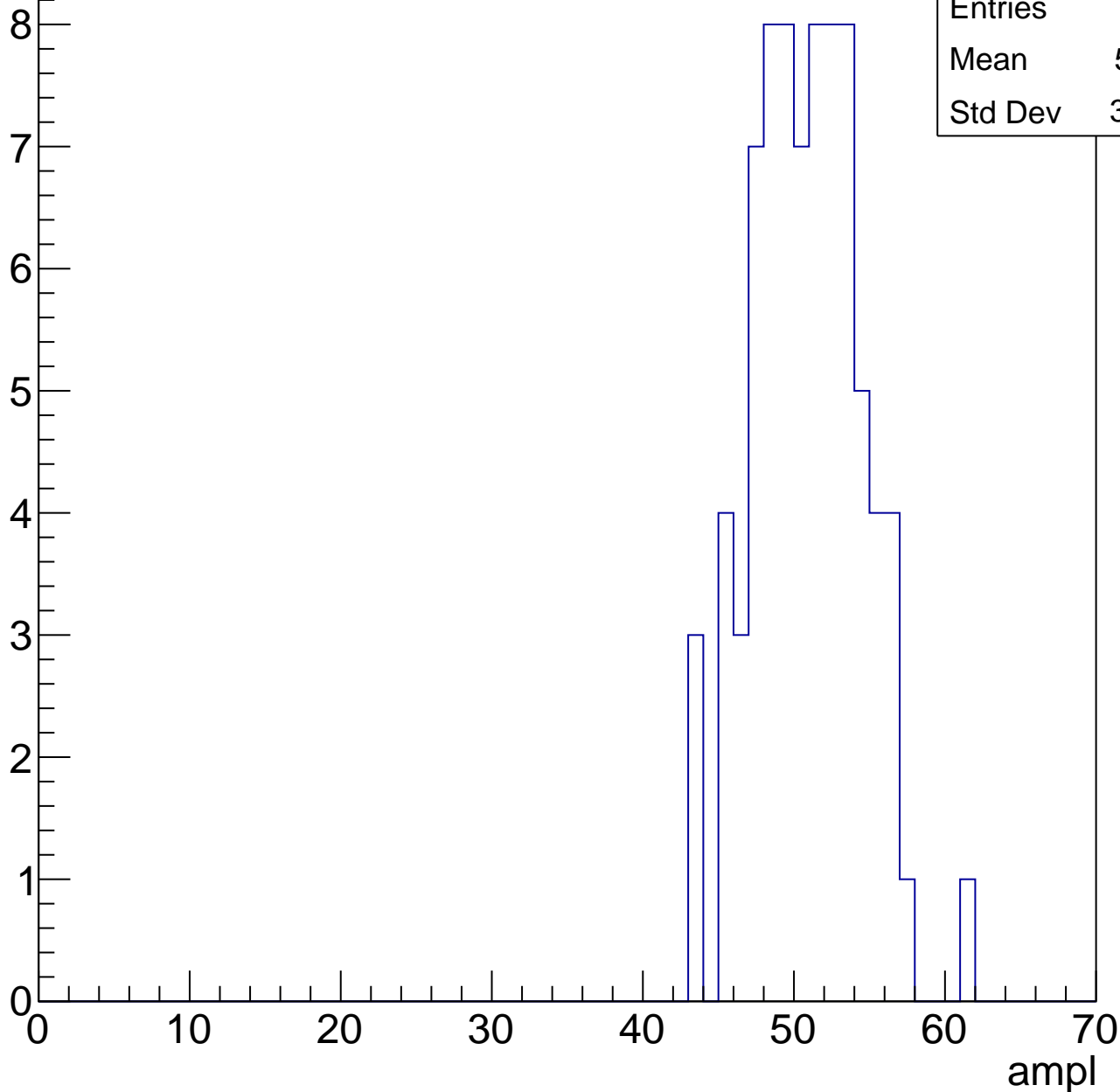


B1L102S, U10-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

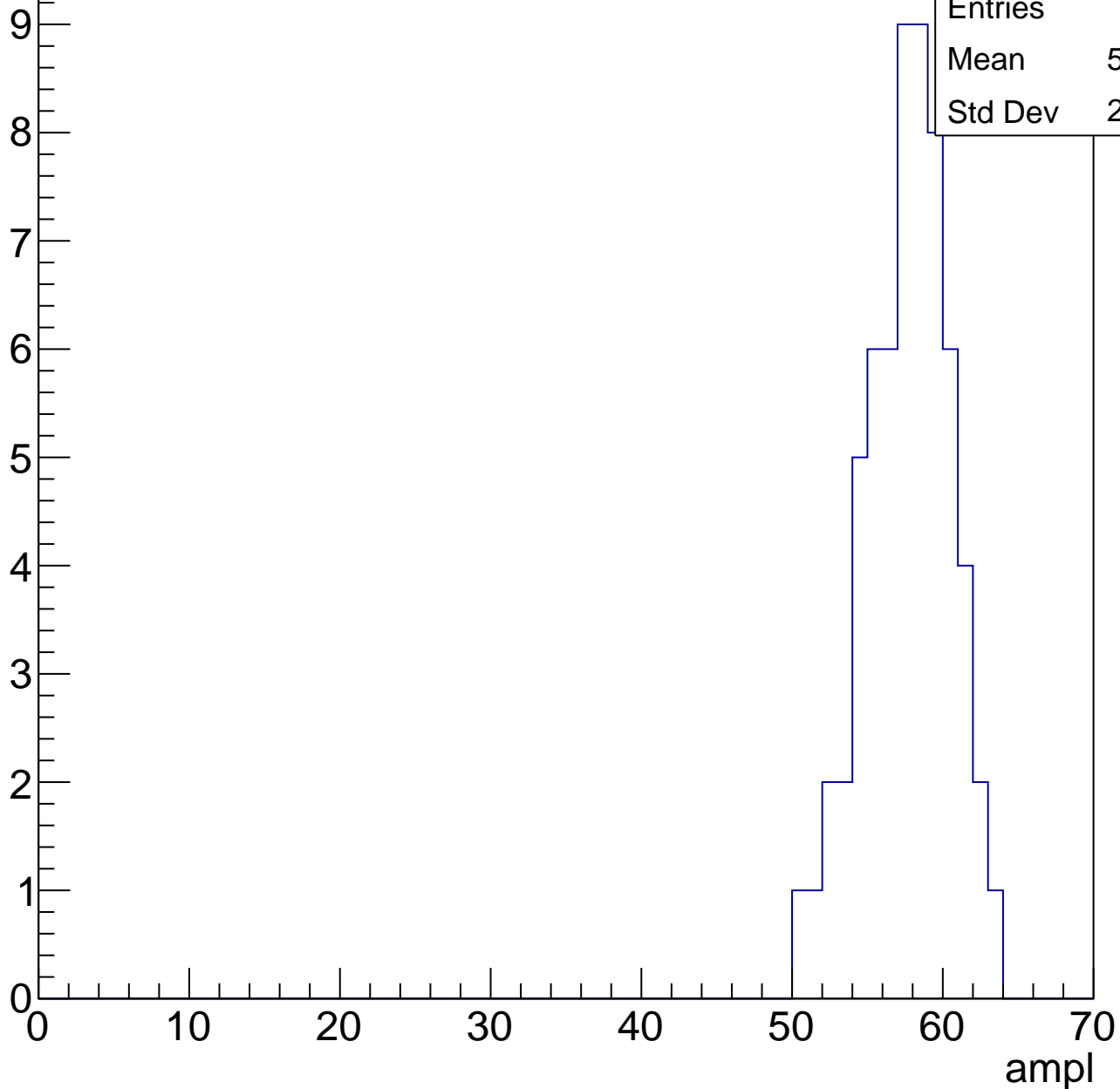
Entries	79
Mean	50.41
Std Dev	3.542



B1L102S, U10-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



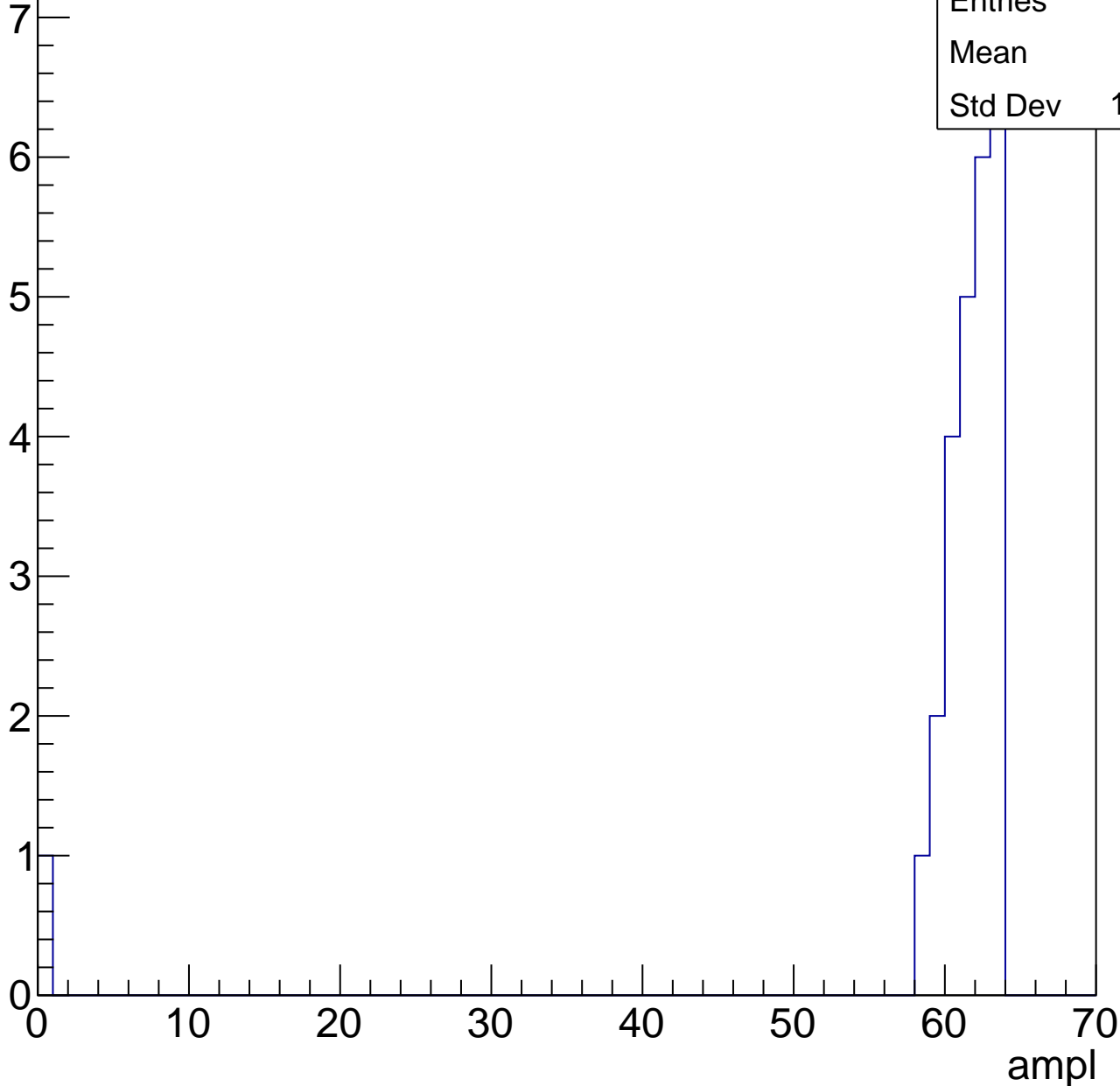
Entries	62
Mean	57.18
Std Dev	2.797

B1L102S, U10-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	59
Std Dev	11.88



B1L102S, U10-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch34, adc0

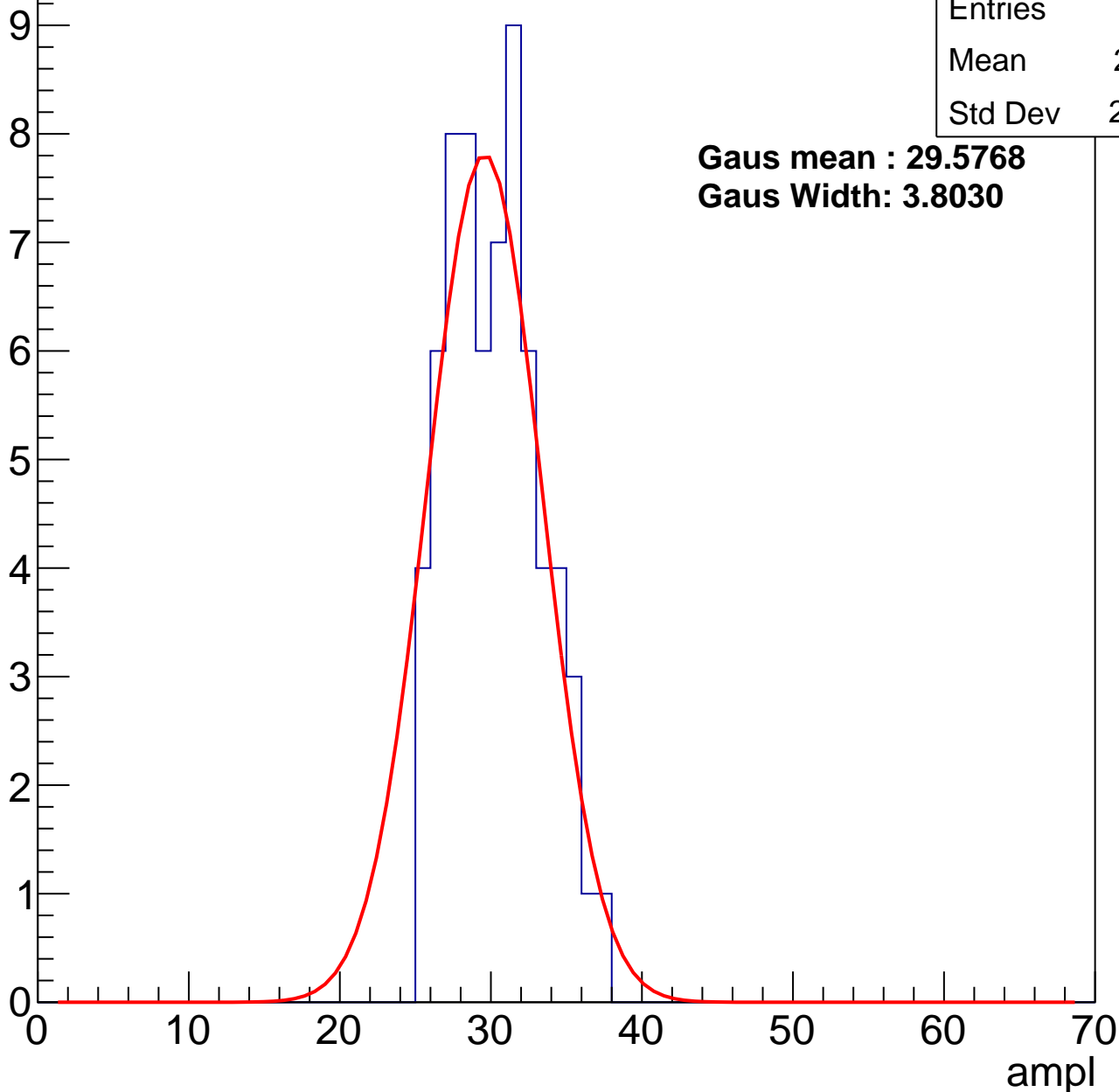
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.81
Std Dev	2.984

Gaus mean : 29.5768

Gaus Width: 3.8030



B1L102S, U10-ch34, adc1

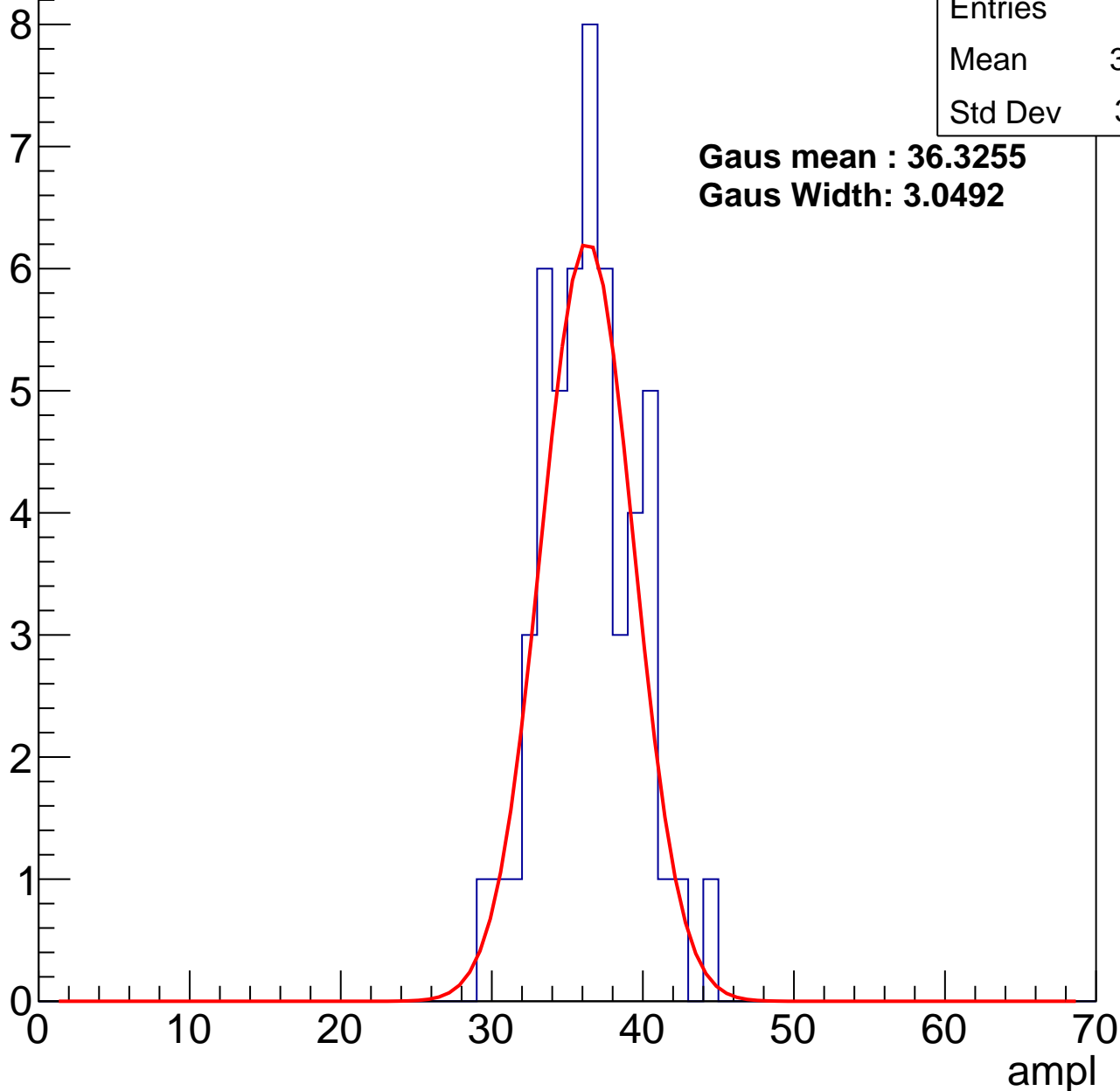
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.98
Std Dev	3.091

Gaus mean : 36.3255

Gaus Width: 3.0492



B1L102S, U10-ch34, adc2

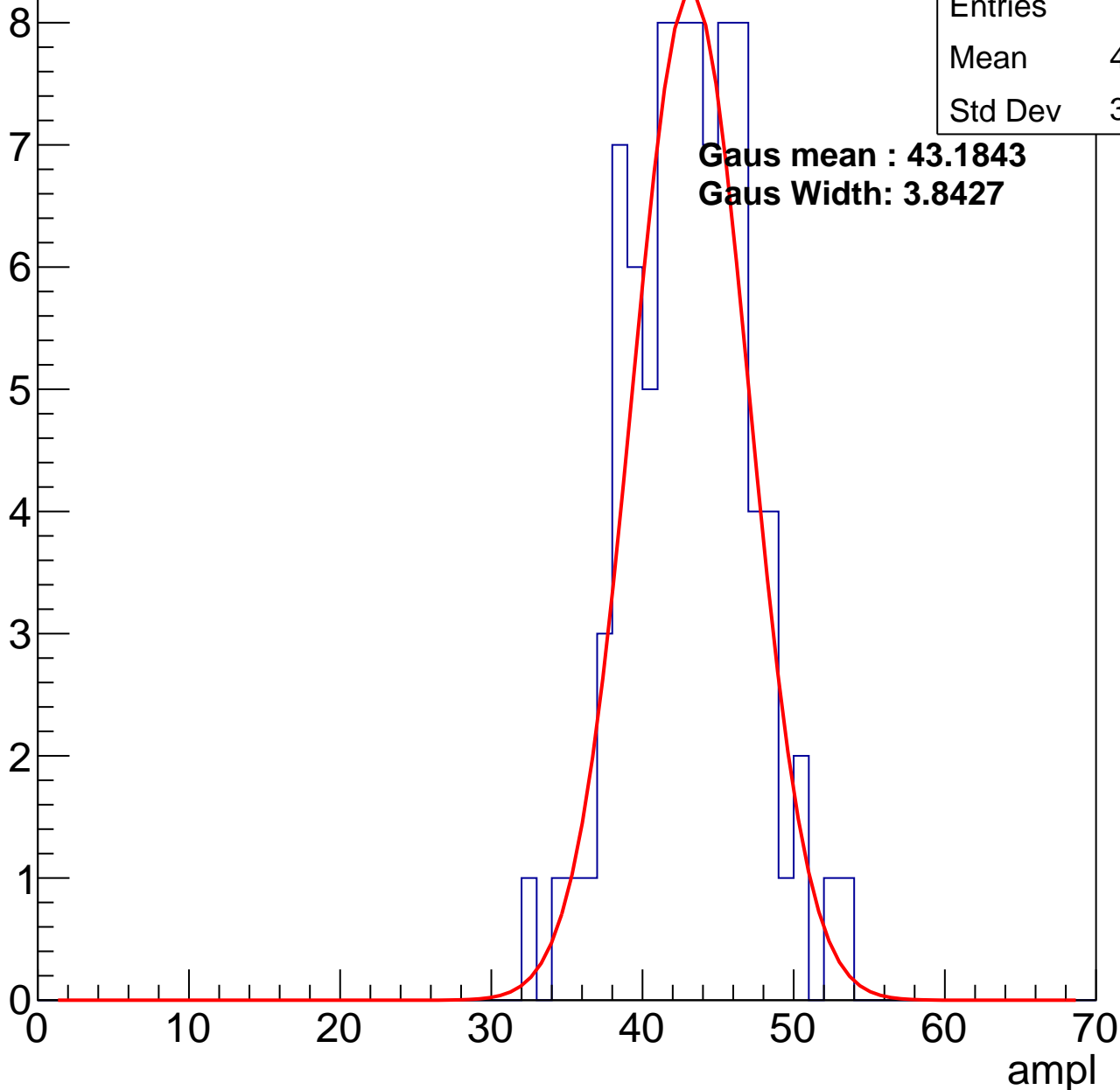
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	42.66
Std Dev	3.999

Gaus mean : 43.1843

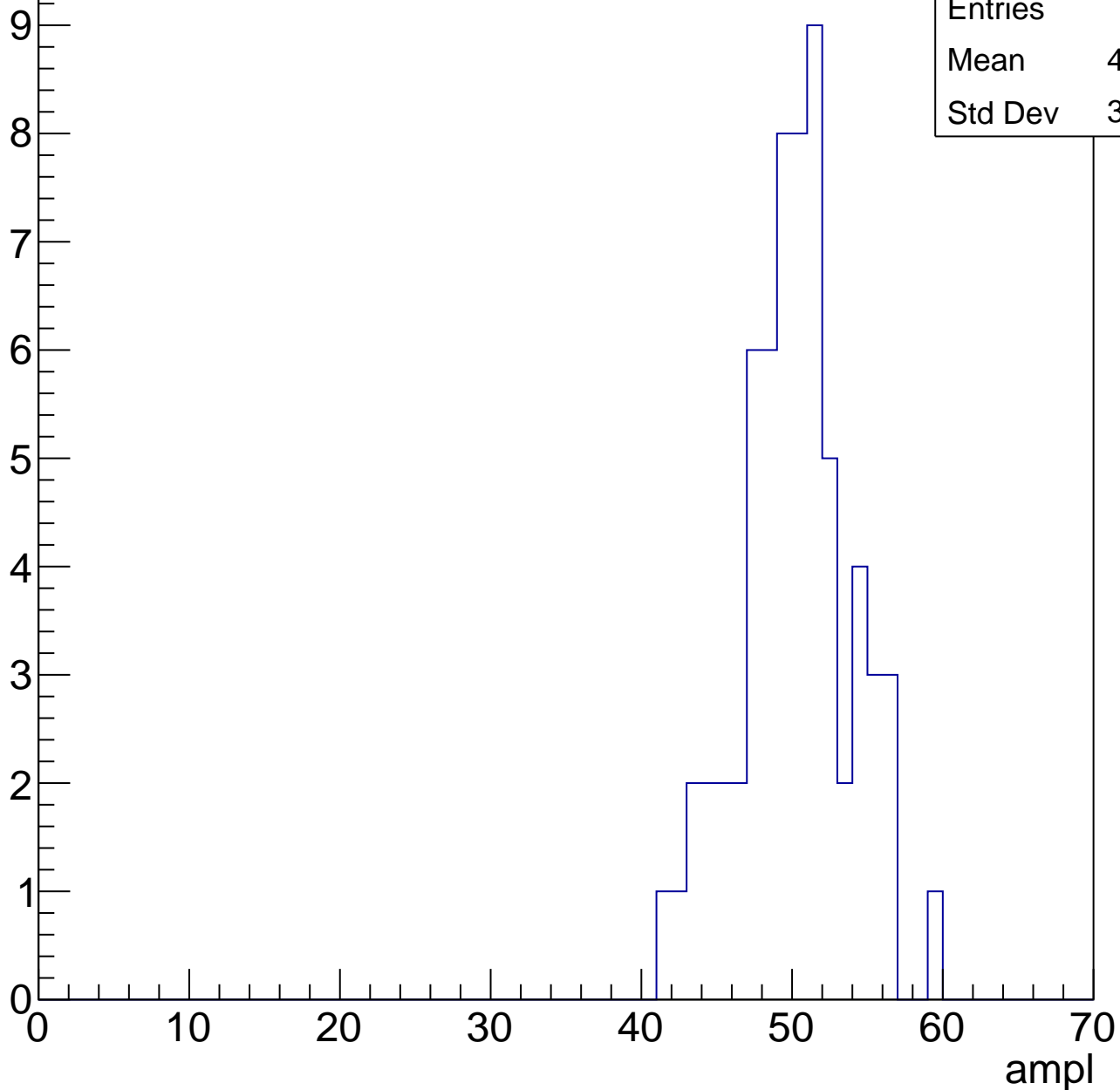
Gaus Width: 3.8427



B1L102S, U10-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

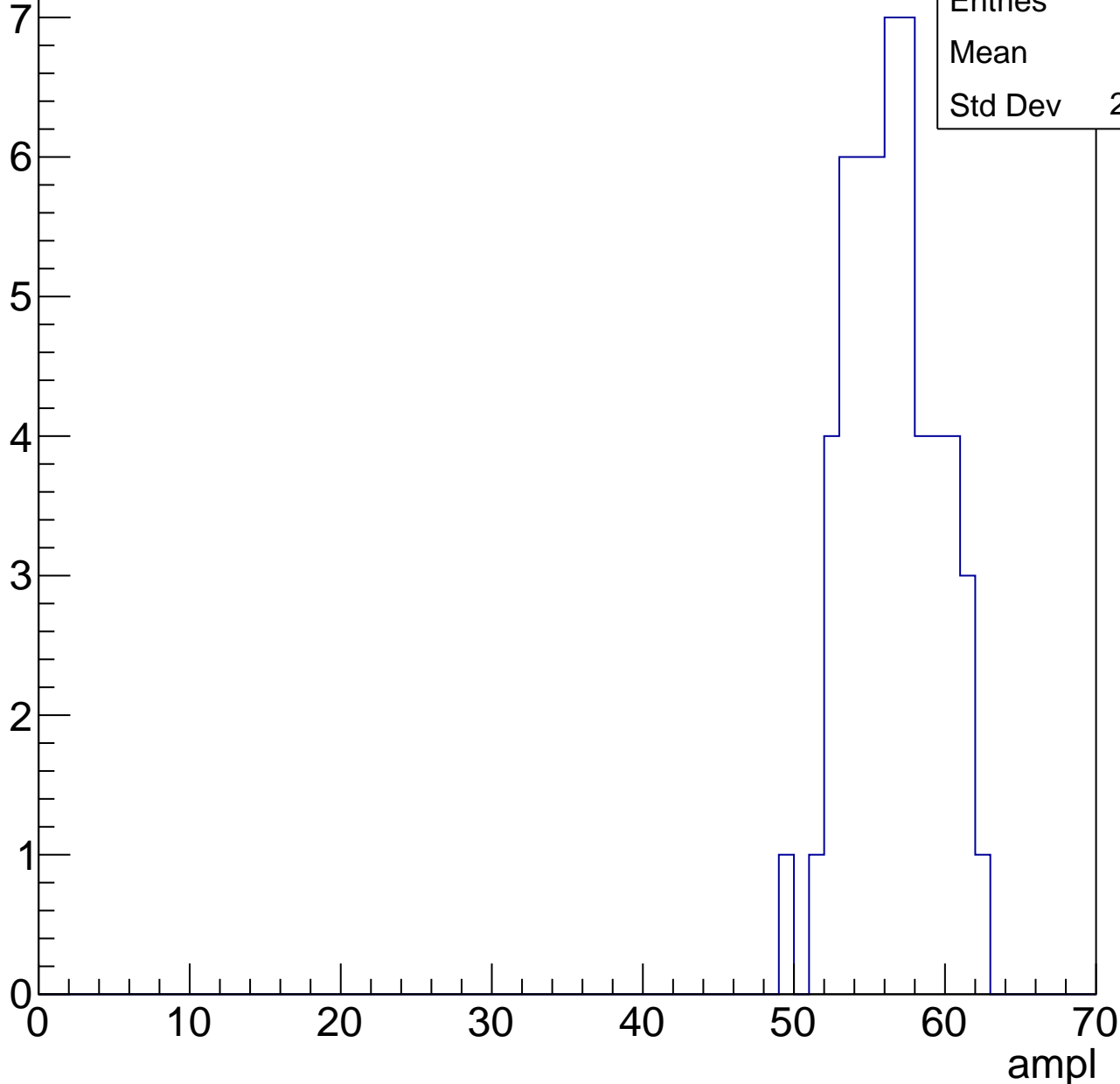


B1L102S, U10-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56
Std Dev	2.906

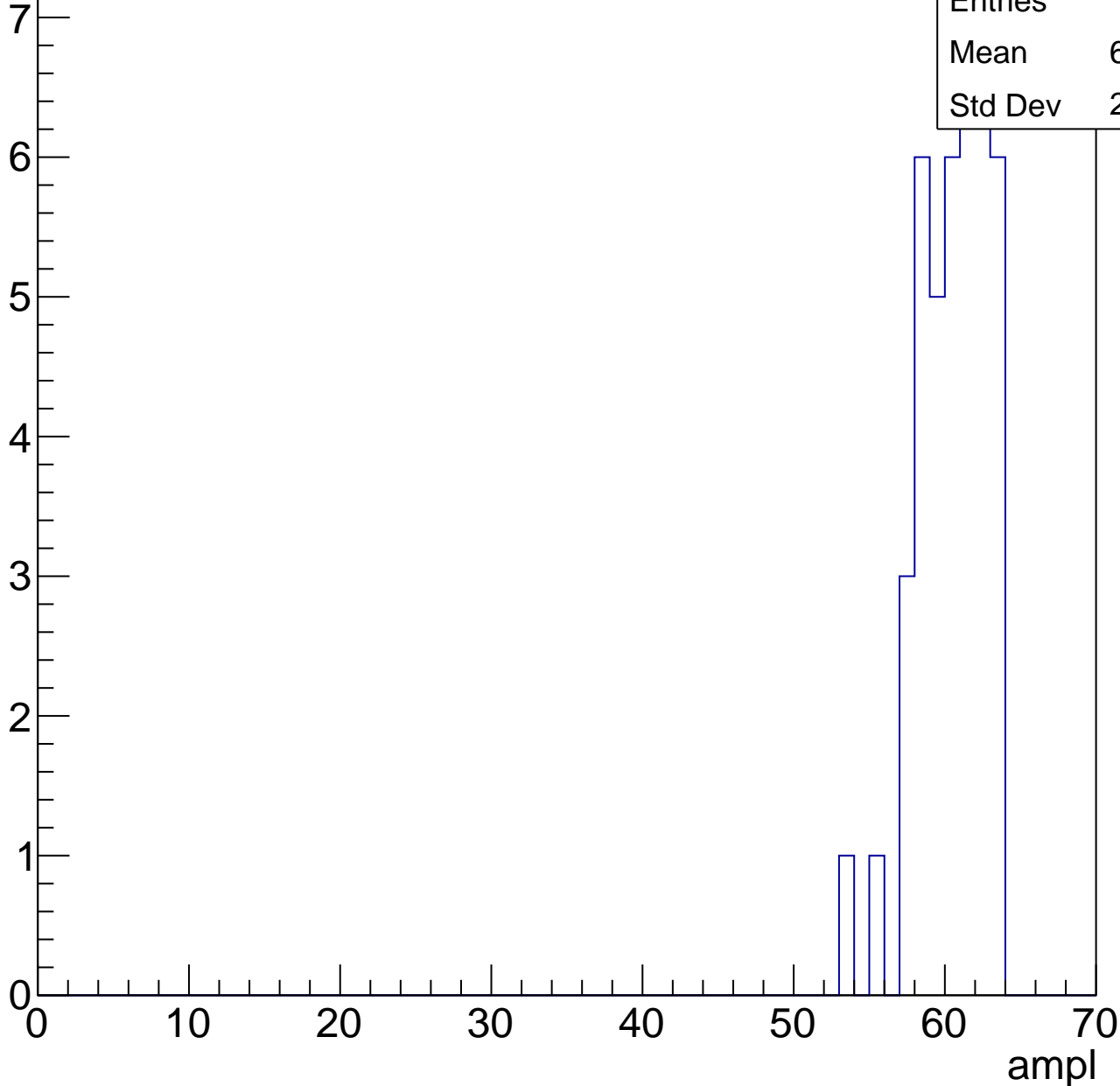


B1L102S, U10-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

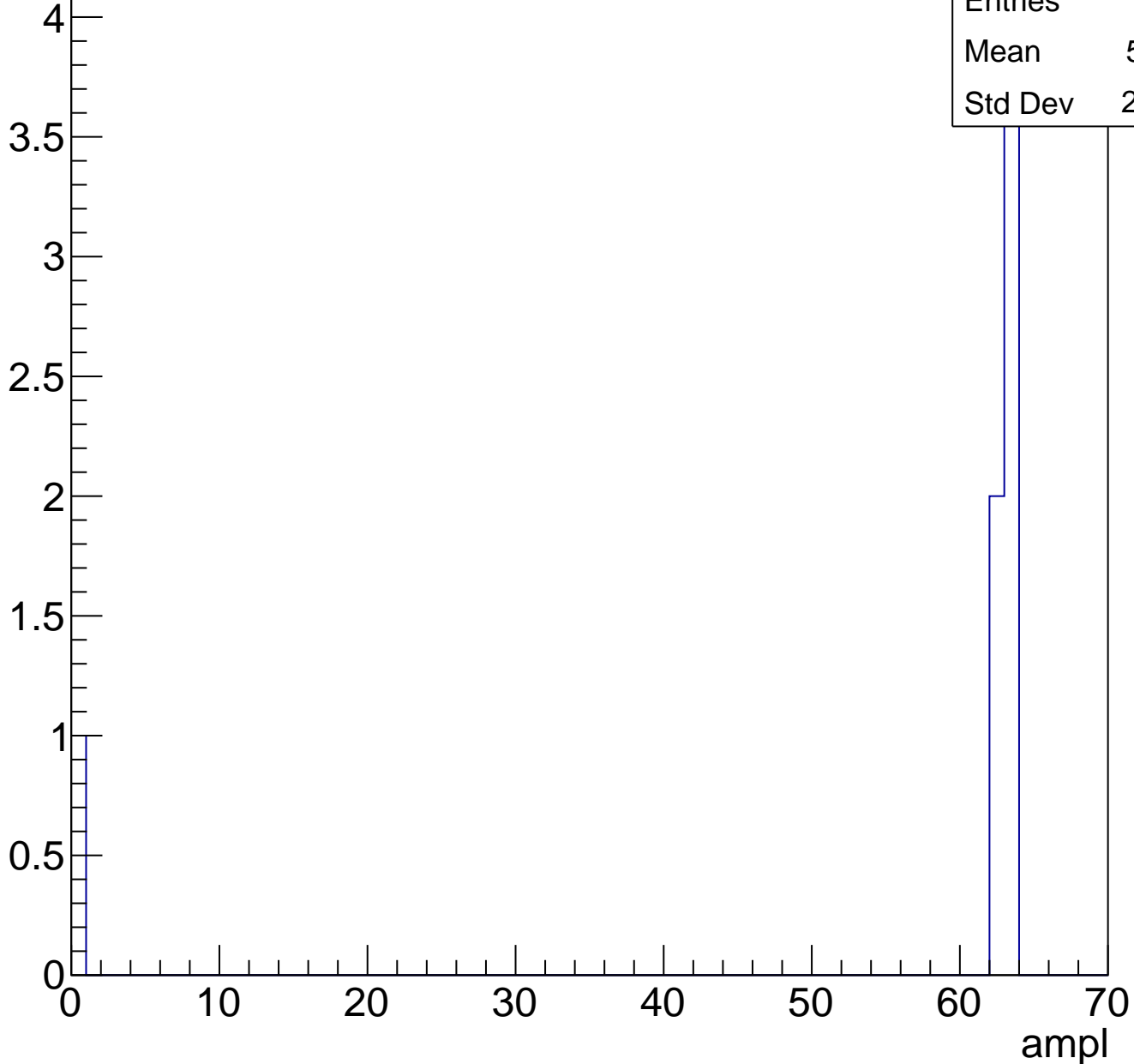
Entries	42
Mean	60.02
Std Dev	2.283



B1L102S, U10-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch35, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	27.12
Std Dev	5.733

Gaus mean : 28.8670

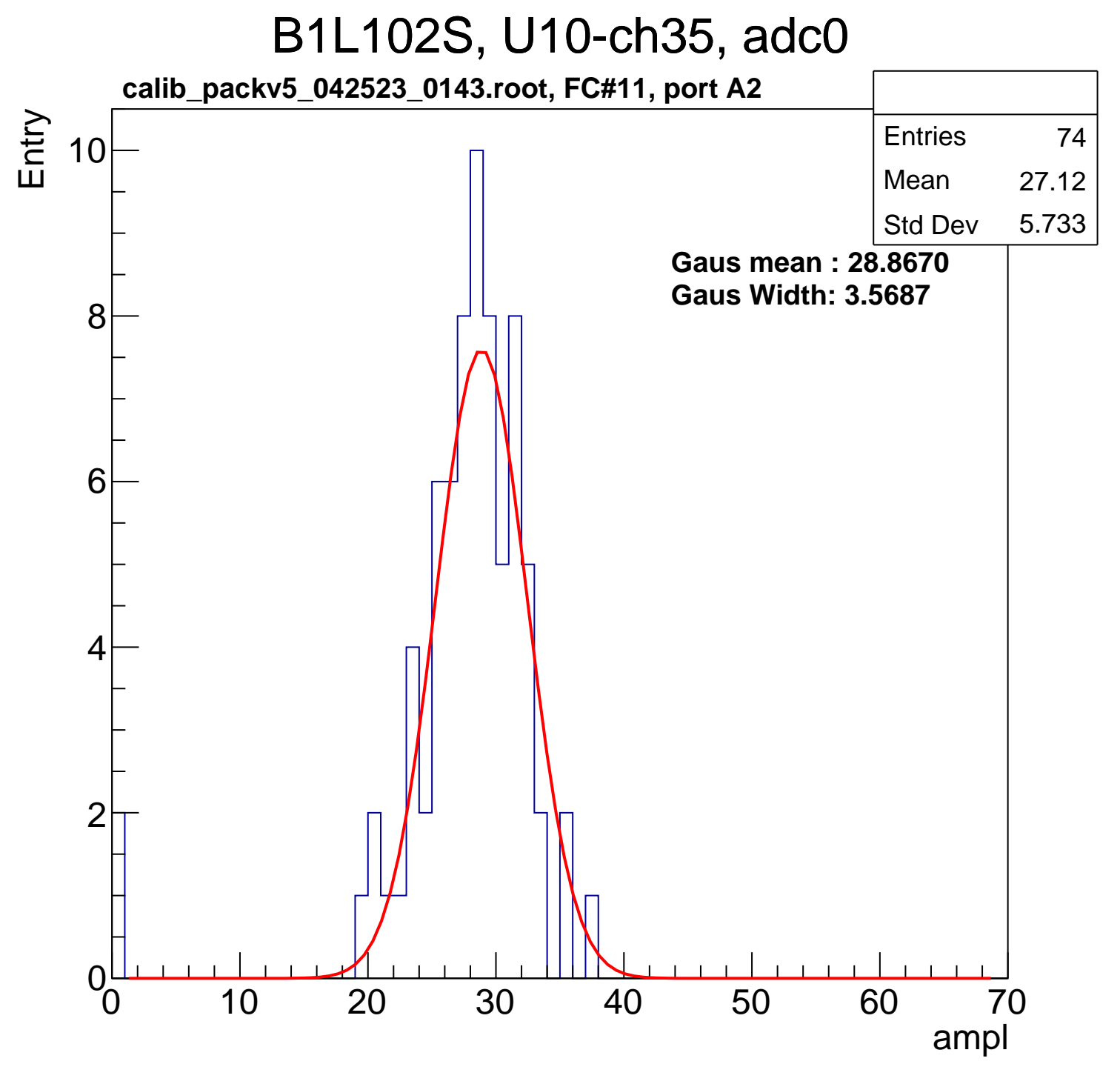
Gaus Width: 3.5687

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch35, adc1

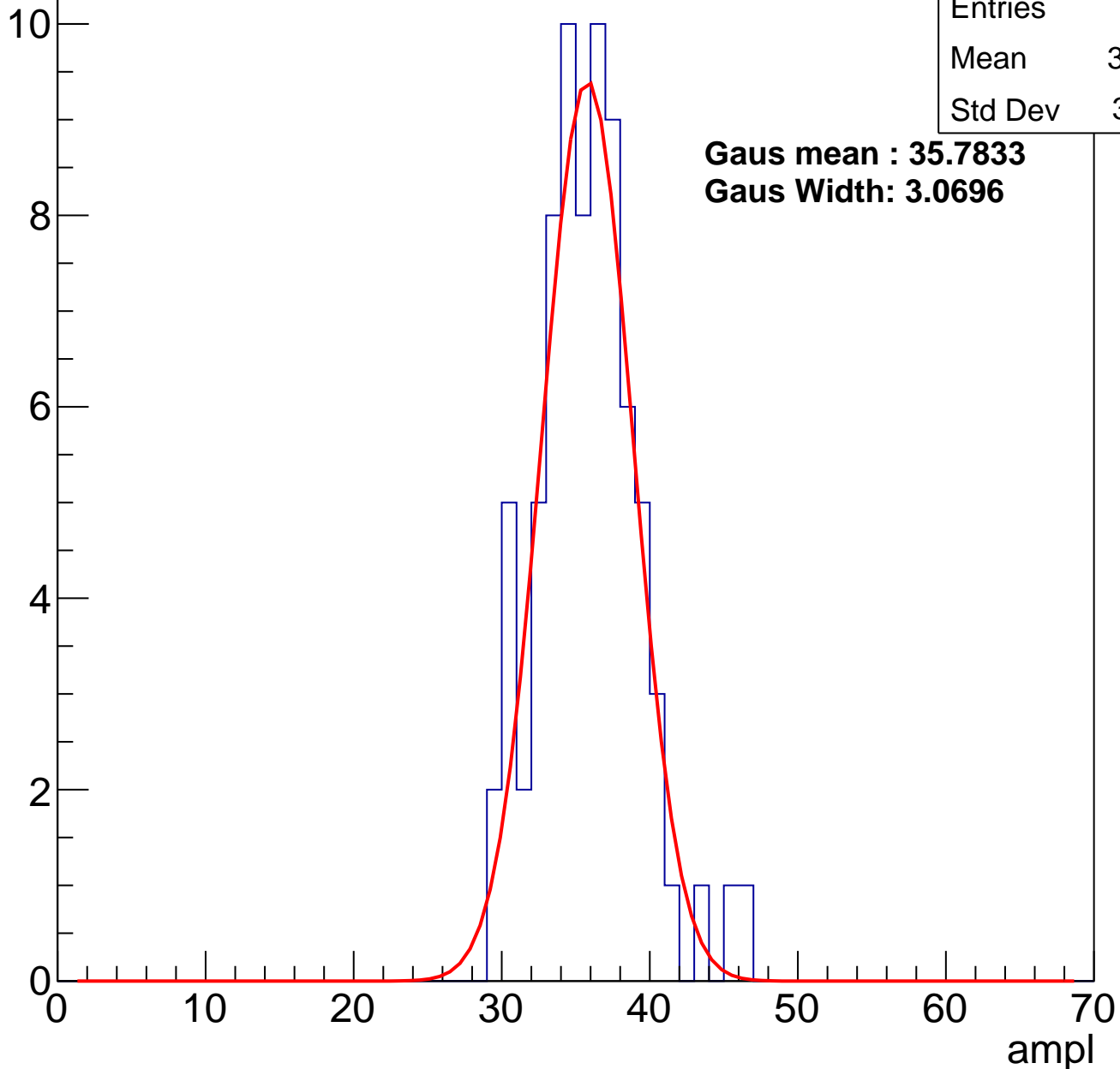
calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.39
Std Dev	3.381

Gaus mean : 35.7833

Gaus Width: 3.0696

Entry



B1L102S, U10-ch35, adc2

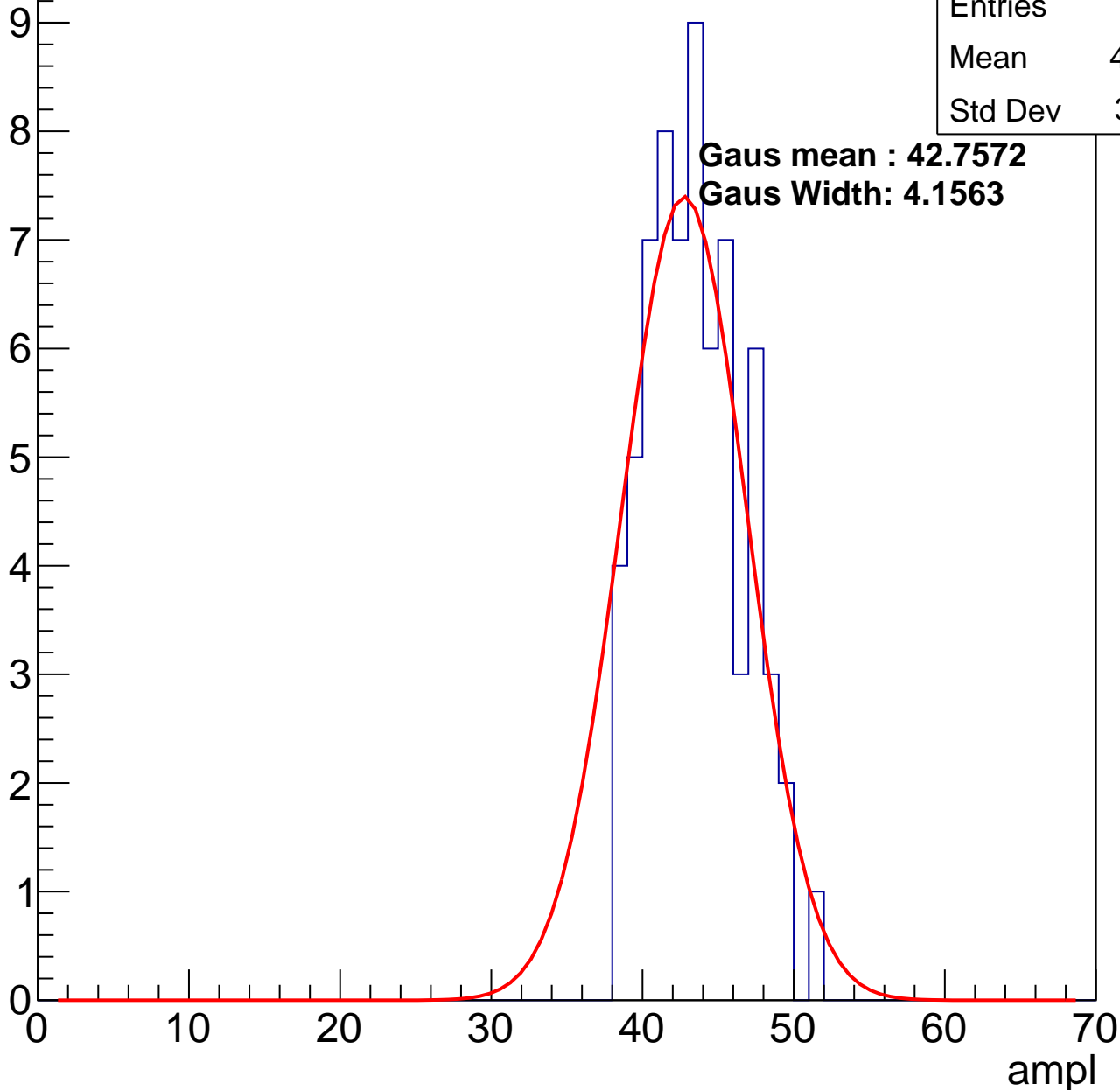
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.06
Std Dev	3.091

Gaus mean : 42.7572

Gaus Width: 4.1563

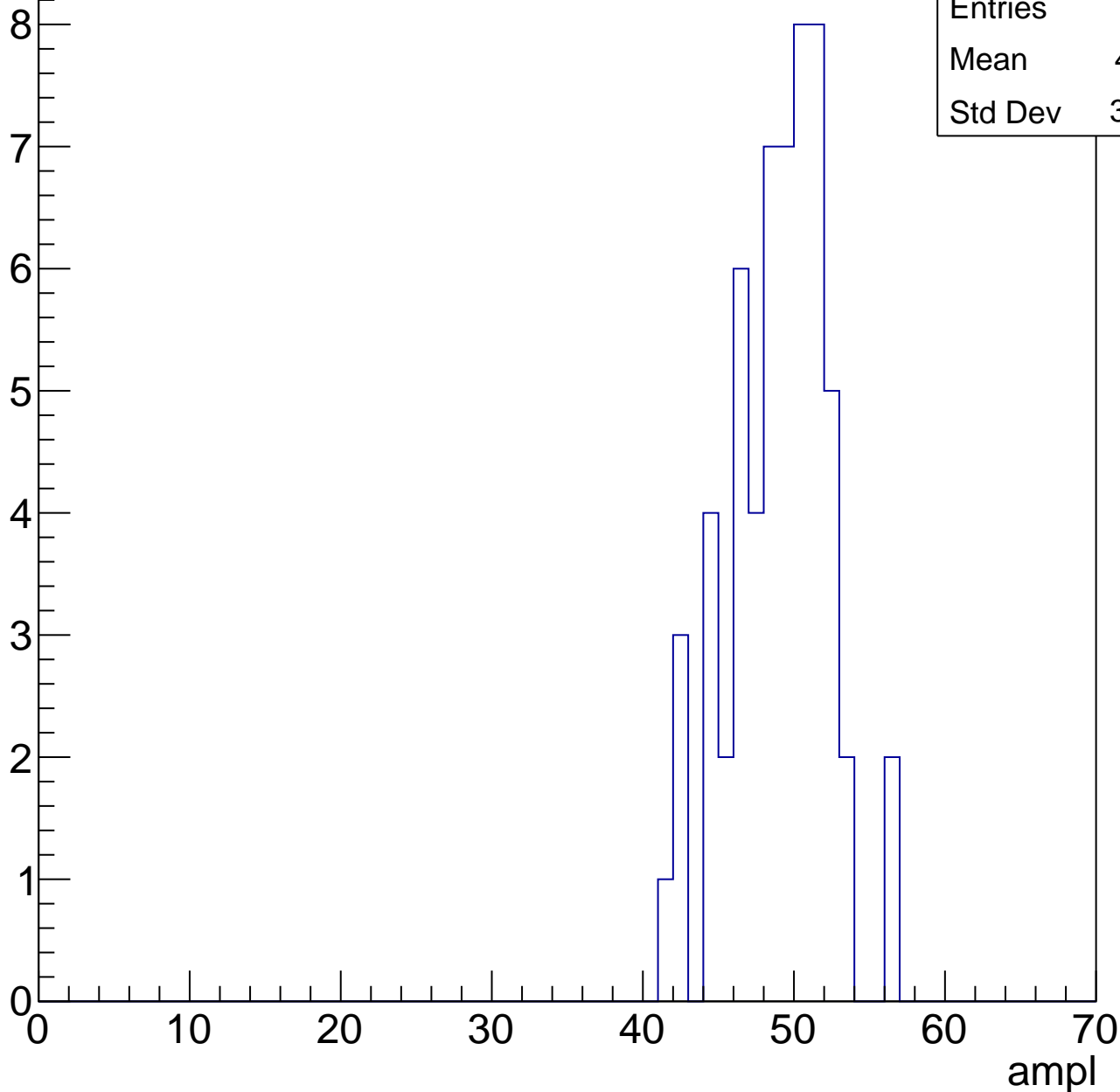


B1L102S, U10-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.51
Std Dev	3.244



B1L102S, U10-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

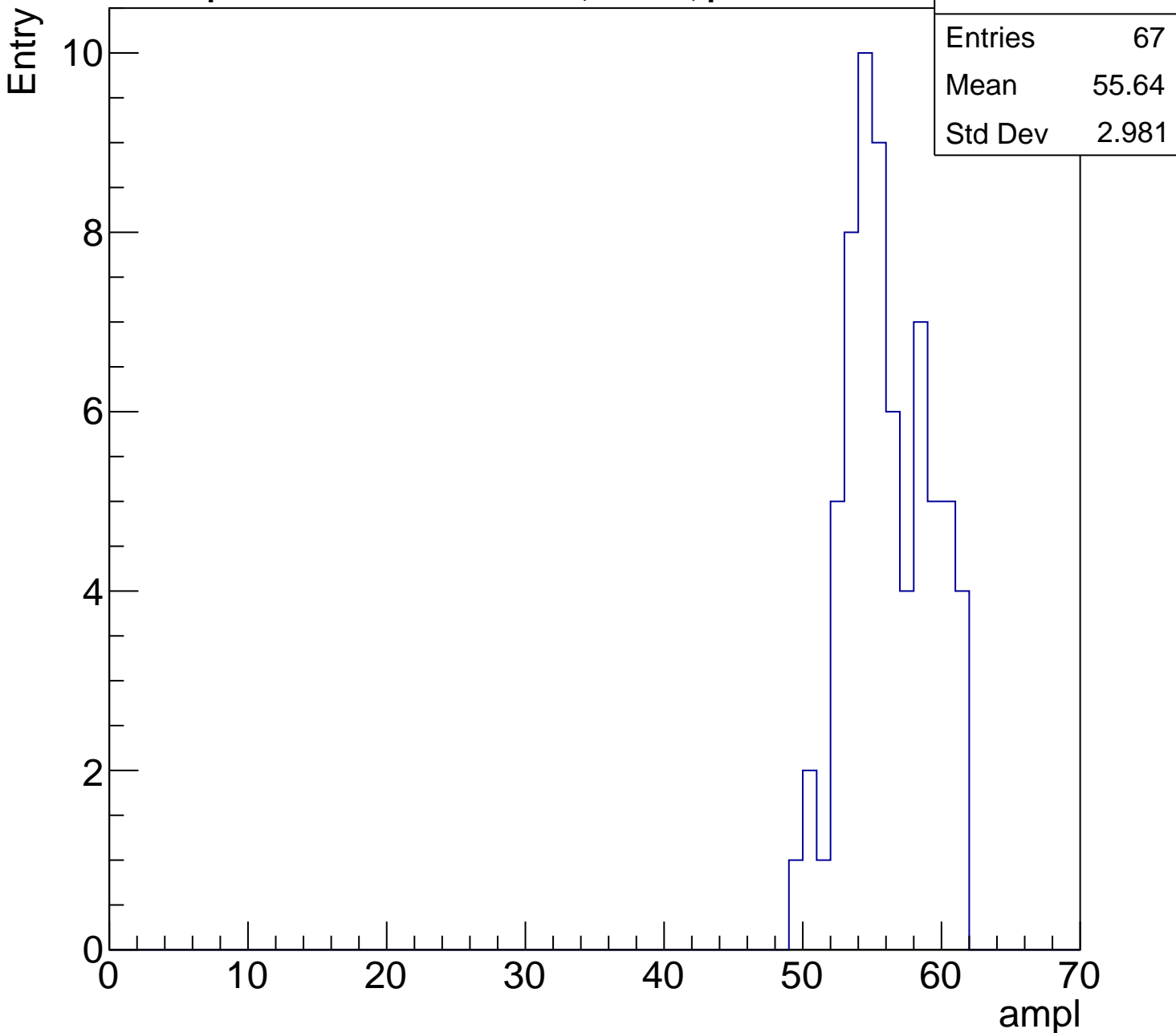
Entries	67
Mean	55.64
Std Dev	2.981

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch35, adc5

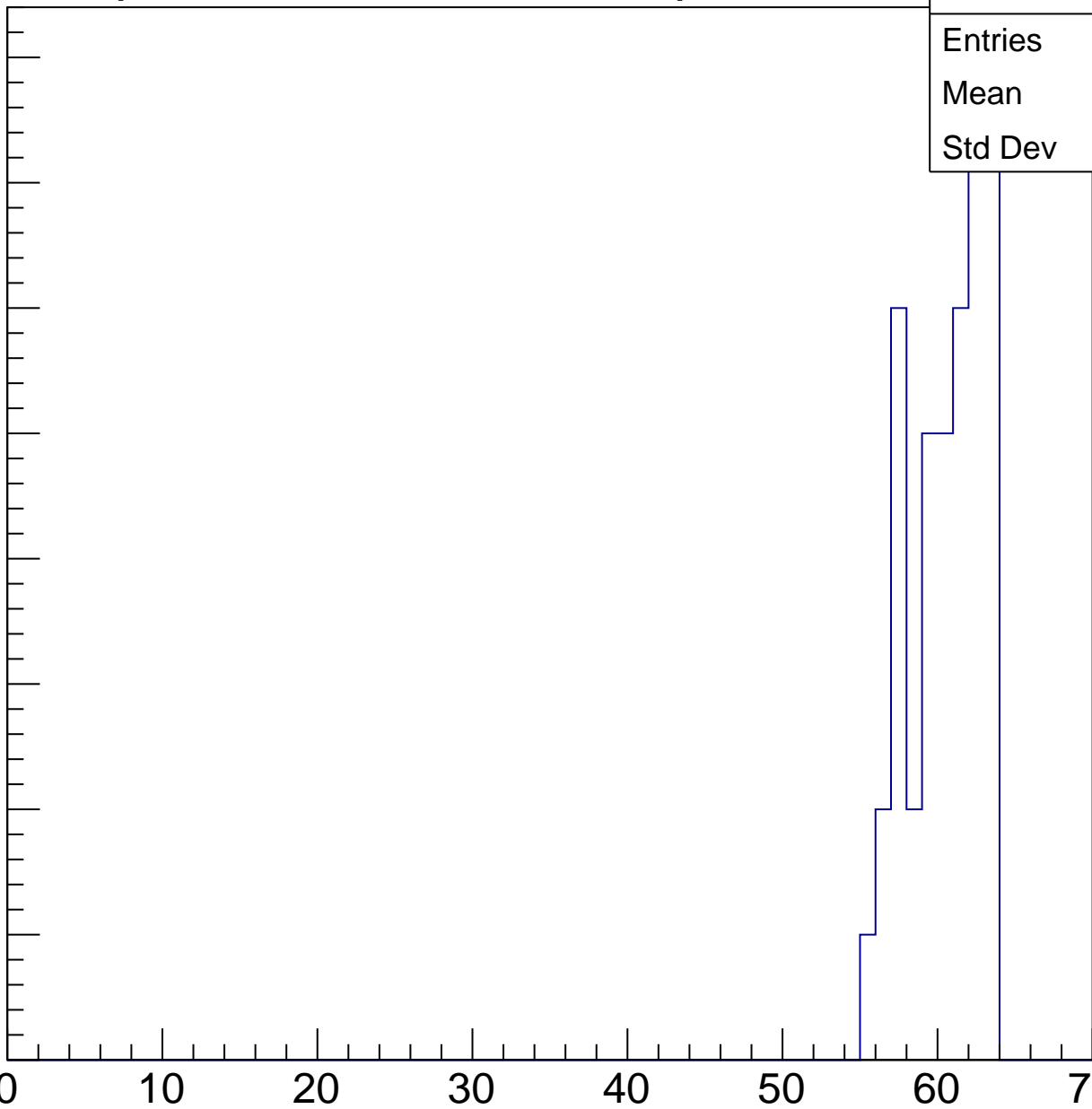
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.14
Std Dev	2.329

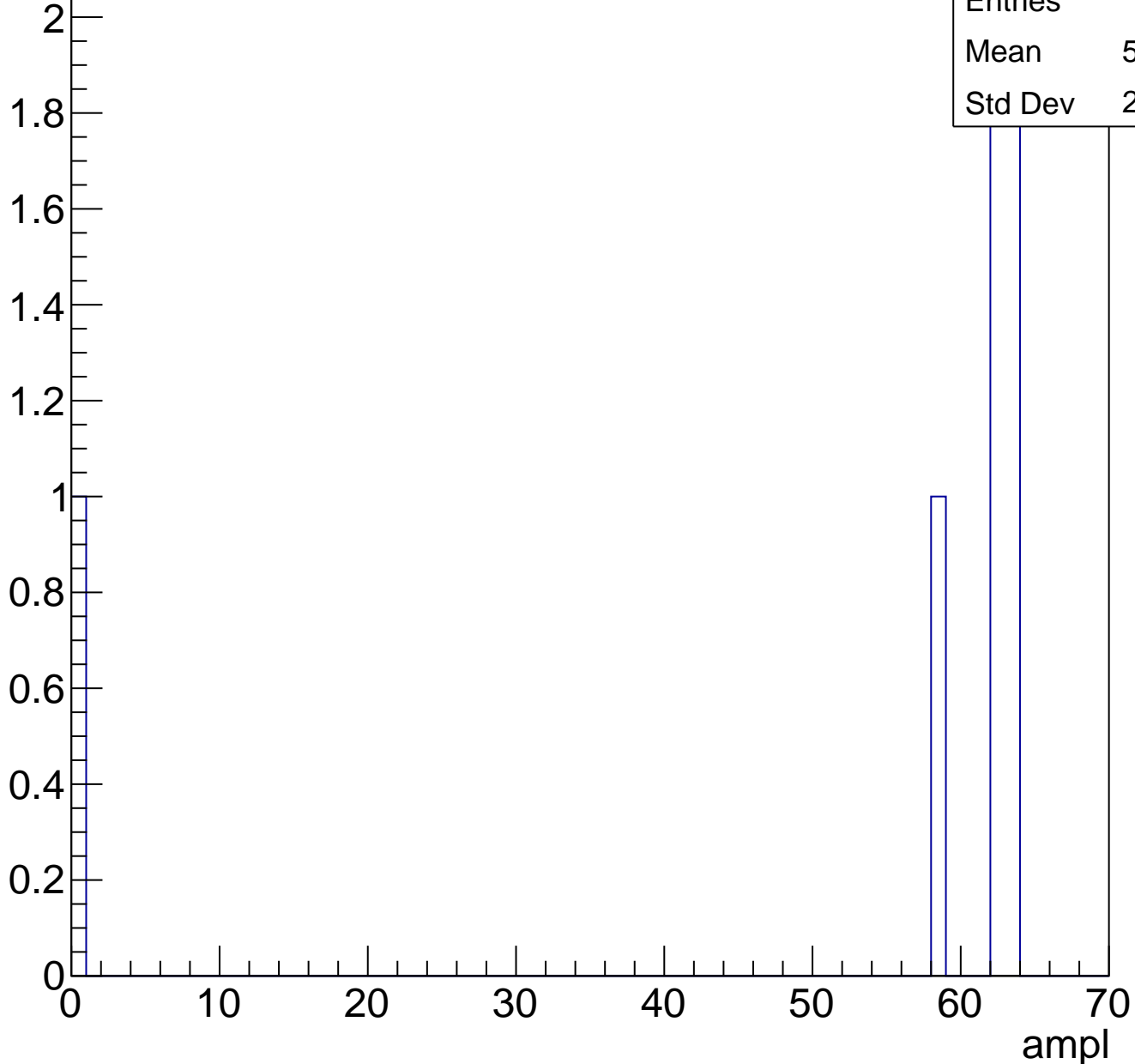
ampl



B1L102S, U10-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	51.33
Std Dev	23.02

B1L102S, U10-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch36, adc0

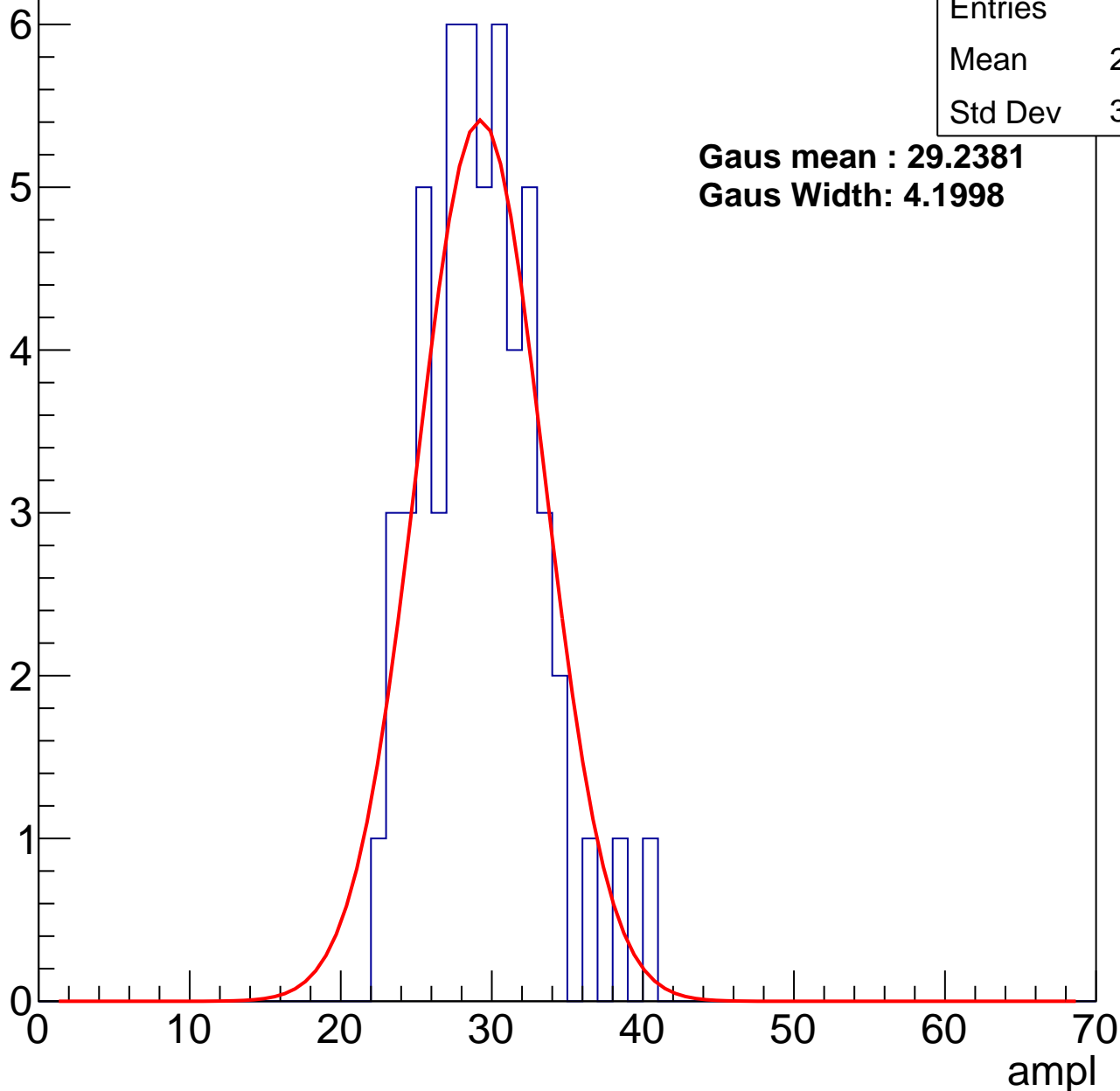
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.84
Std Dev	3.774

Gaus mean : 29.2381

Gaus Width: 4.1998



B1L102S, U10-ch36, adc1

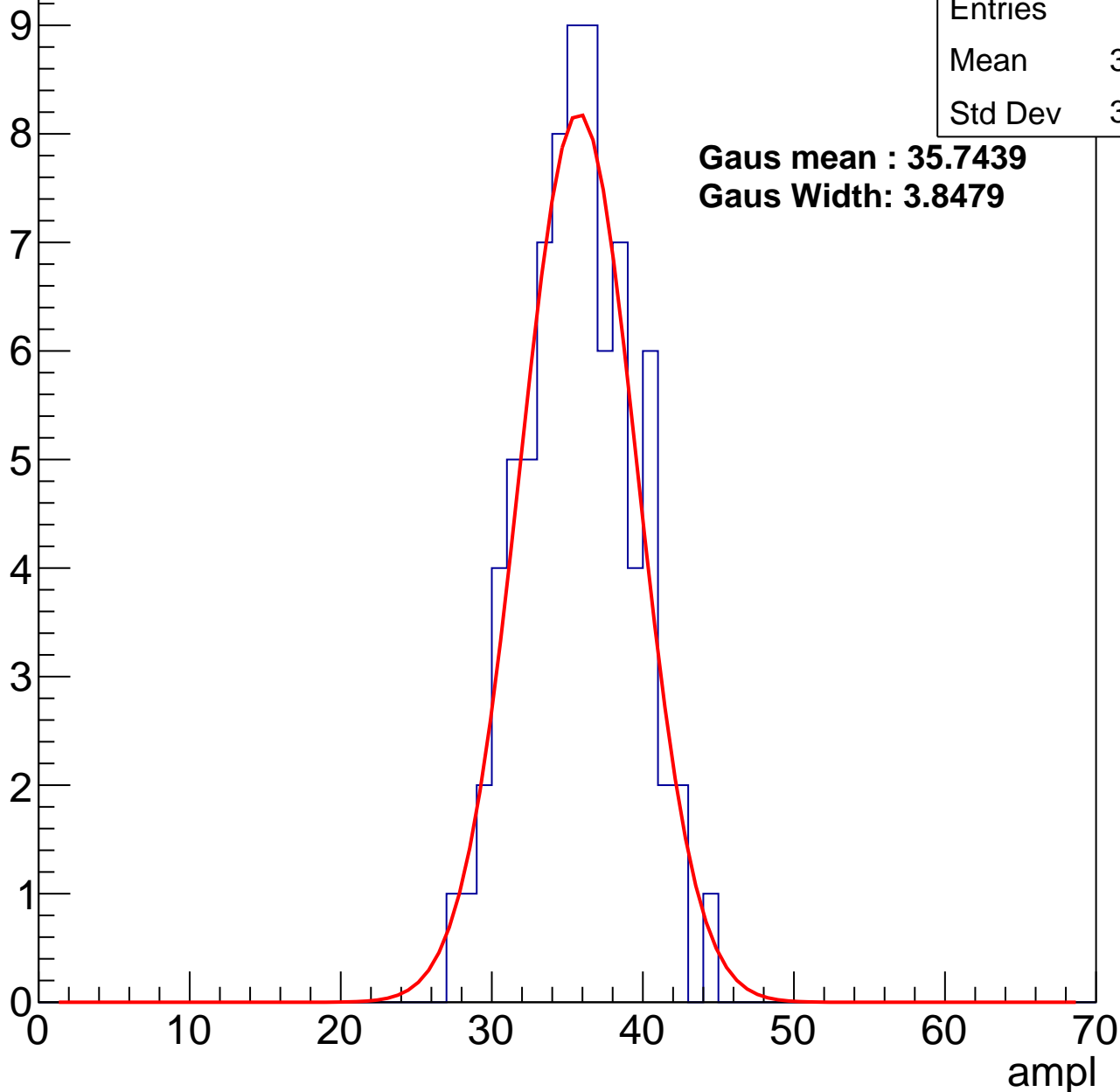
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.24
Std Dev	3.562

Gaus mean : 35.7439

Gaus Width: 3.8479



B1L102S, U10-ch36, adc2

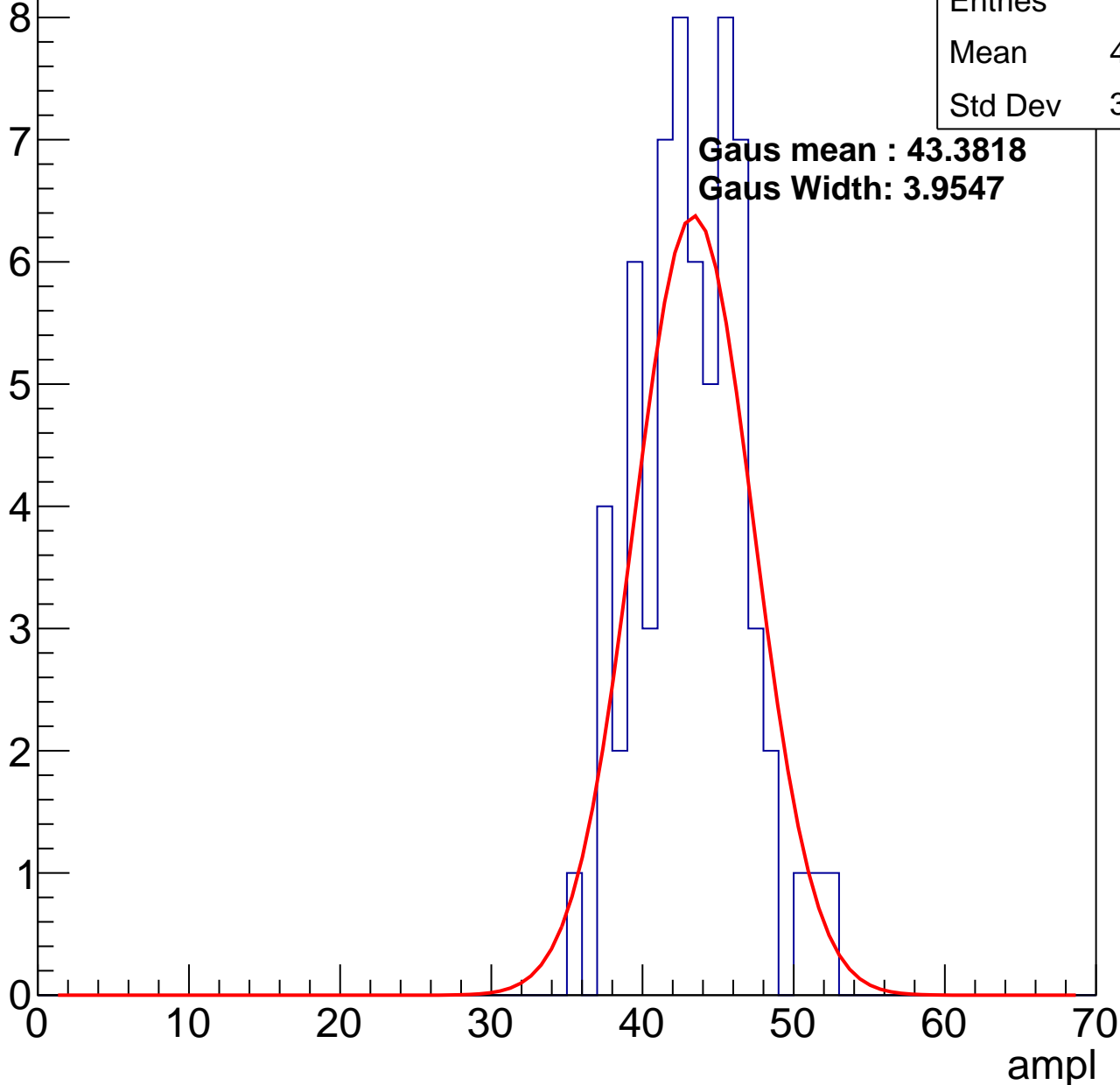
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.86
Std Dev	3.525

Gaus mean : 43.3818

Gaus Width: 3.9547

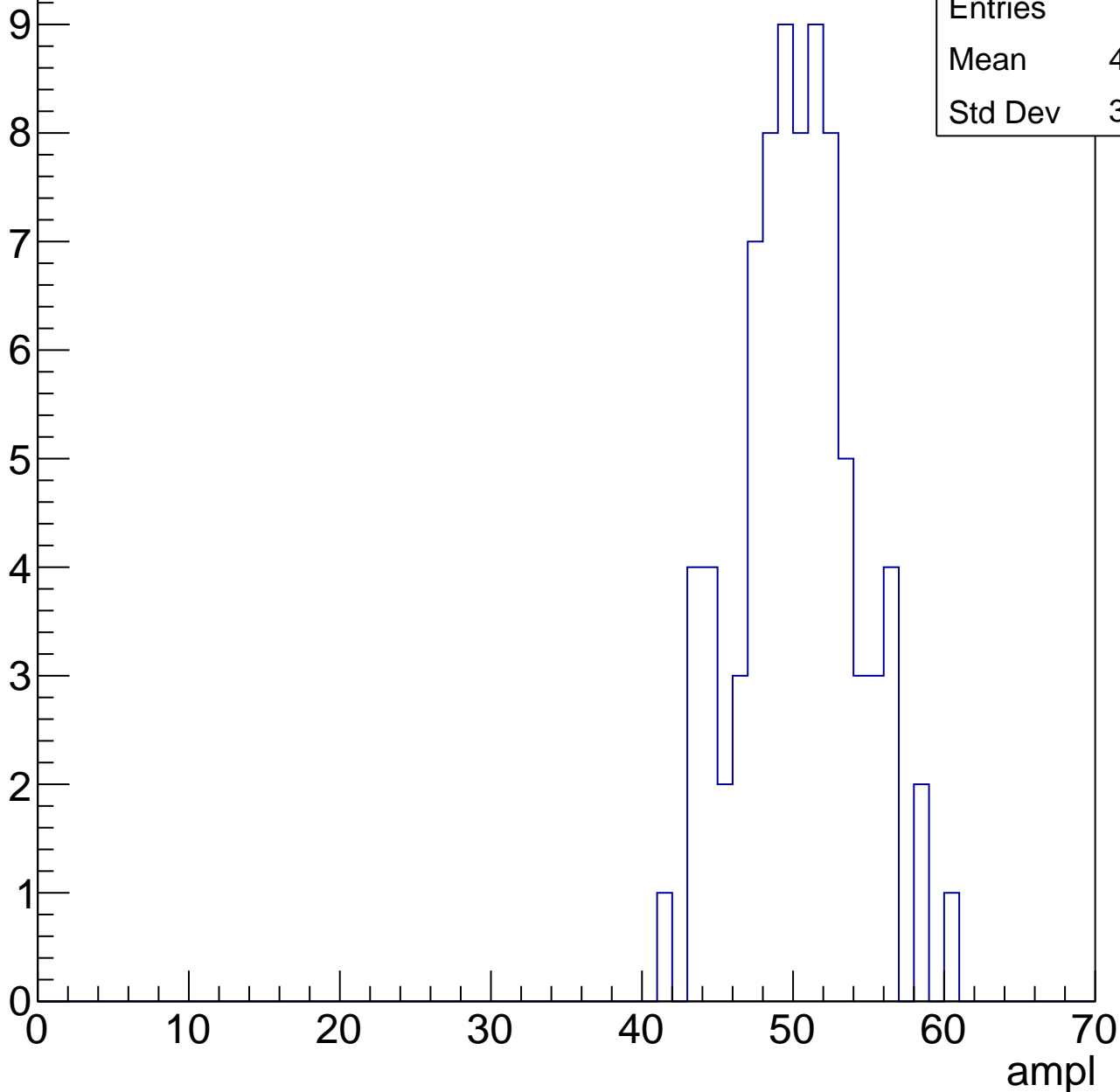


B1L102S, U10-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	49.85
Std Dev	3.853

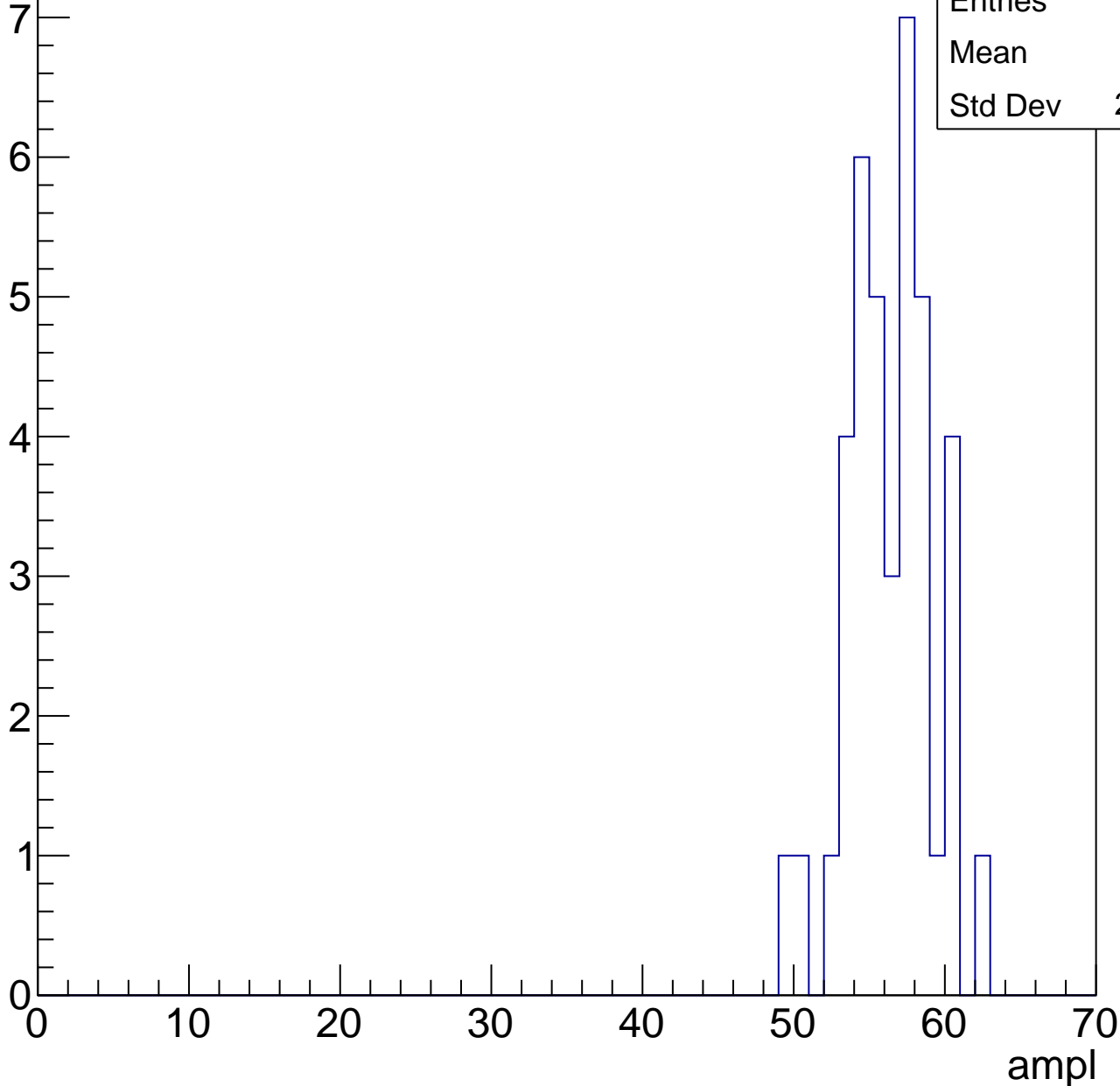


B1L102S, U10-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	55.9
Std Dev	2.781

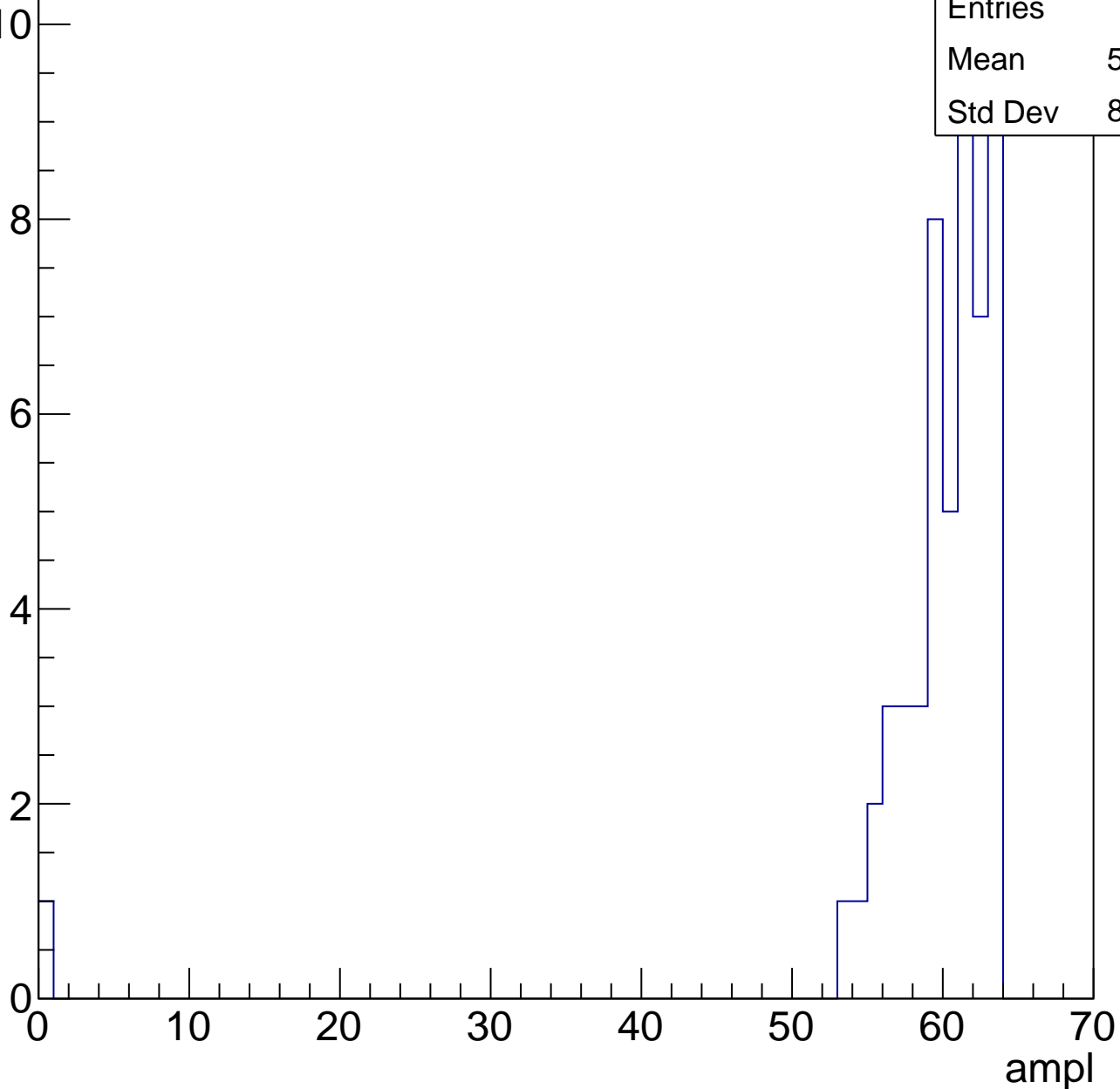


B1L102S, U10-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.74
Std Dev	8.539



B1L102S, U10-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch37, adc0

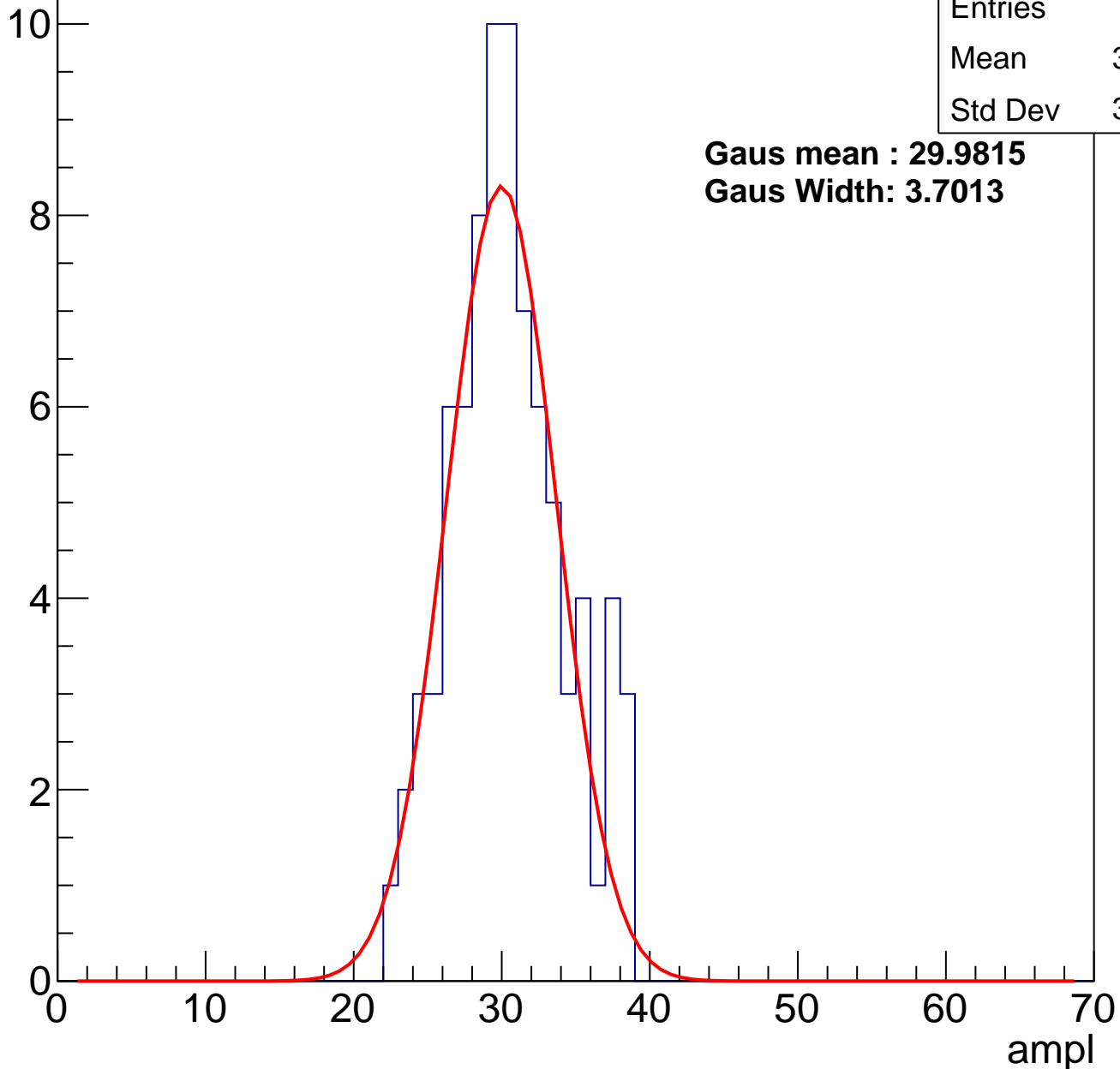
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	30.01
Std Dev	3.801

Gaus mean : 29.9815

Gaus Width: 3.7013

Entry



B1L102S, U10-ch37, adc1

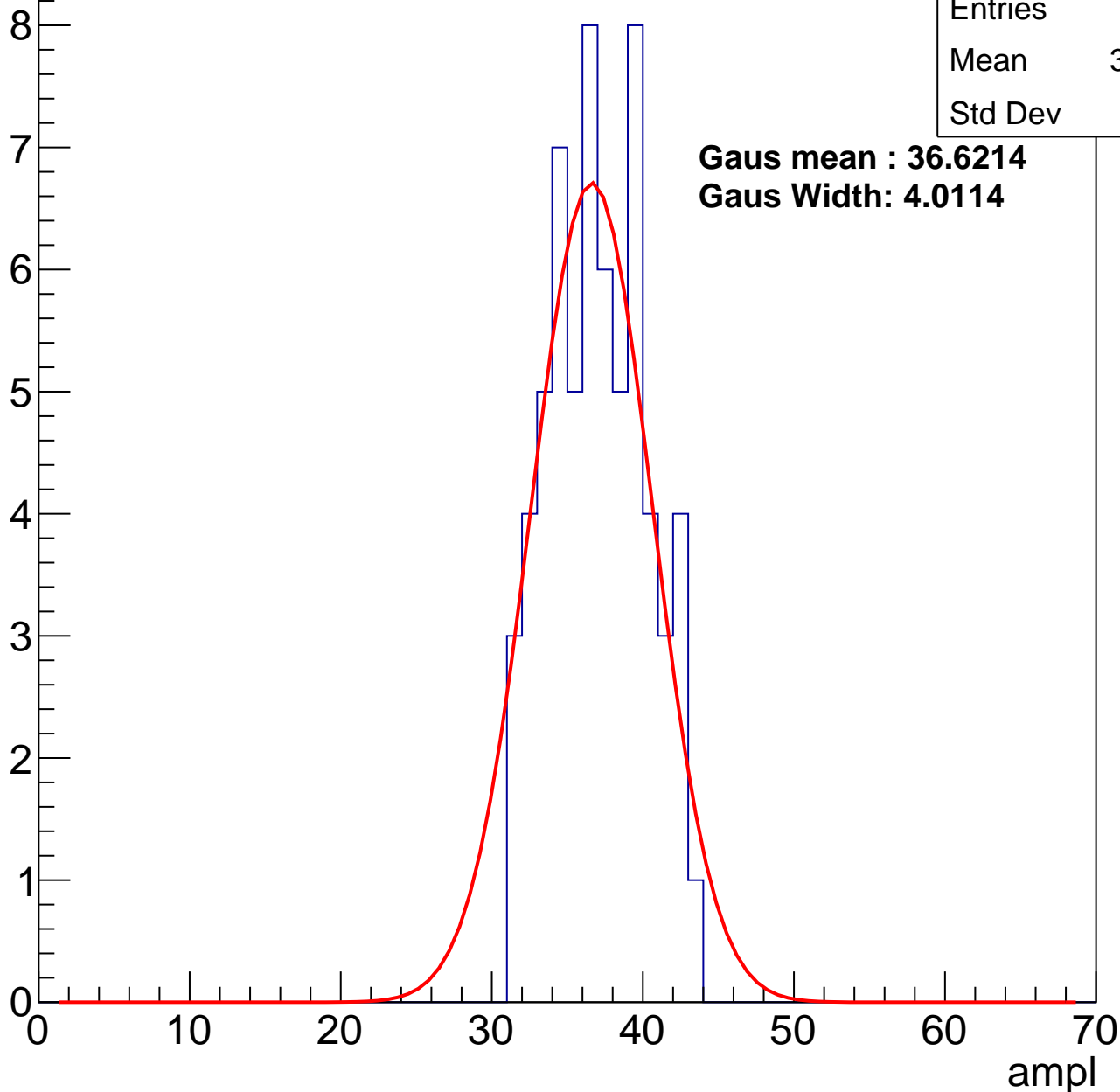
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.59
Std Dev	3.15

Gaus mean : 36.6214

Gaus Width: 4.0114



B1L102S, U10-ch37, adc2

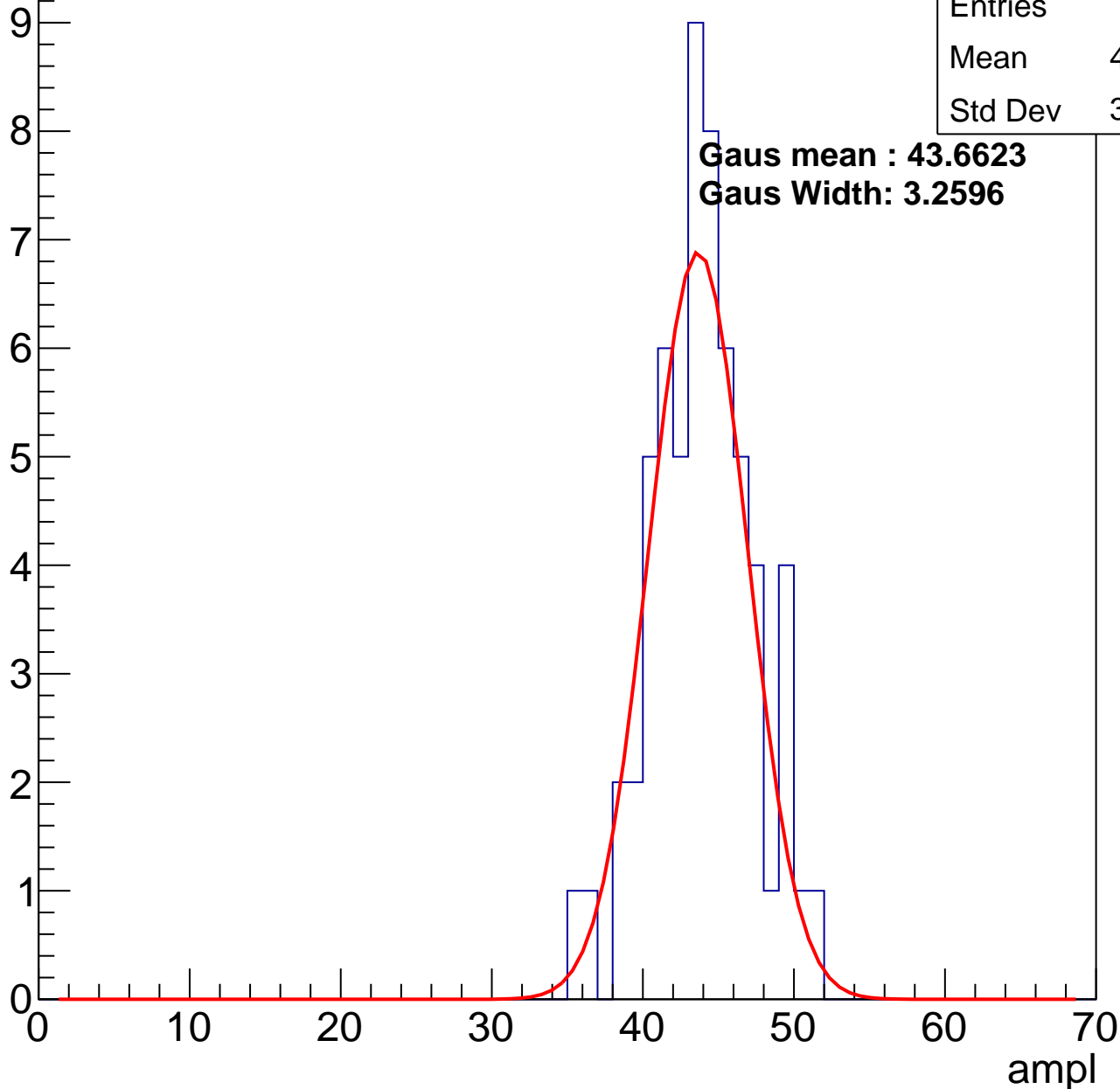
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.49
Std Dev	3.332

Gaus mean : 43.6623

Gaus Width: 3.2596

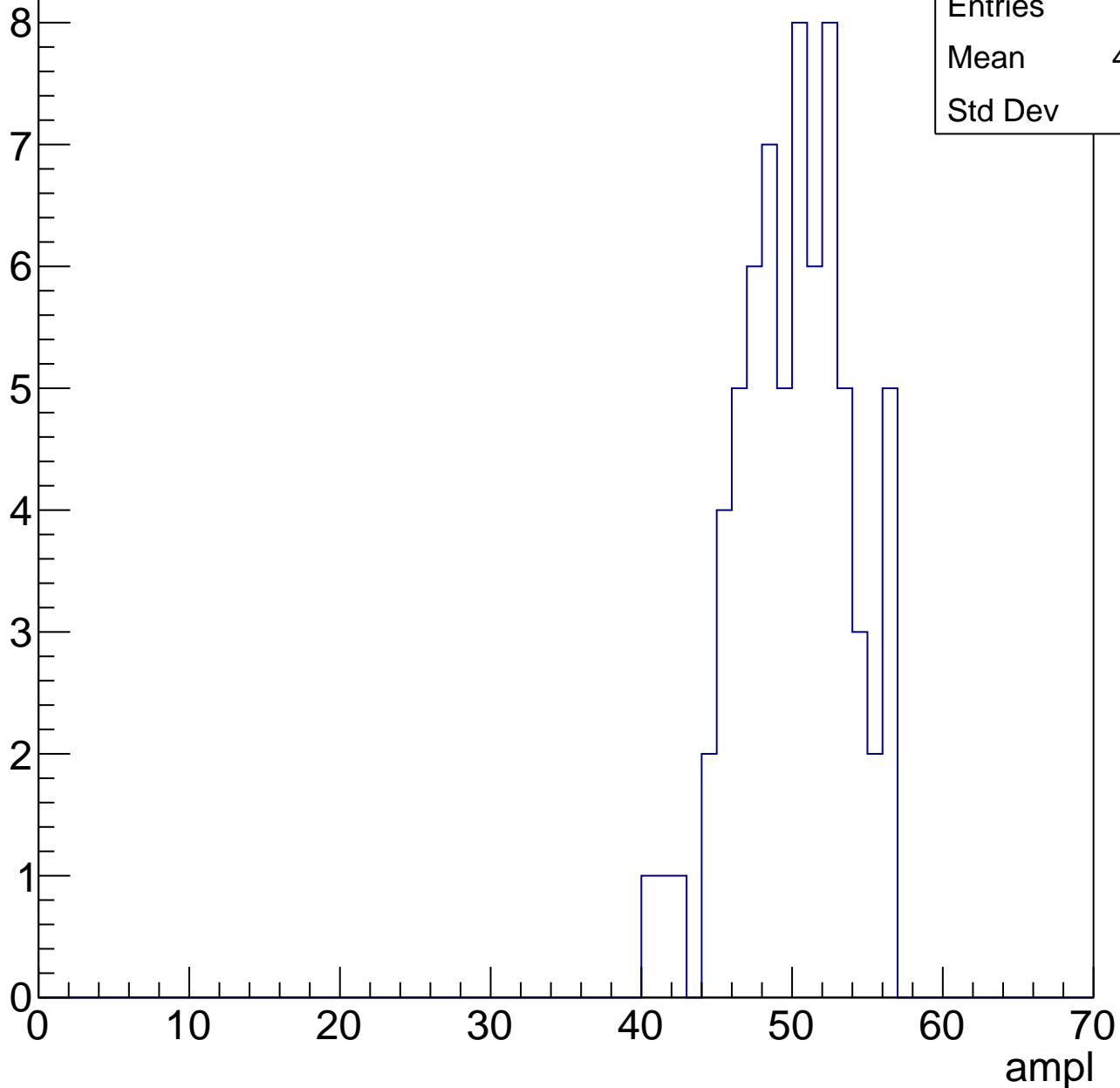


B1L102S, U10-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

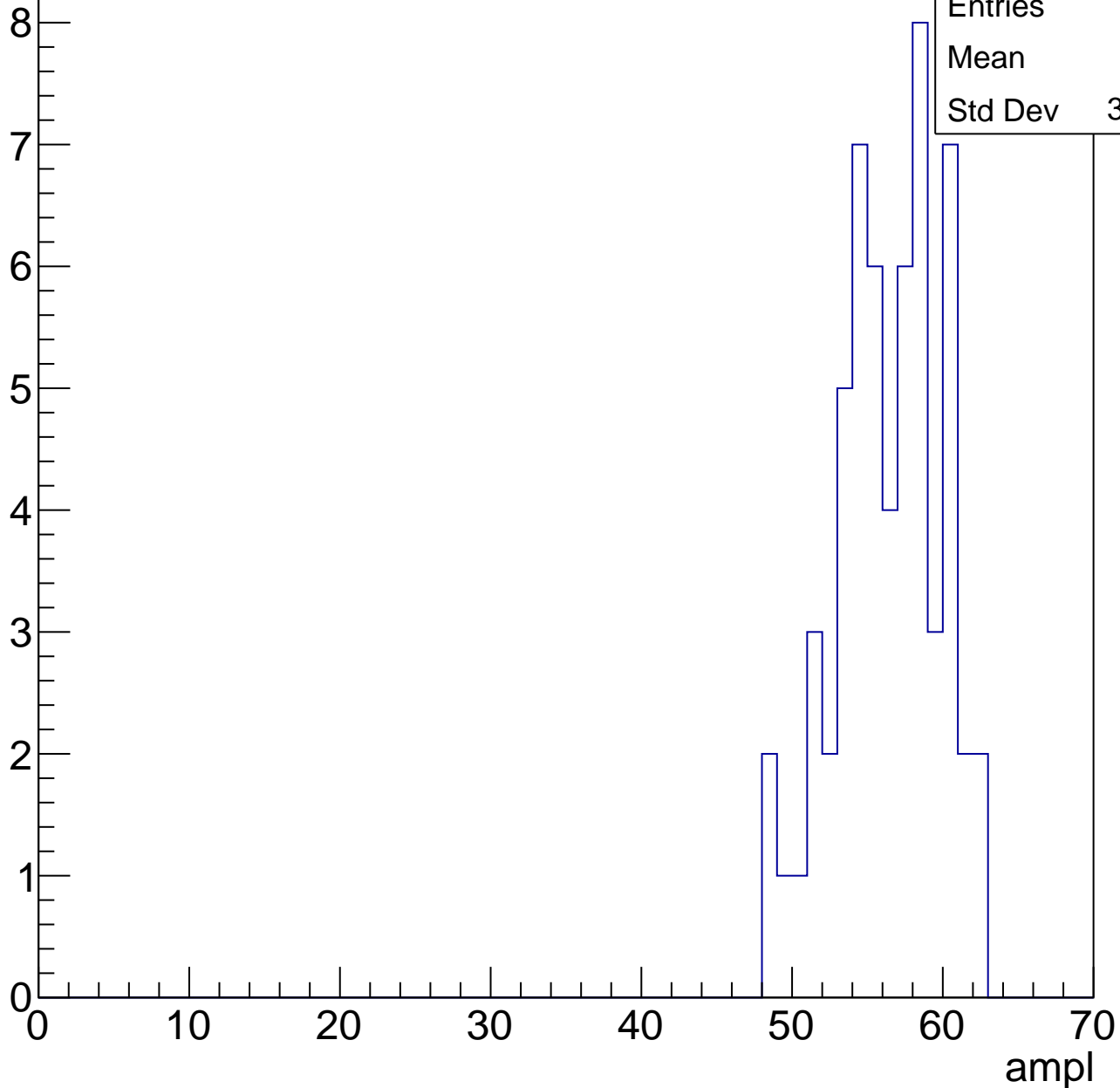
Entries	69
Mean	49.61
Std Dev	3.68



B1L102S, U10-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



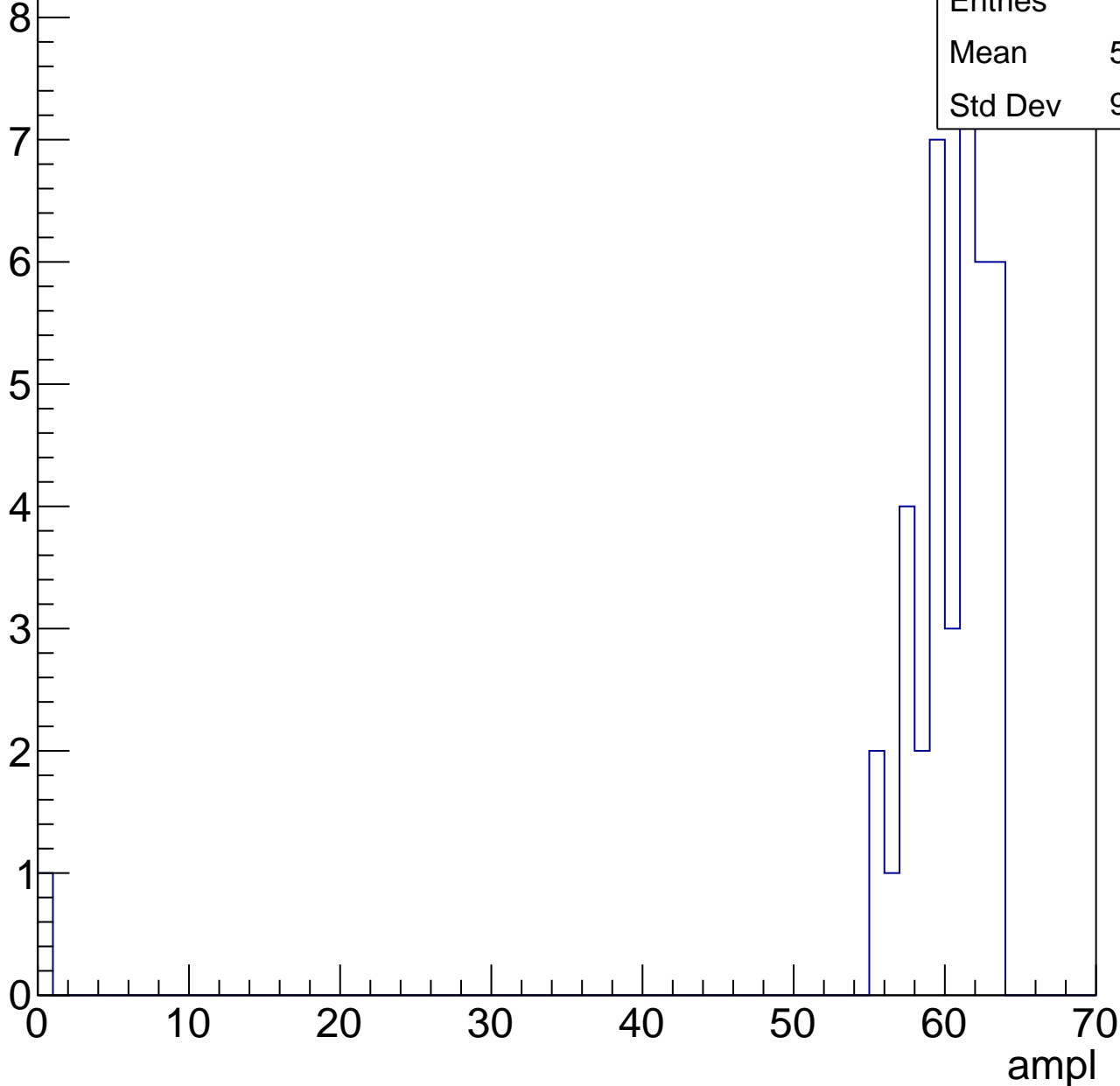
Entries	59
Mean	55.9
Std Dev	3.438

B1L102S, U10-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.52
Std Dev	9.638



B1L102S, U10-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch38, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	27.98
Std Dev	4.83

Gaus mean : 28.4901

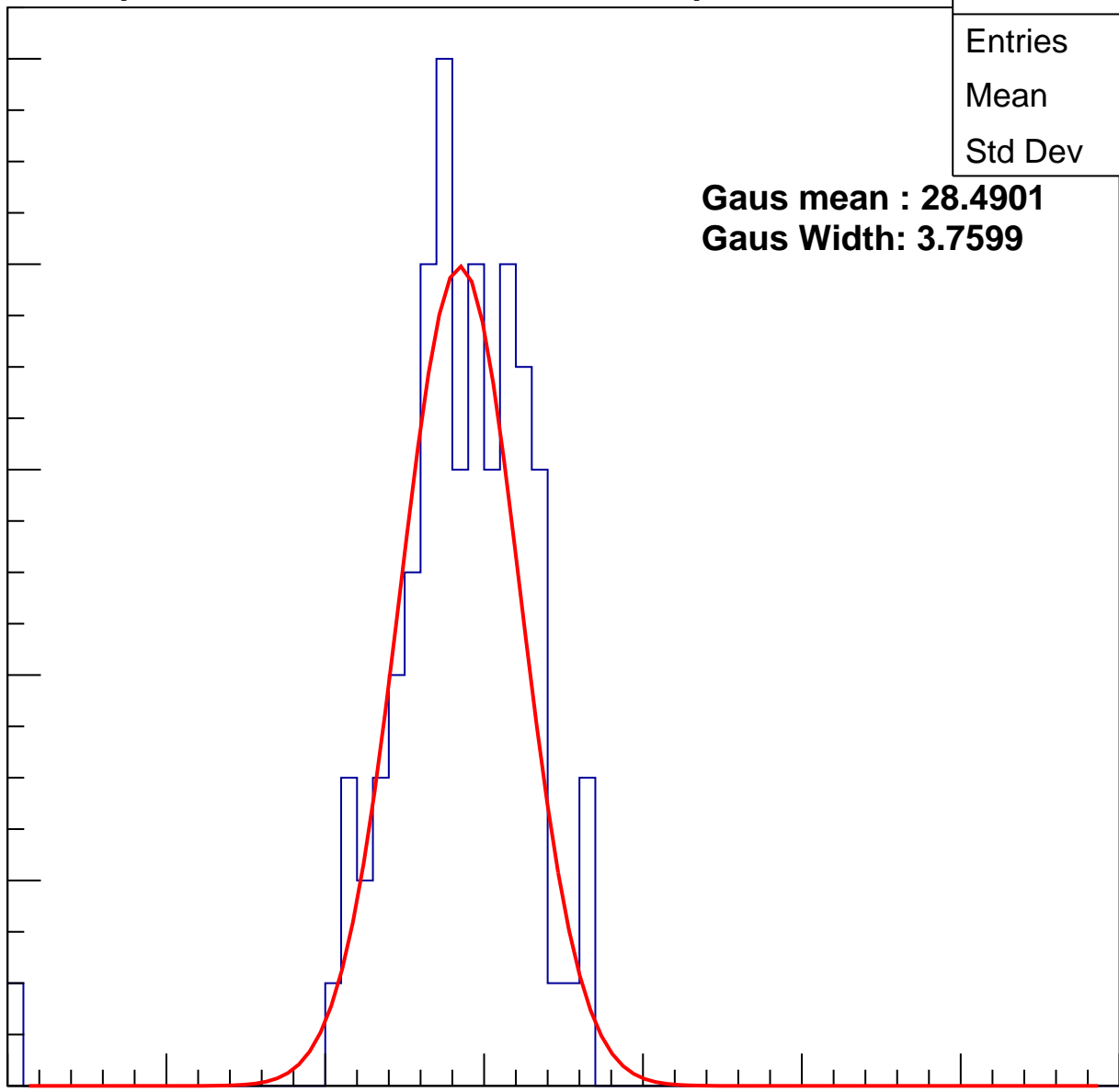
Gaus Width: 3.7599

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch38, adc1

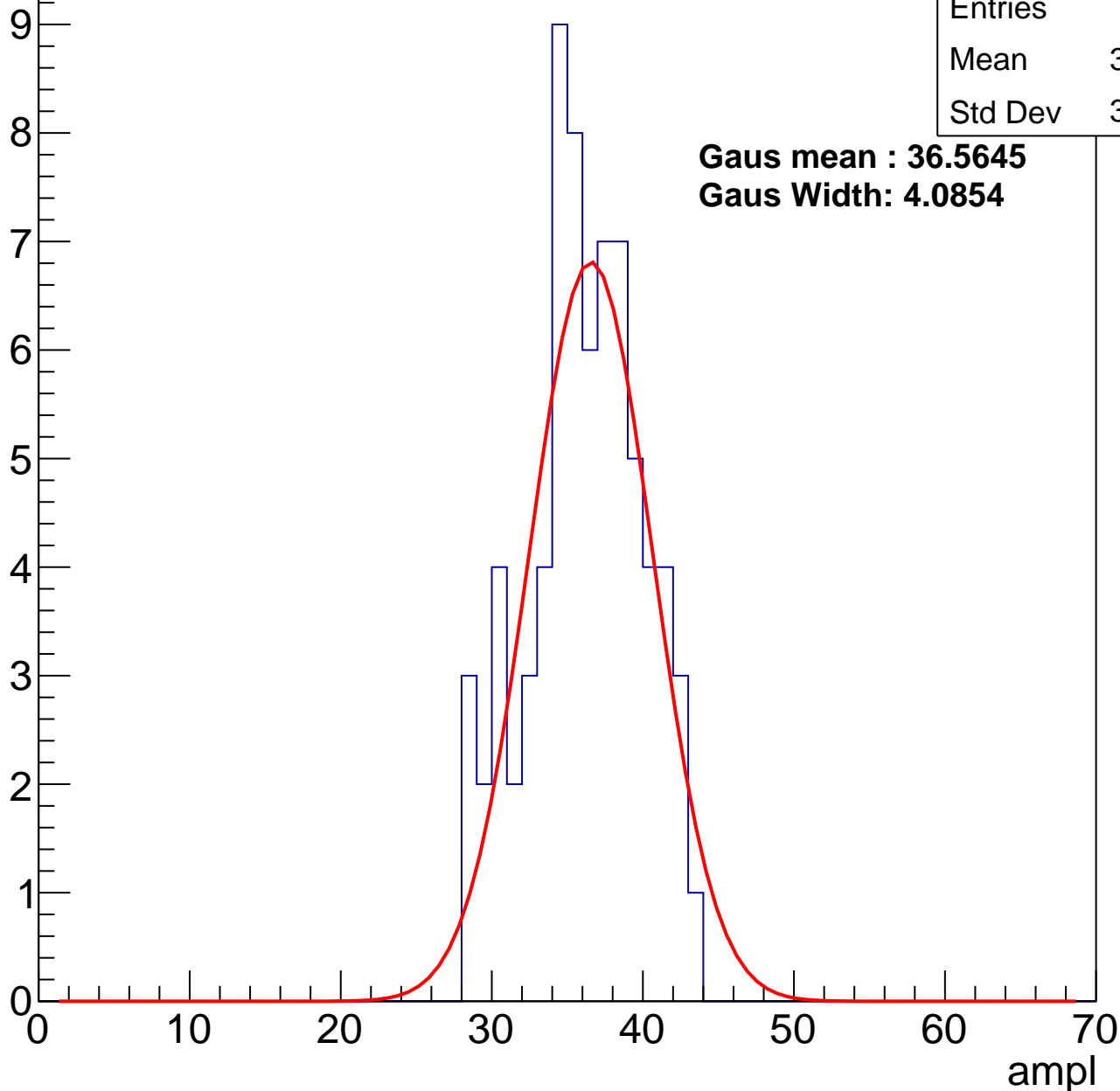
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.65
Std Dev	3.735

Gaus mean : 36.5645

Gaus Width: 4.0854



B1L102S, U10-ch38, adc2

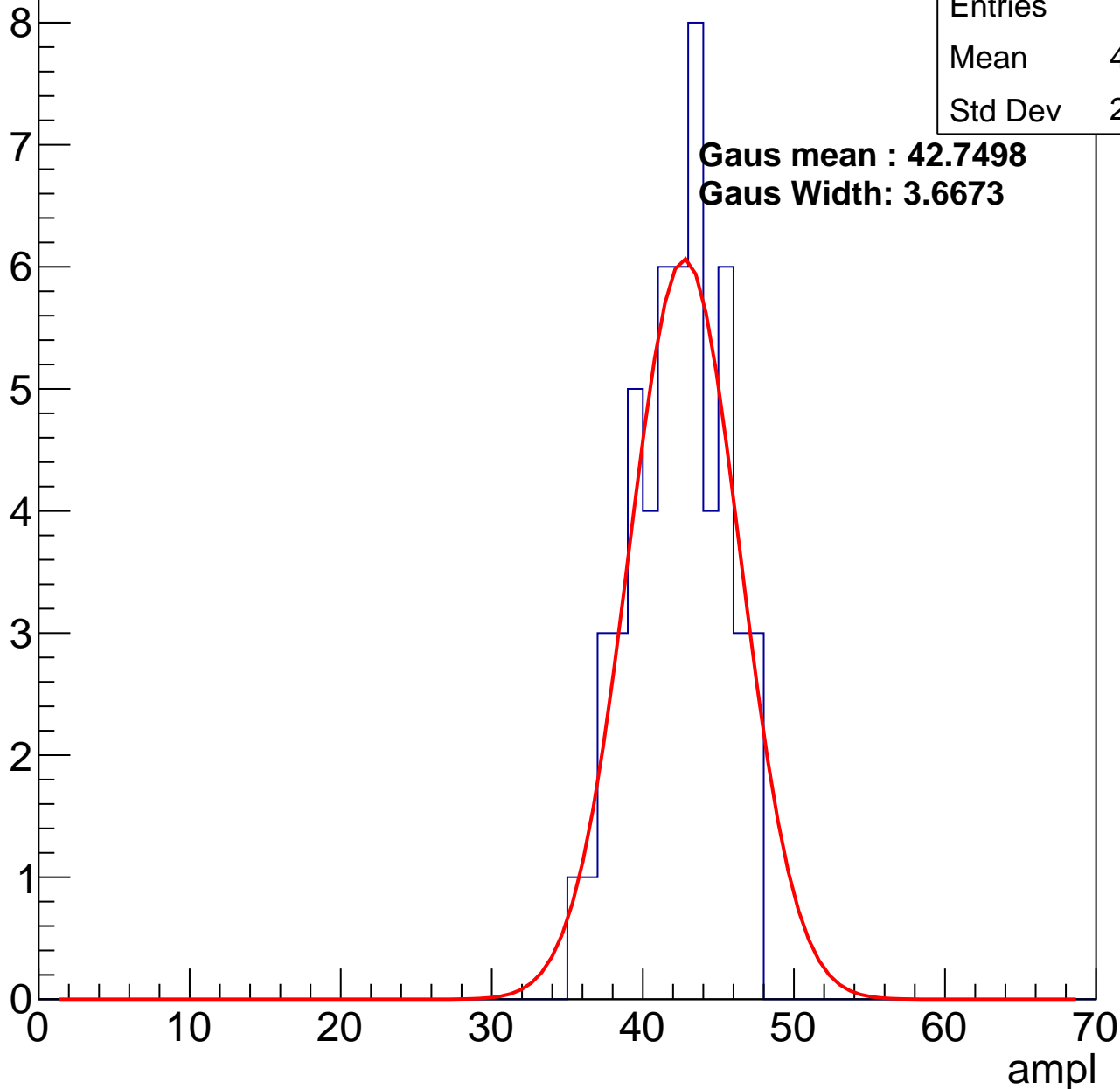
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.85
Std Dev	2.993

Gaus mean : 42.7498

Gaus Width: 3.6673

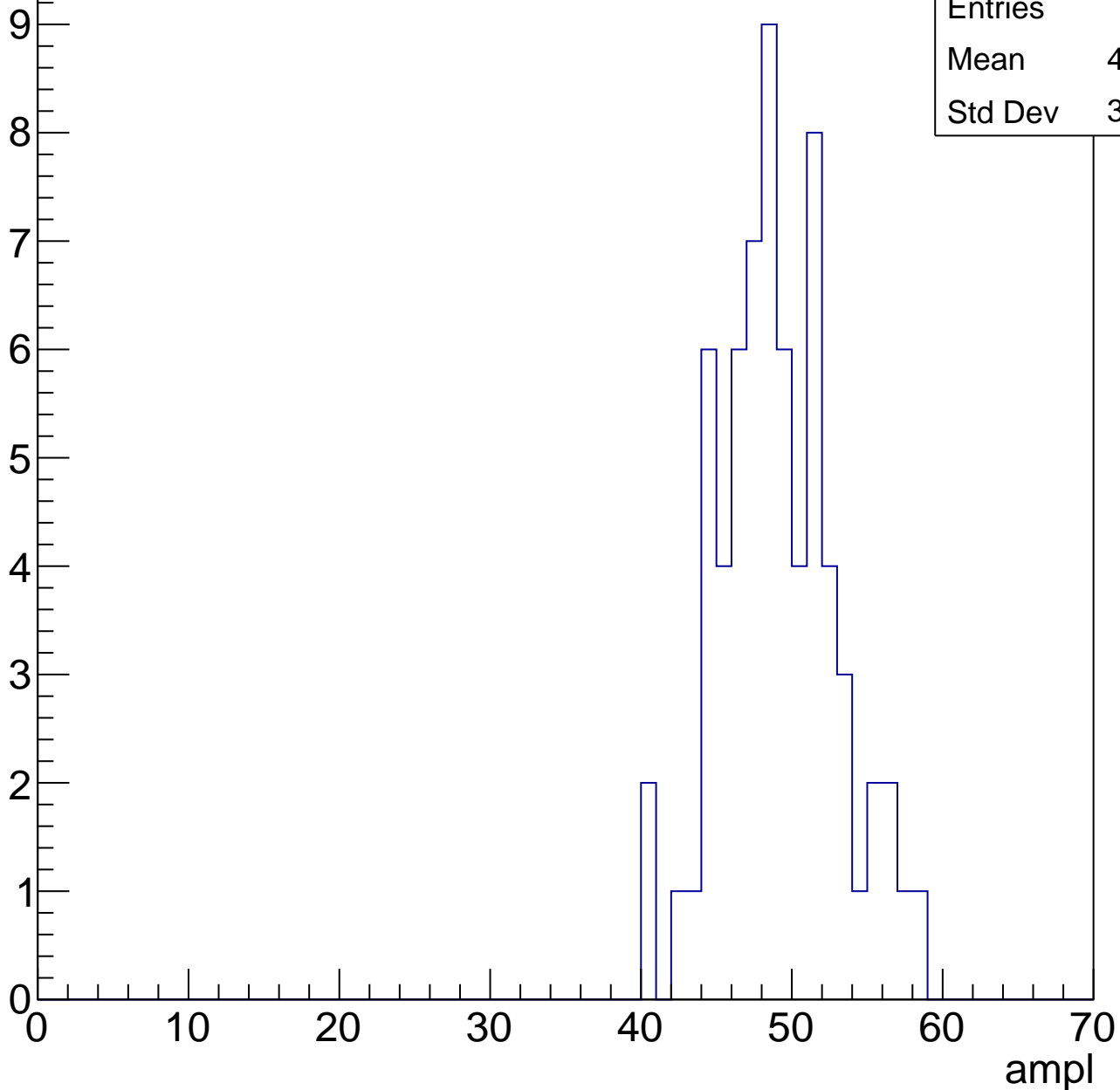


B1L102S, U10-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.62
Std Dev	3.839

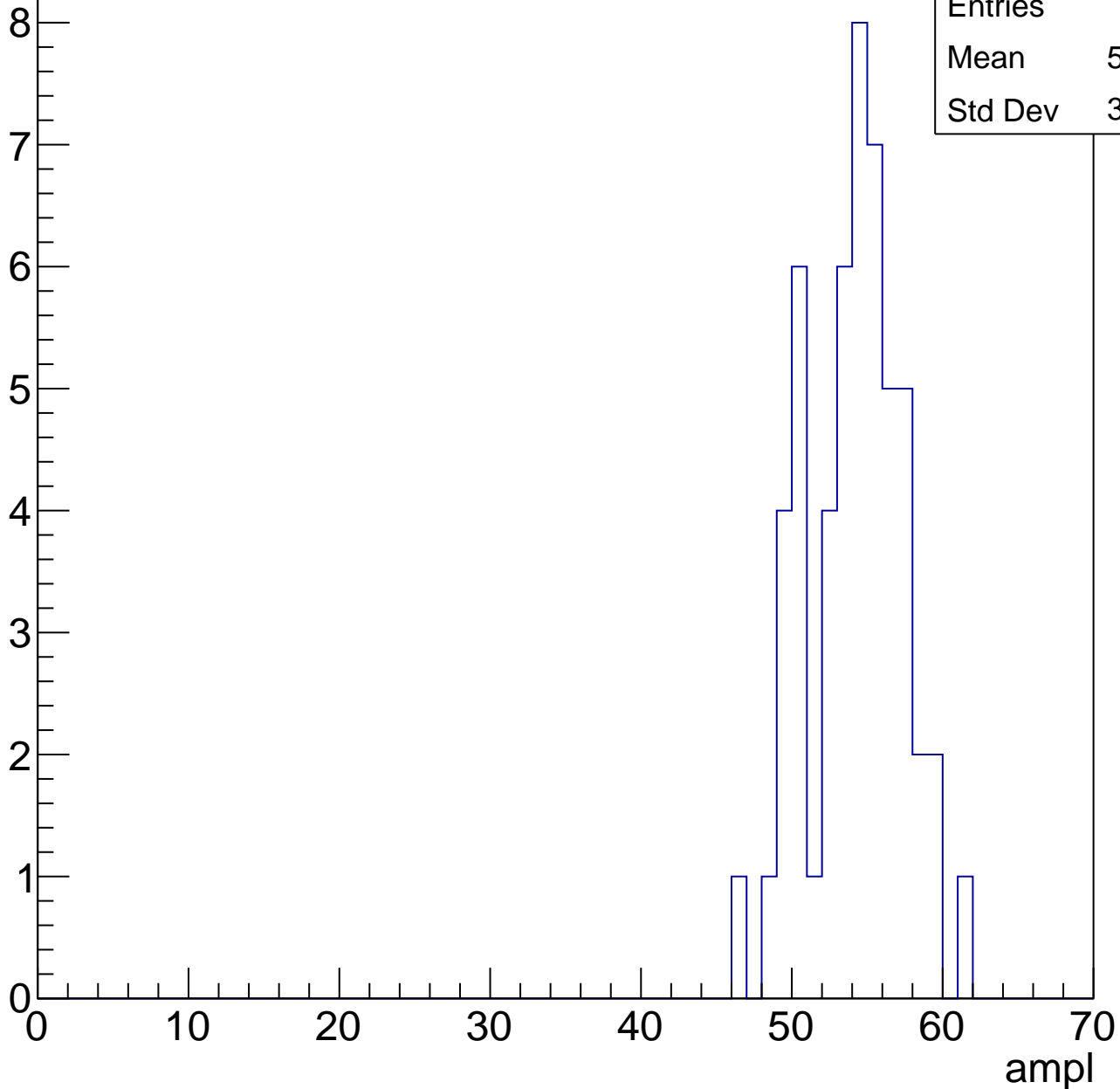


B1L102S, U10-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	53.66
Std Dev	3.144

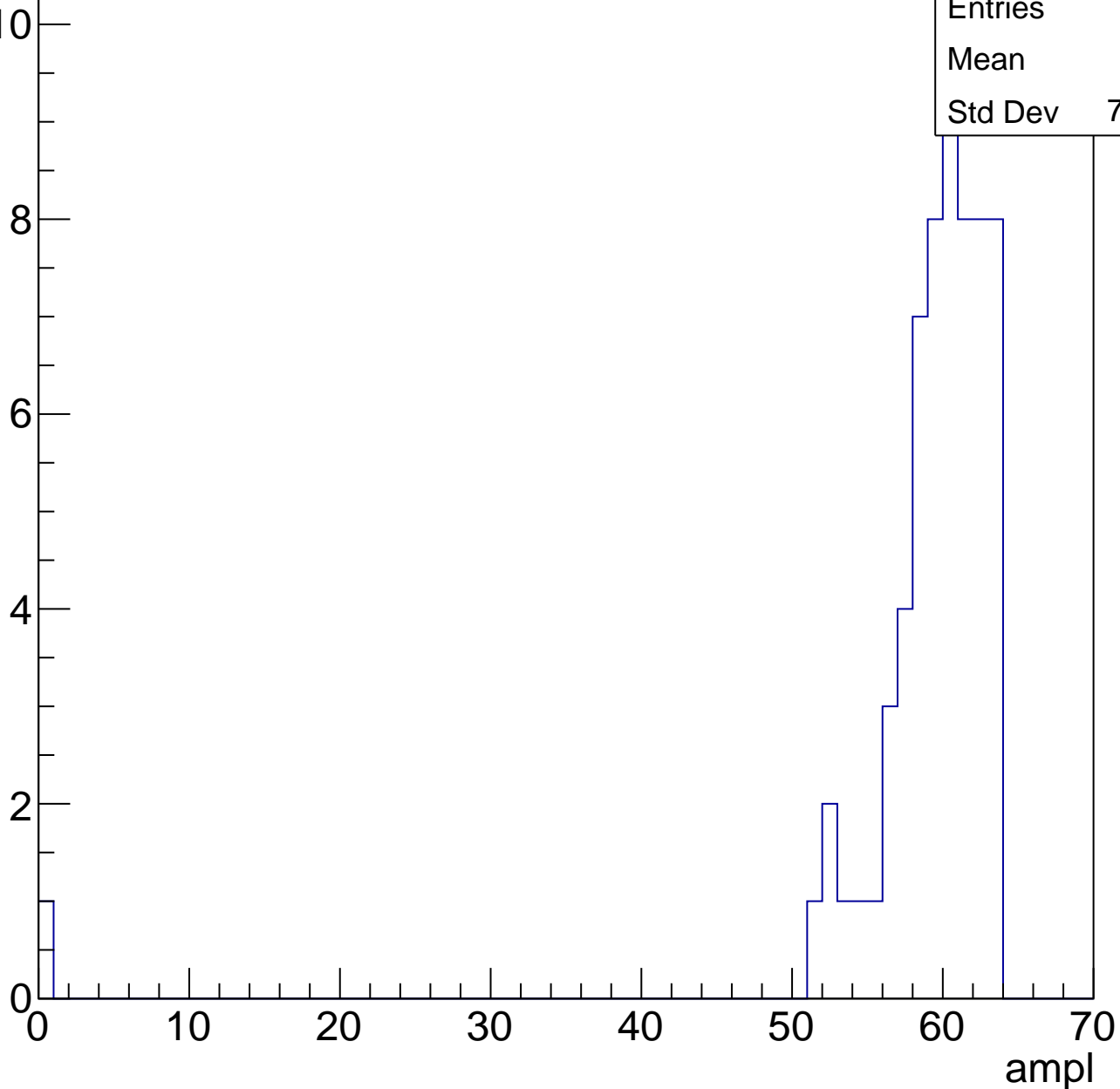


B1L102S, U10-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	58.4
Std Dev	7.957



B1L102S, U10-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L102S, U10-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch39, adc0

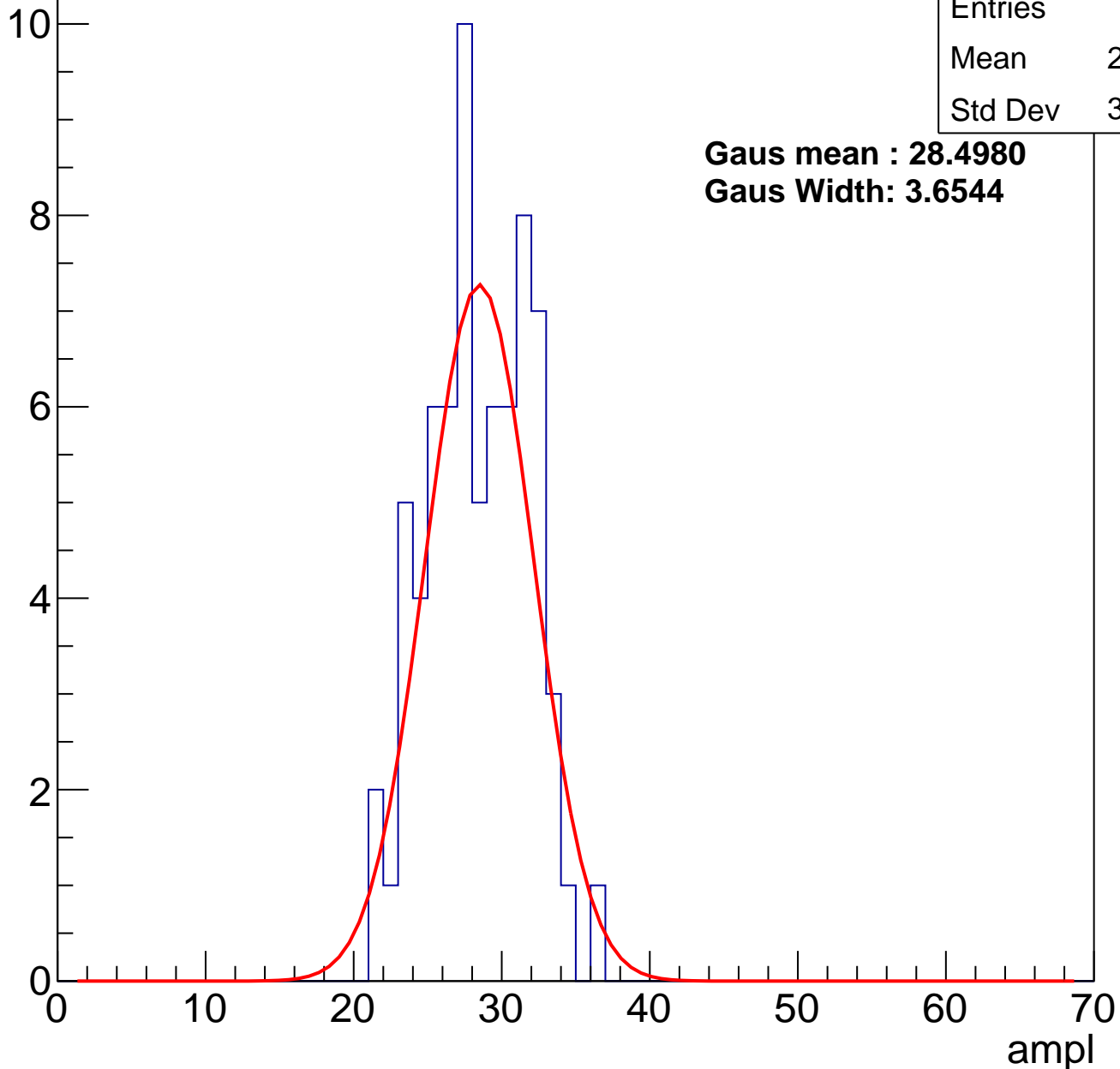
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	27.97
Std Dev	3.357

Gaus mean : 28.4980

Gaus Width: 3.6544

Entry



B1L102S, U10-ch39, adc1

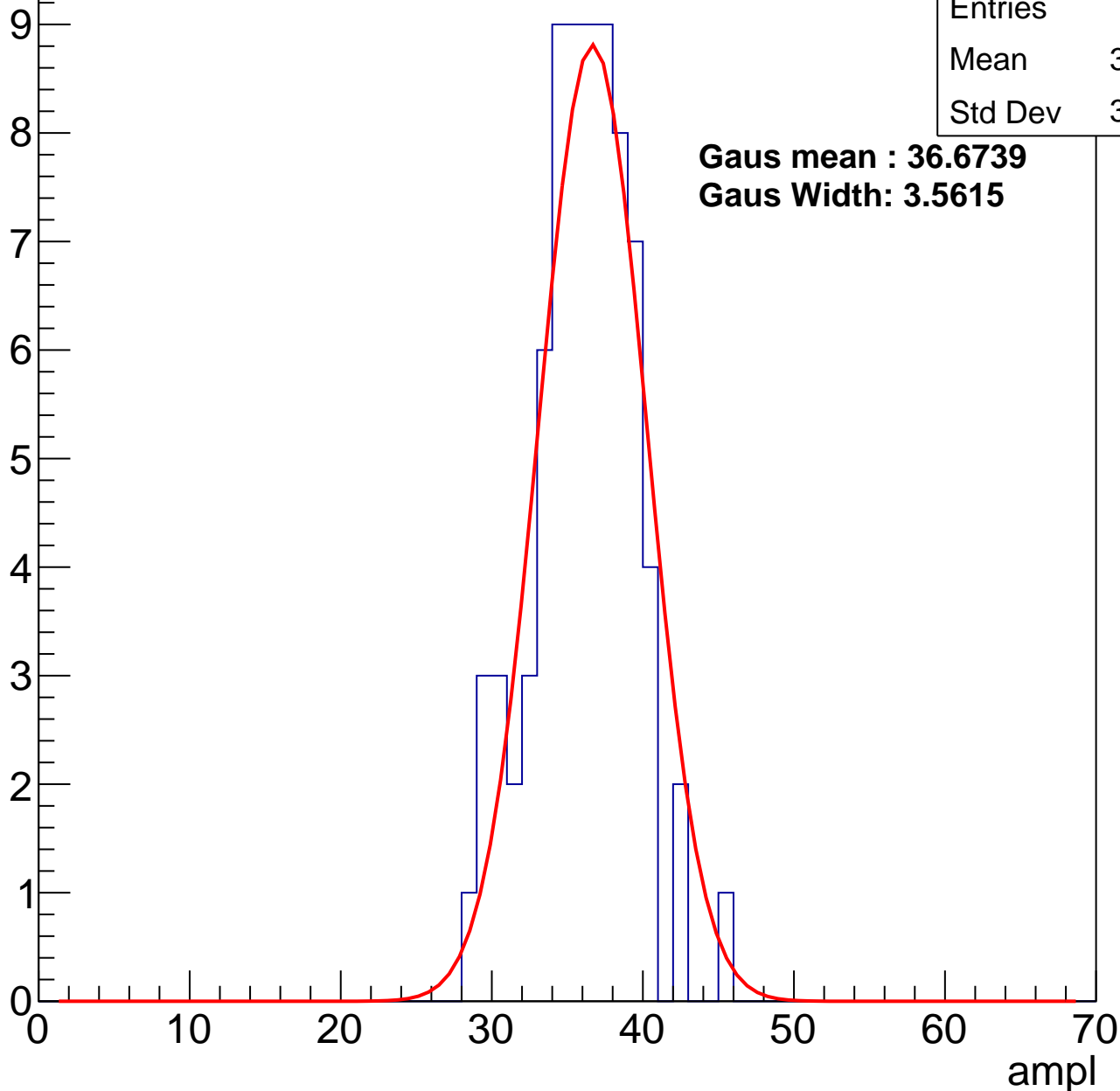
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.59
Std Dev	3.293

Gaus mean : 36.6739

Gaus Width: 3.5615



B1L102S, U10-ch39, adc2

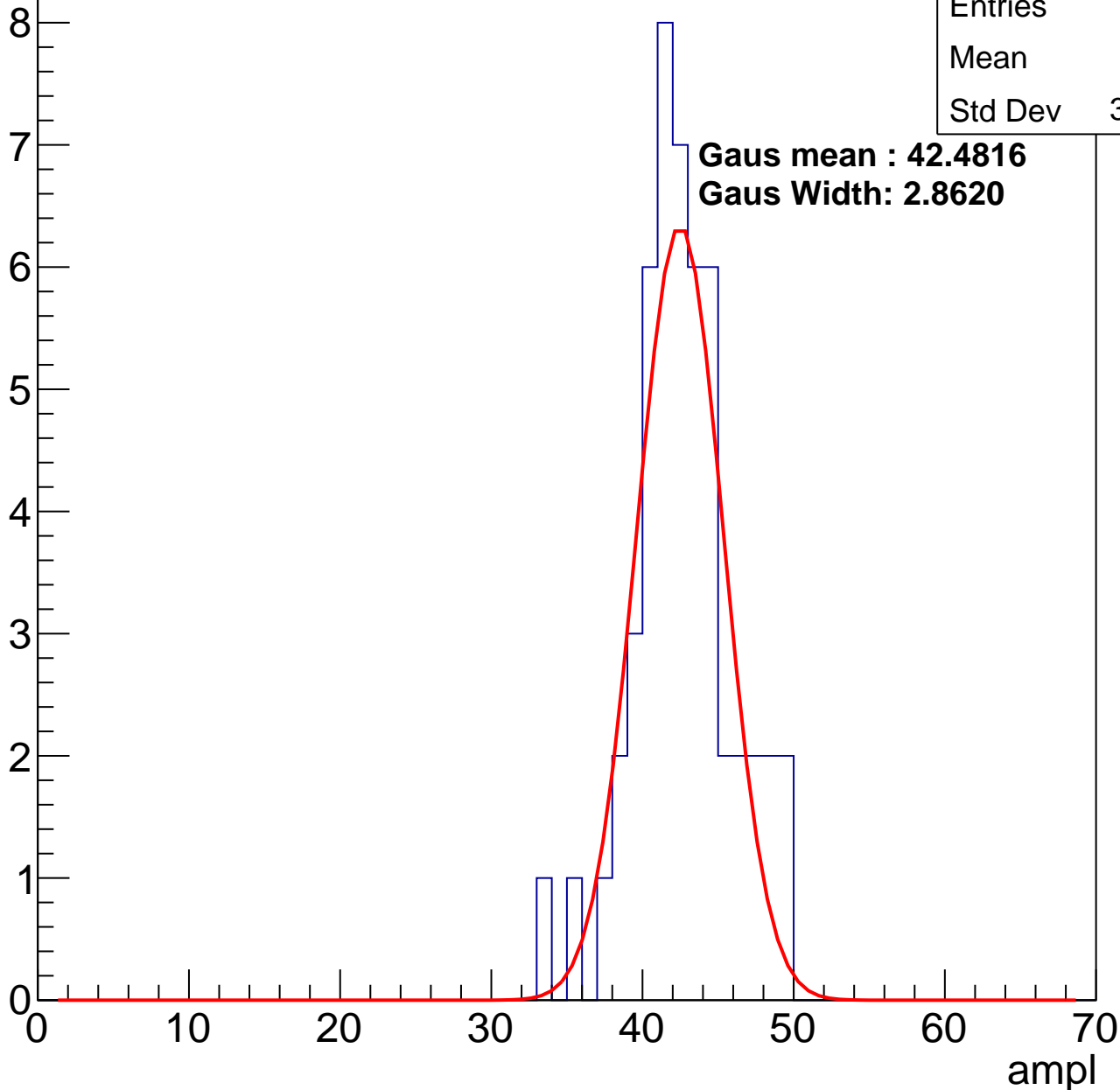
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.2
Std Dev	3.266

Gaus mean : 42.4816

Gaus Width: 2.8620

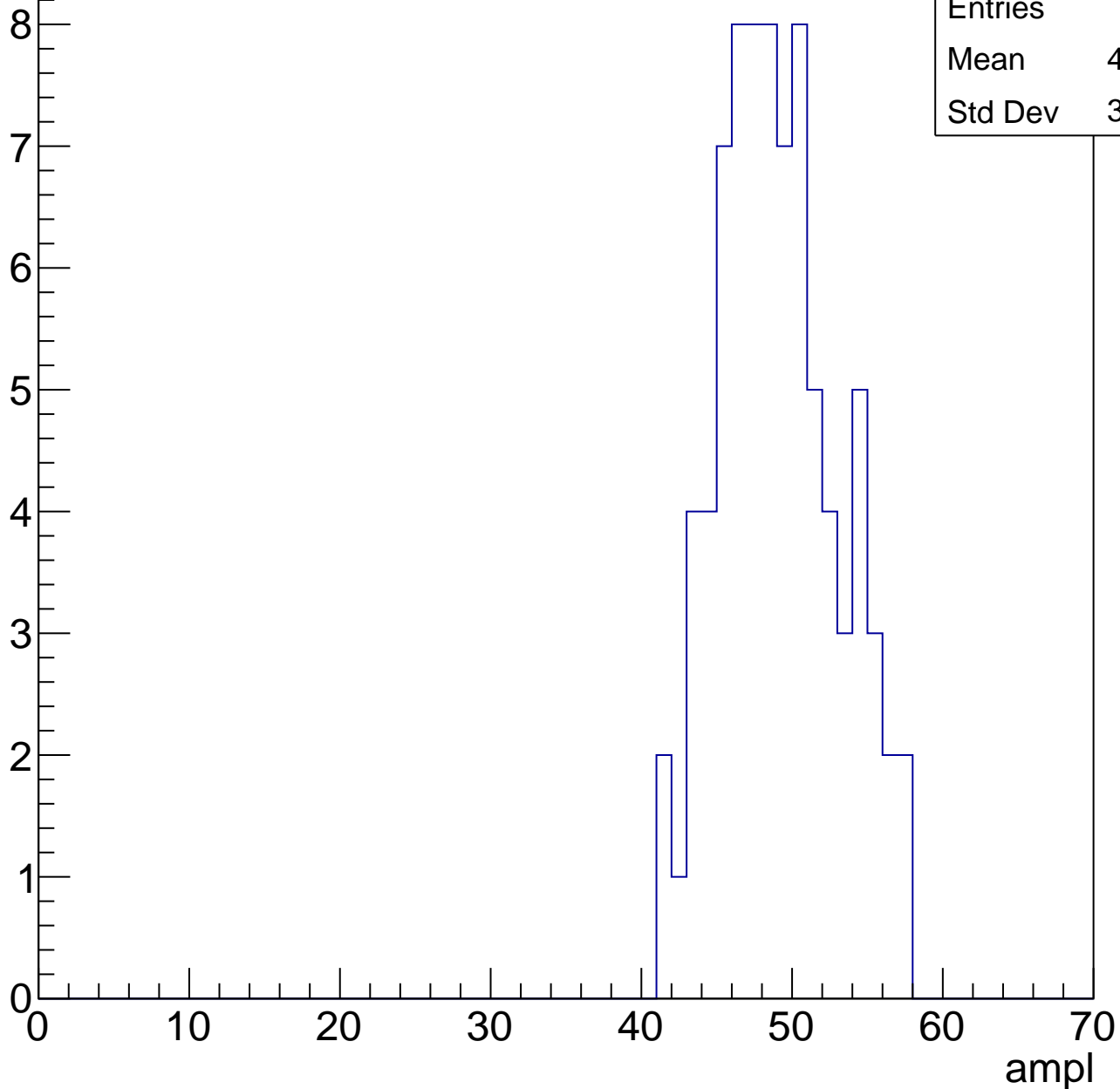


B1L102S, U10-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	48.65
Std Dev	3.859

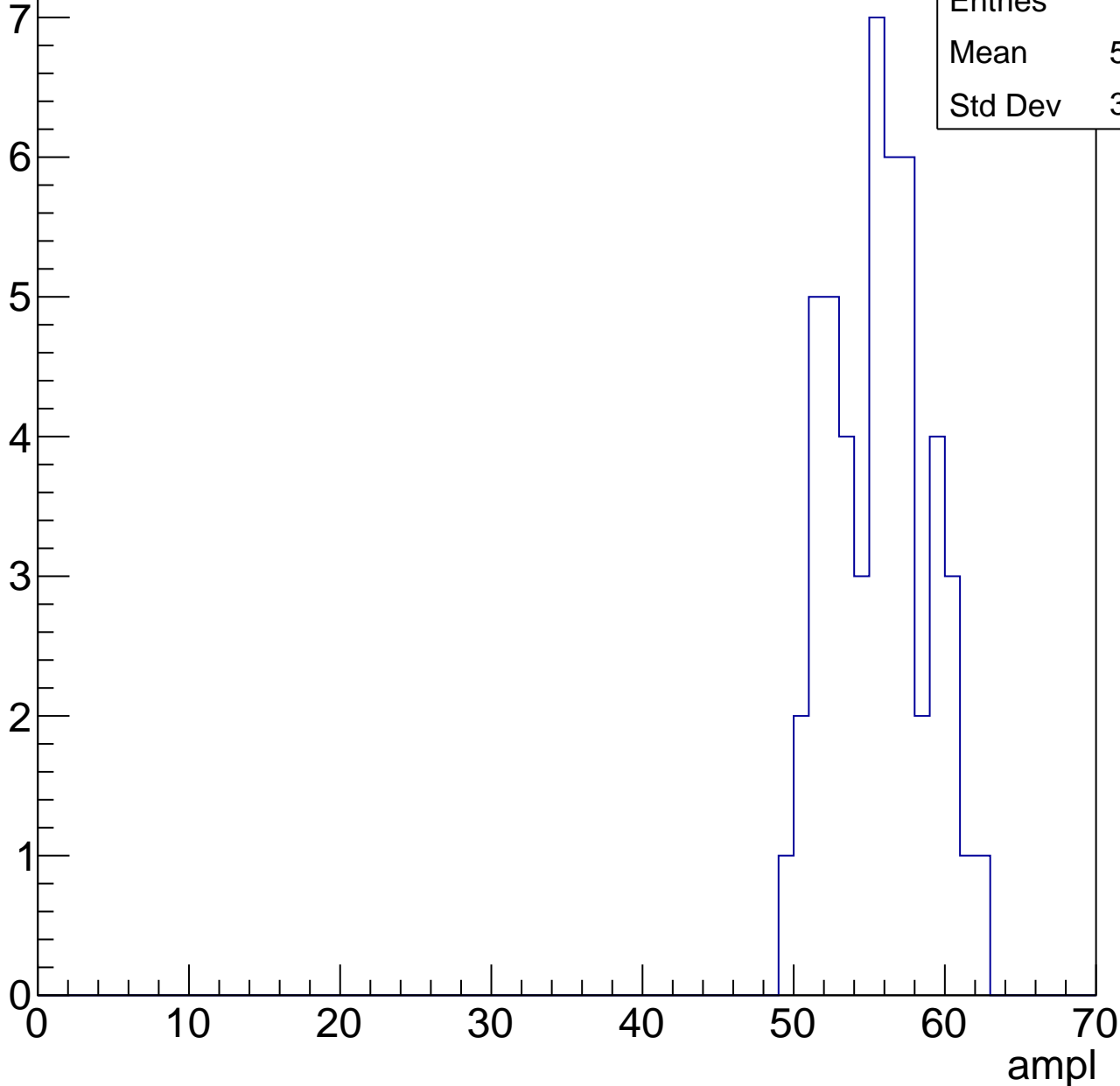


B1L102S, U10-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.12
Std Dev	3.166

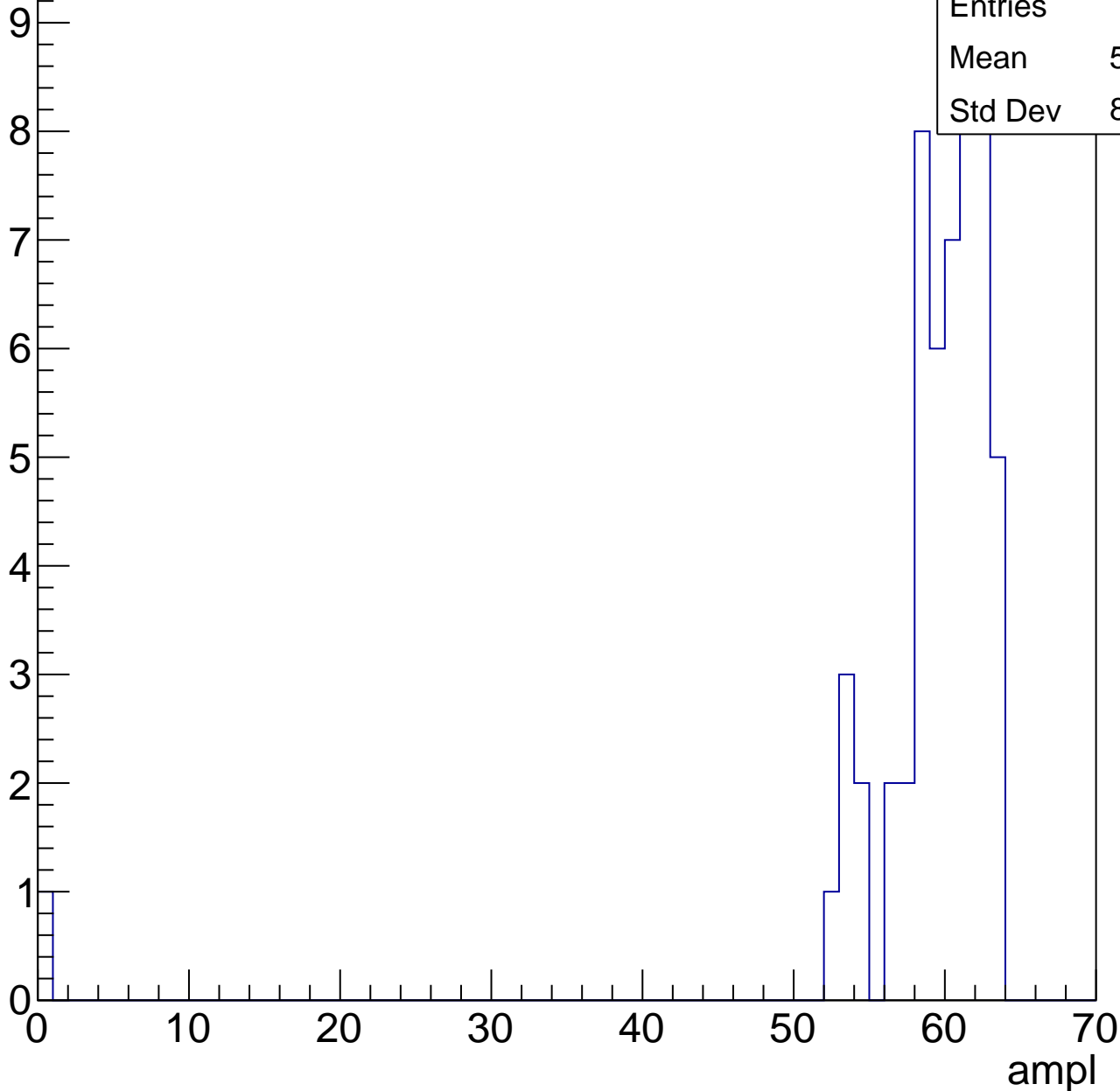


B1L102S, U10-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.27
Std Dev	8.417



B1L102S, U10-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch40, adc0

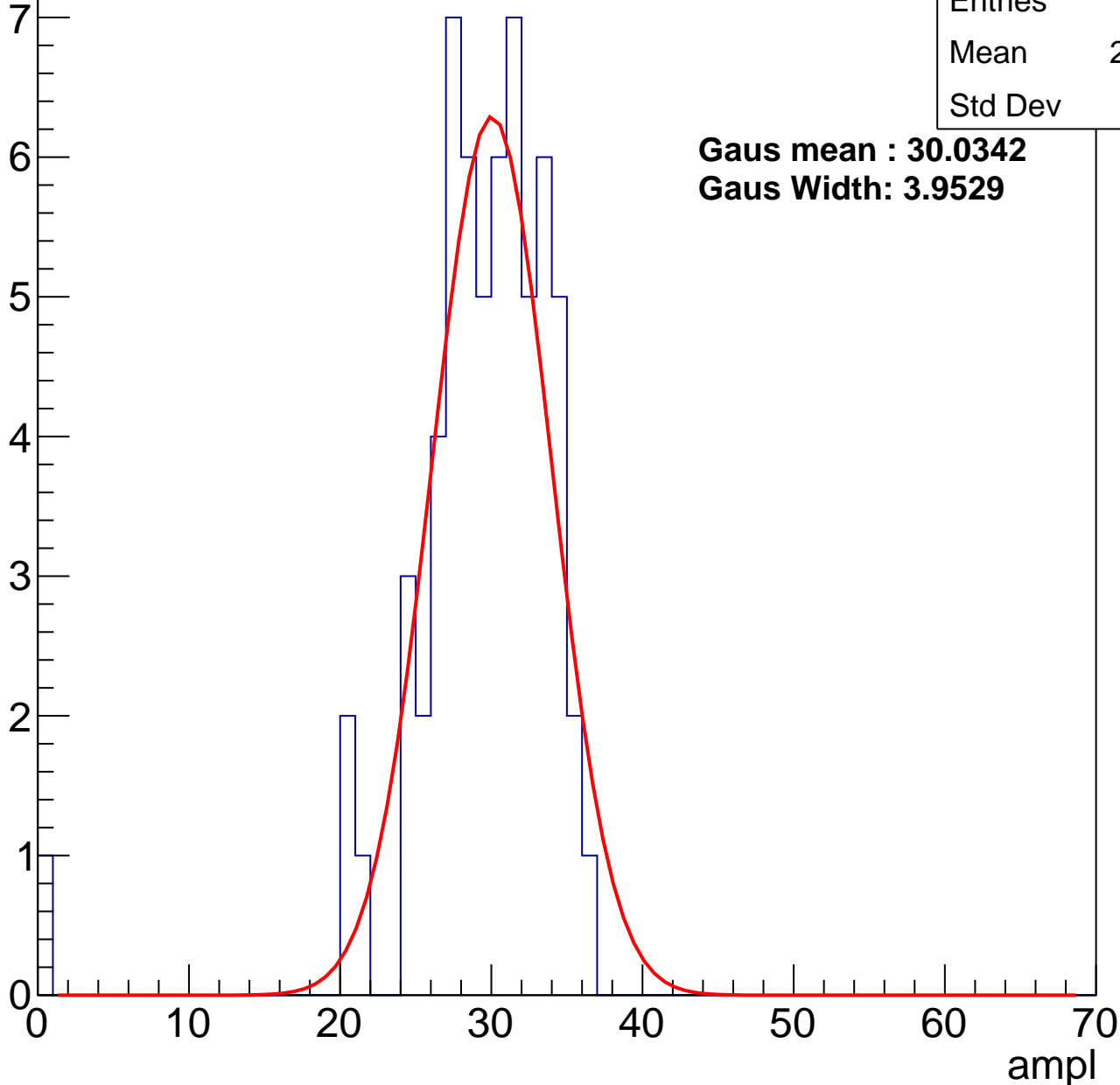
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.89
Std Dev	5.14

Gaus mean : 30.0342

Gaus Width: 3.9529



B1L102S, U10-ch40, adc1

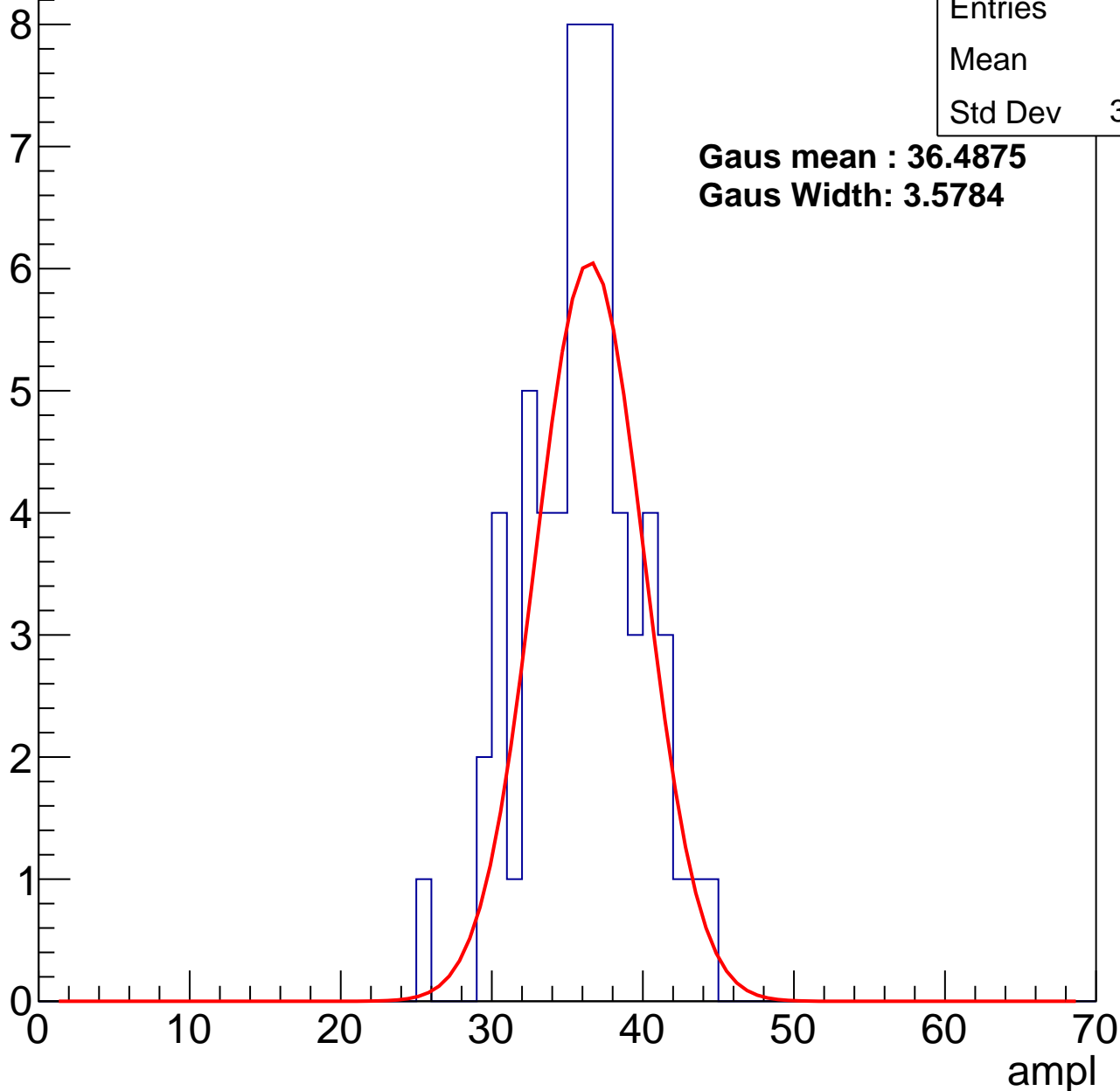
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.6
Std Dev	3.718

Gaus mean : 36.4875

Gaus Width: 3.5784



B1L102S, U10-ch40, adc2

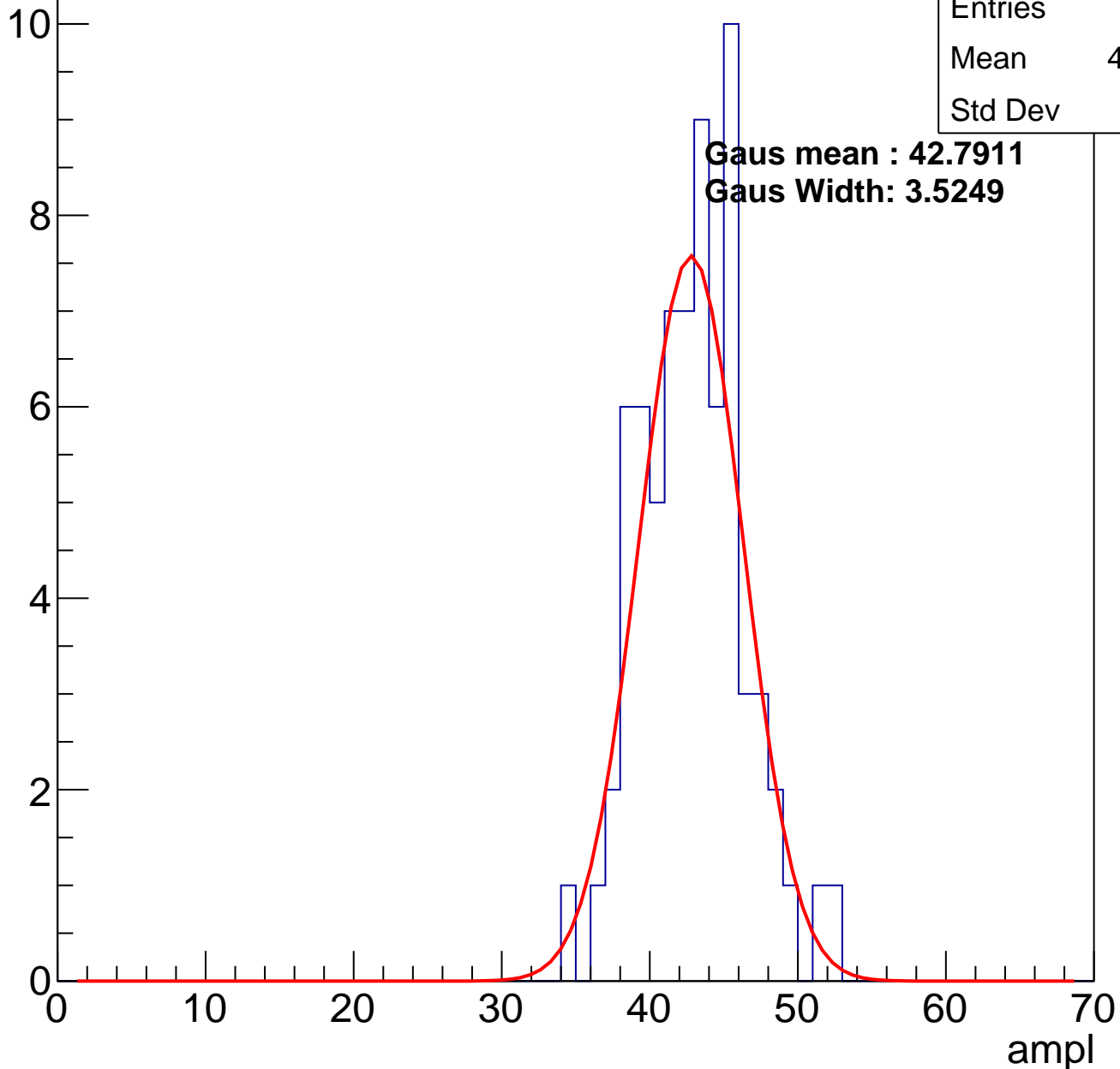
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	42.46
Std Dev	3.48

Gaus mean : 42.7911

Gaus Width: 3.5249

Entry

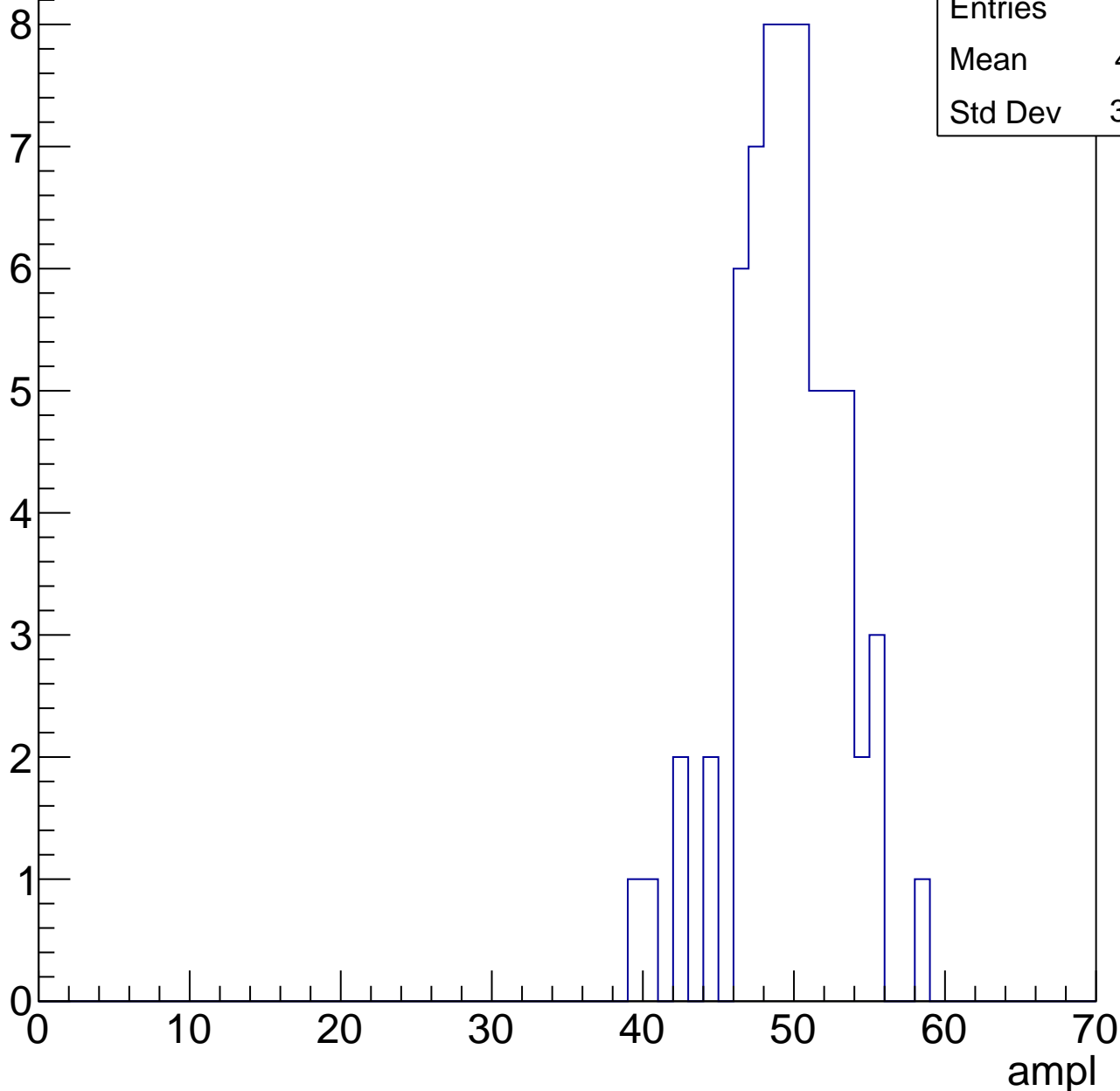


B1L102S, U10-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.11
Std Dev	3.558

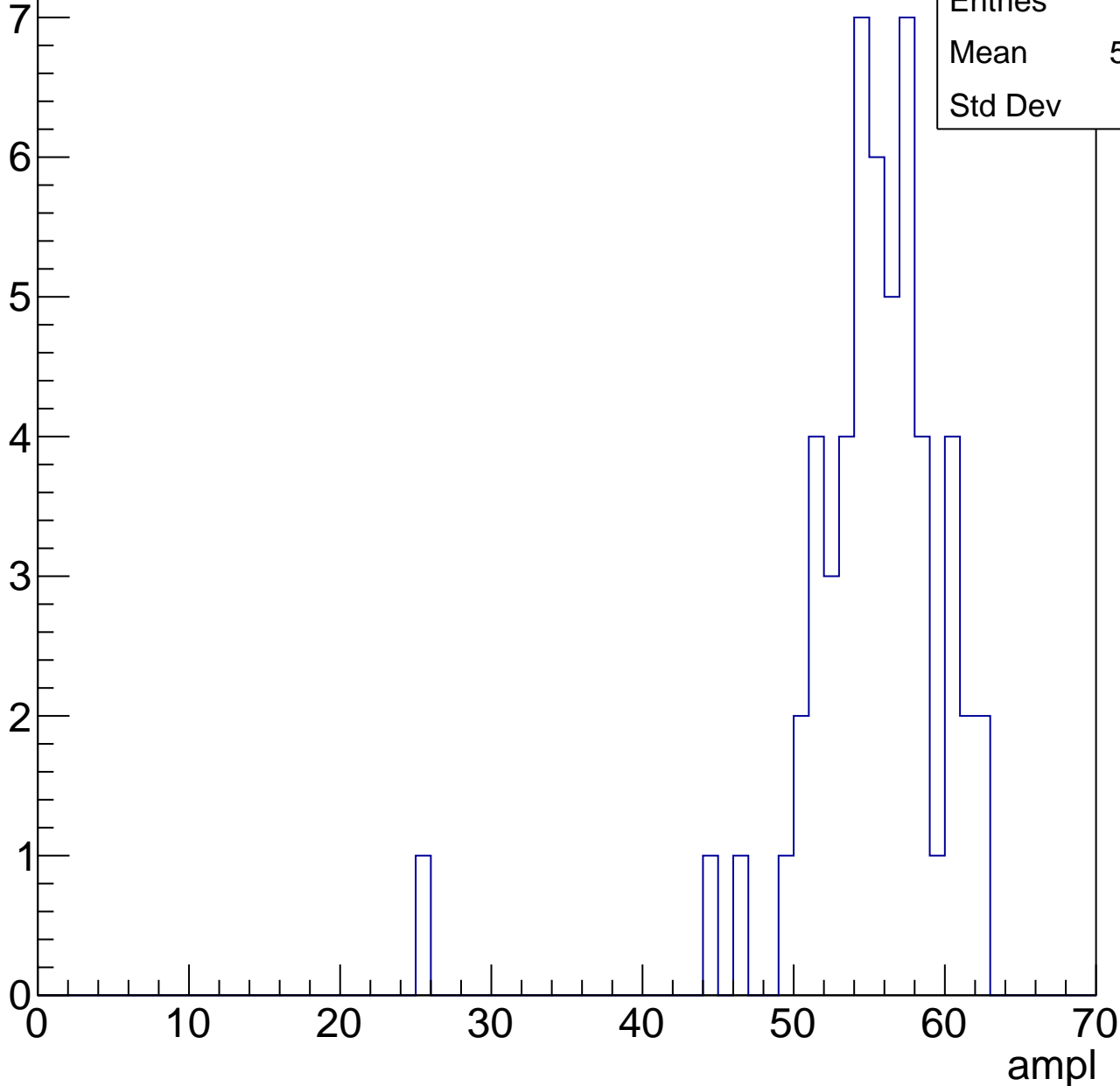


B1L102S, U10-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

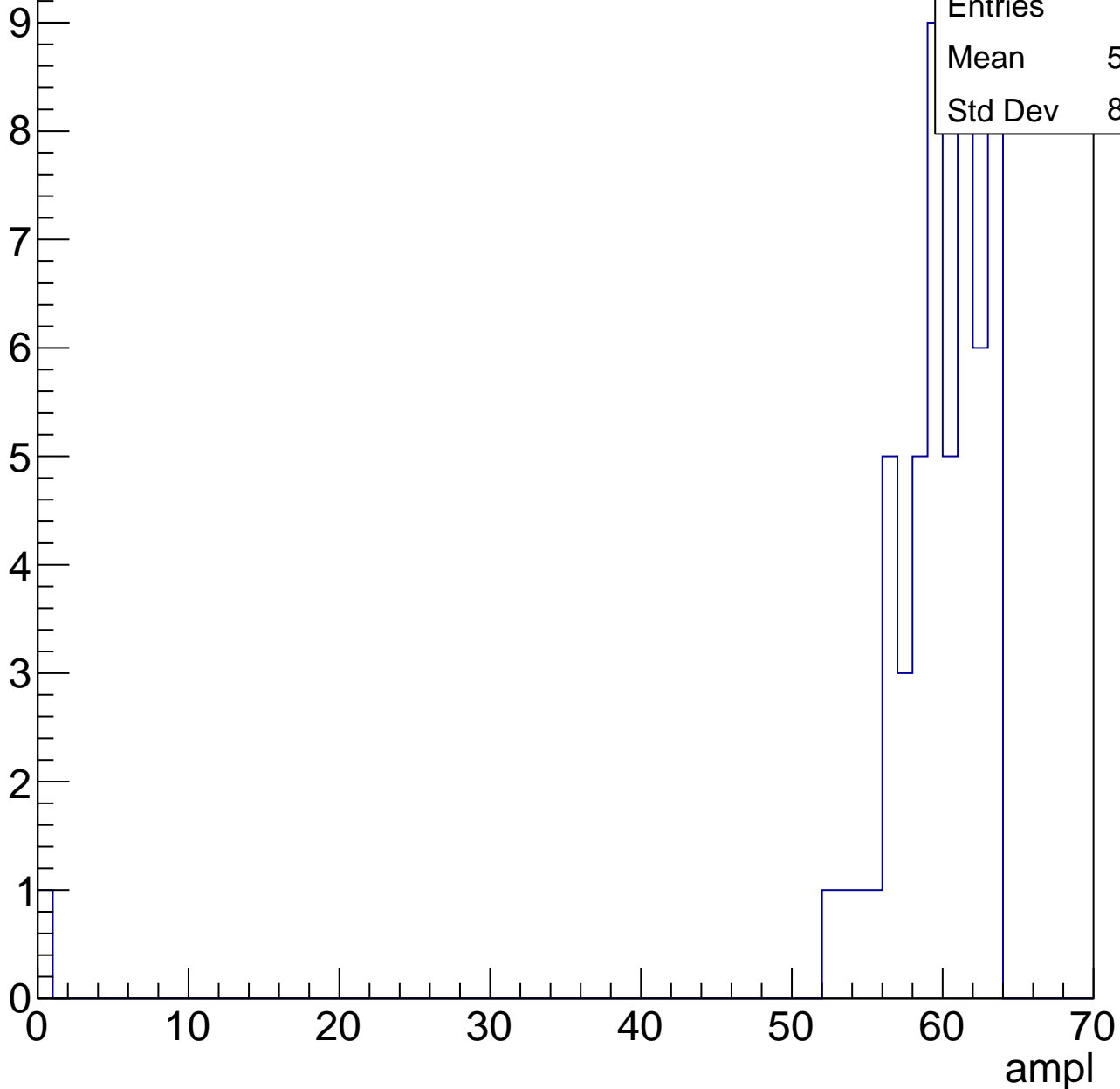
Entries	55
Mean	54.55
Std Dev	5.47



B1L102S, U10-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch41, adc0

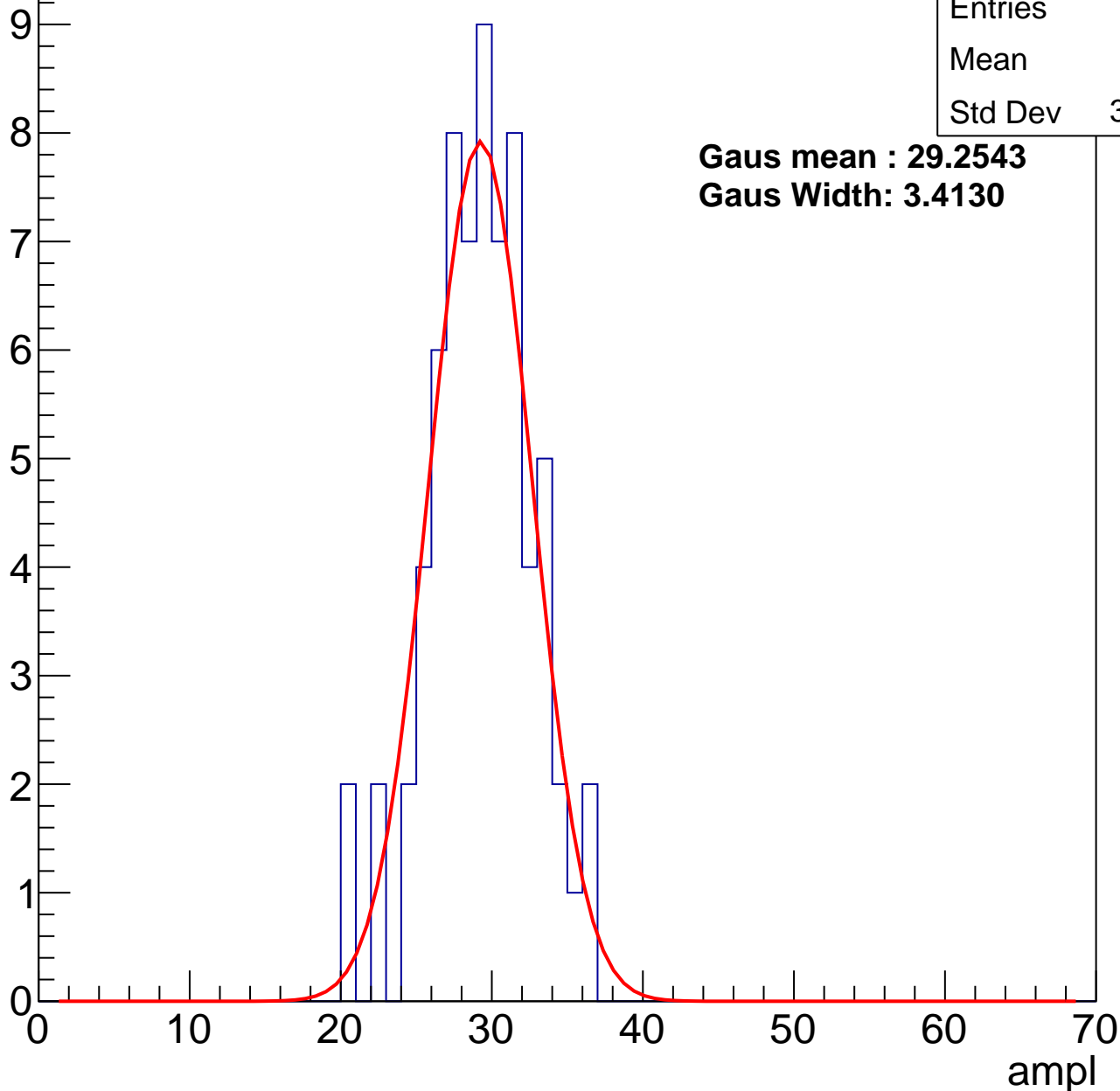
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.8
Std Dev	3.412

Gaus mean : 29.2543

Gaus Width: 3.4130



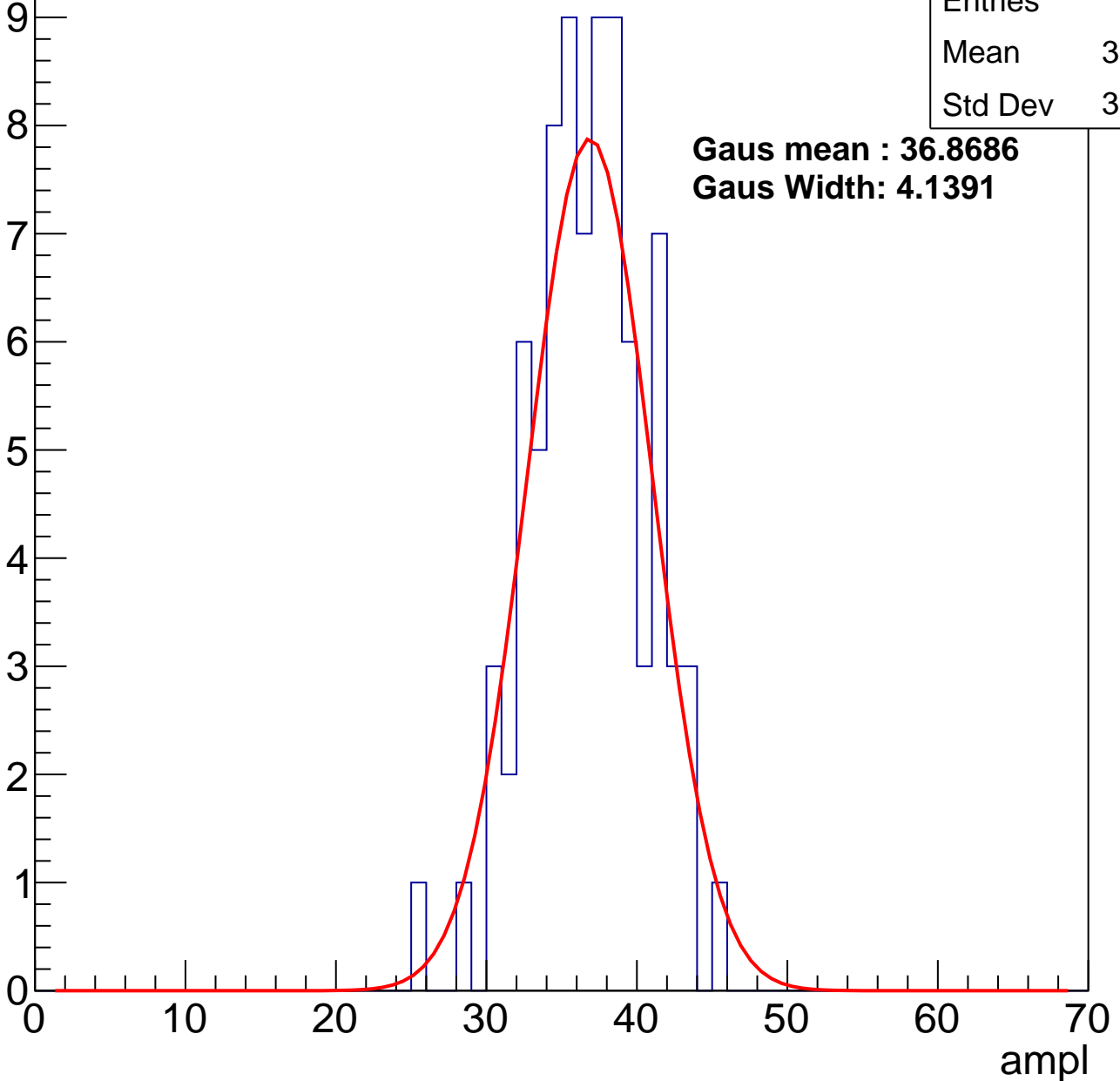
B1L102S, U10-ch41, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.35
Std Dev	3.759

Gaus mean : 36.8686
Gaus Width: 4.1391



B1L102S, U10-ch41, adc2

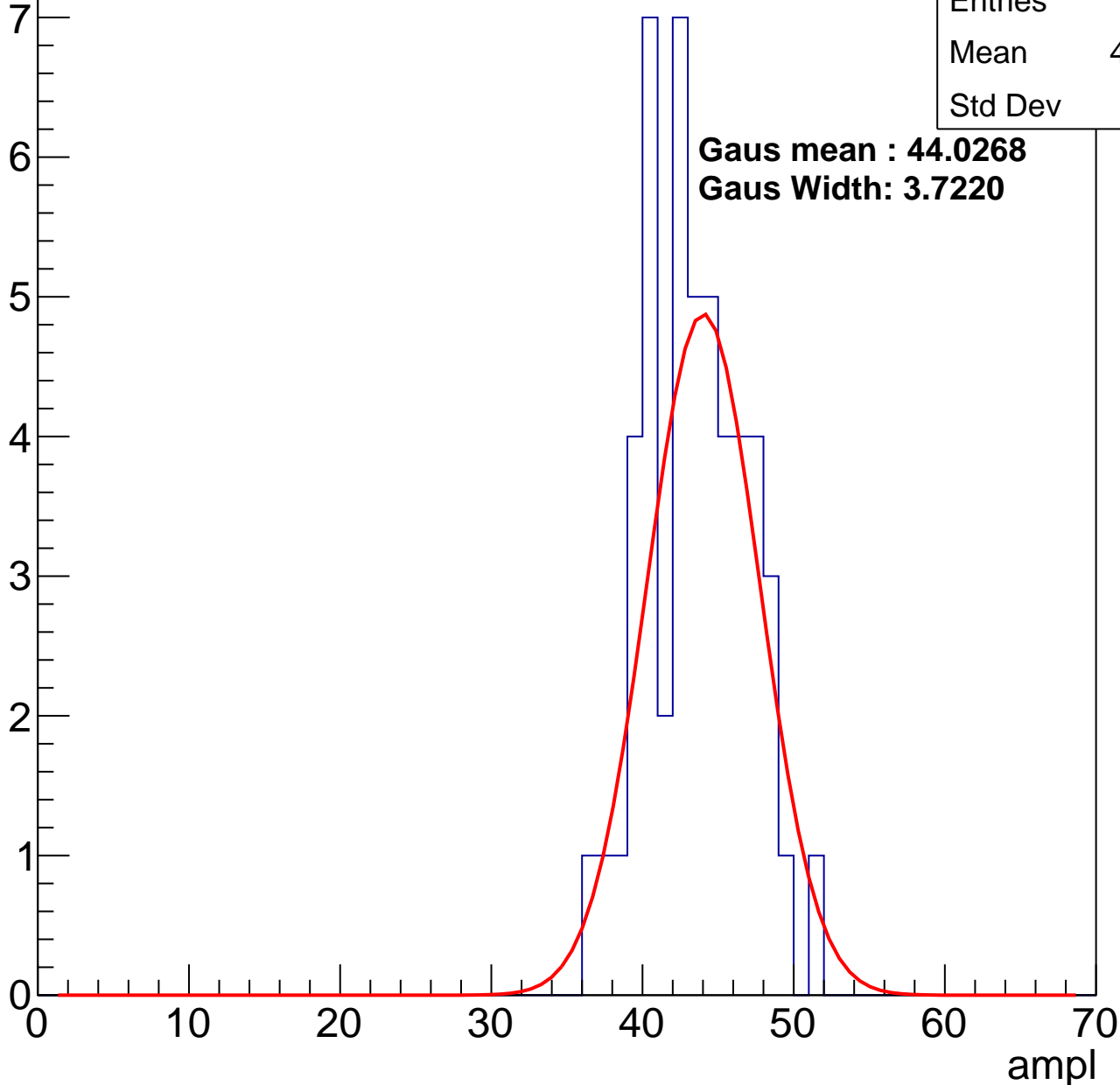
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.08
Std Dev	3.31

Gaus mean : 44.0268

Gaus Width: 3.7220

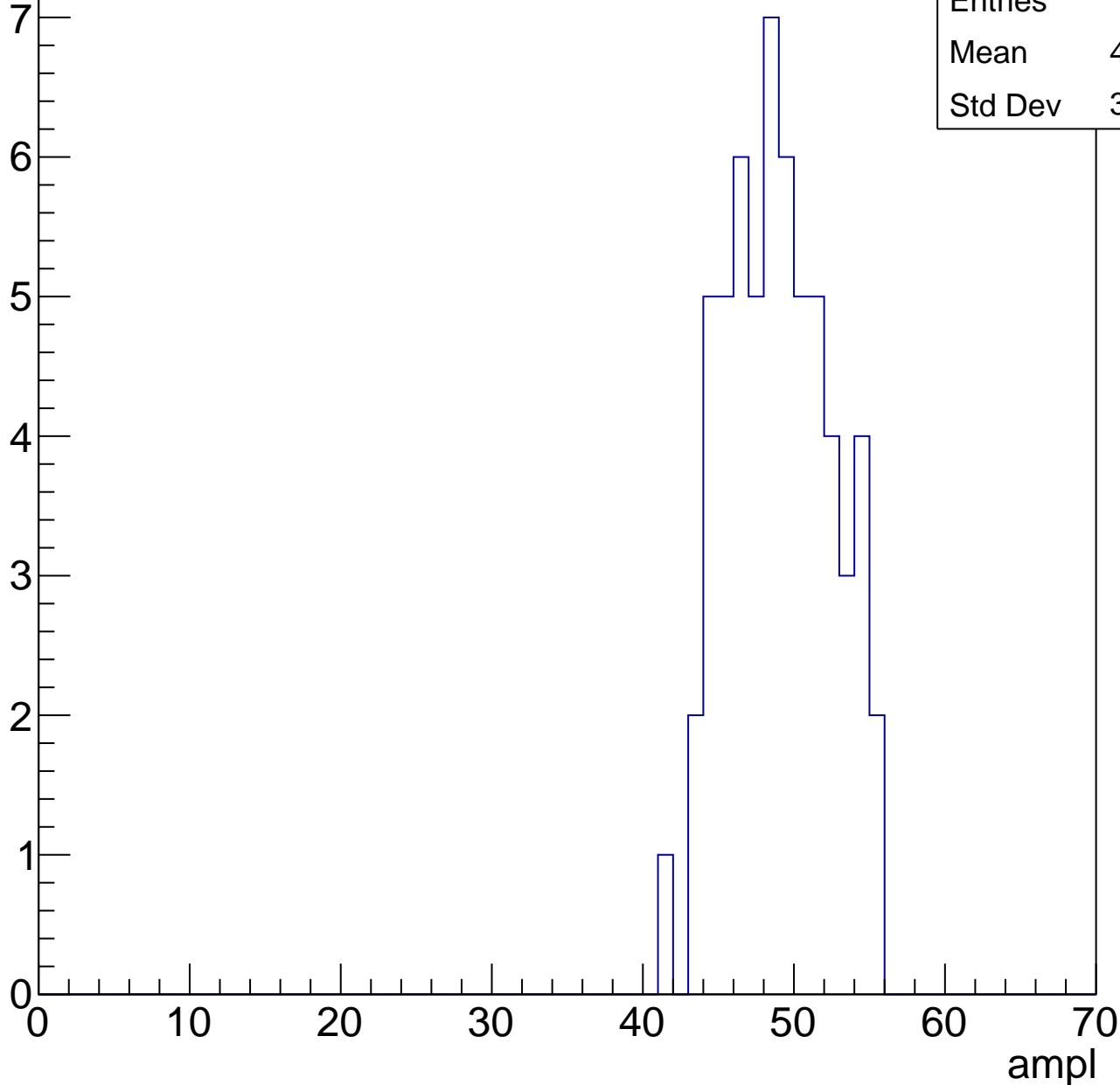


B1L102S, U10-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.52
Std Dev	3.389

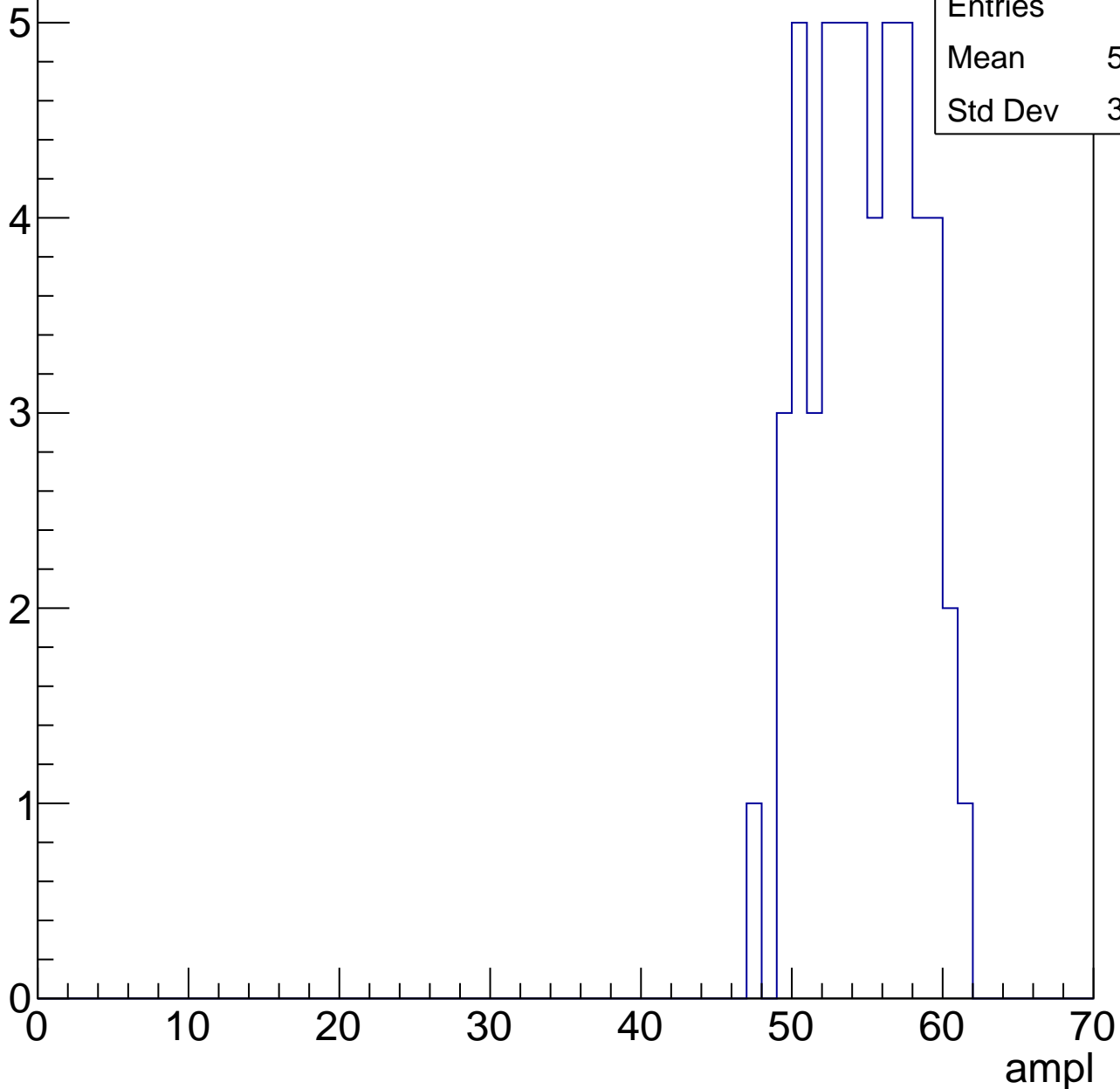


B1L102S, U10-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.35
Std Dev	3.407



B1L102S, U10-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	54
Mean	57.65
Std Dev	8.518

ampl

0

10

20

30

40

50

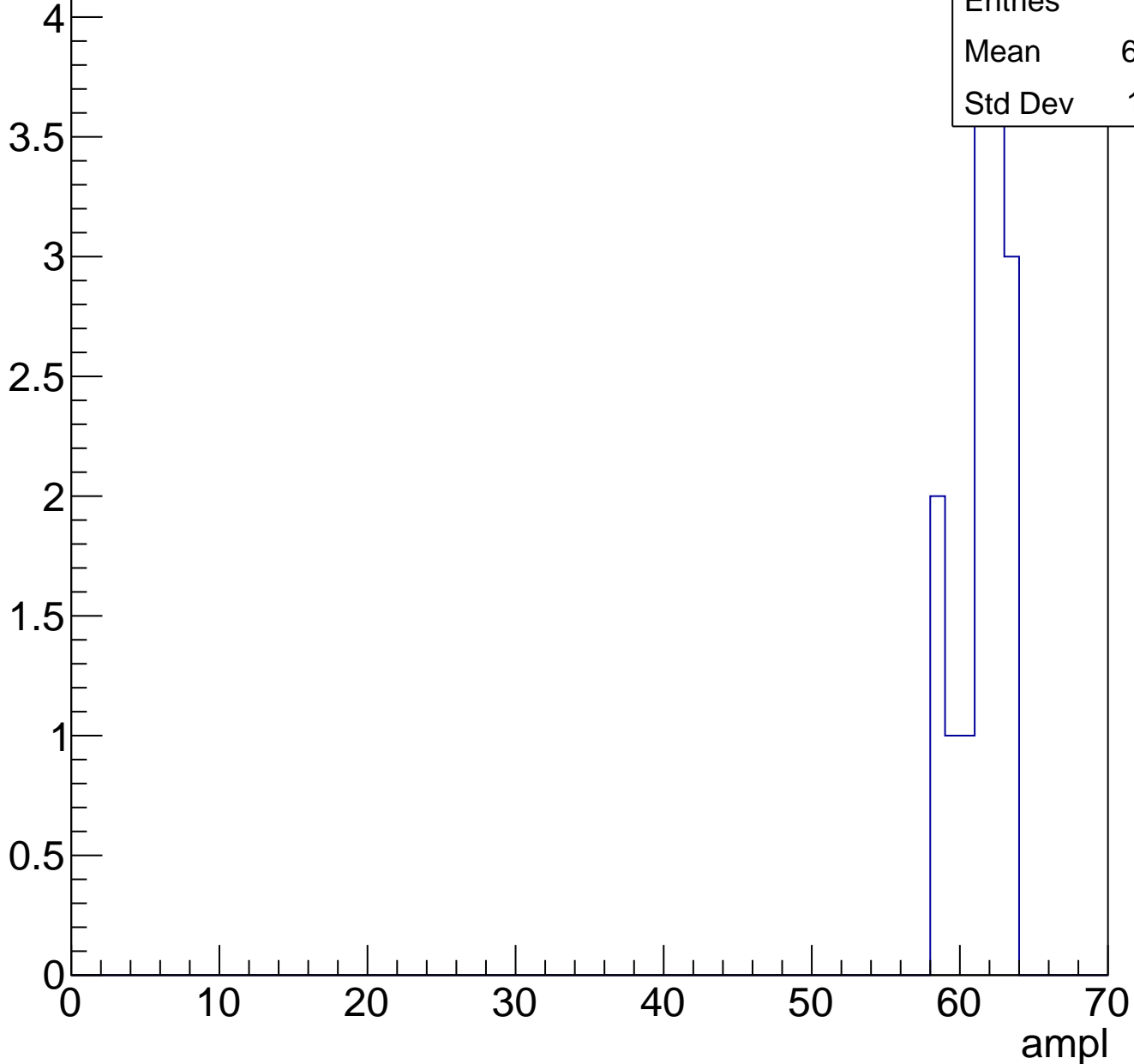
60

70

B1L102S, U10-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch42, adc0

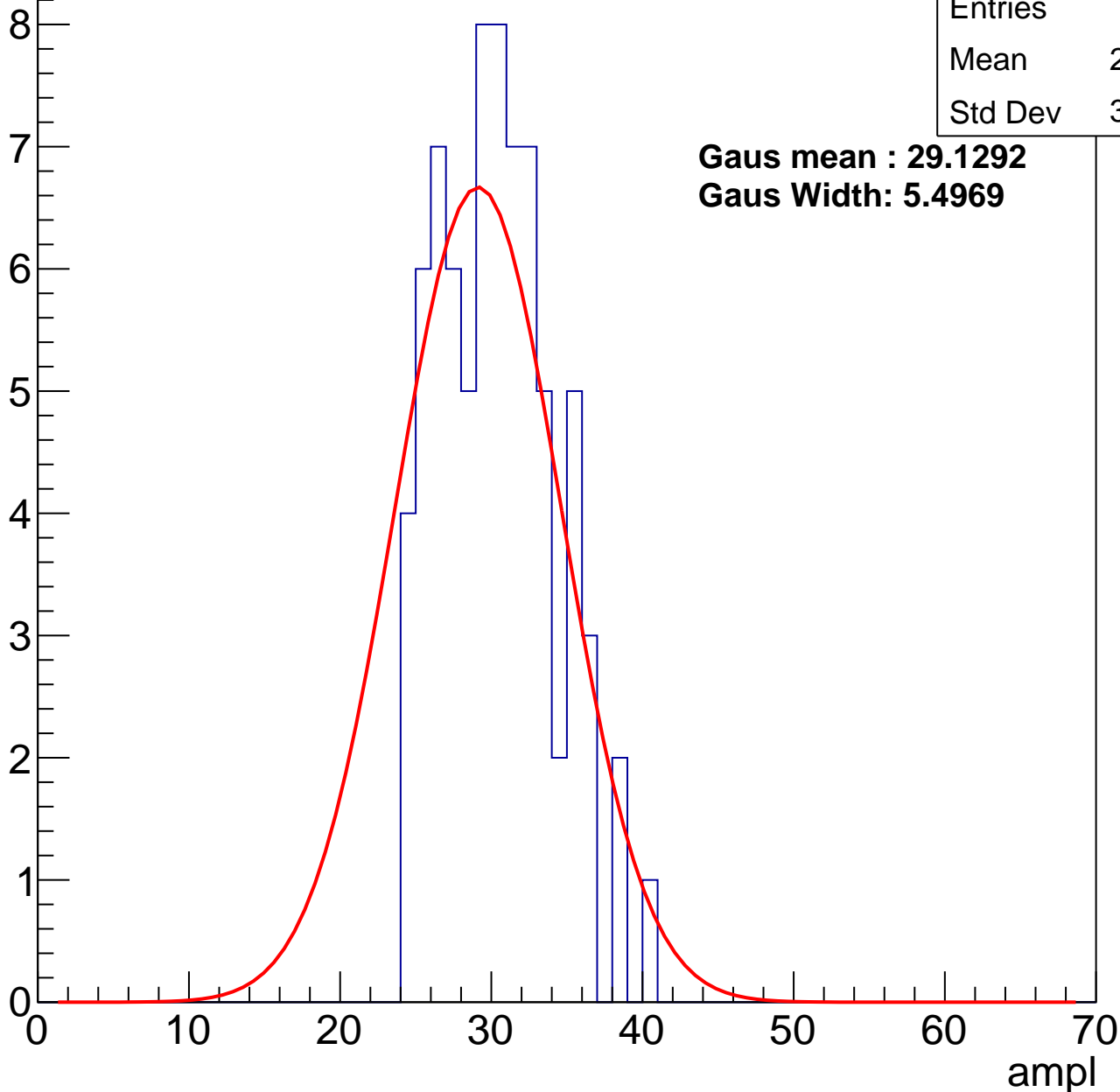
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.93
Std Dev	3.736

Gaus mean : 29.1292

Gaus Width: 5.4969



B1L102S, U10-ch42, adc1

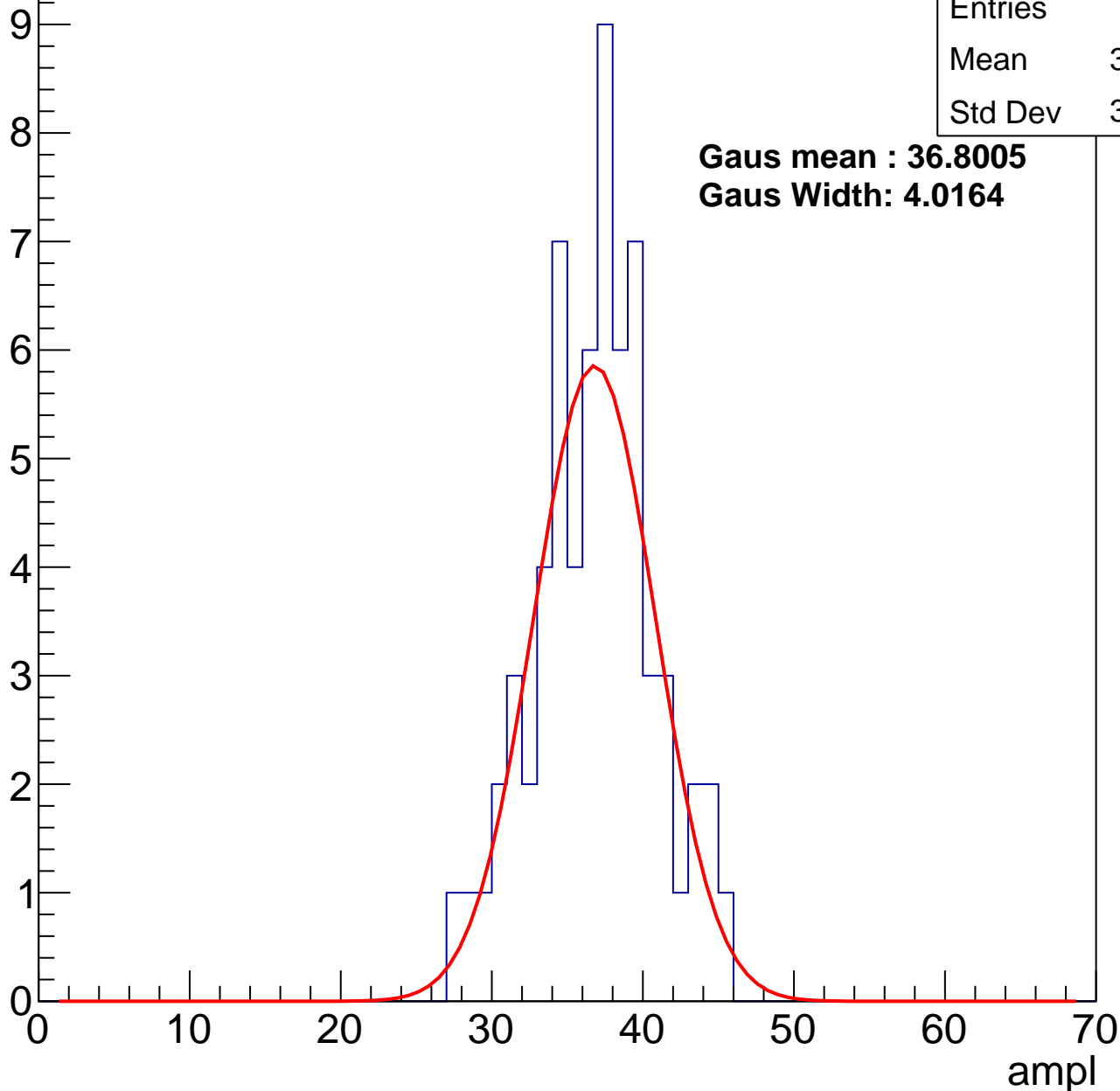
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.38
Std Dev	3.909

Gaus mean : 36.8005

Gaus Width: 4.0164



B1L102S, U10-ch42, adc2

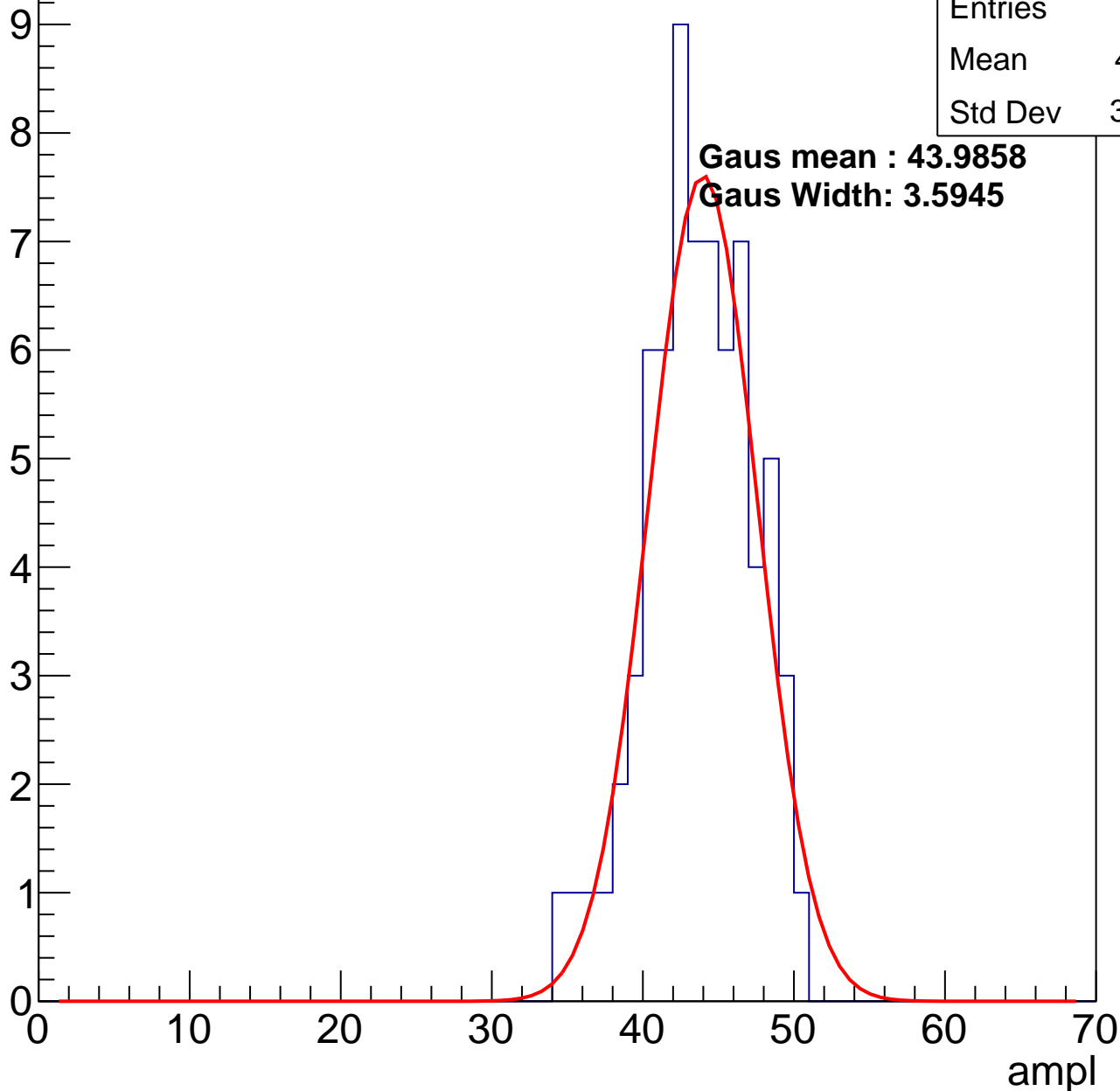
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.21
Std Dev	3.488

Gaus mean : 43.9858

Gaus Width: 3.5945

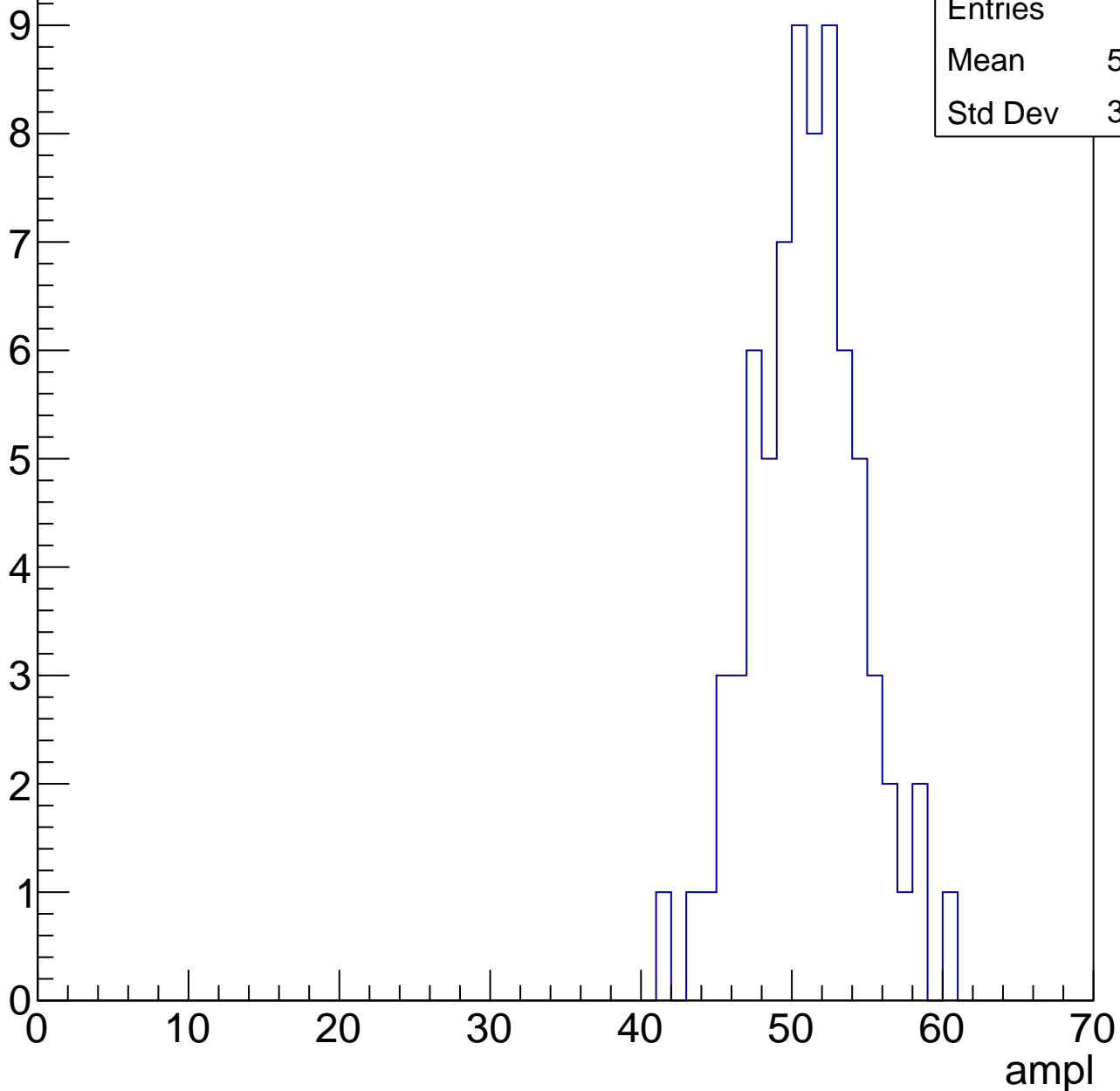


B1L102S, U10-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

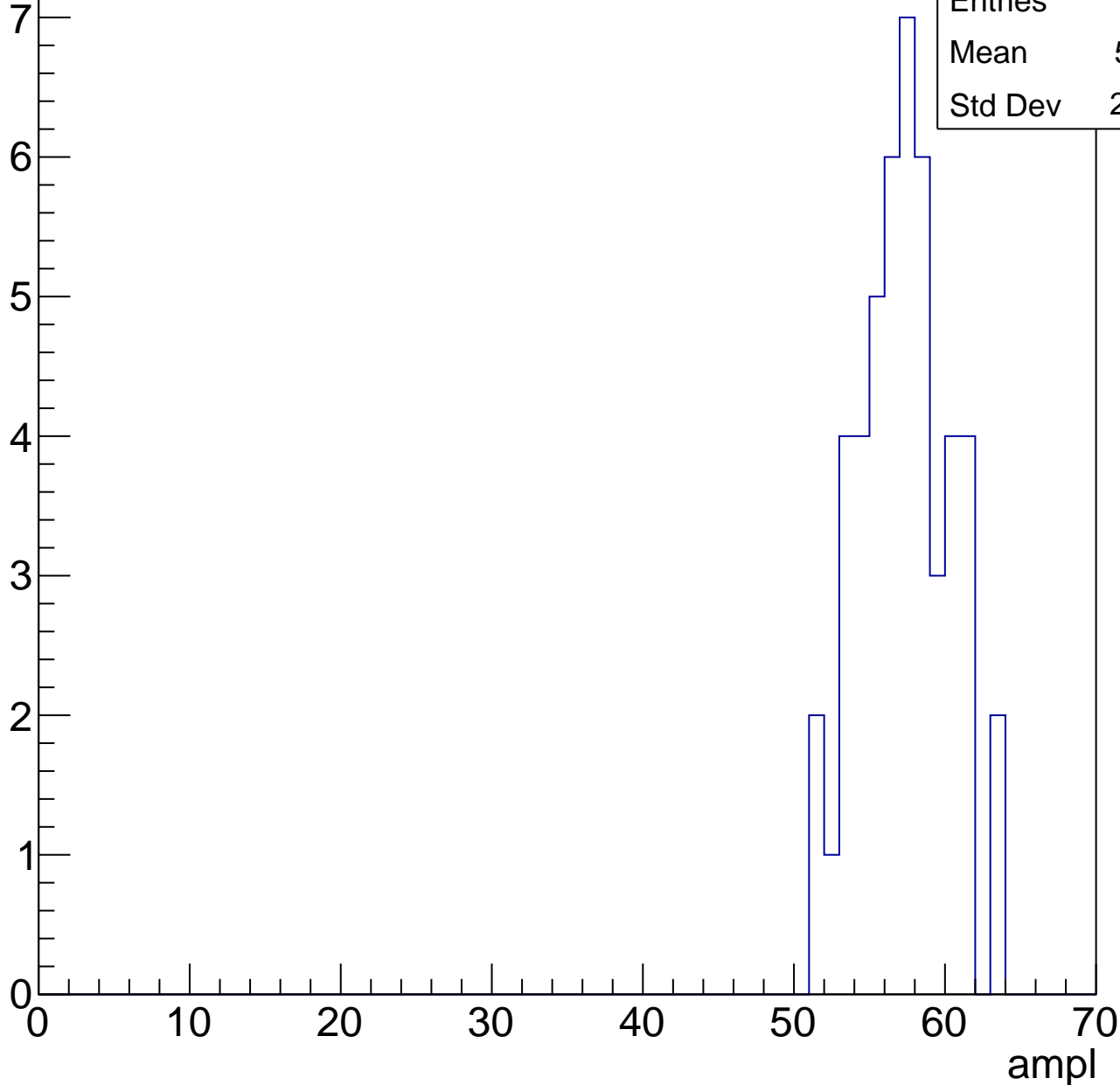
Entries	73
Mean	50.55
Std Dev	3.608



B1L102S, U10-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

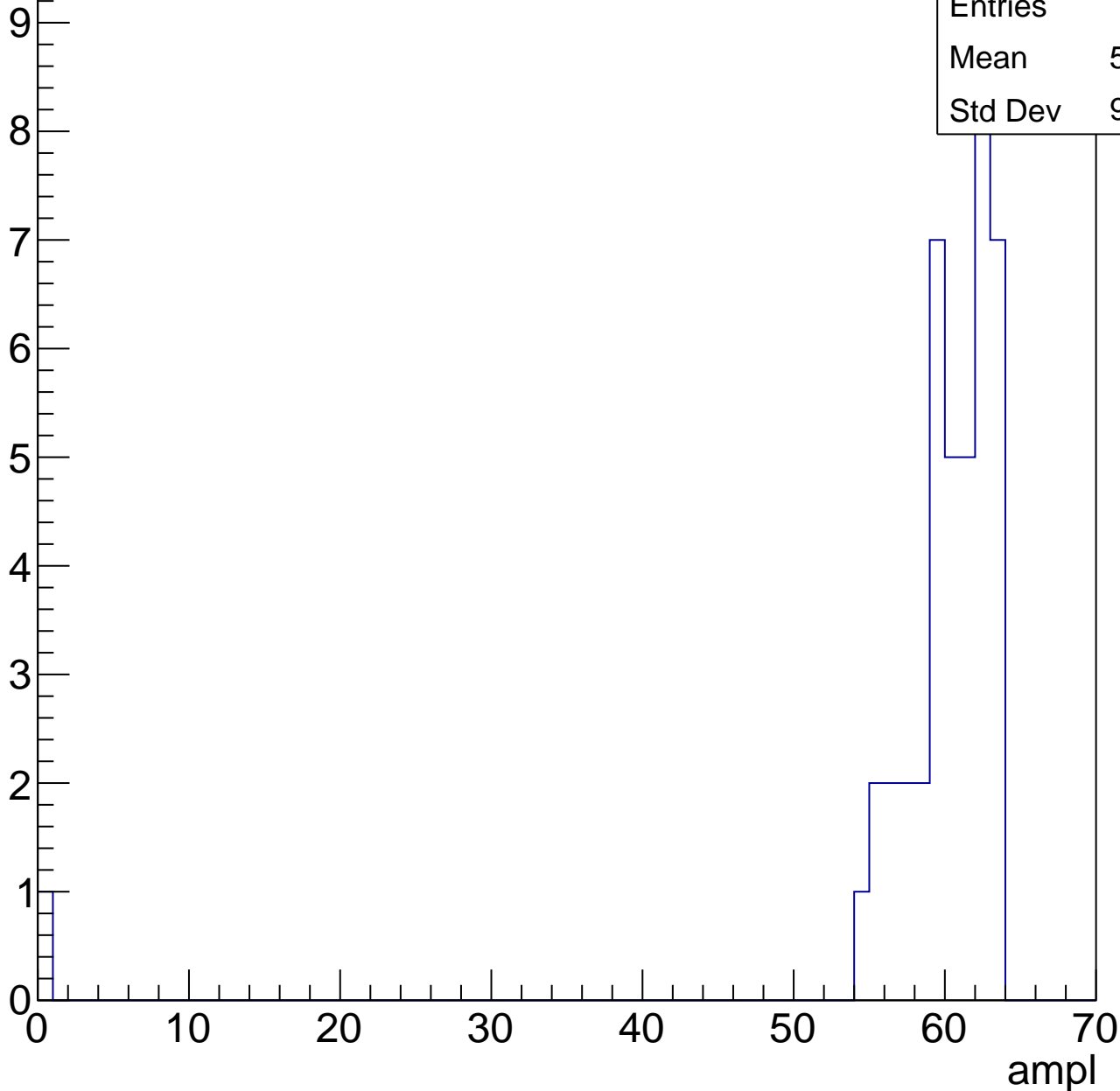


B1L102S, U10-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.67
Std Dev	9.375



B1L102S, U10-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch43, adc0

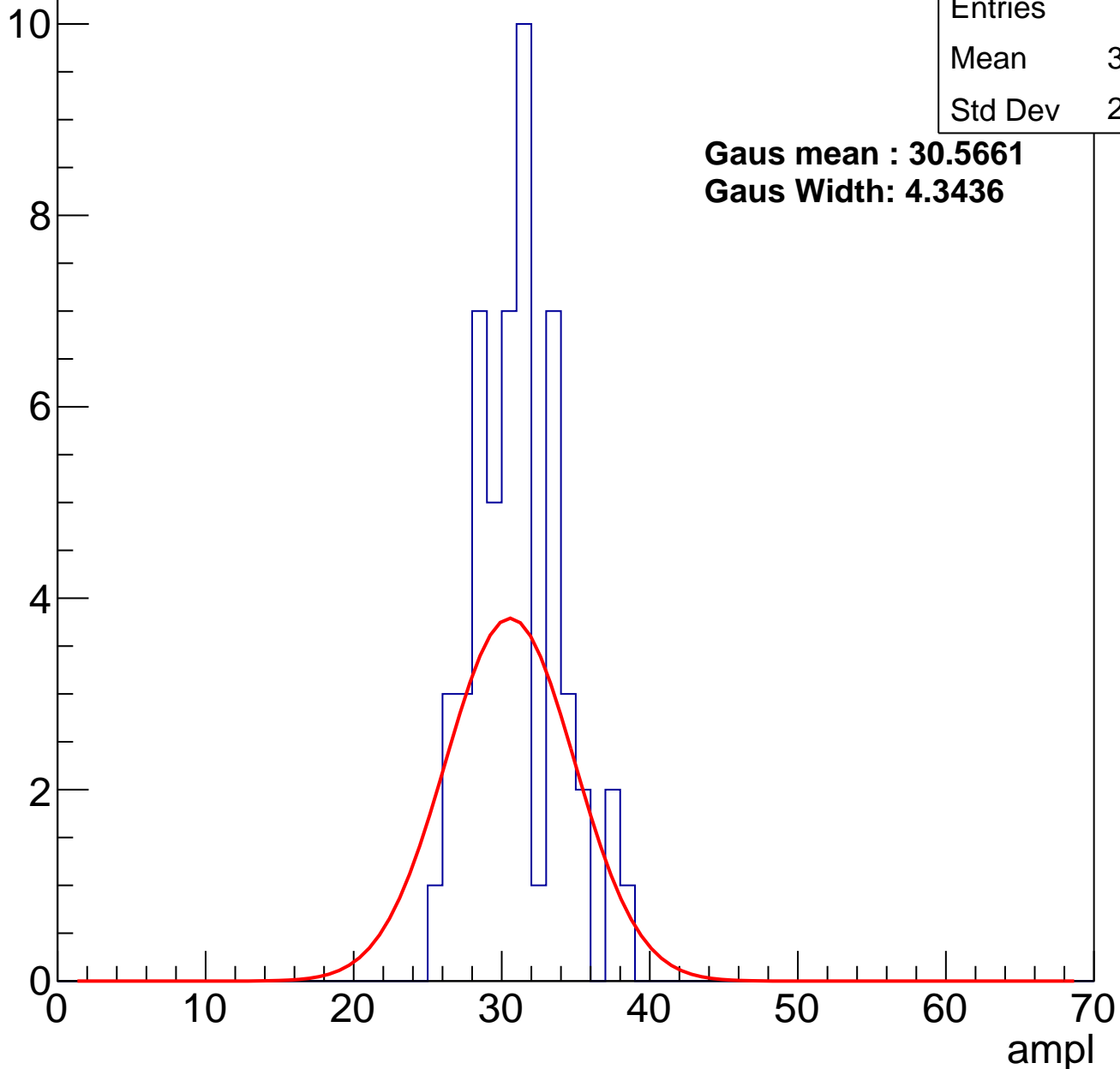
calib_packv5_042523_0143.root, FC#11, port A2

Entries	52
Mean	30.62
Std Dev	2.943

Gaus mean : 30.5661

Gaus Width: 4.3436

Entry



B1L102S, U10-ch43, adc1

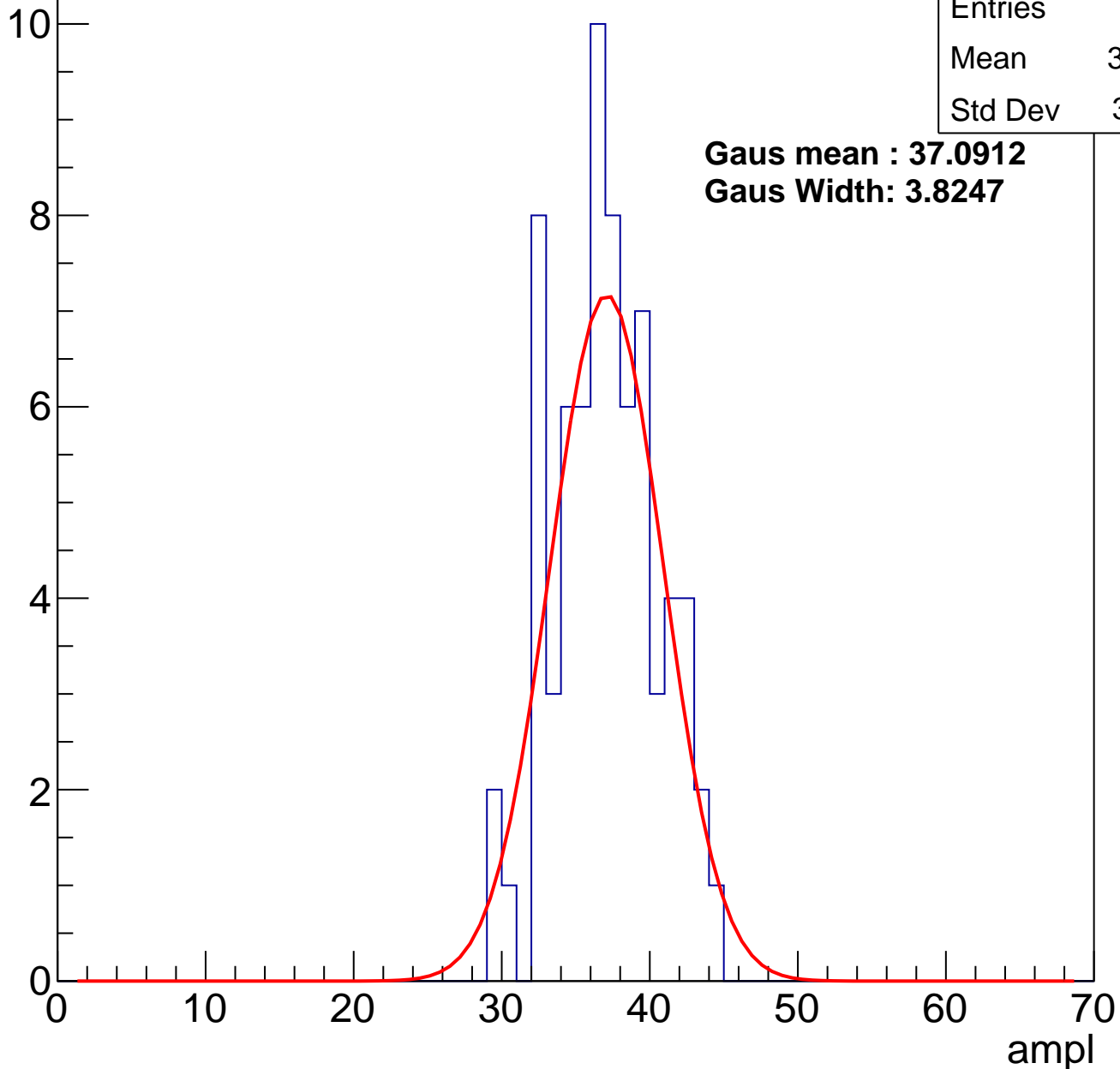
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	36.56
Std Dev	3.451

Gaus mean : 37.0912

Gaus Width: 3.8247

Entry



B1L102S, U10-ch43, adc2

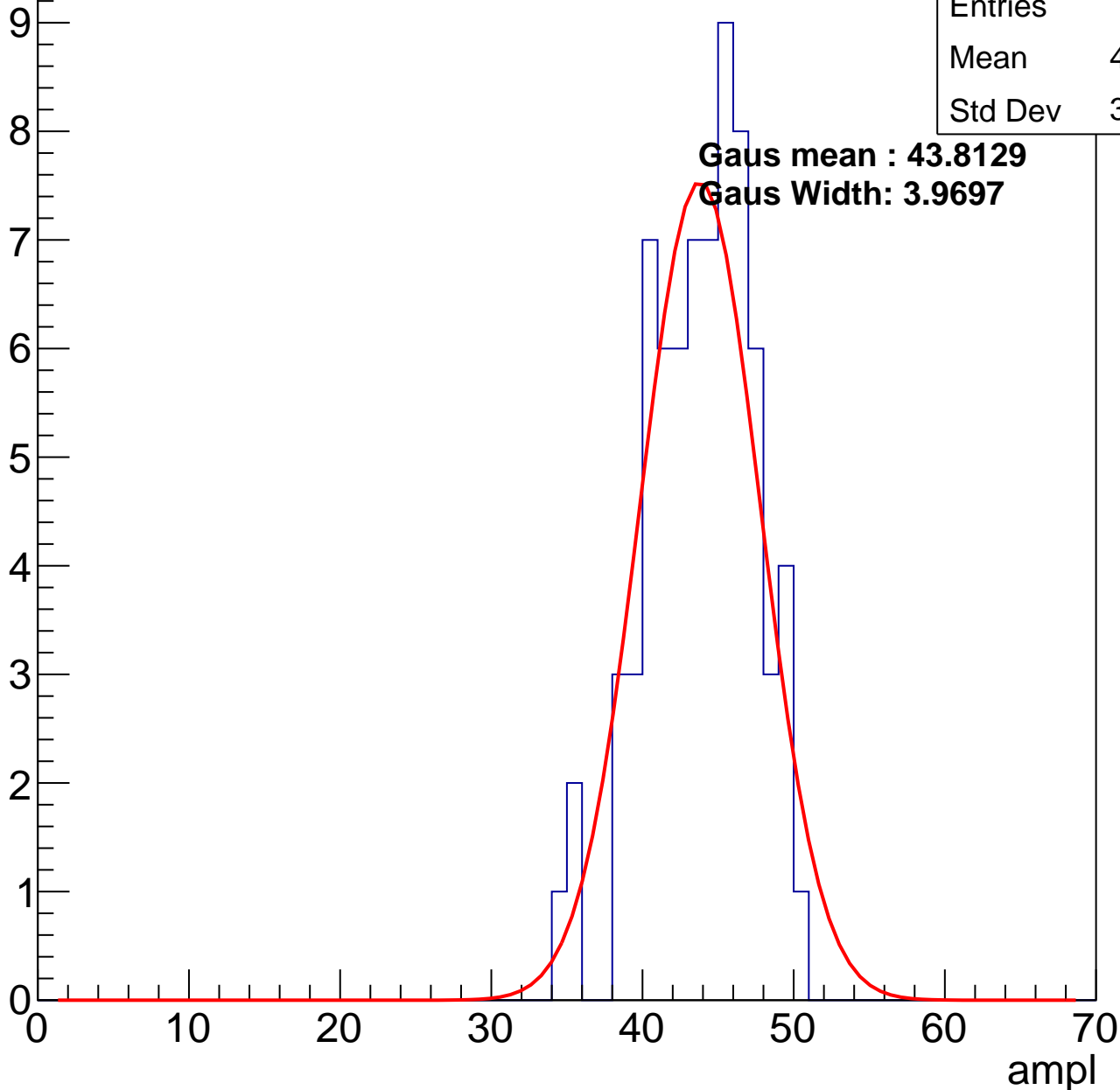
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.38
Std Dev	3.498

Gaus mean : 43.8129

Gaus Width: 3.9697

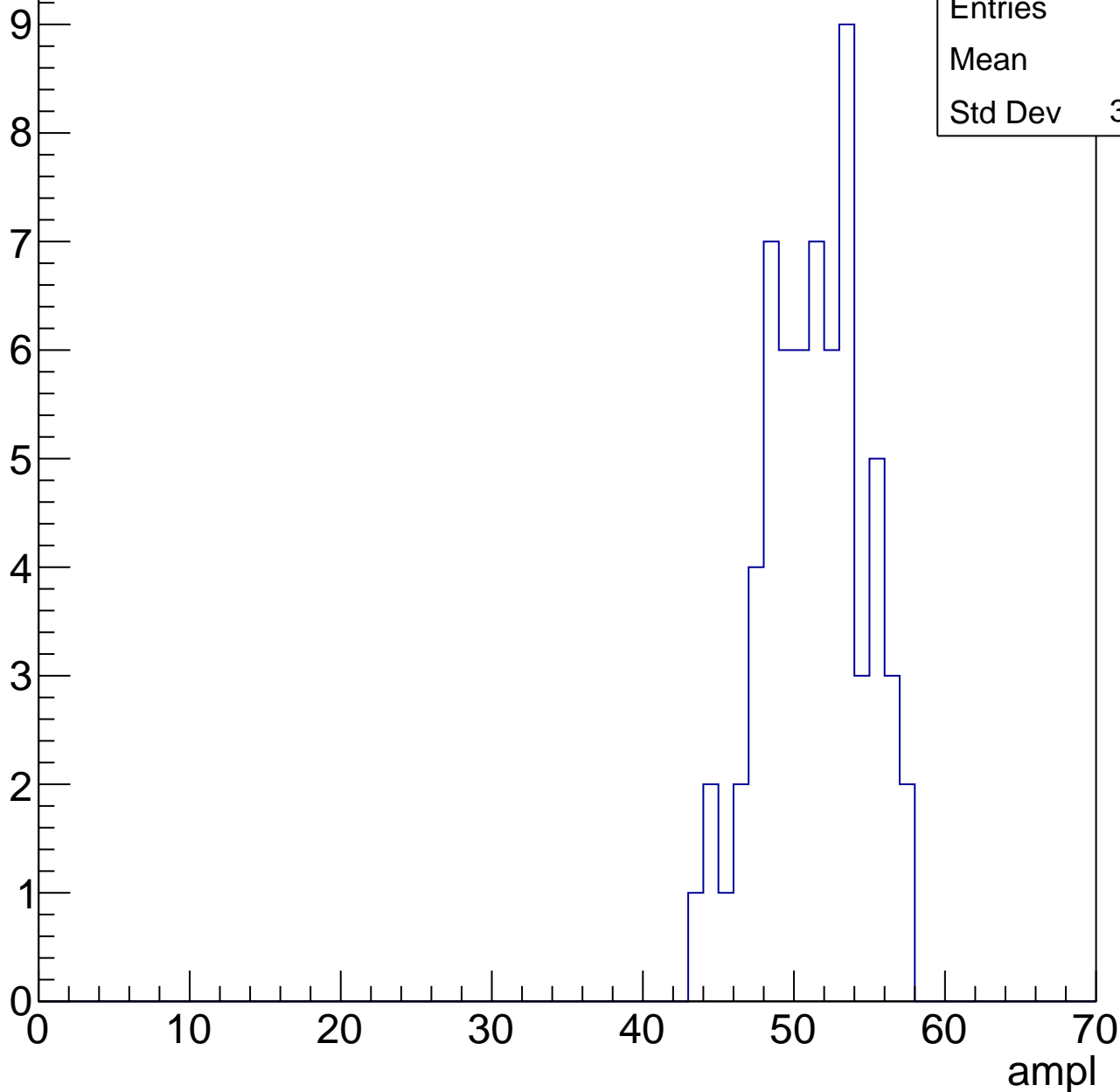


B1L102S, U10-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.8
Std Dev	3.303

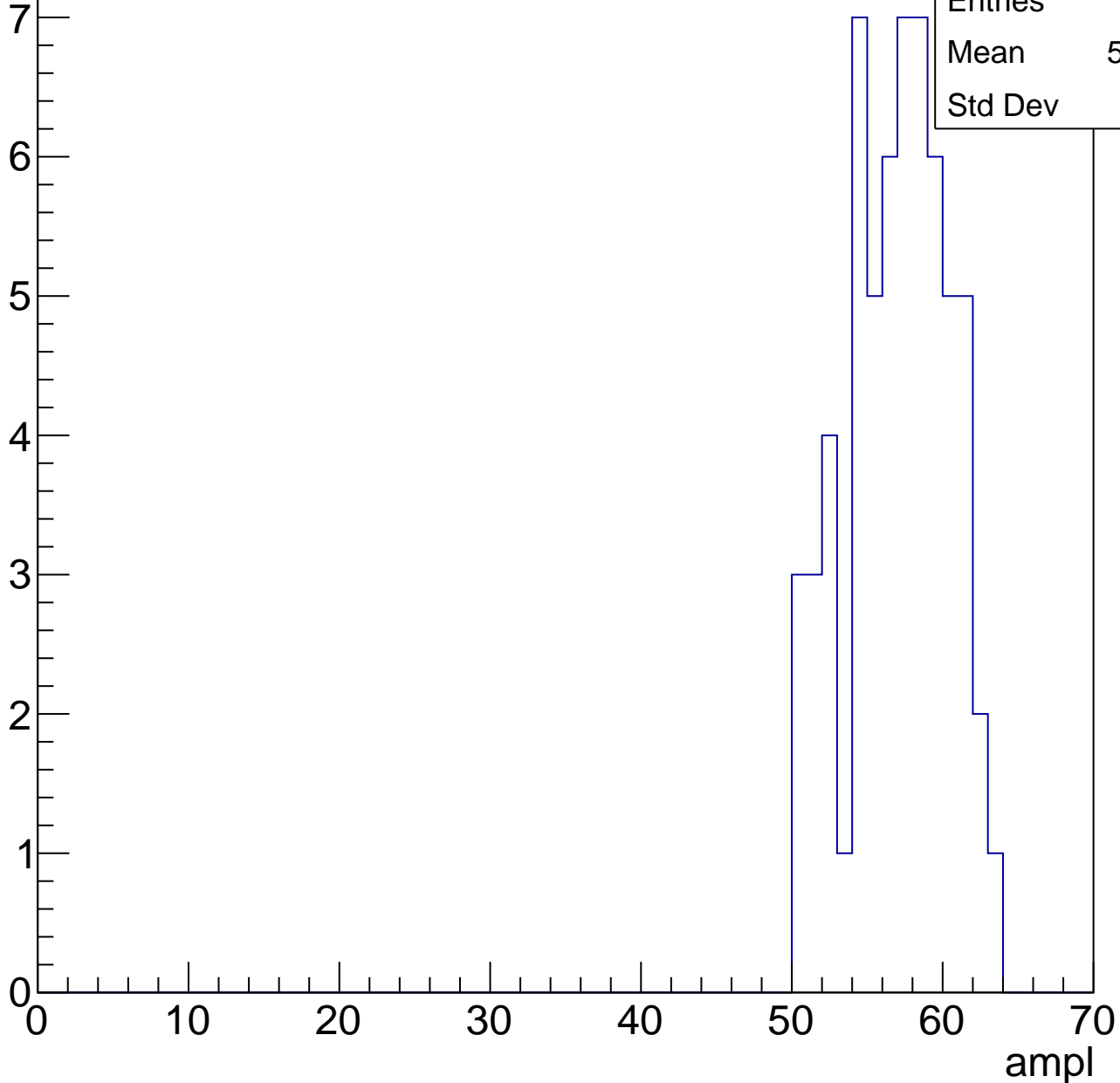


B1L102S, U10-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.52
Std Dev	3.33

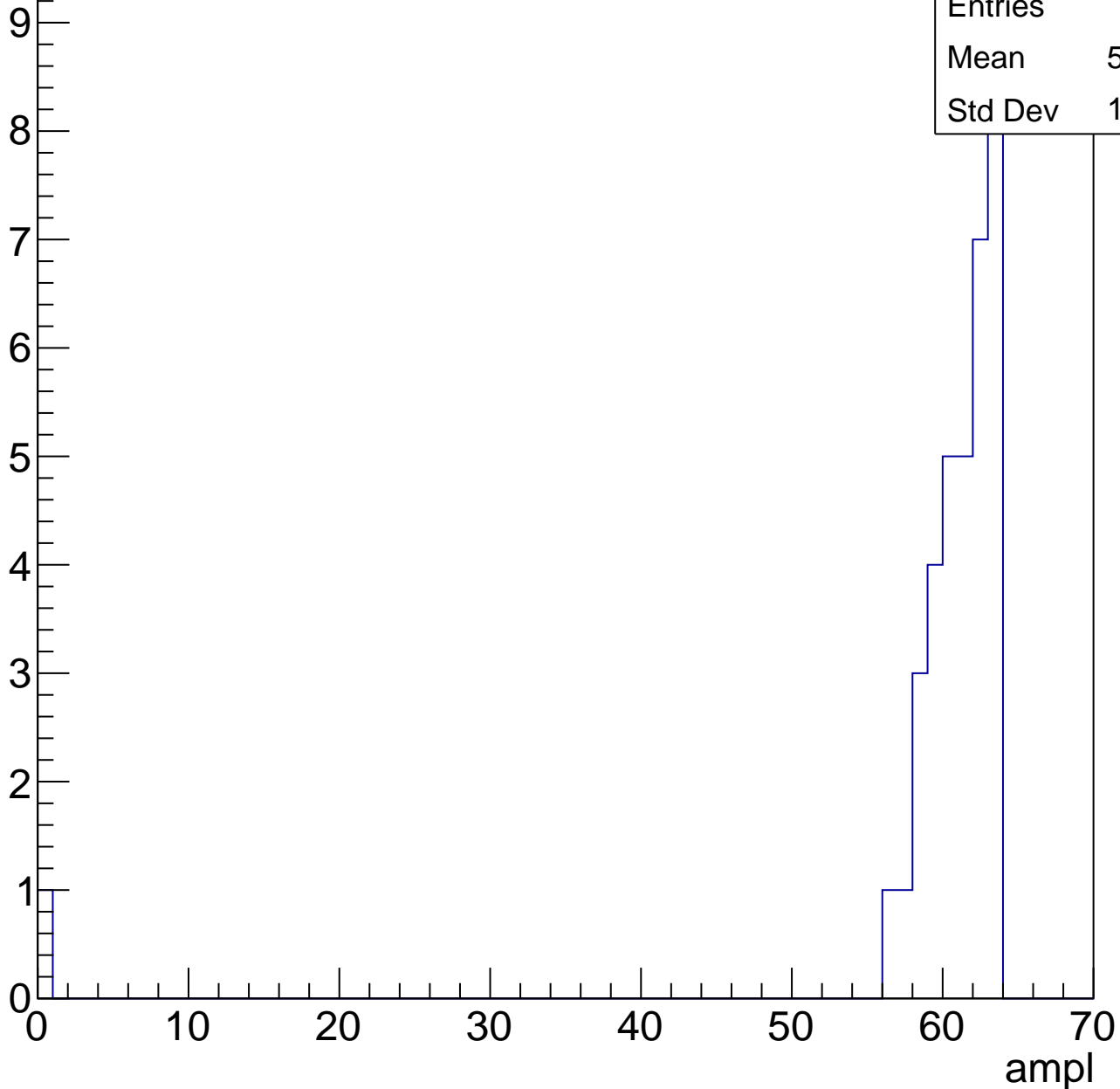


B1L102S, U10-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.14
Std Dev	10.18



B1L102S, U10-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L102S, U10-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch44, adc0

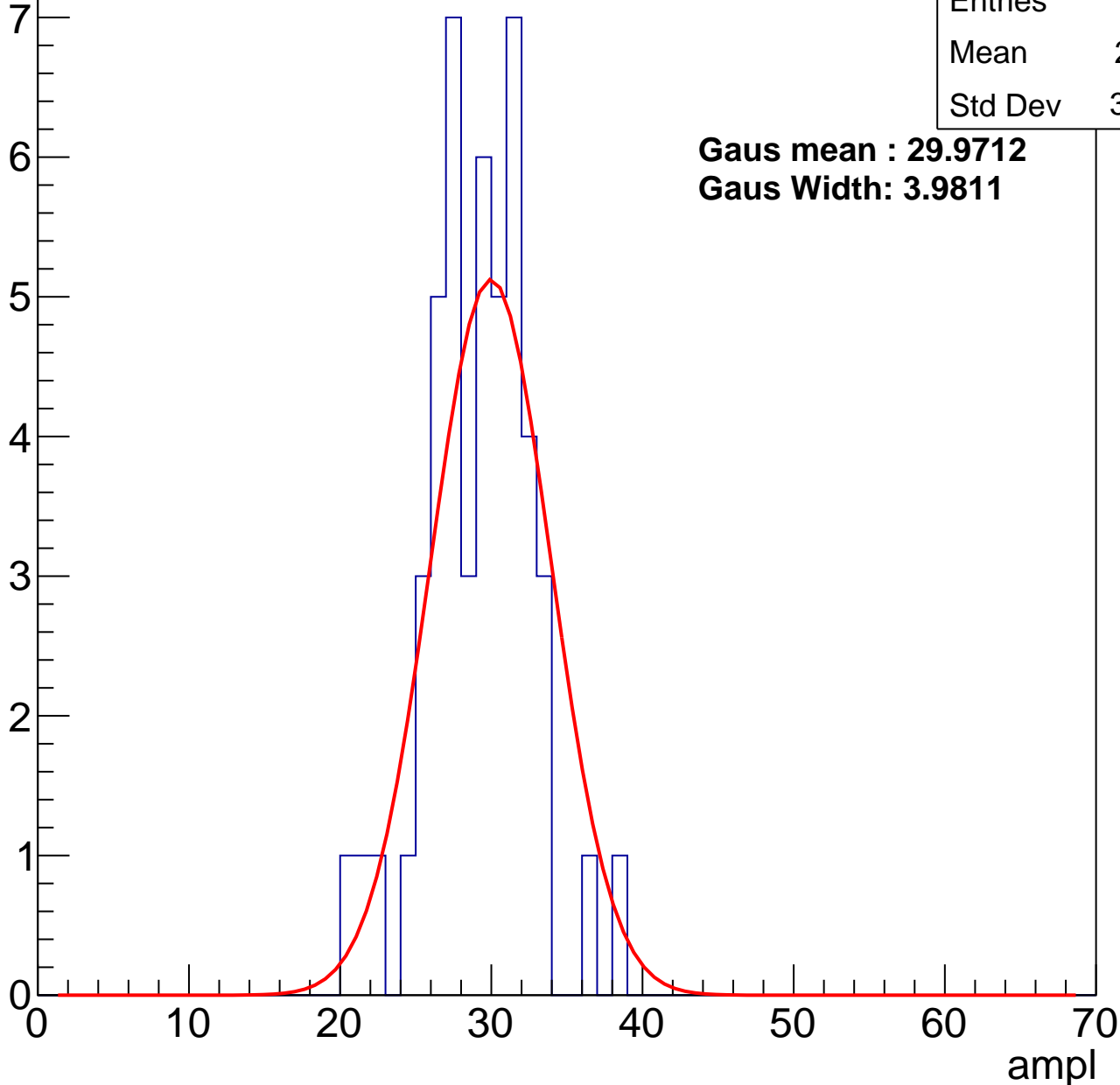
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	28.71
Std Dev	3.458

Gaus mean : 29.9712

Gaus Width: 3.9811



B1L102S, U10-ch44, adc1

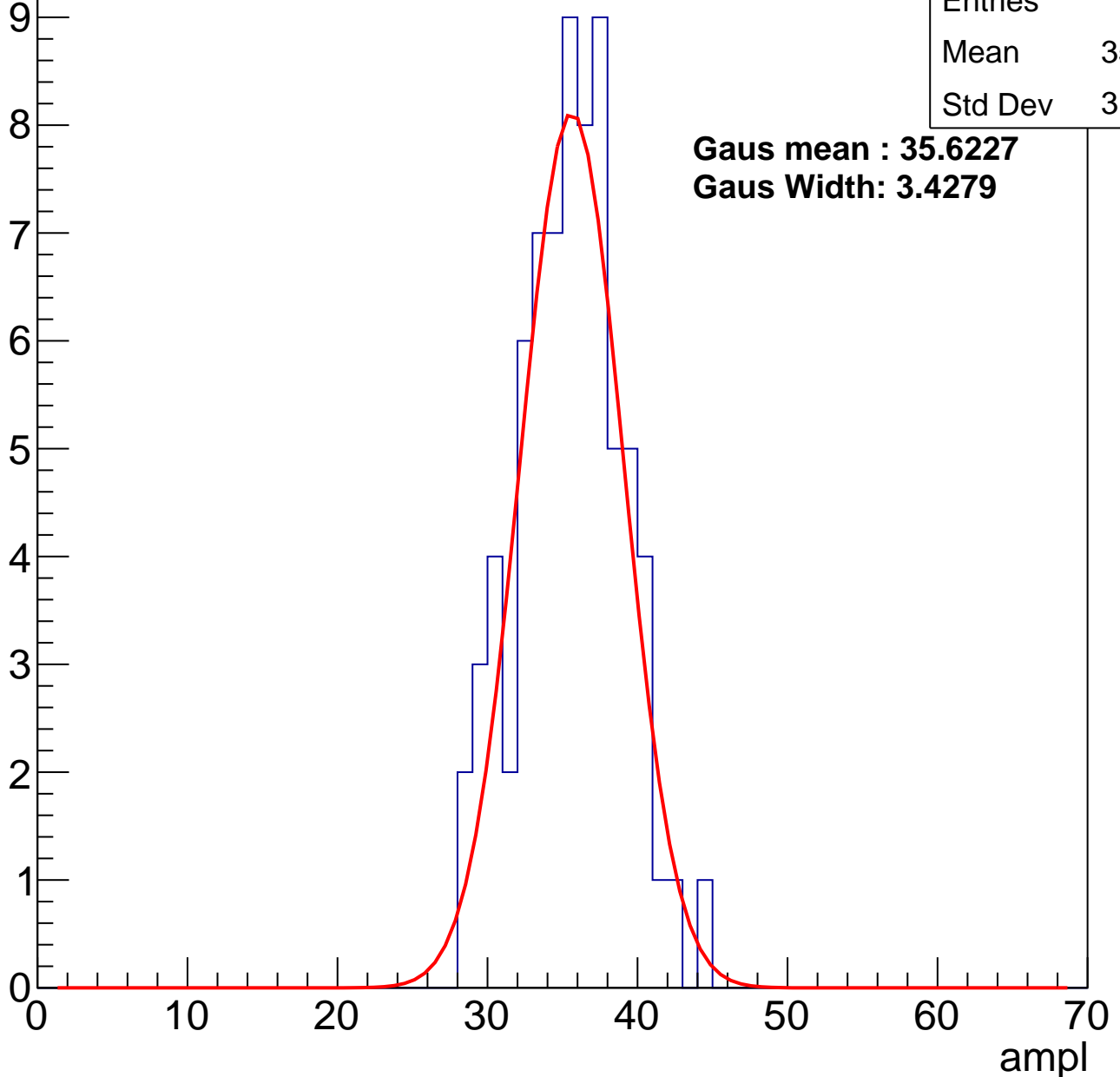
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.05
Std Dev	3.413

Gaus mean : 35.6227

Gaus Width: 3.4279



B1L102S, U10-ch44, adc2

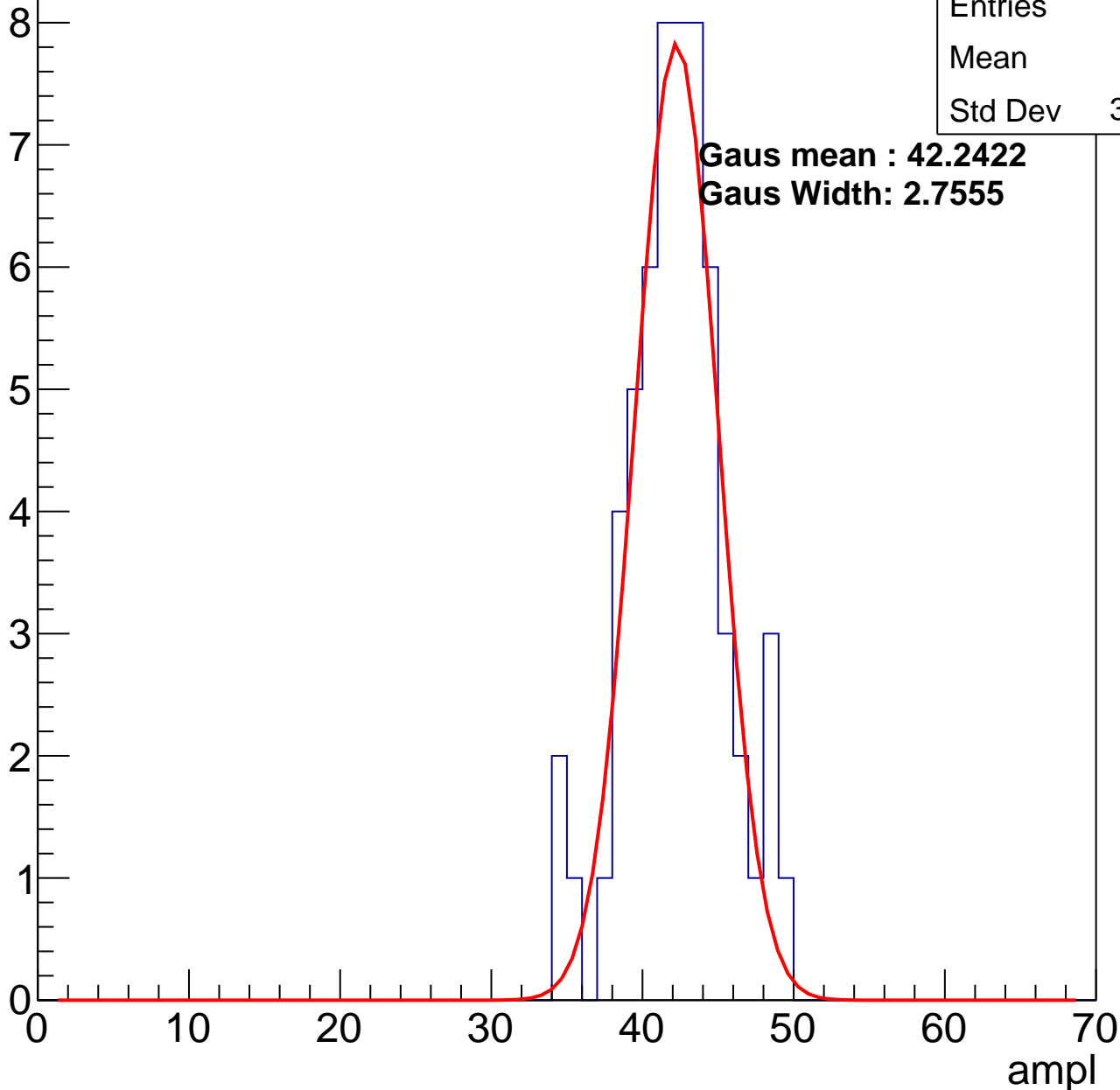
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.8
Std Dev	3.225

Gaus mean : 42.2422

Gaus Width: 2.7555

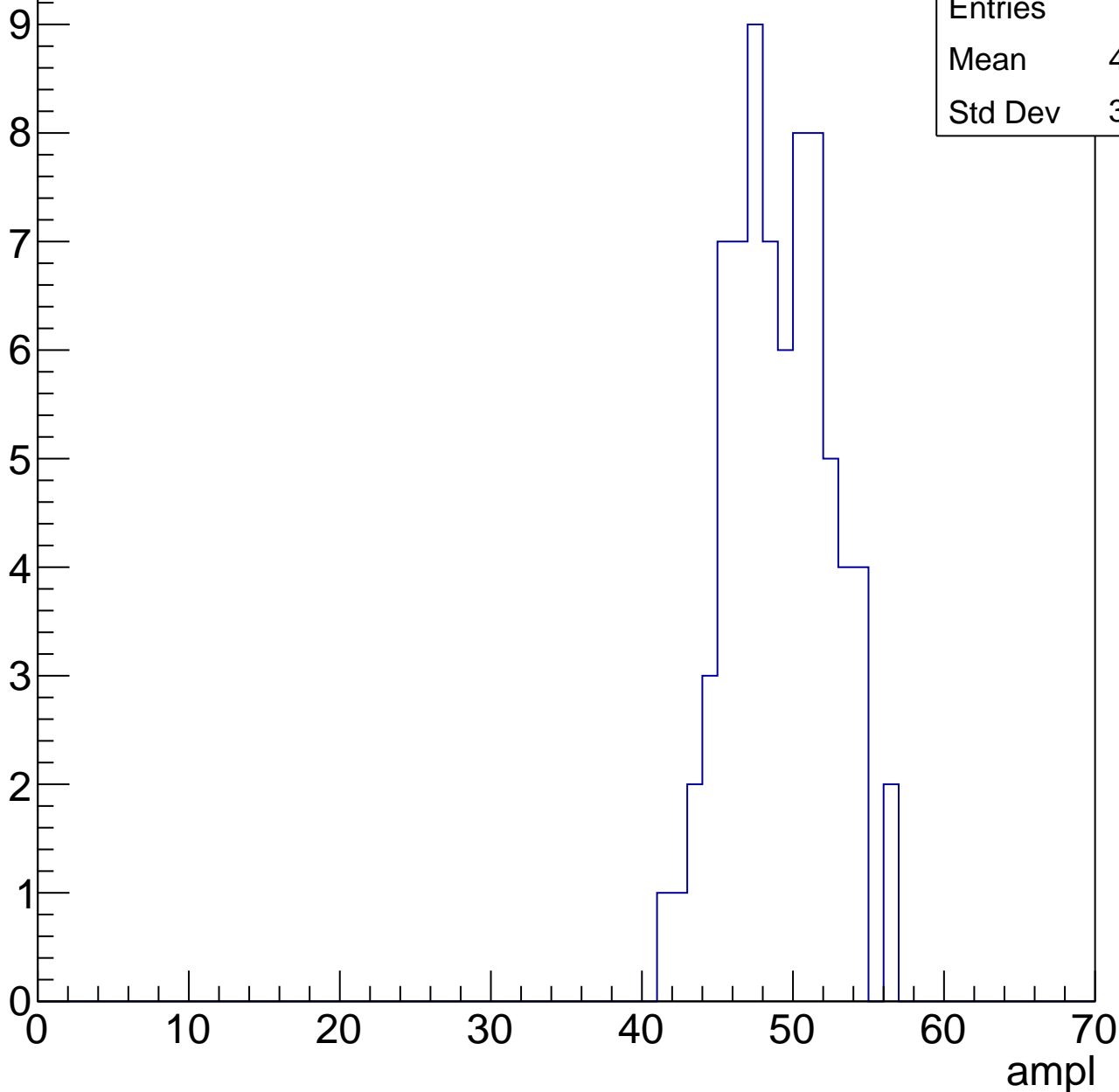


B1L102S, U10-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.64
Std Dev	3.319

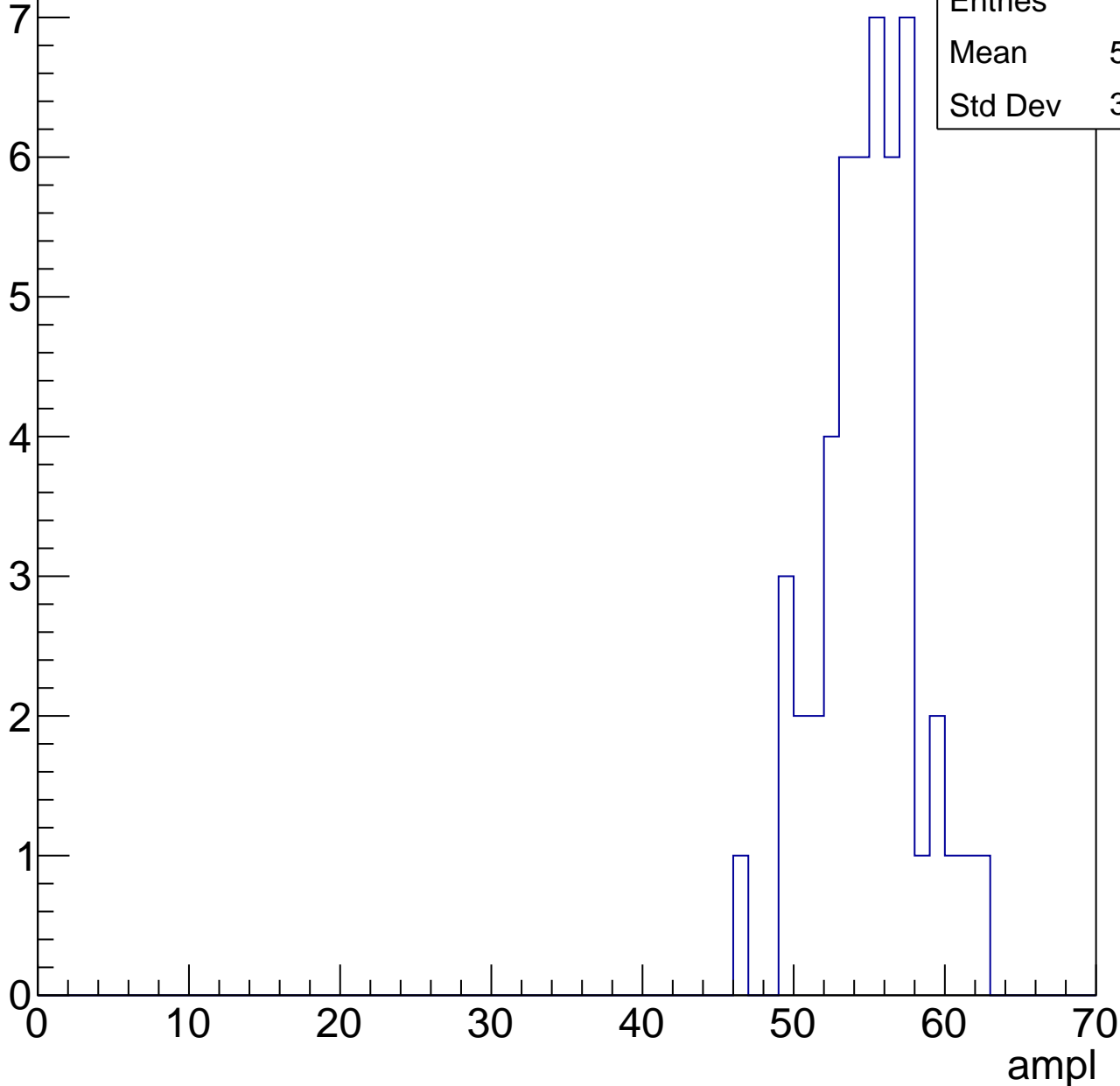


B1L102S, U10-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.48
Std Dev	3.189

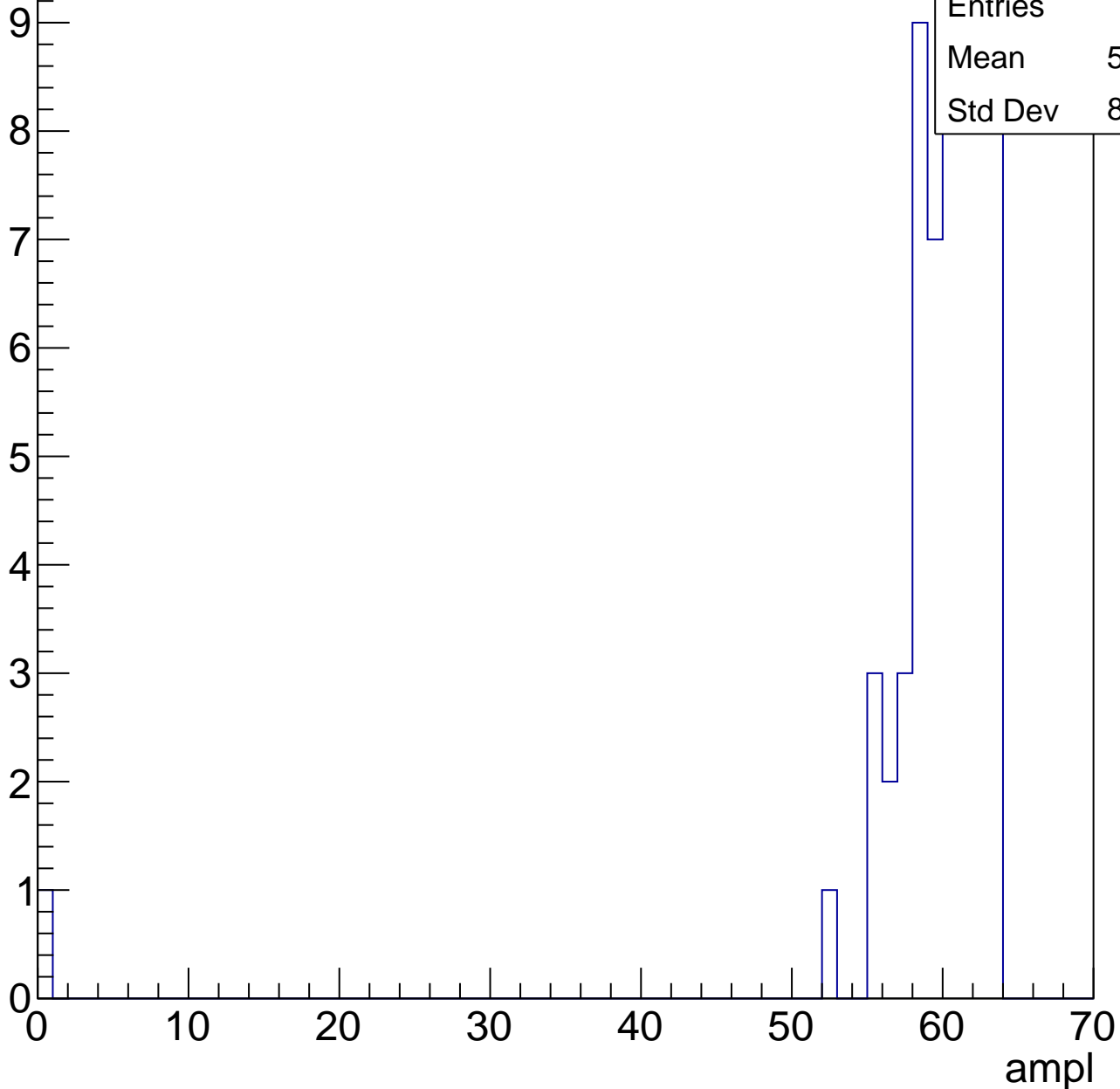


B1L102S, U10-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

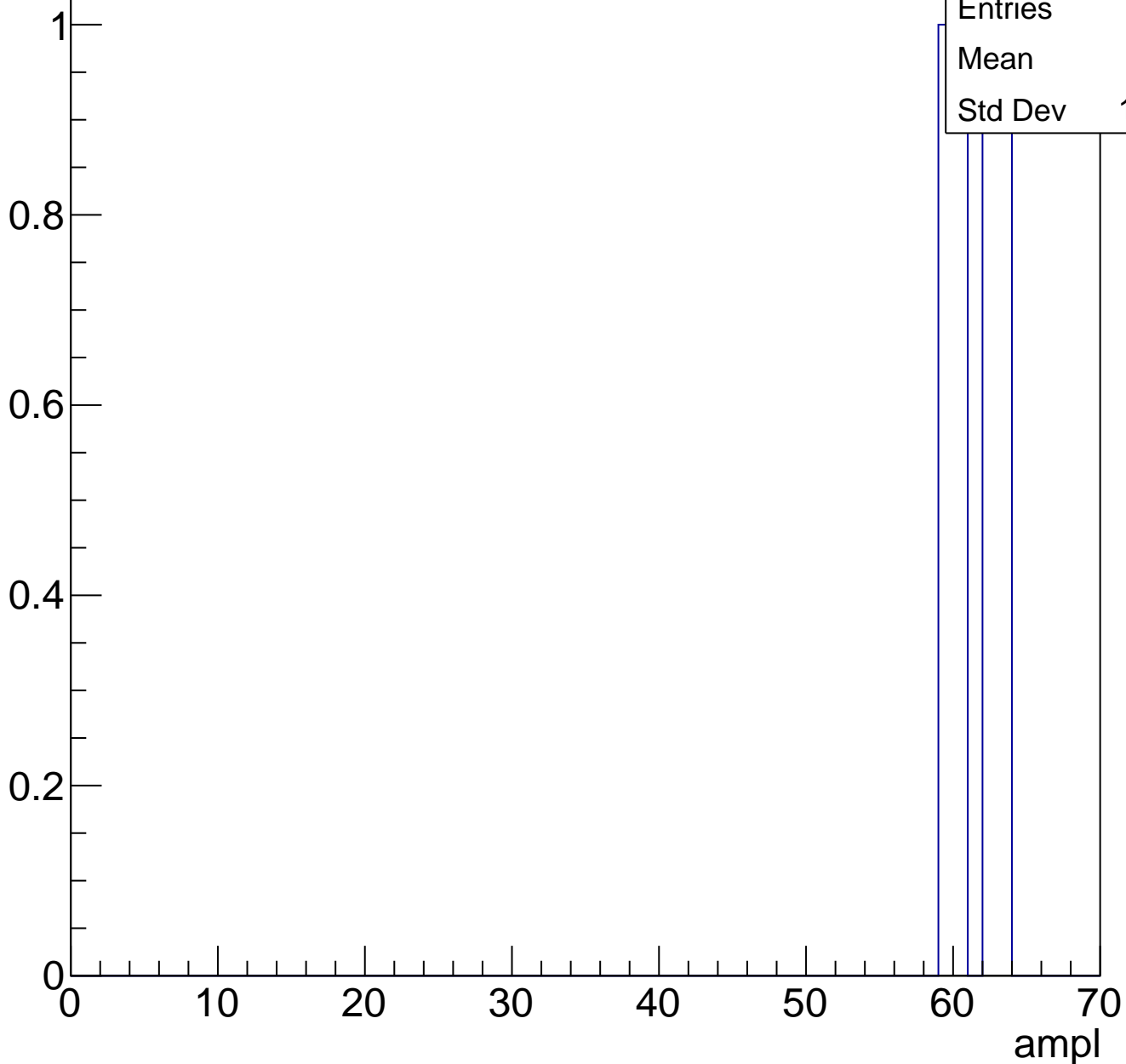
Entries	60
Mean	58.78
Std Dev	8.036



B1L102S, U10-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch45, adc0

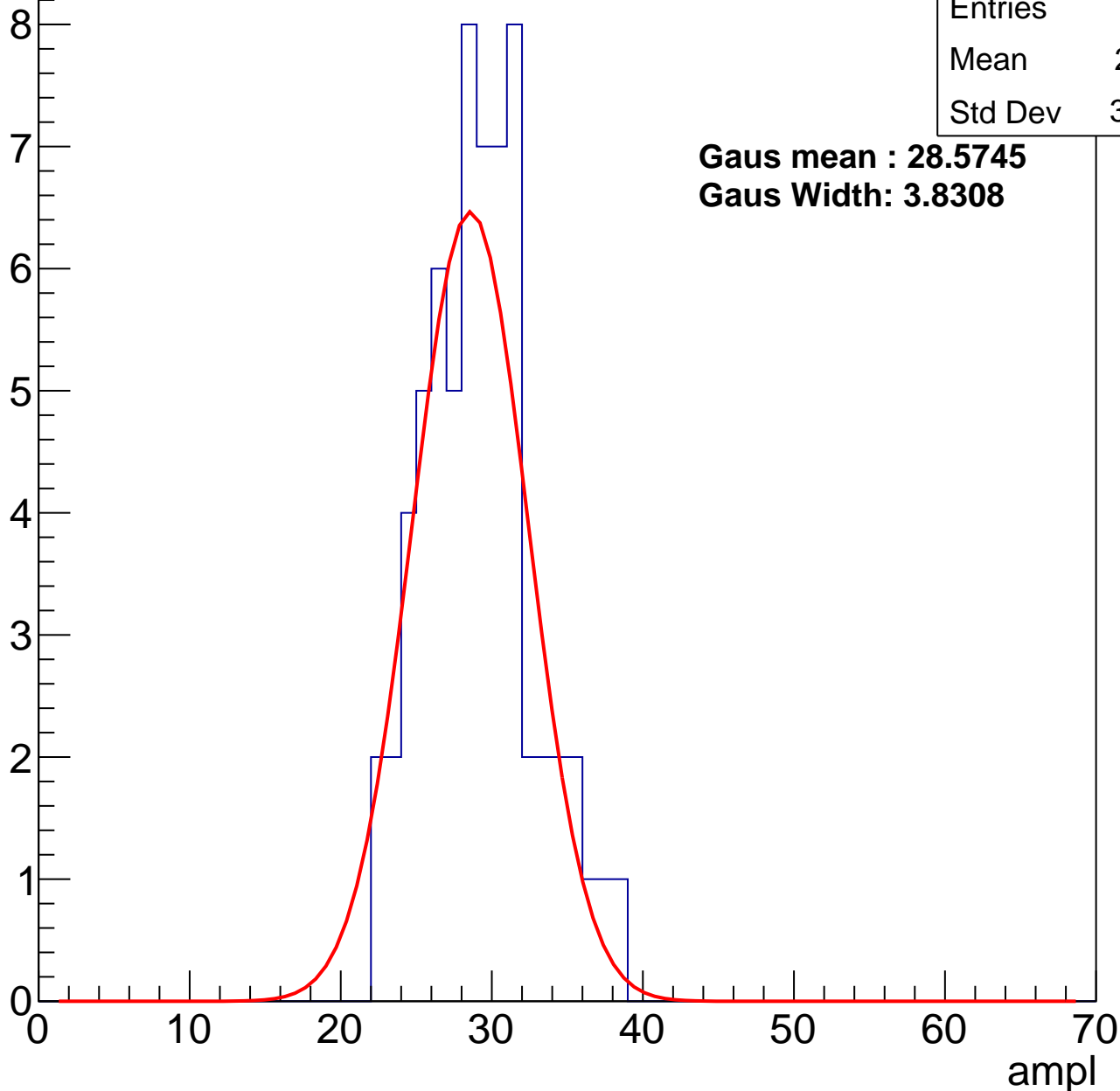
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.71
Std Dev	3.577

Gaus mean : 28.5745

Gaus Width: 3.8308



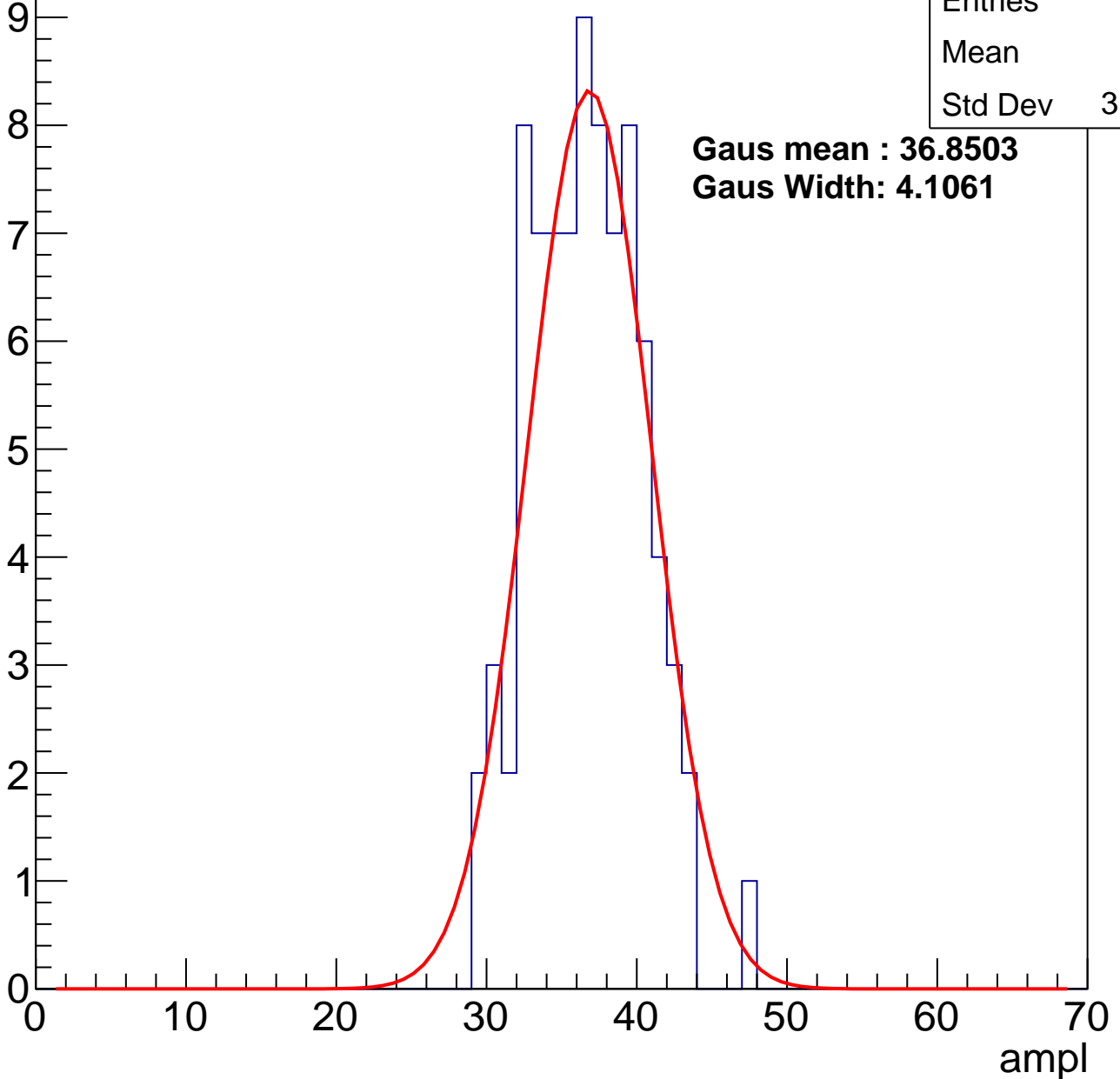
B1L102S, U10-ch45, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	36.2
Std Dev	3.634

Gaus mean : 36.8503
Gaus Width: 4.1061

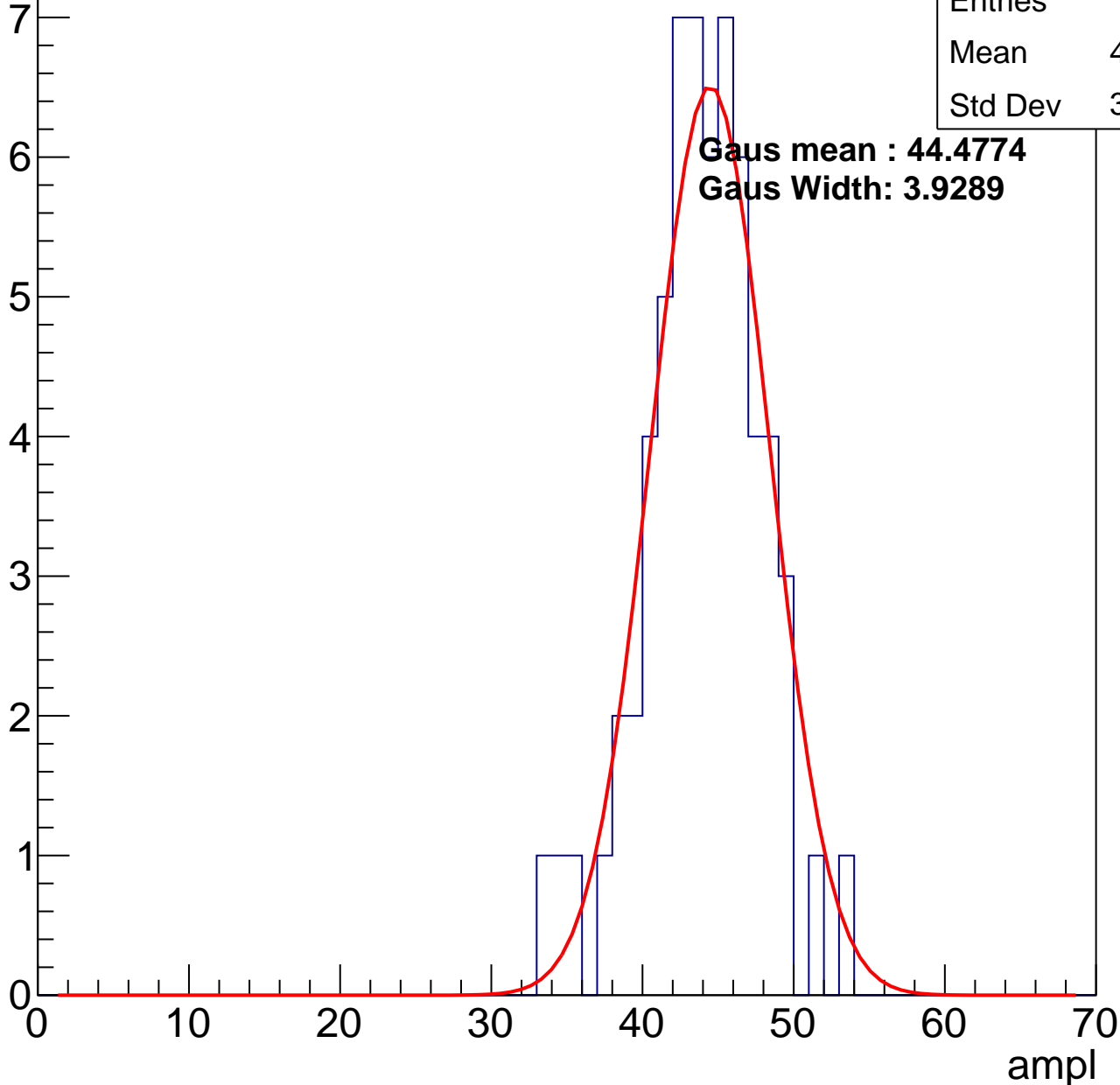


B1L102S, U10-ch45, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.48
Std Dev	3.854

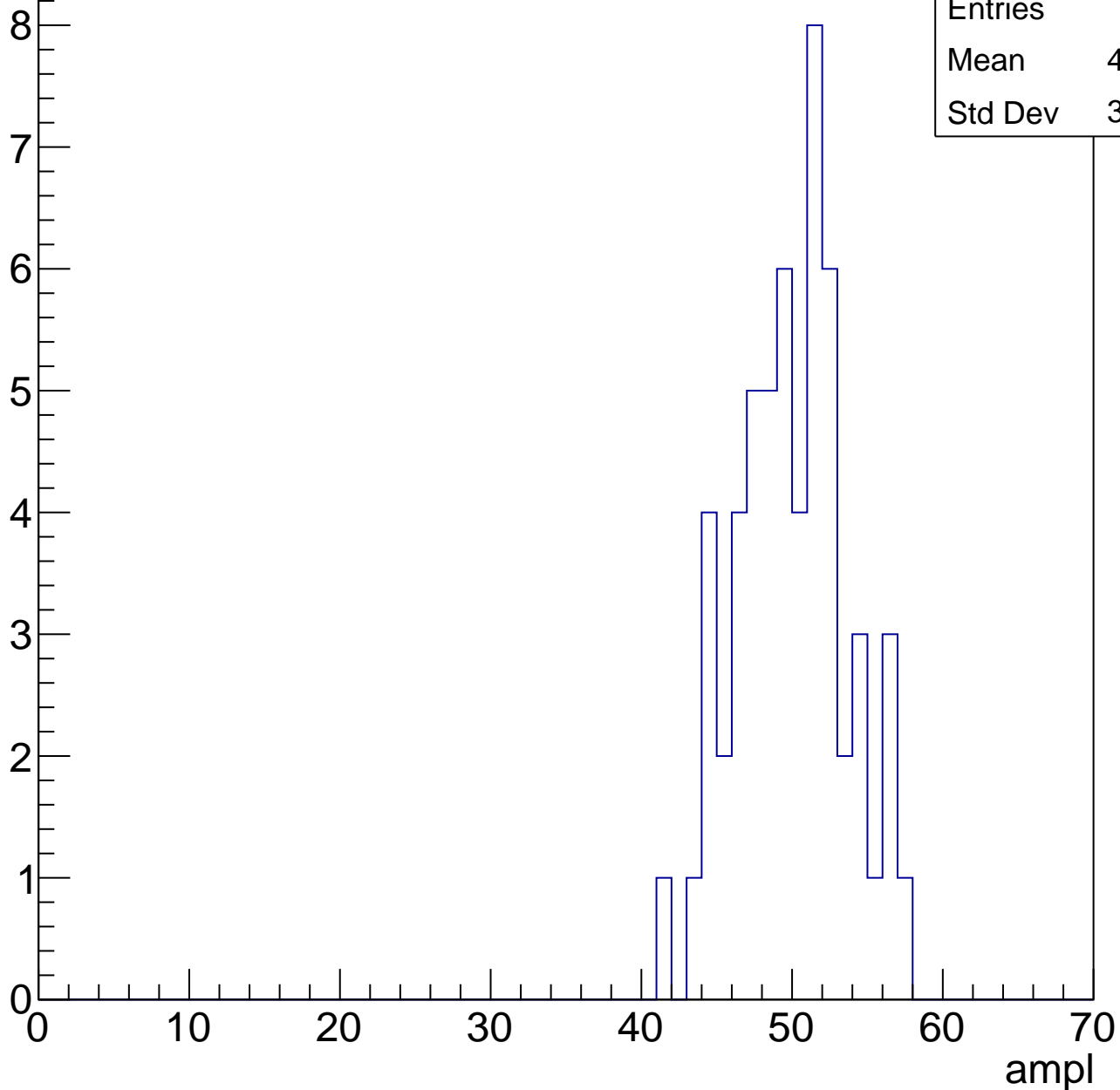


B1L102S, U10-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.48
Std Dev	3.596

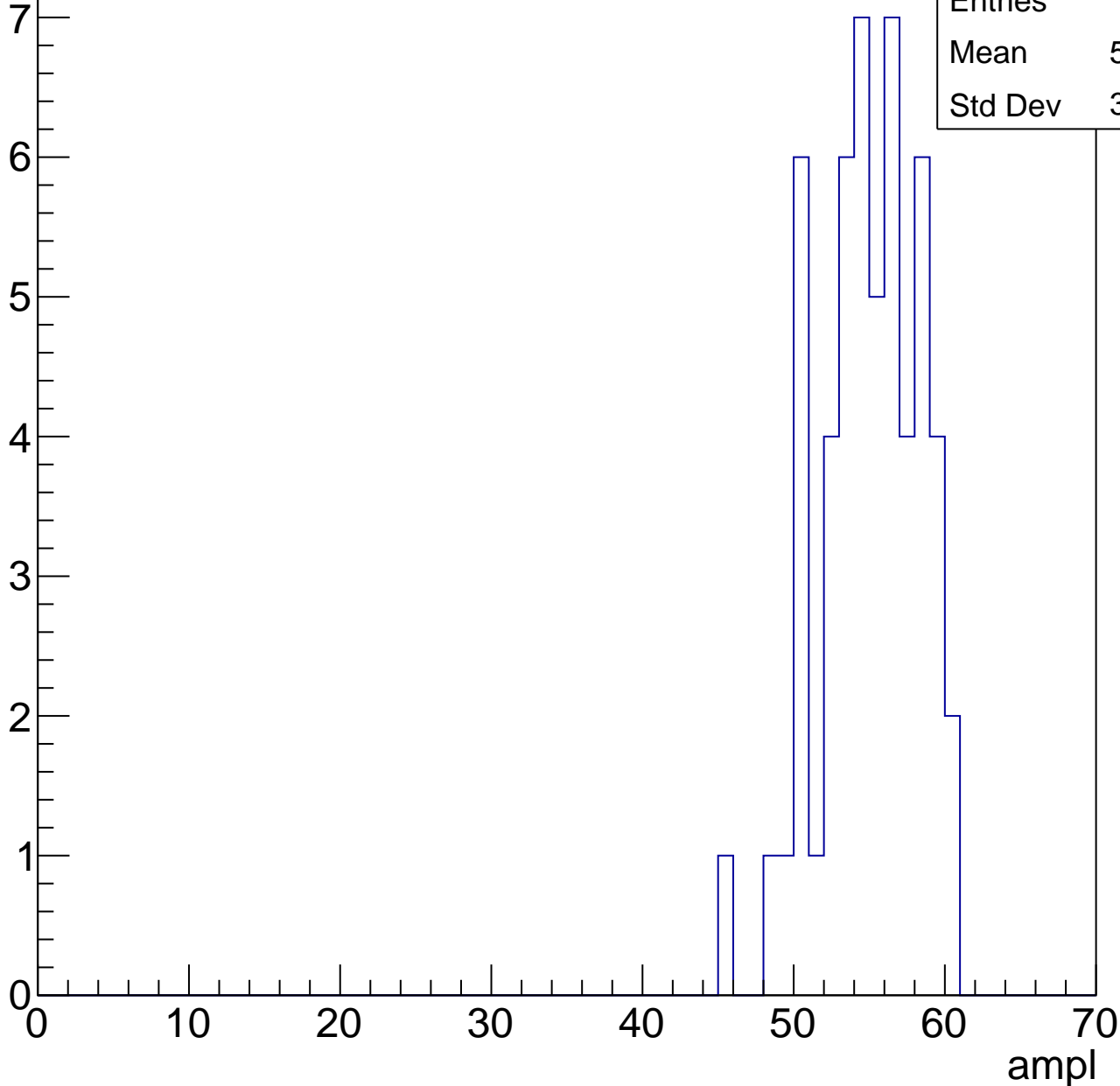


B1L102S, U10-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.47
Std Dev	3.286



B1L102S, U10-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	55
Mean	58.95
Std Dev	8.411

Entry

10

8

6

4

2

0

0

10

20

30

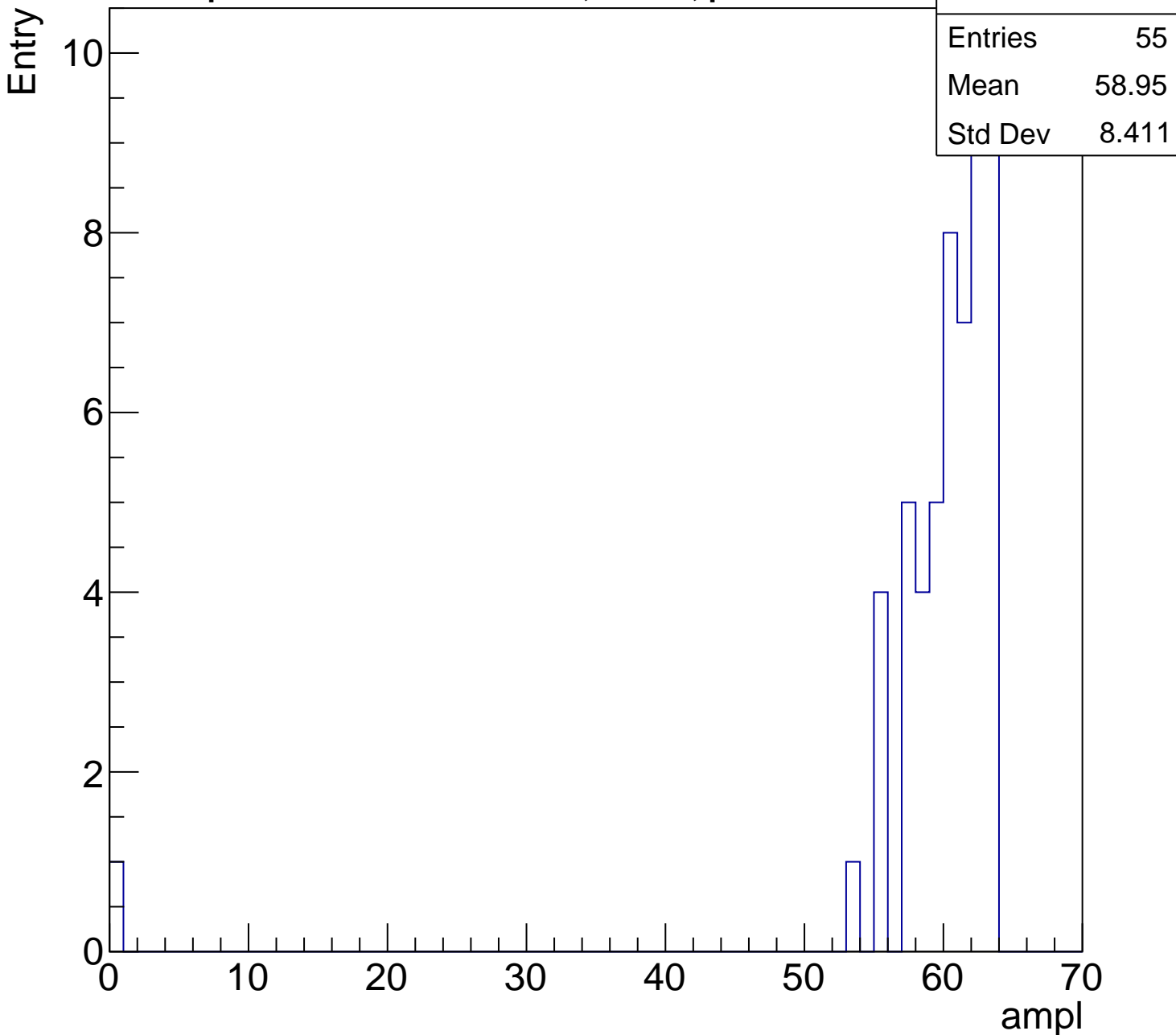
40

50

ampl

60

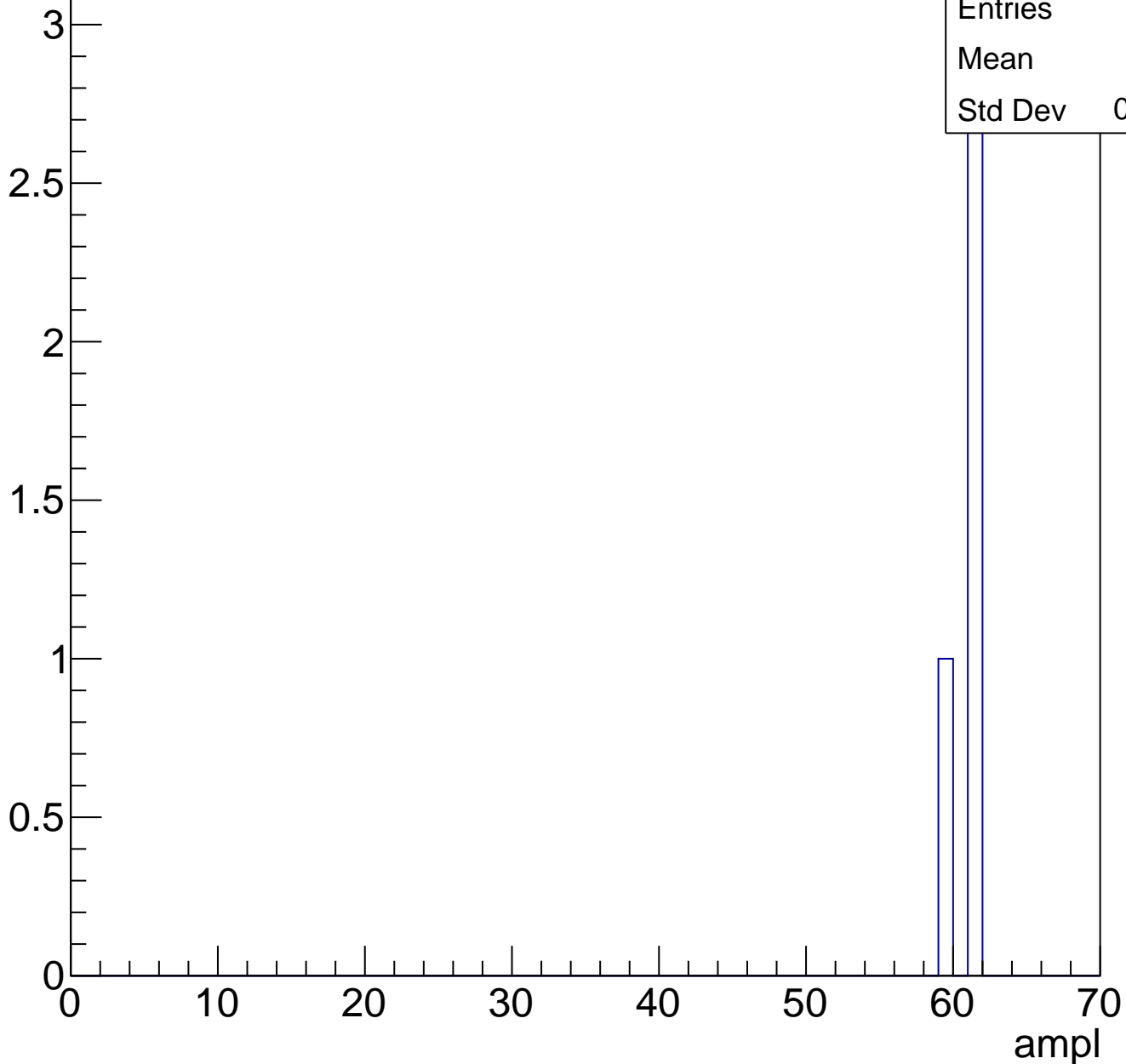
70



B1L102S, U10-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch46, adc0

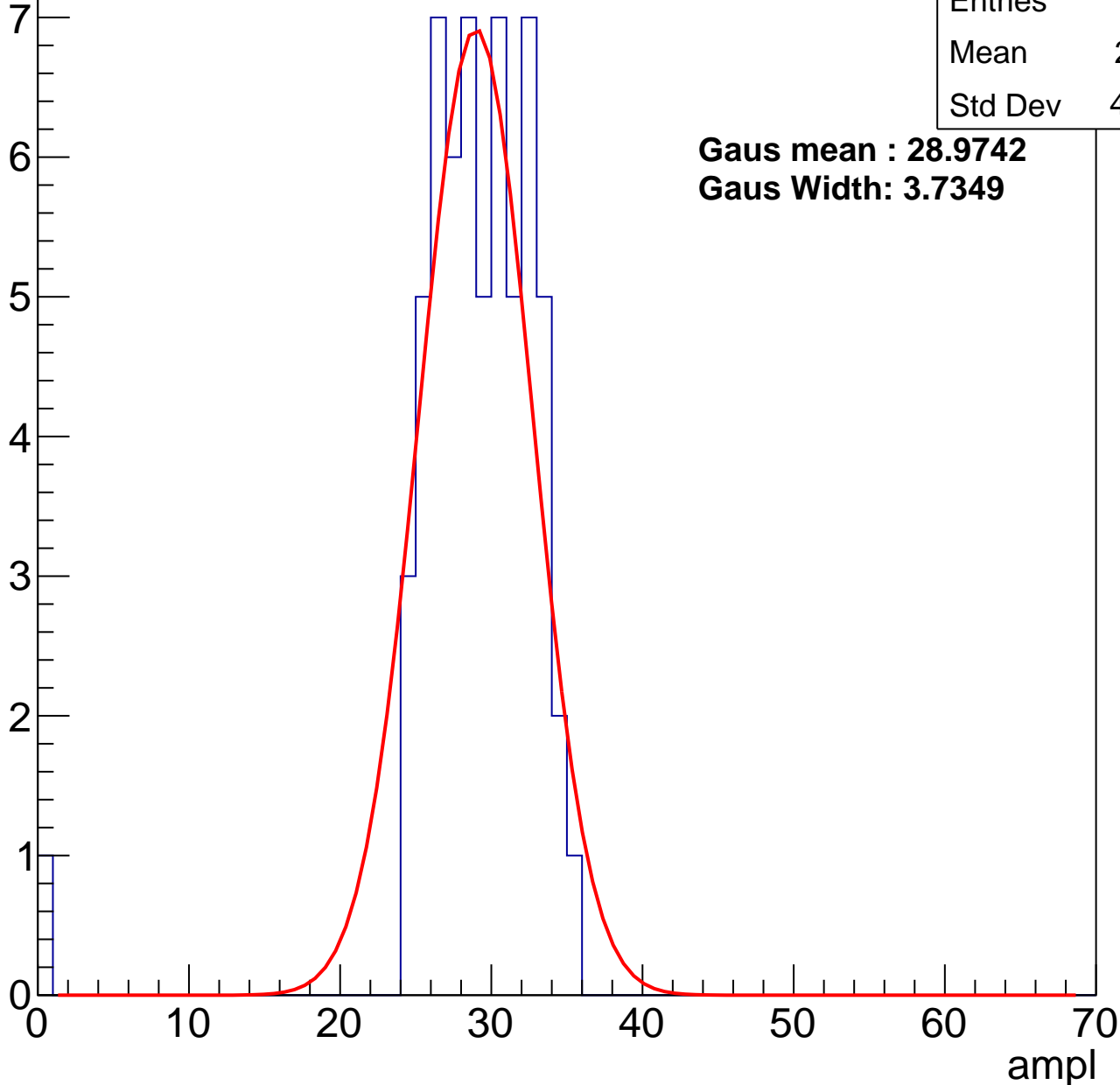
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.51
Std Dev	4.672

Gaus mean : 28.9742

Gaus Width: 3.7349



B1L102S, U10-ch46, adc1

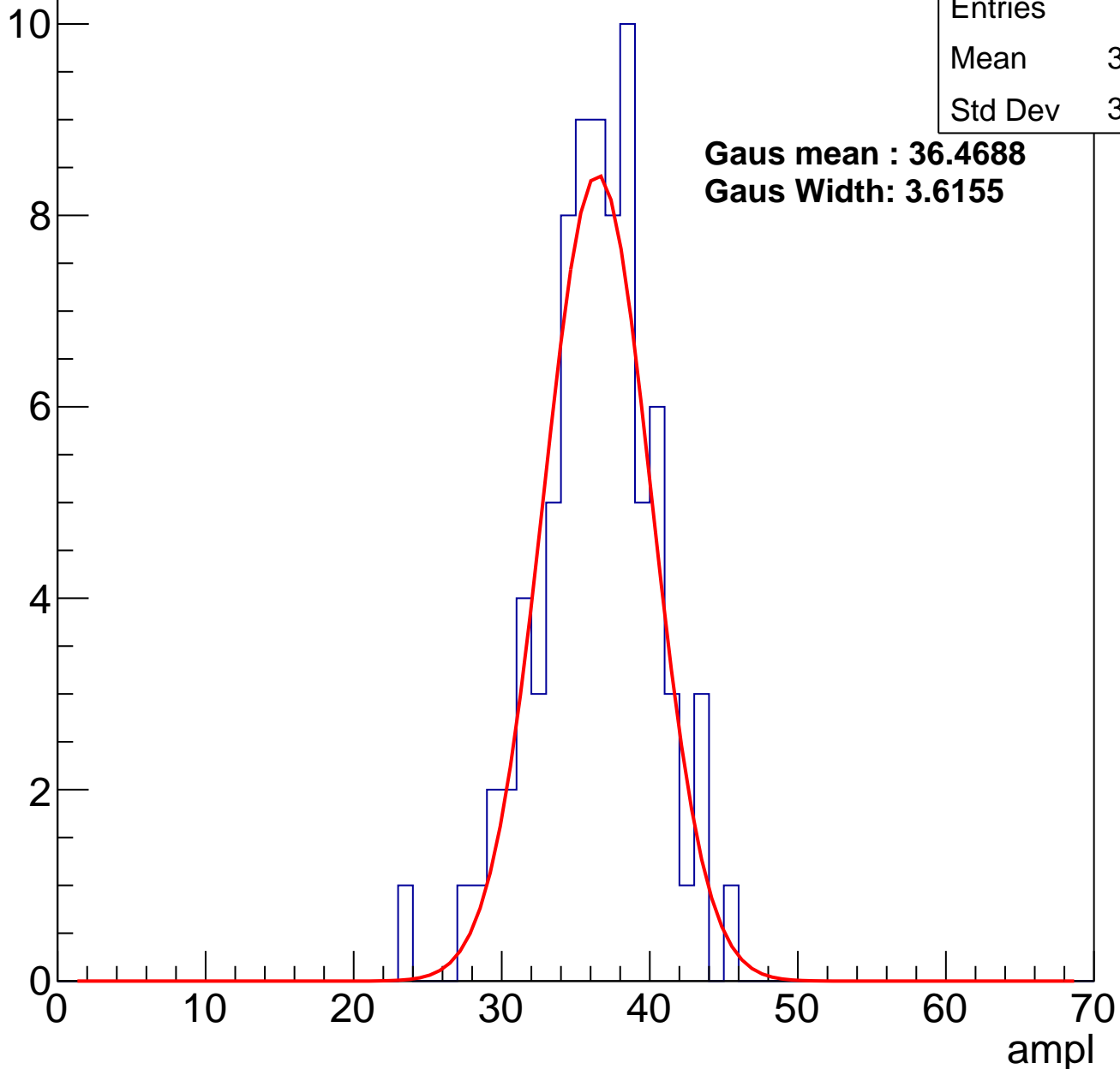
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	35.88
Std Dev	3.896

Gaus mean : 36.4688

Gaus Width: 3.6155

Entry



B1L102S, U10-ch46, adc2

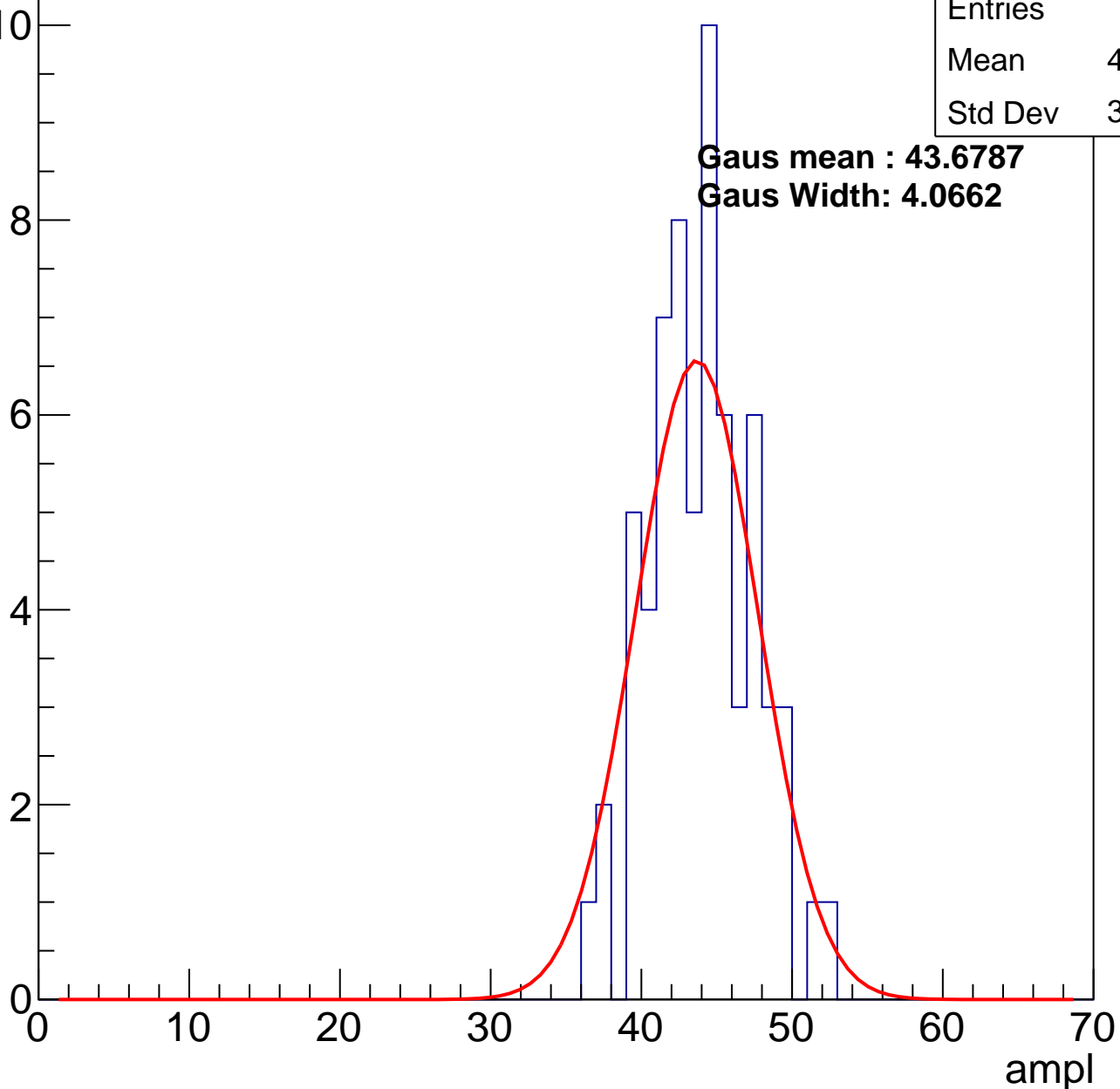
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.49
Std Dev	3.379

Gaus mean : 43.6787

Gaus Width: 4.0662

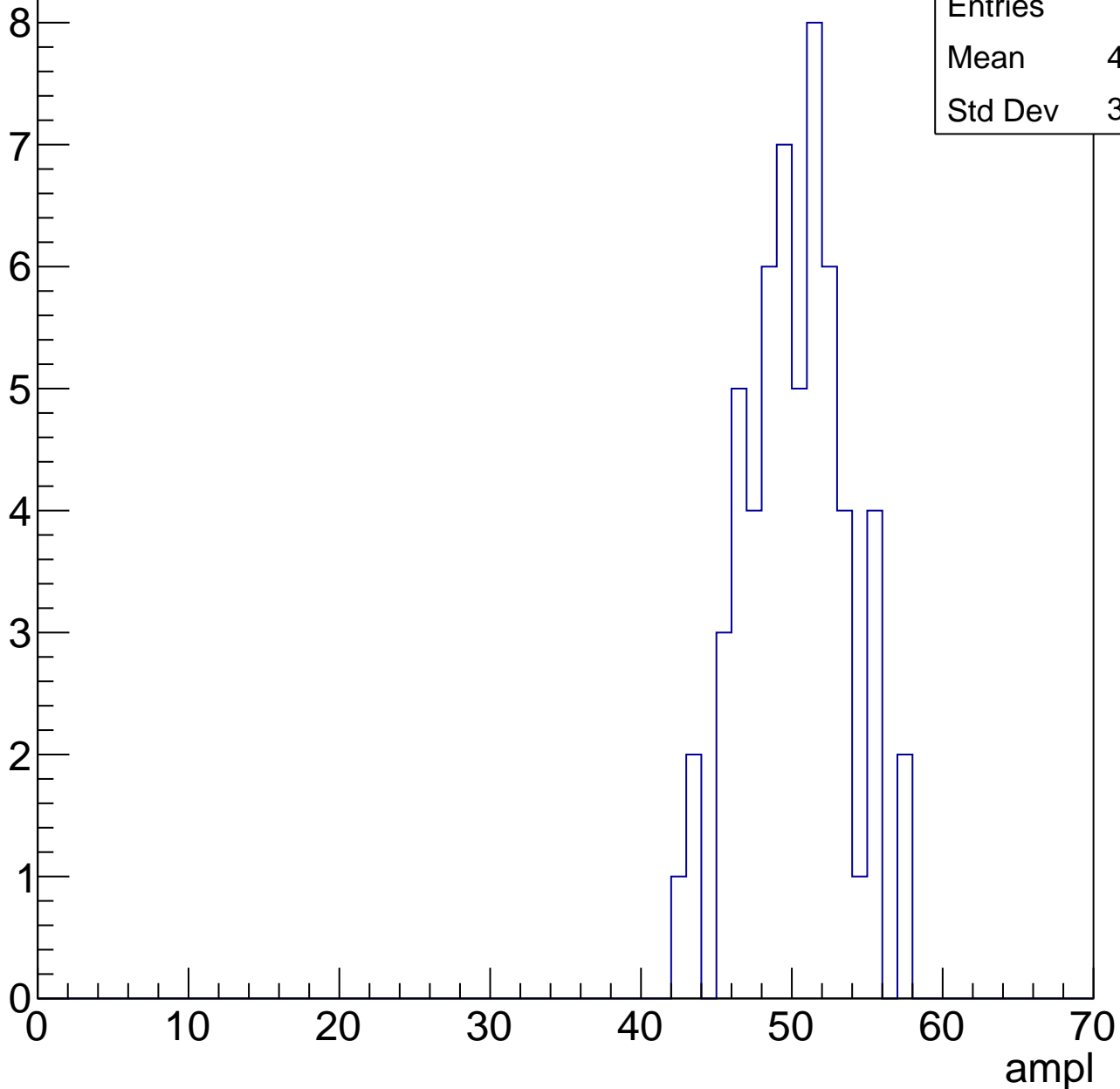


B1L102S, U10-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

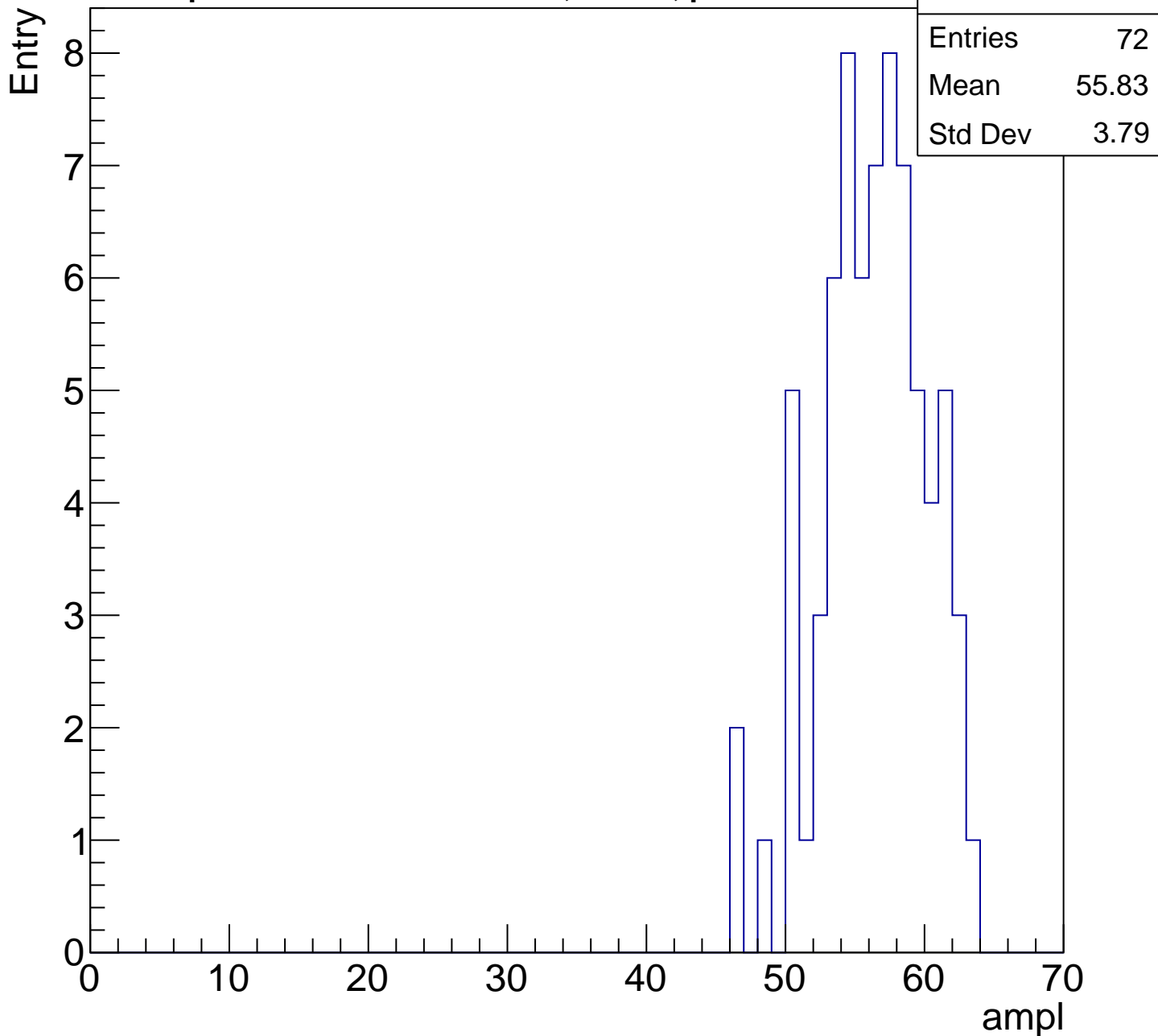
Entry

Entries	58
Mean	49.69
Std Dev	3.374



B1L102S, U10-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

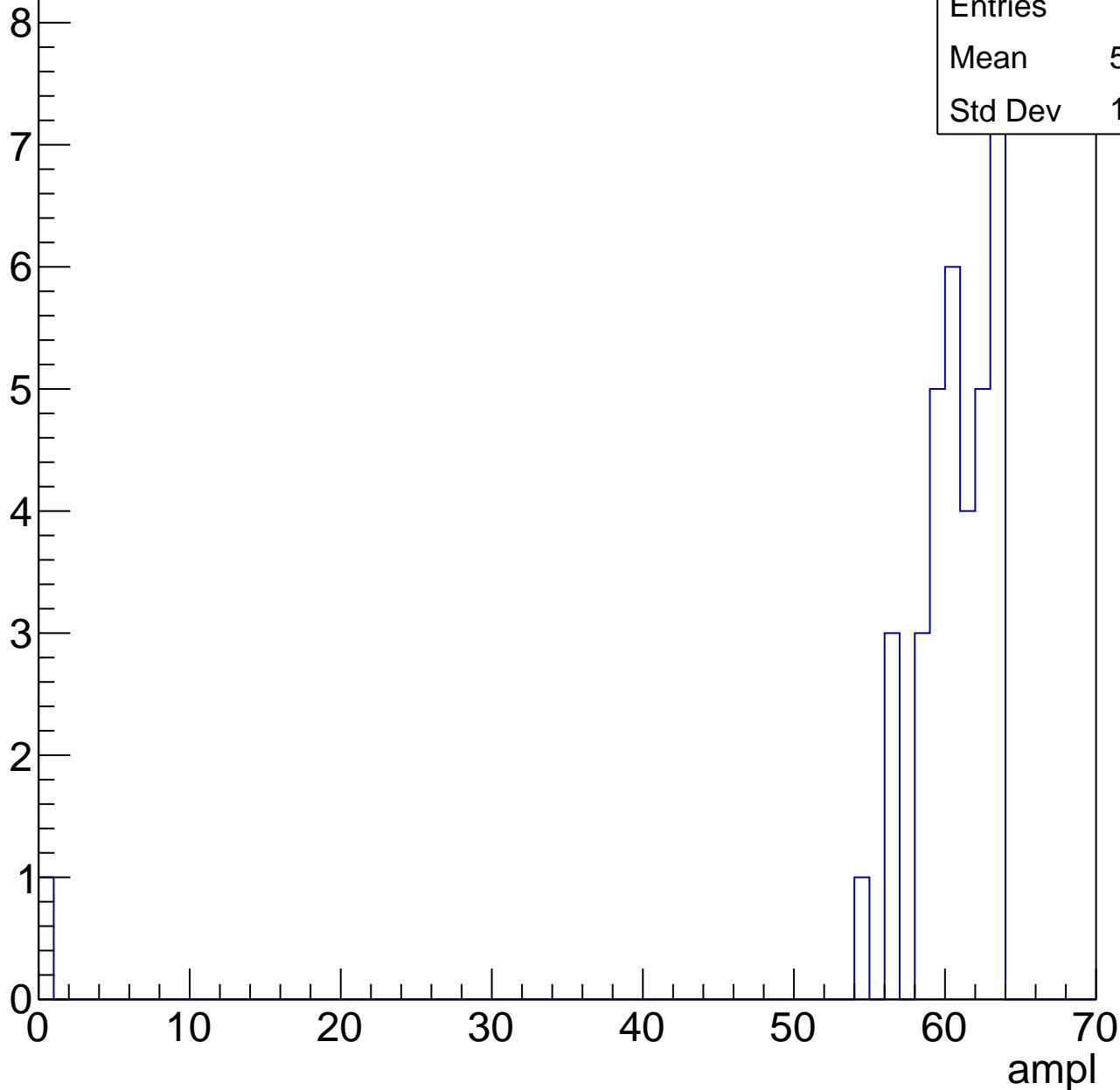


B1L102S, U10-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

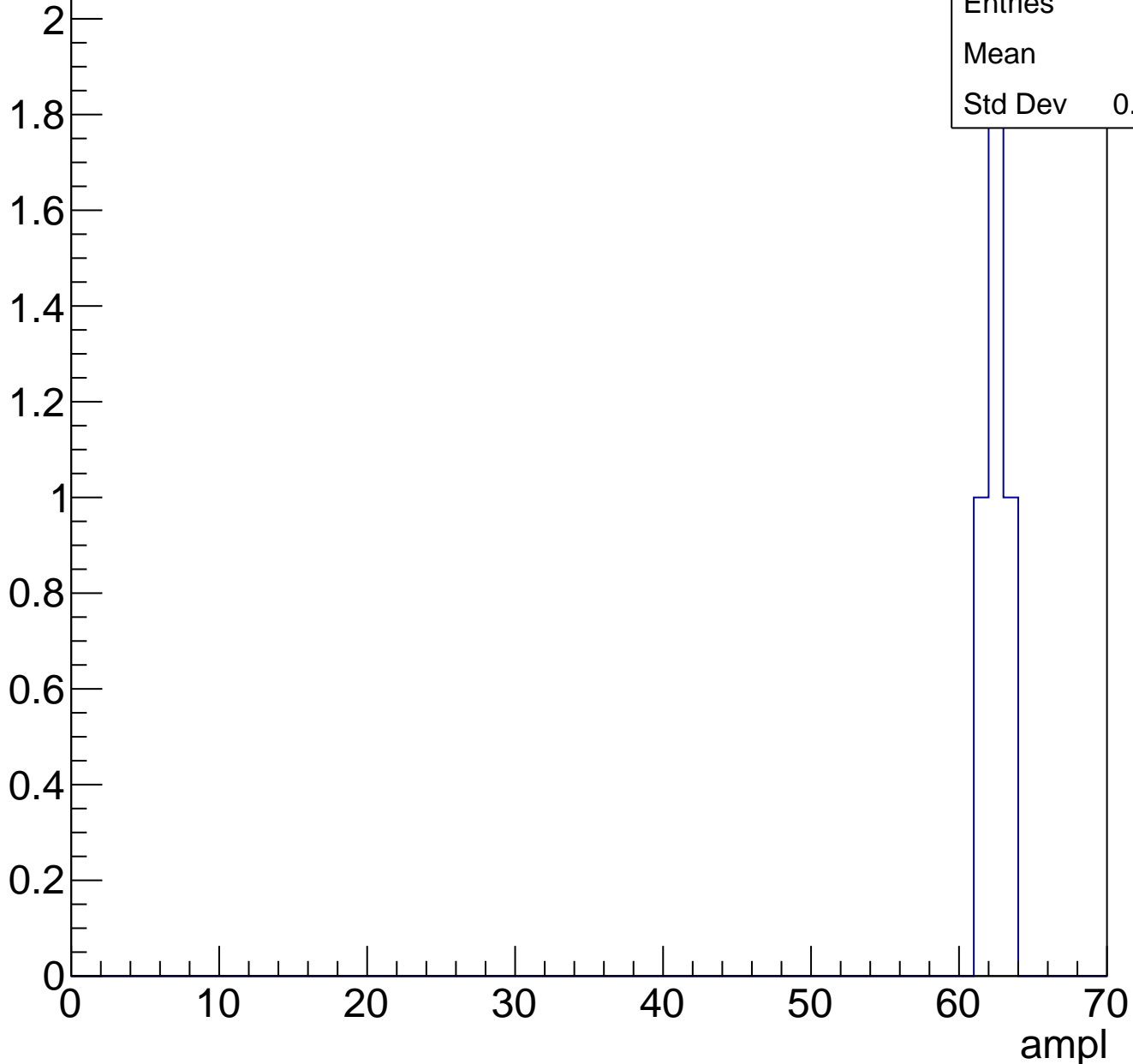
Entries	36
Mean	58.58
Std Dev	10.17



B1L102S, U10-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch47, adc0

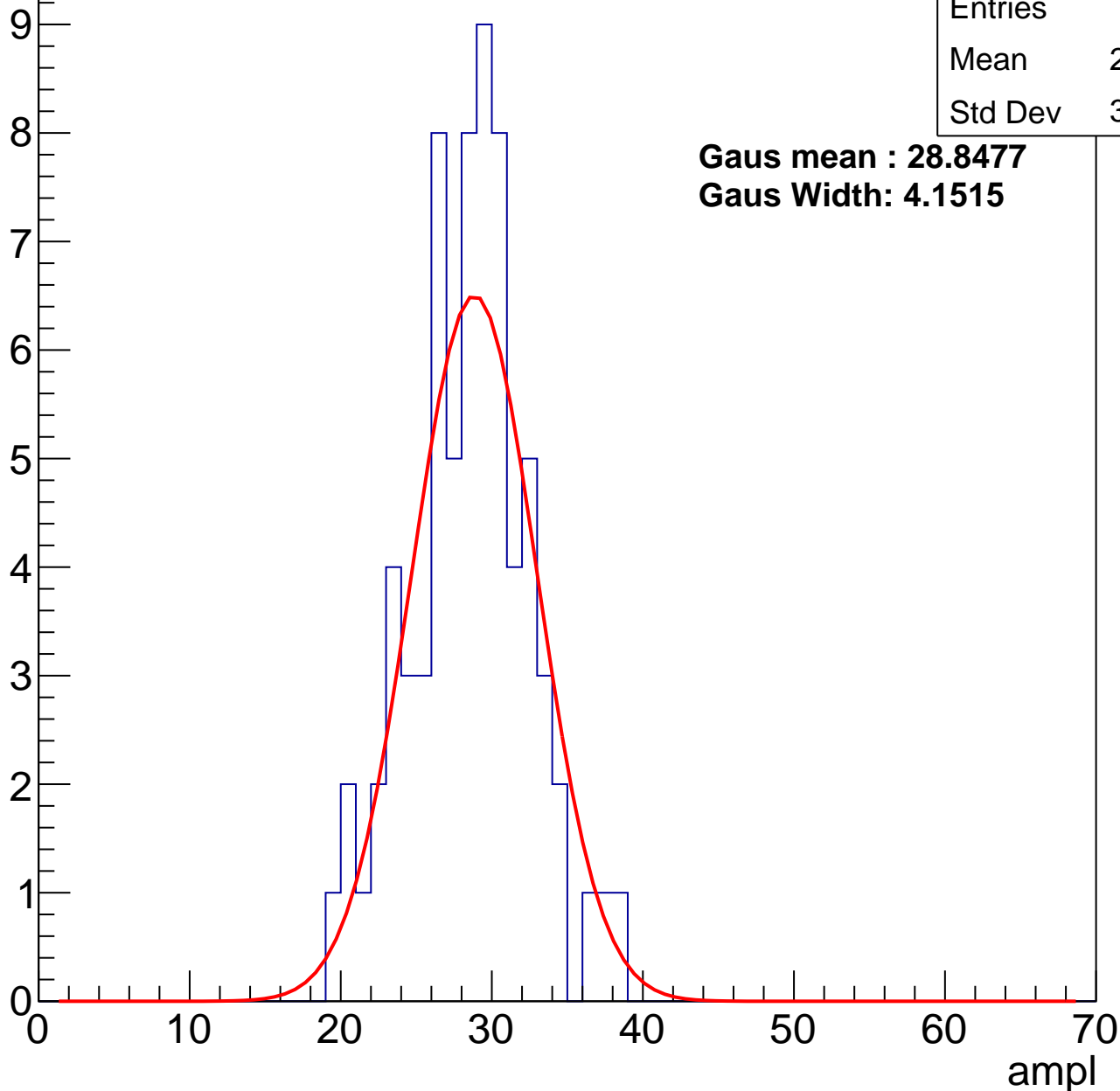
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.07
Std Dev	3.919

Gaus mean : 28.8477

Gaus Width: 4.1515



B1L102S, U10-ch47, adc1

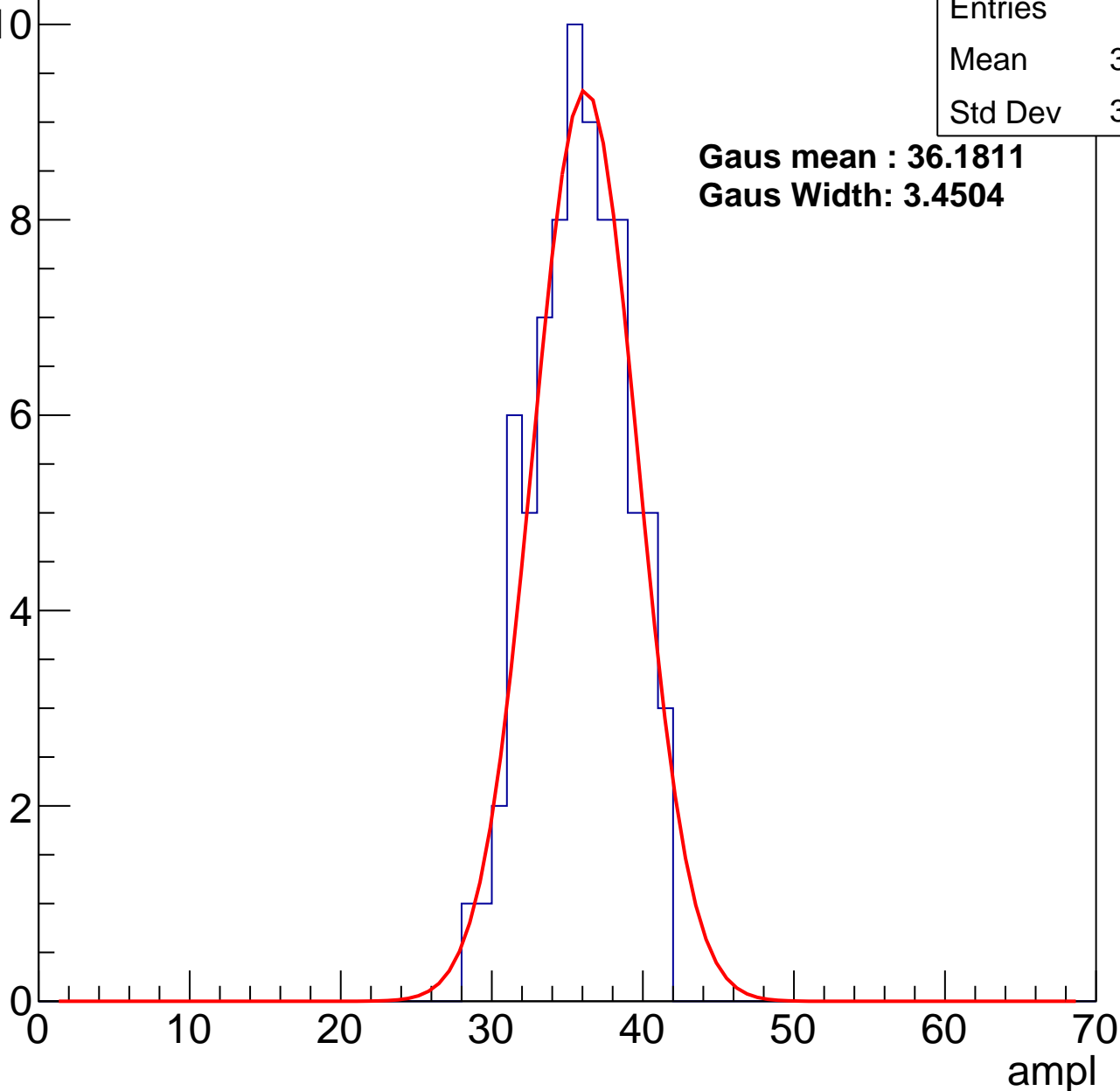
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.36
Std Dev	3.055

Gaus mean : 36.1811

Gaus Width: 3.4504



B1L102S, U10-ch47, adc2

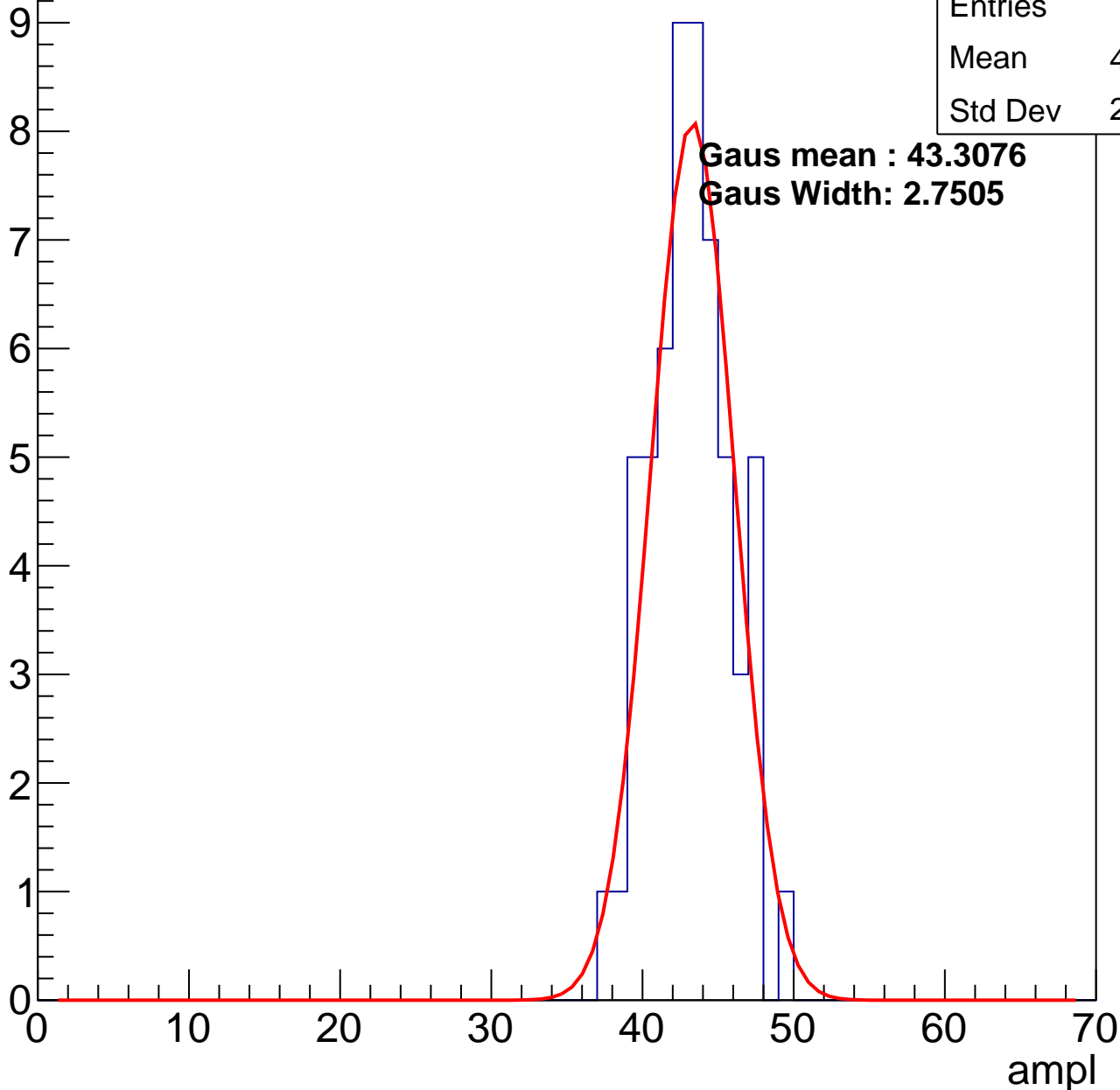
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.74
Std Dev	2.599

Gaus mean : 43.3076

Gaus Width: 2.7505

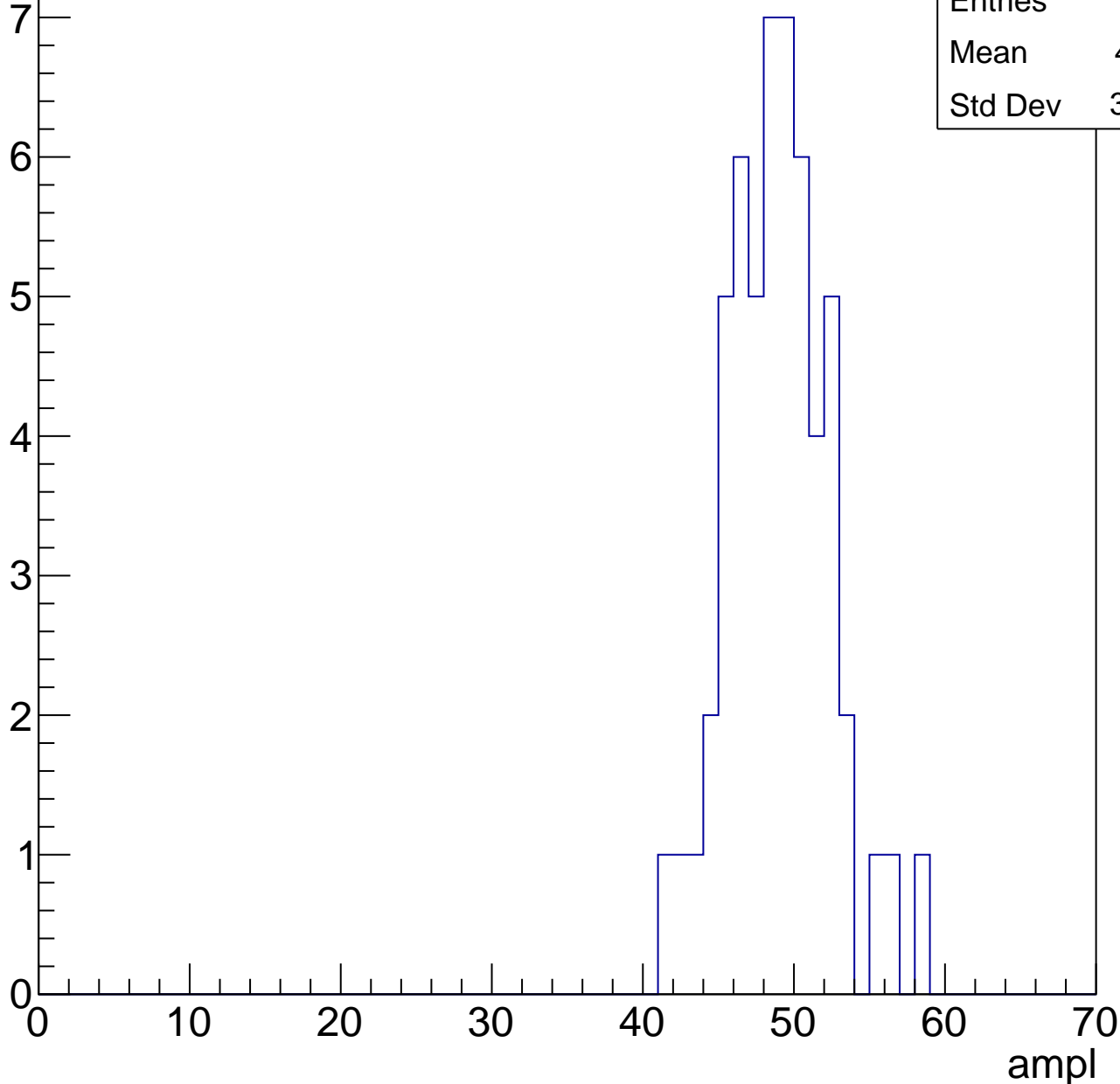


B1L102S, U10-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.51
Std Dev	3.335

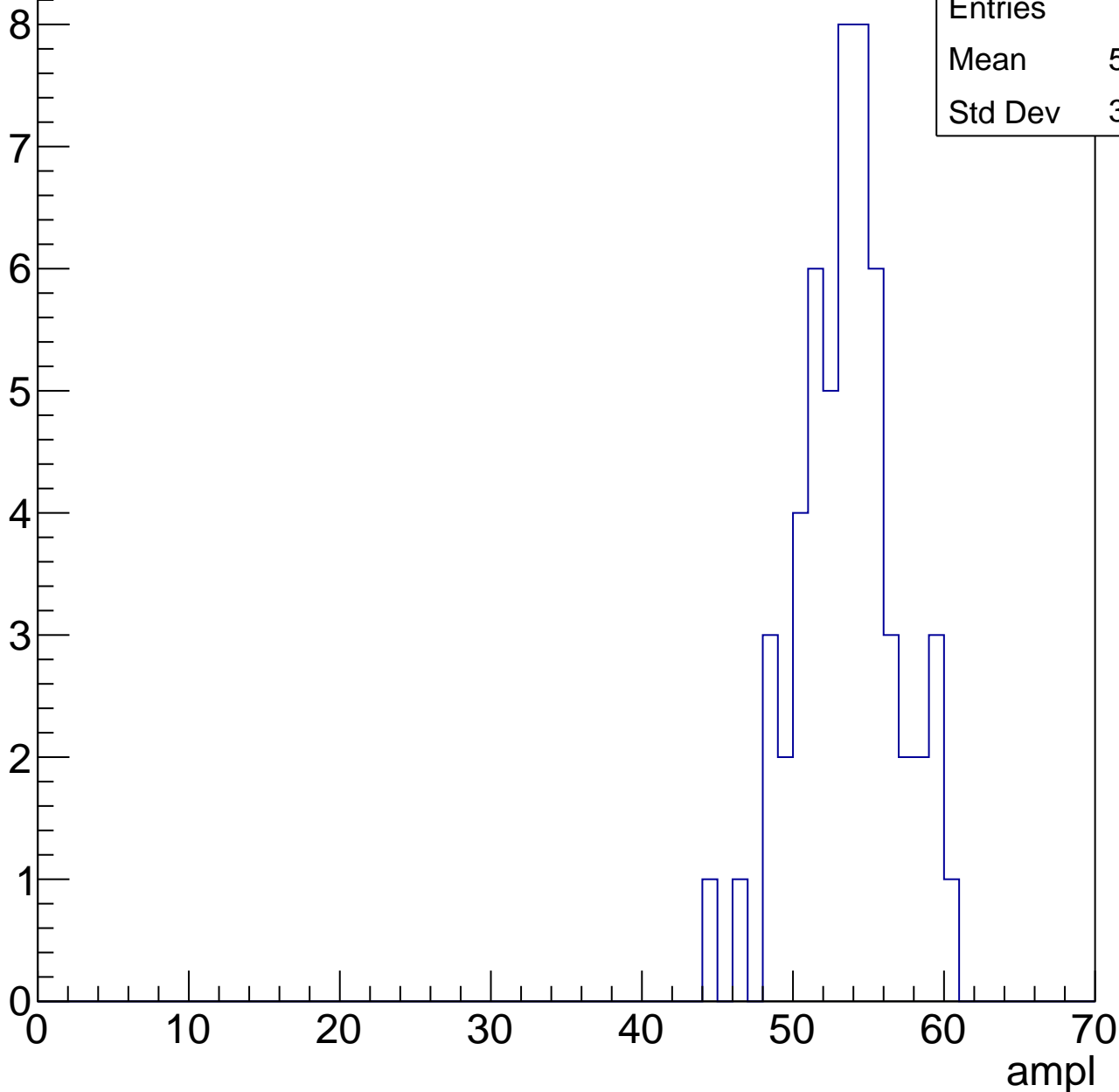


B1L102S, U10-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.07
Std Dev	3.302

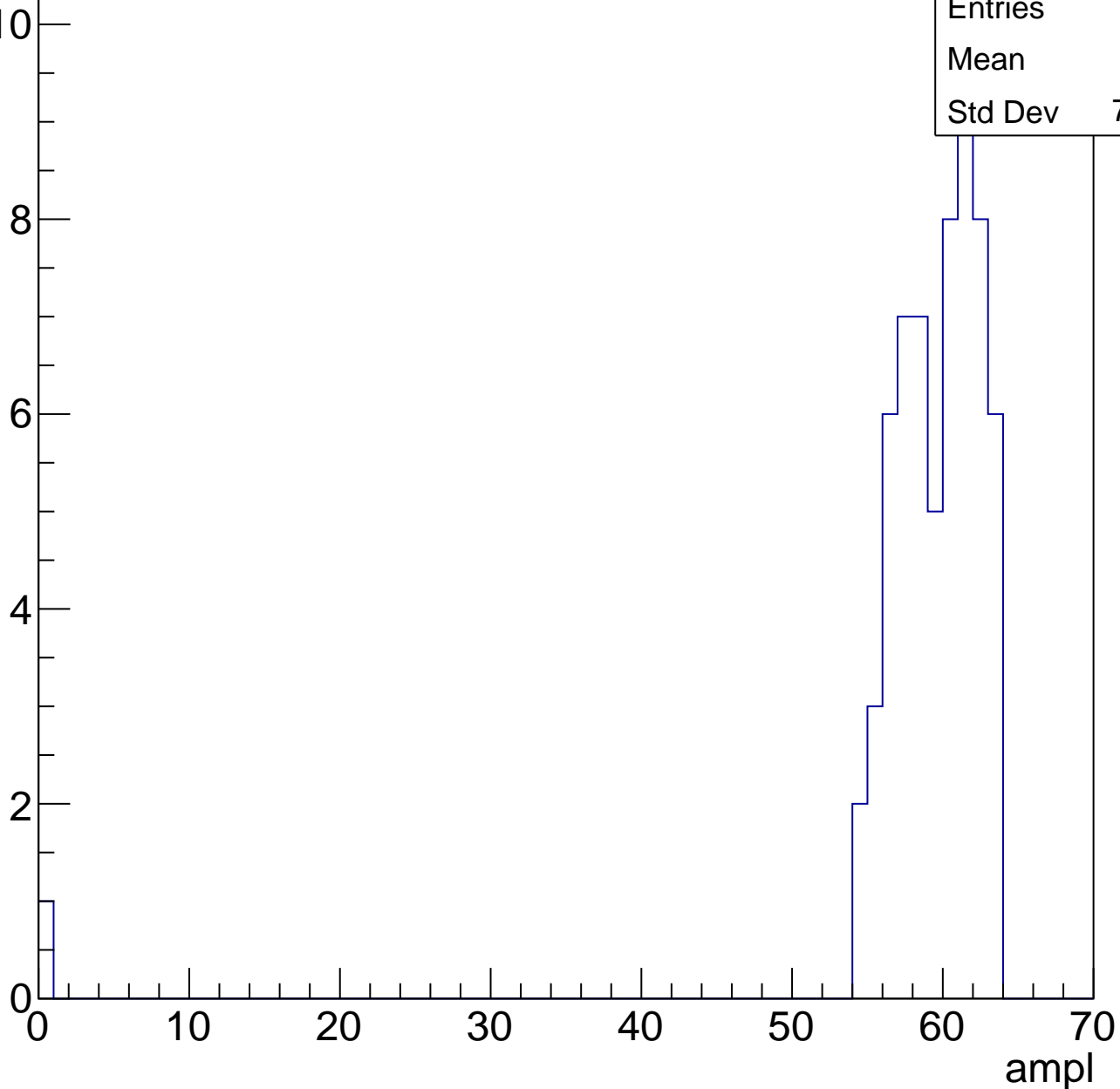


B1L102S, U10-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

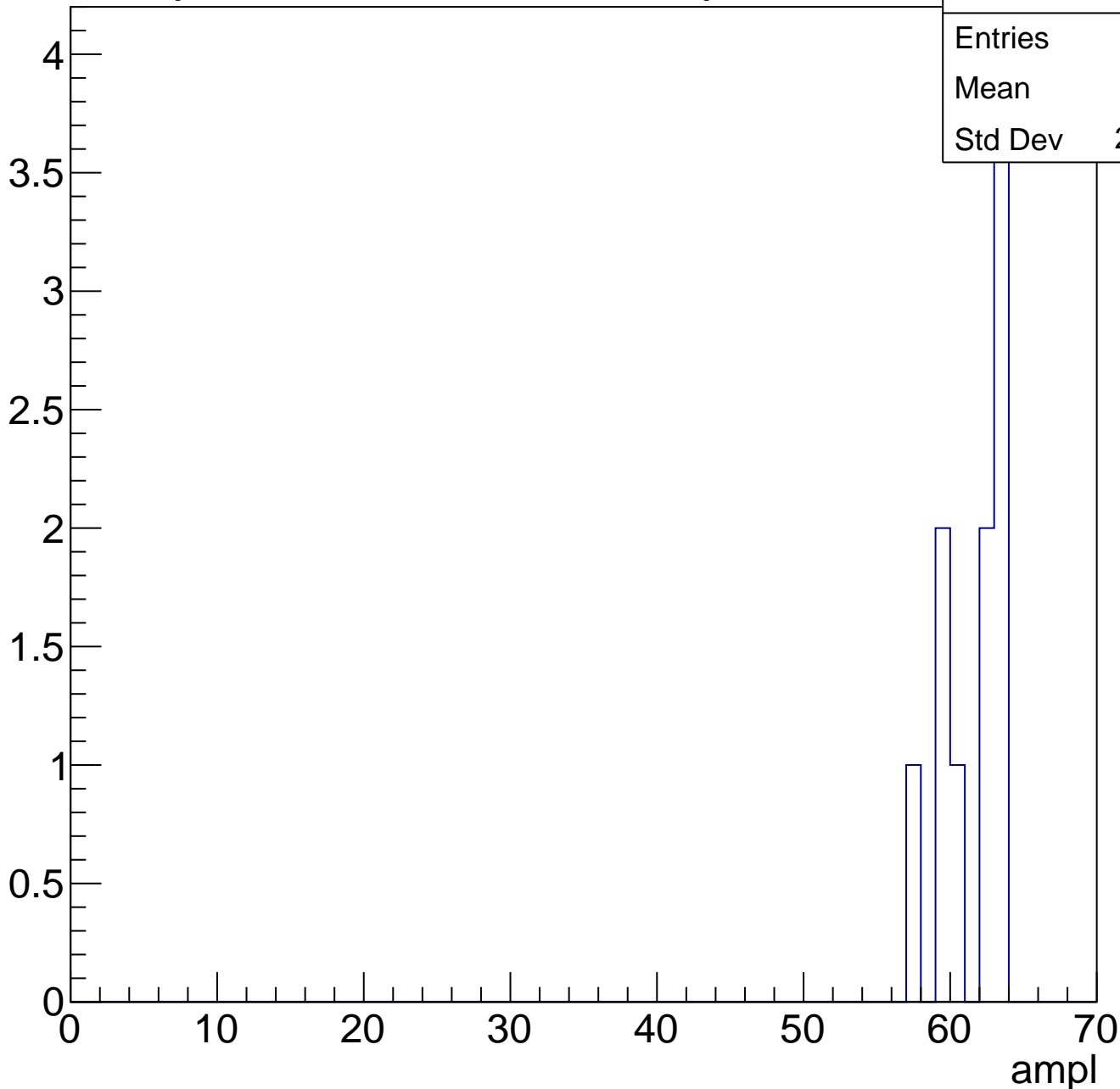
Entries	63
Mean	58.3
Std Dev	7.821



B1L102S, U10-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch48, adc0

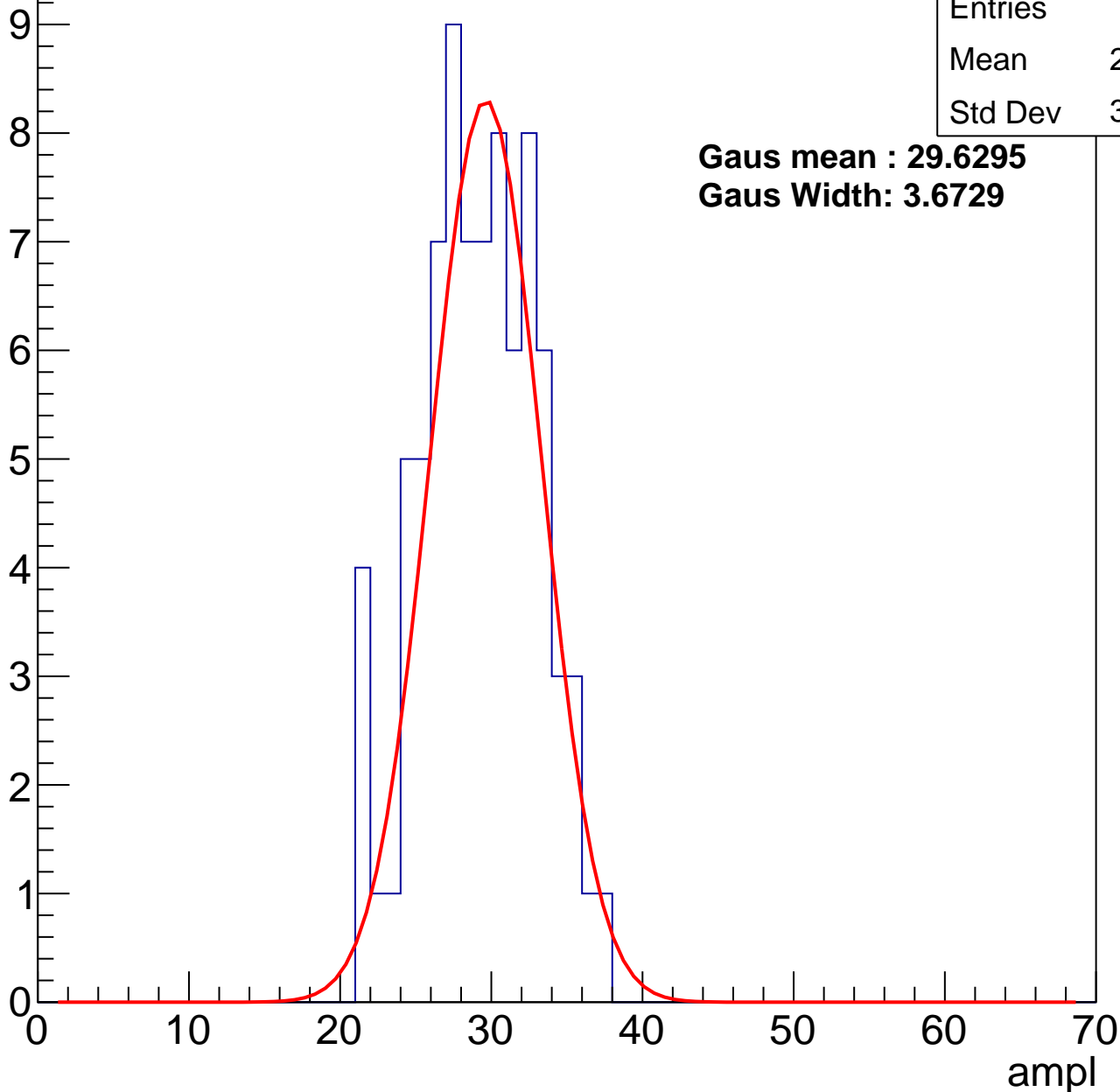
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.76
Std Dev	3.734

Gaus mean : 29.6295

Gaus Width: 3.6729



B1L102S, U10-ch48, adc1

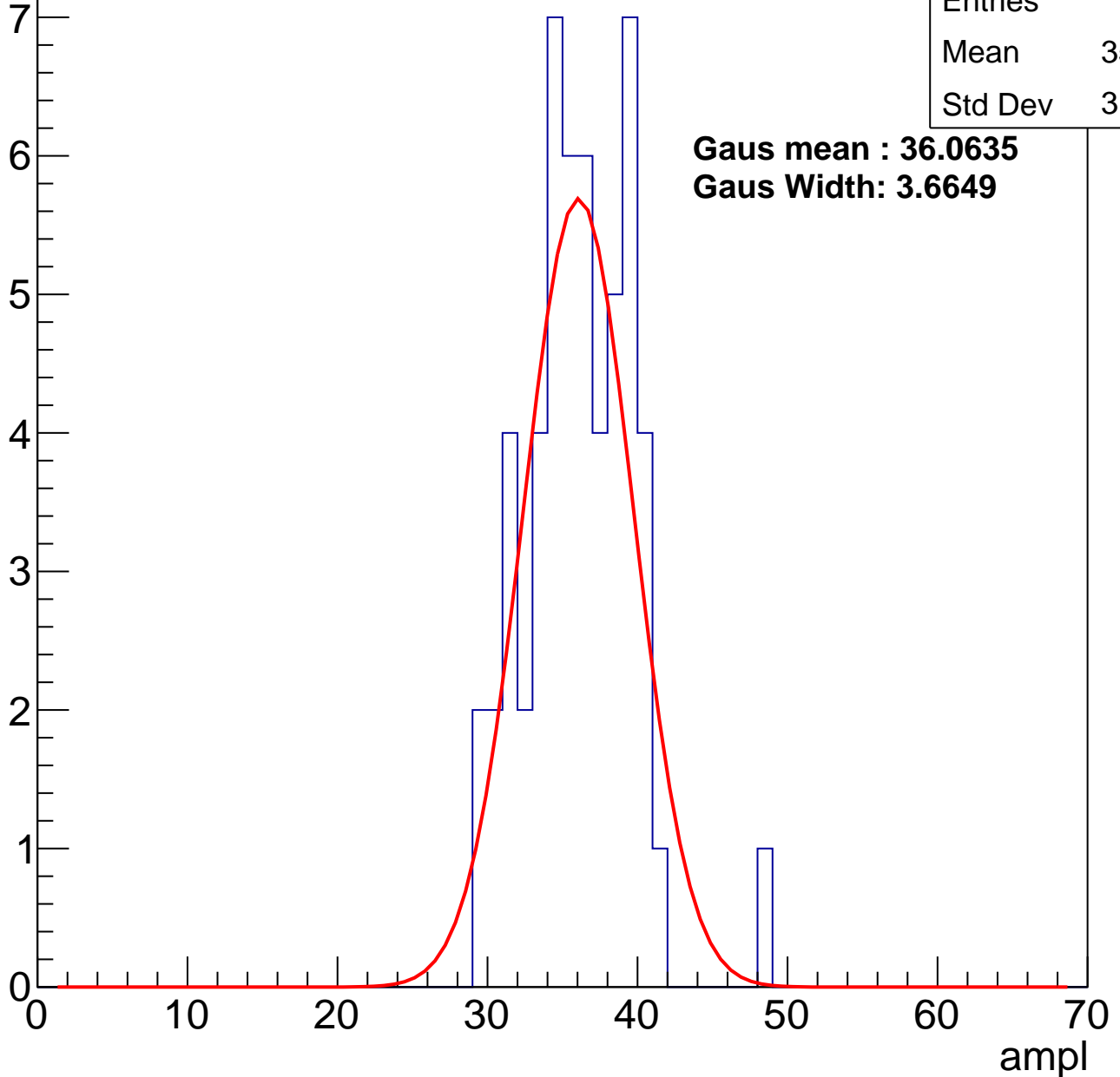
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	35.67
Std Dev	3.532

Gaus mean : 36.0635

Gaus Width: 3.6649



B1L102S, U10-ch48, adc2

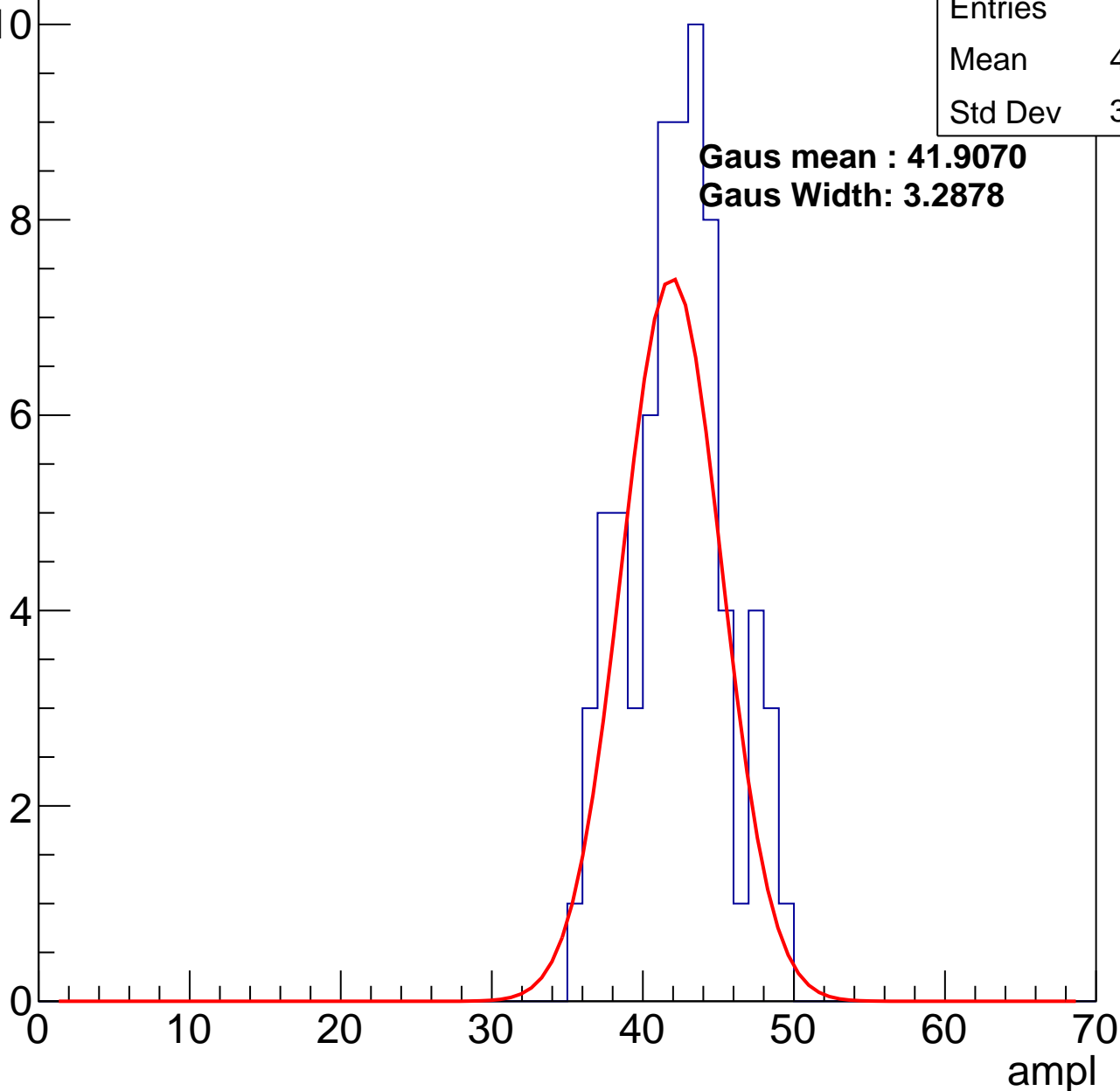
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.82
Std Dev	3.272

Gaus mean : 41.9070

Gaus Width: 3.2878

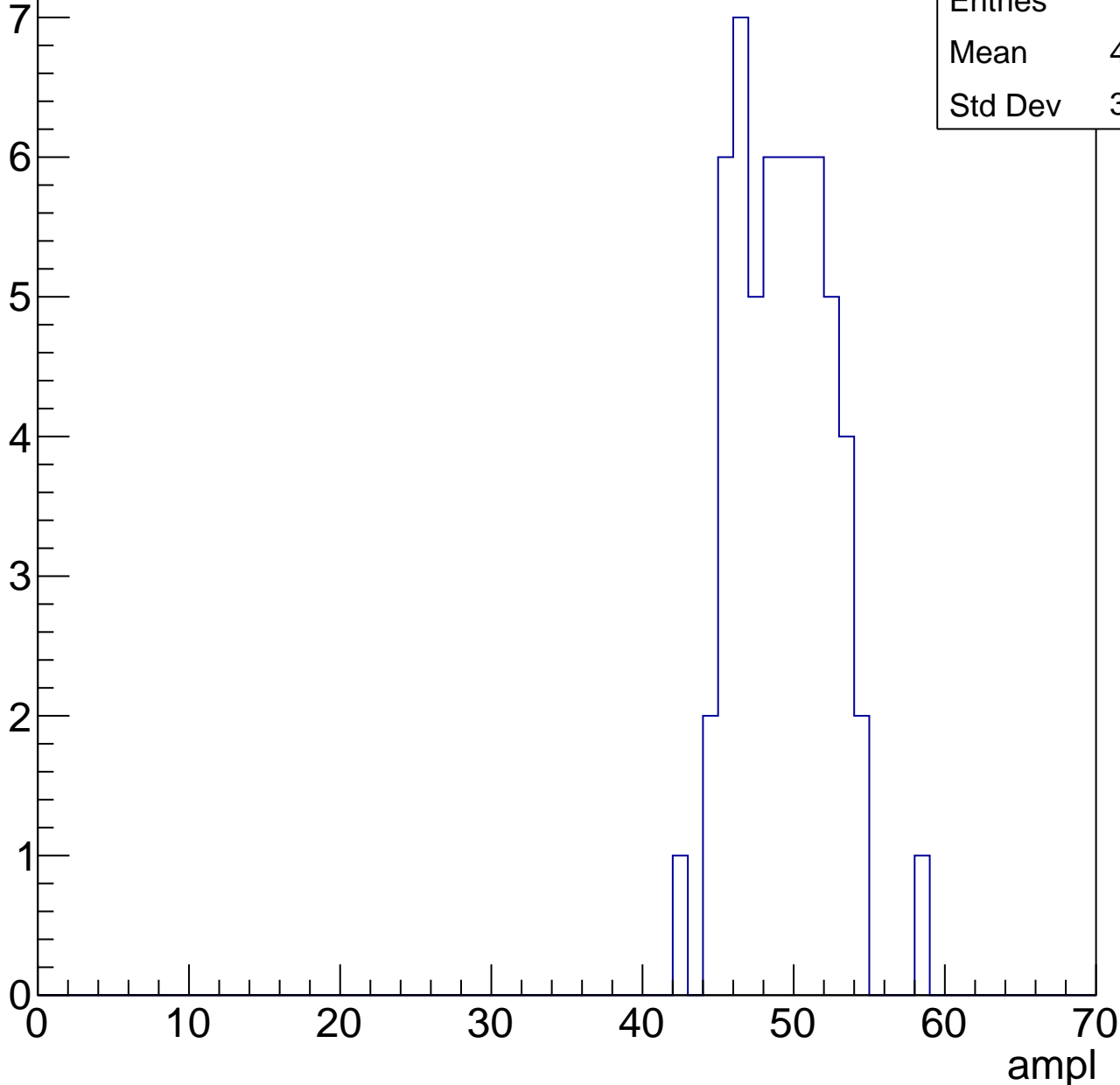


B1L102S, U10-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.82
Std Dev	3.113

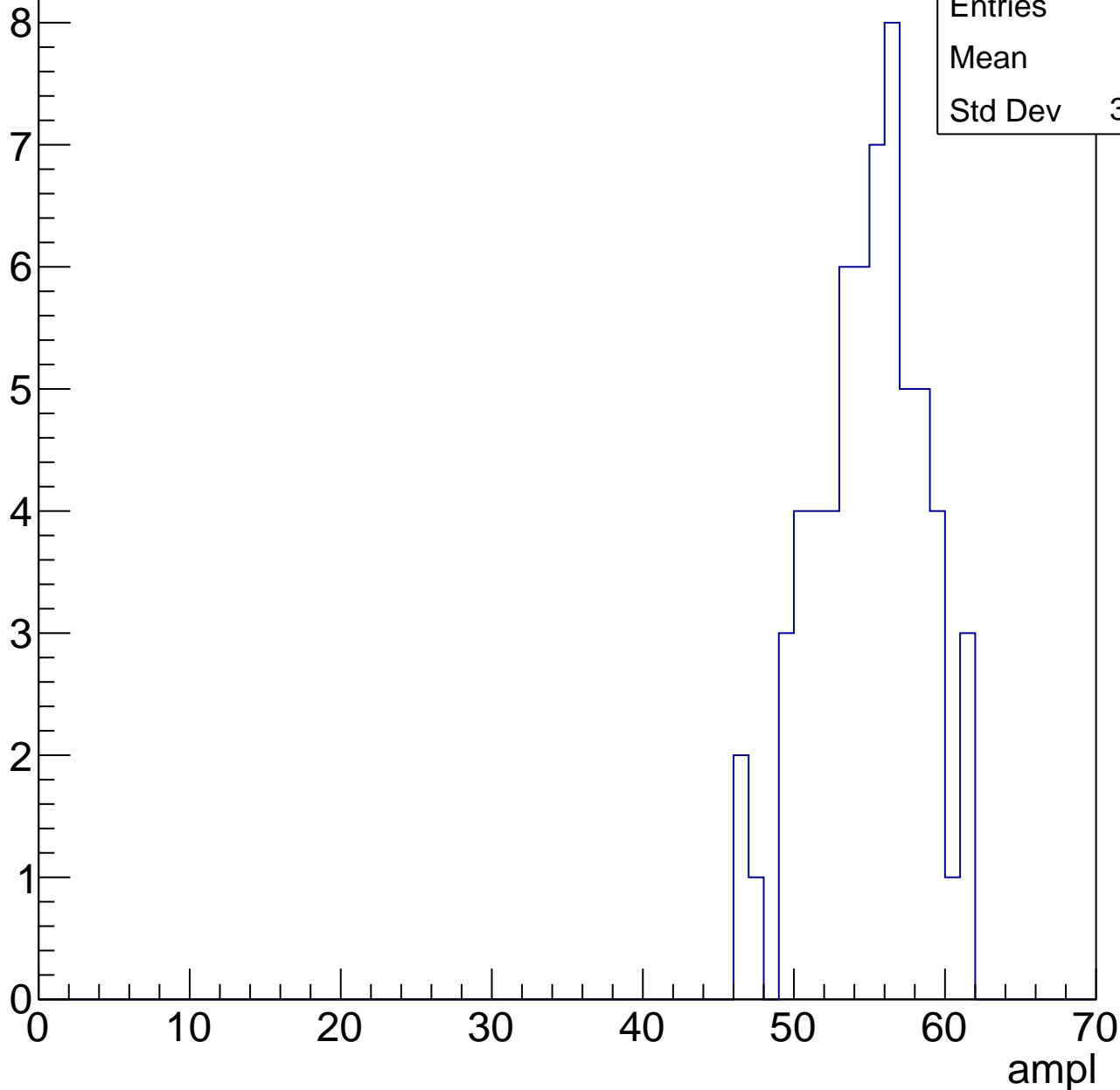


B1L102S, U10-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

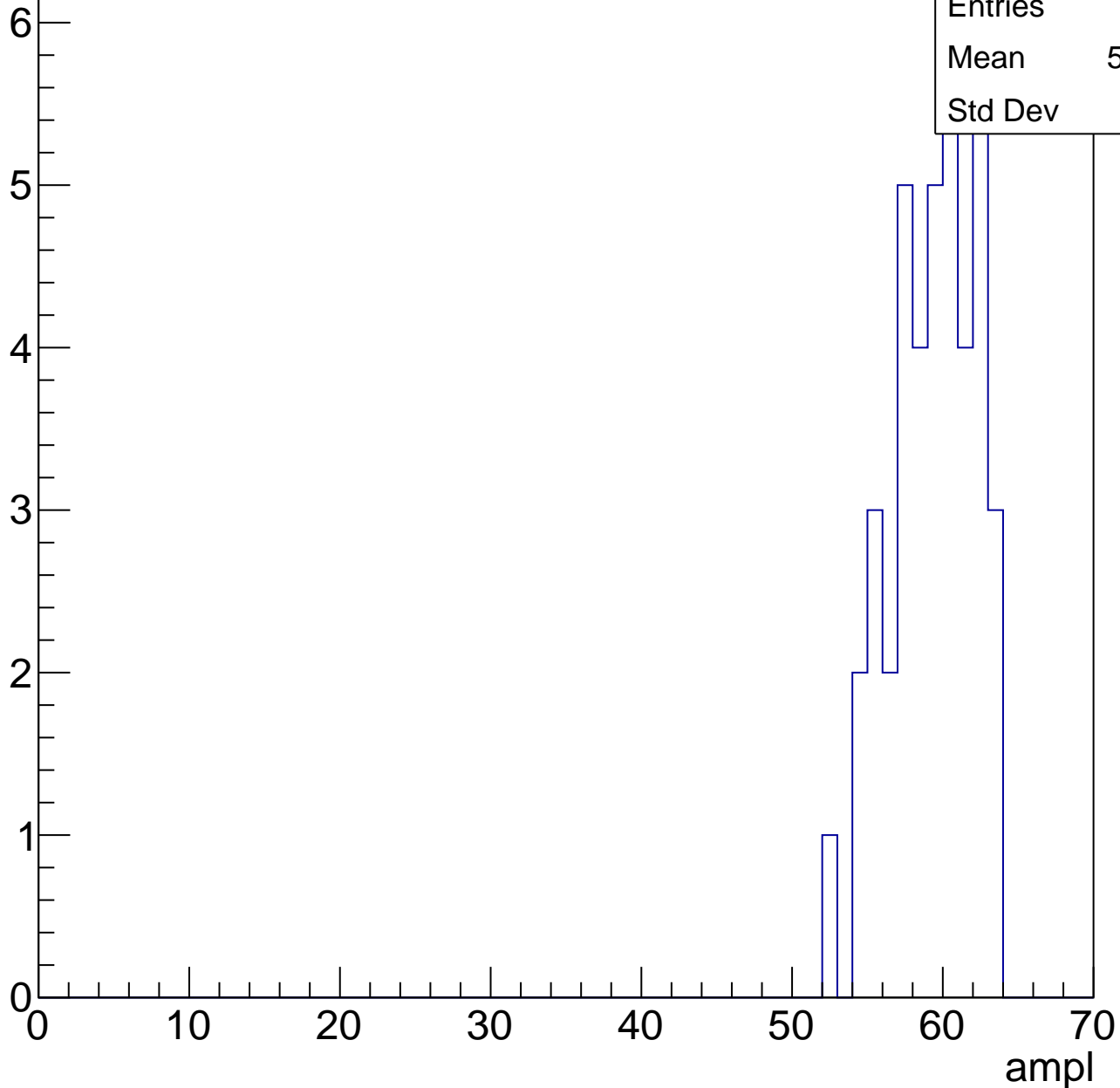
Entries	63
Mean	54.4
Std Dev	3.588



B1L102S, U10-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



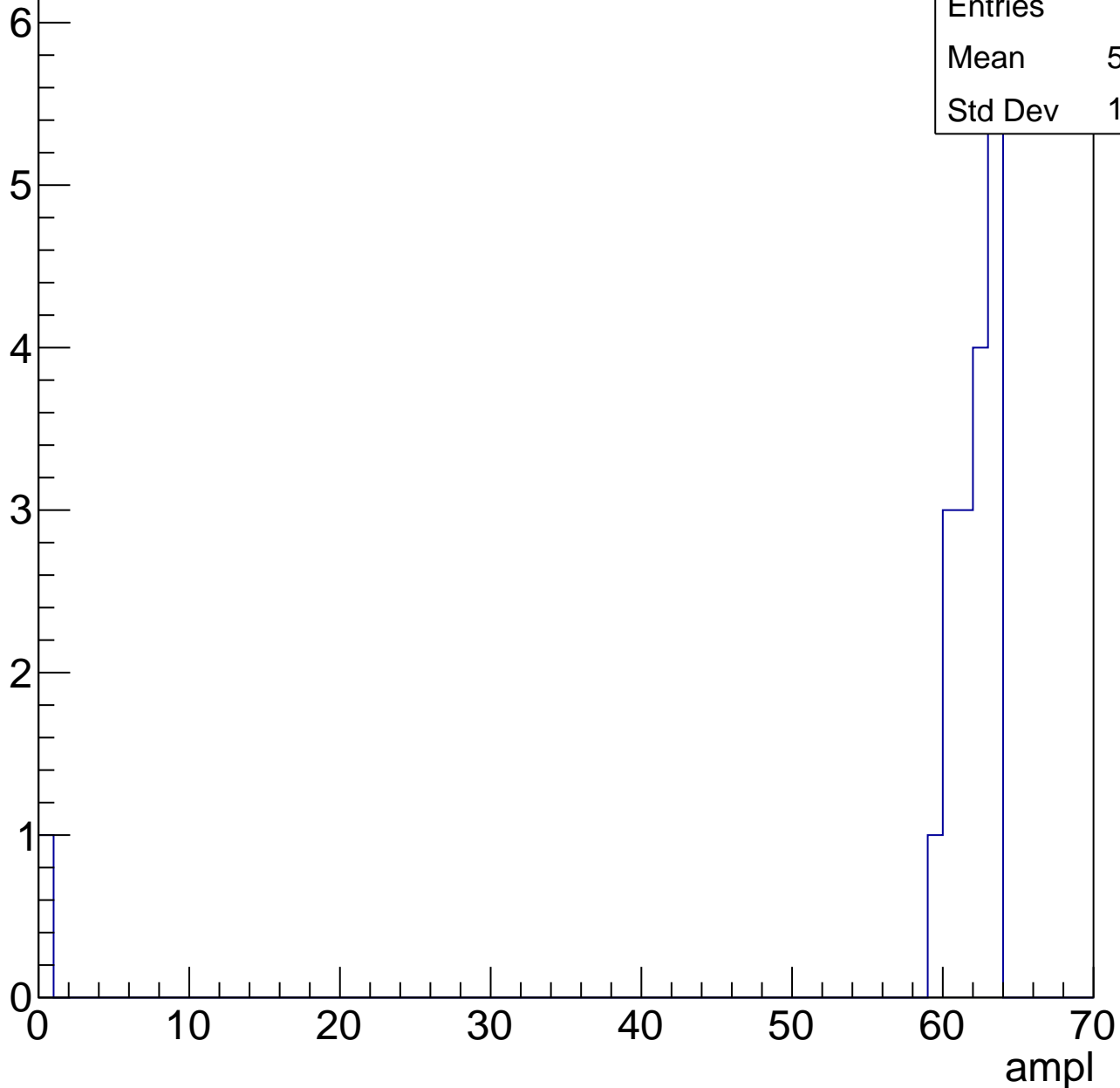
Entries	41
Mean	58.88
Std Dev	2.76

B1L102S, U10-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58.22
Std Dev	14.18



B1L102S, U10-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch49, adc0

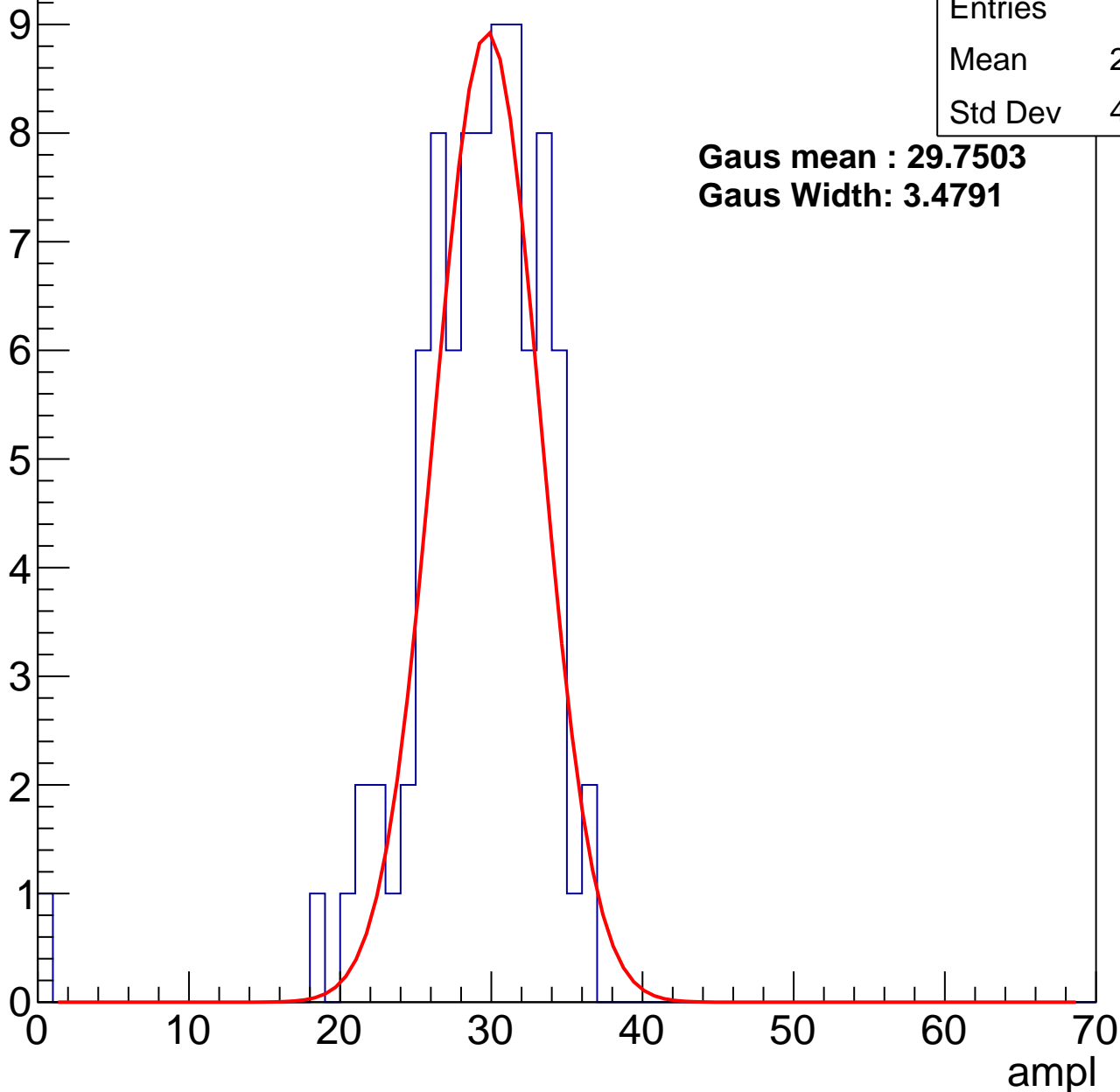
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	28.59
Std Dev	4.856

Gaus mean : 29.7503

Gaus Width: 3.4791



B1L102S, U10-ch49, adc1

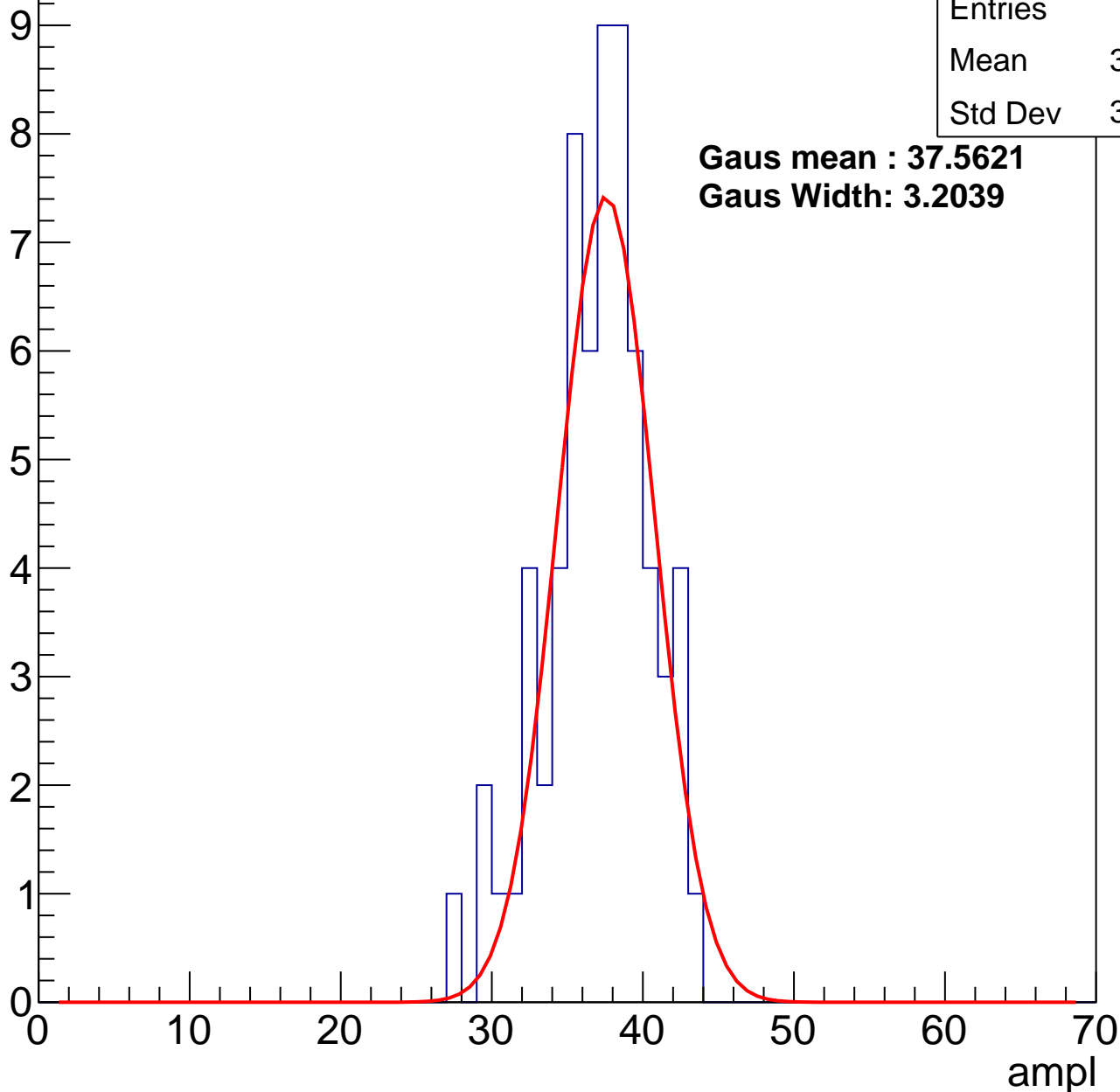
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.54
Std Dev	3.415

Gaus mean : 37.5621

Gaus Width: 3.2039

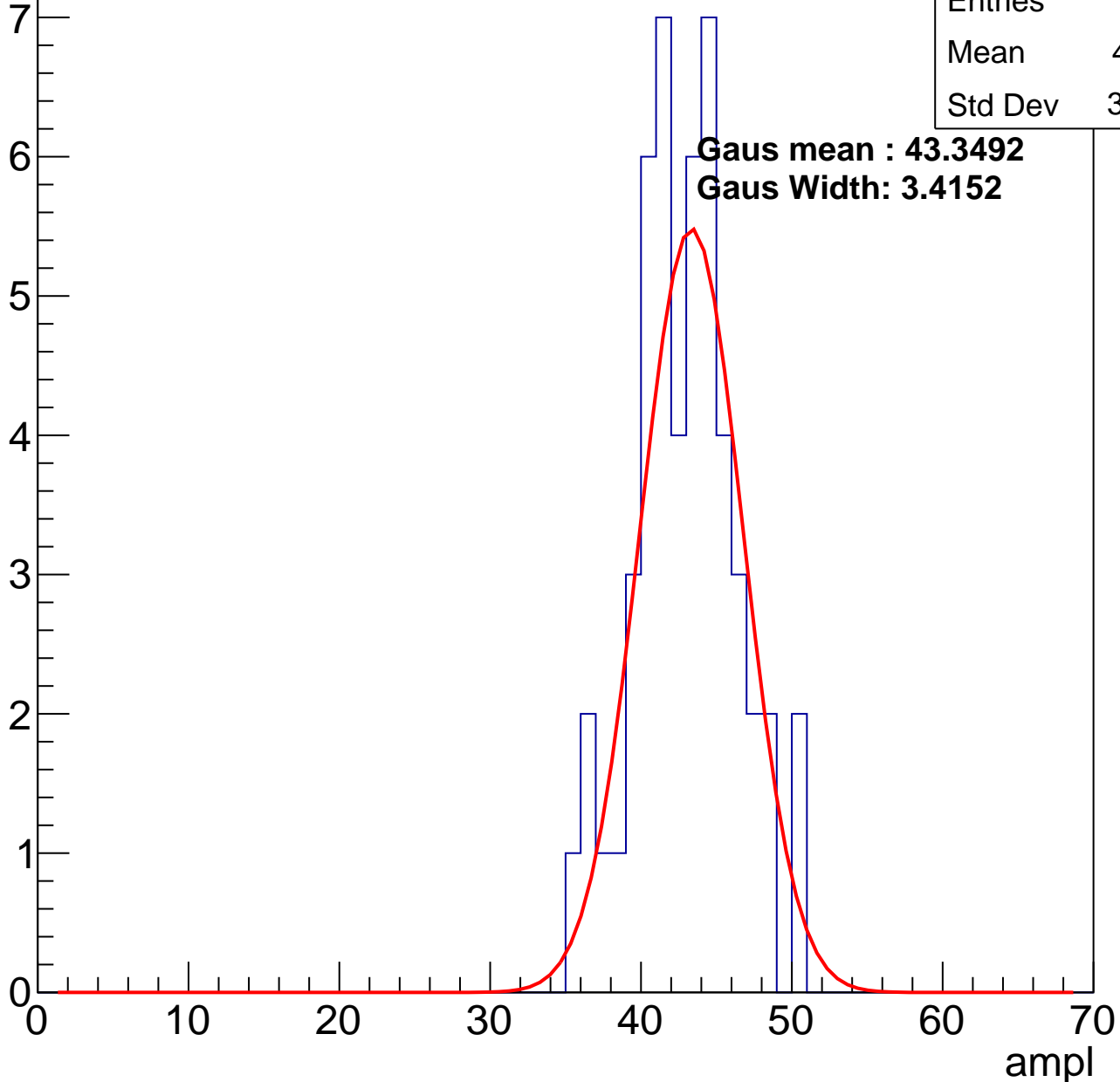


B1L102S, U10-ch49, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.51
Std Dev	3.357

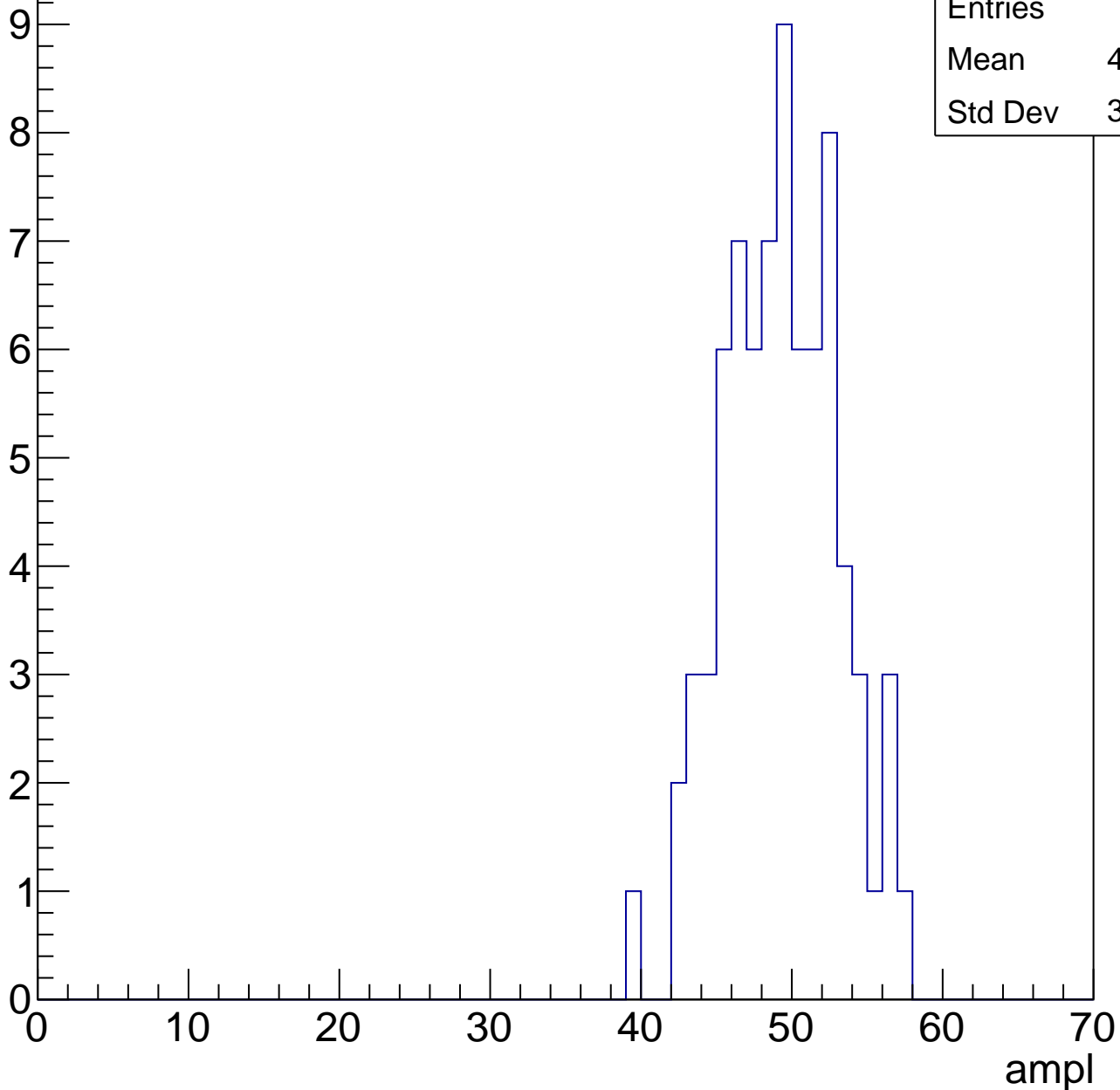


B1L102S, U10-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	48.83
Std Dev	3.729

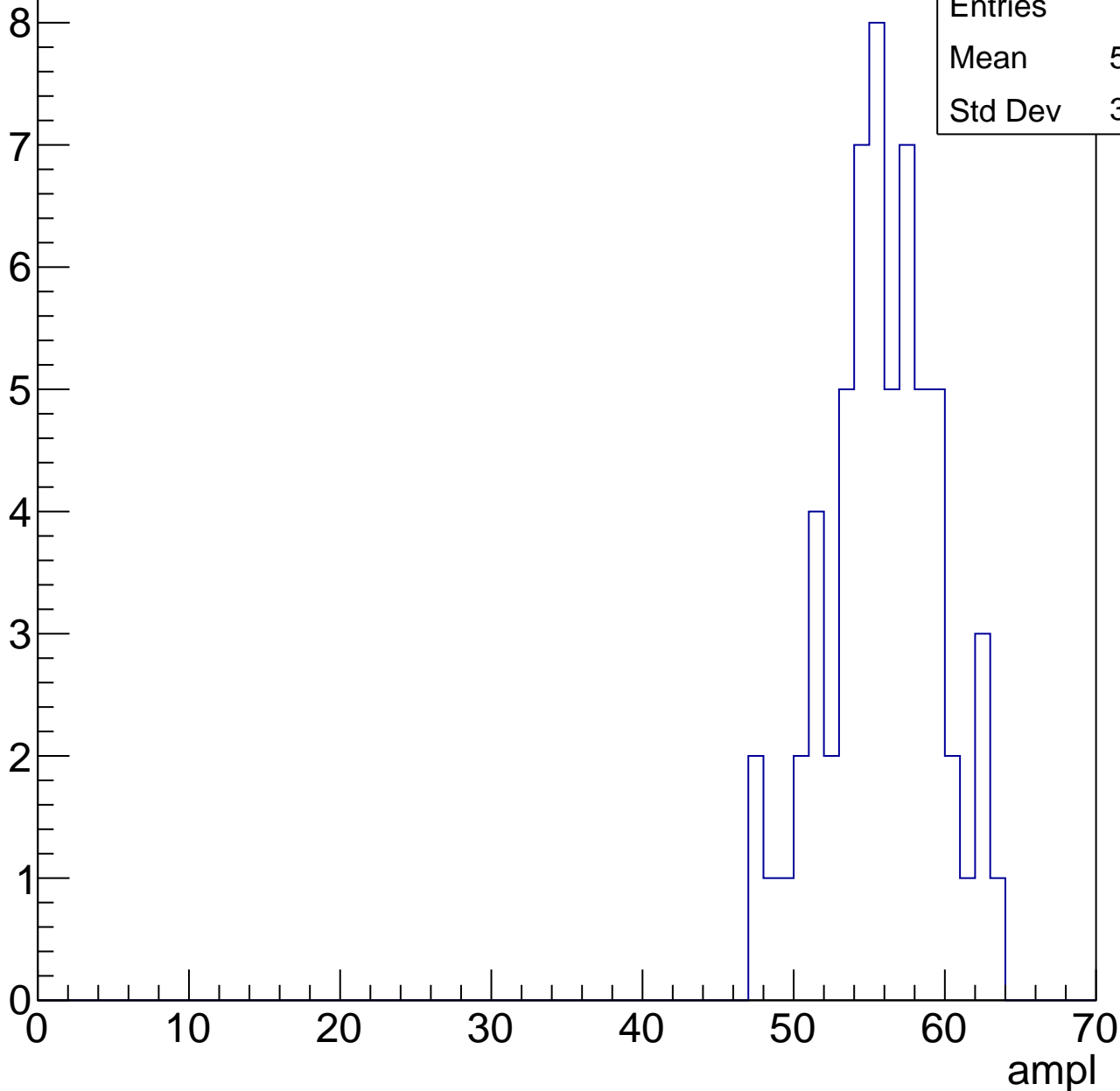


B1L102S, U10-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.34
Std Dev	3.666

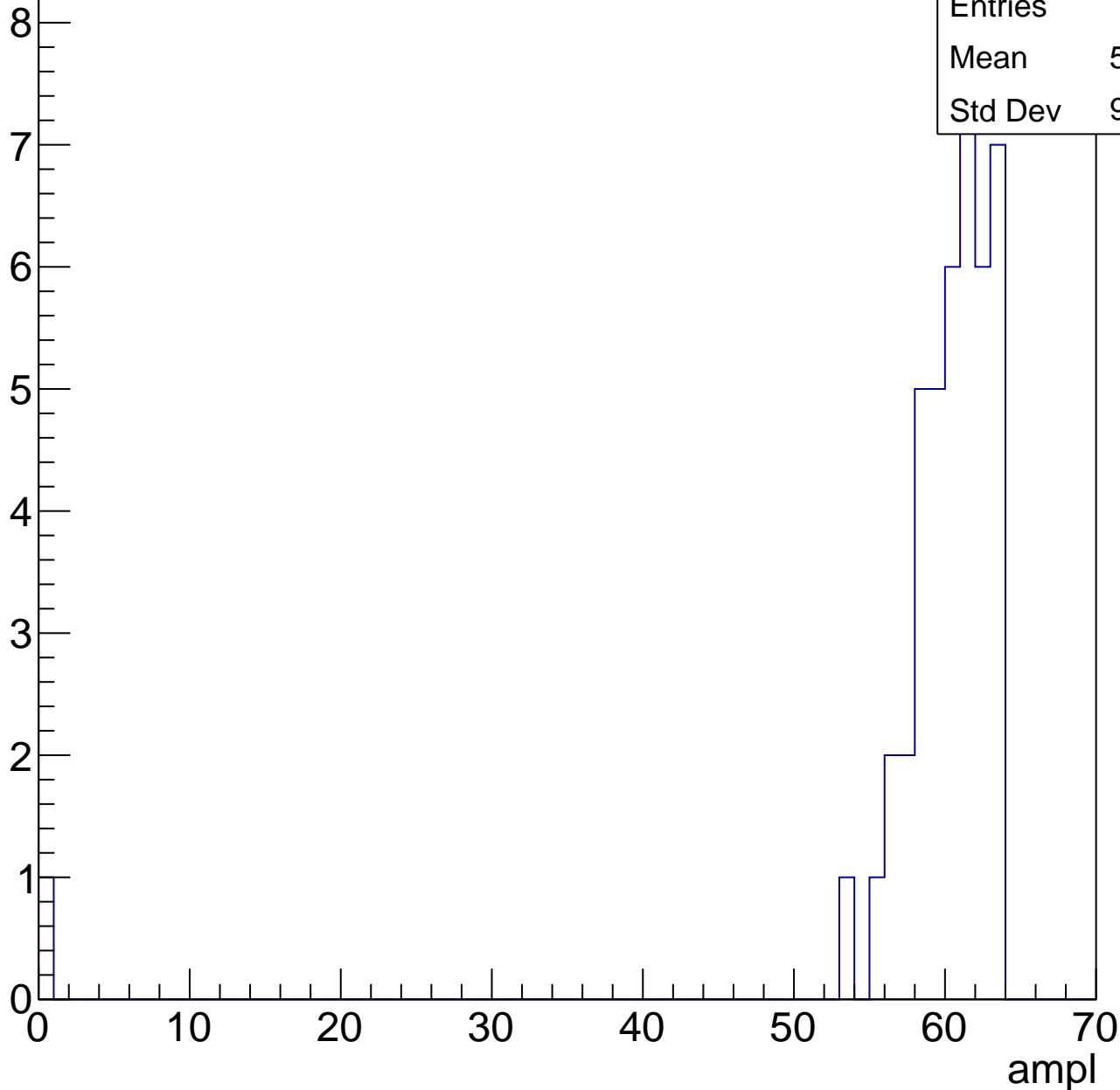


B1L102S, U10-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.64
Std Dev	9.247



B1L102S, U10-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

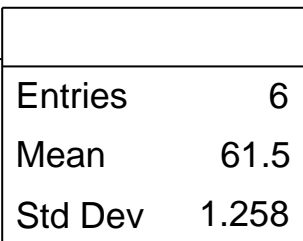
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch50, adc0

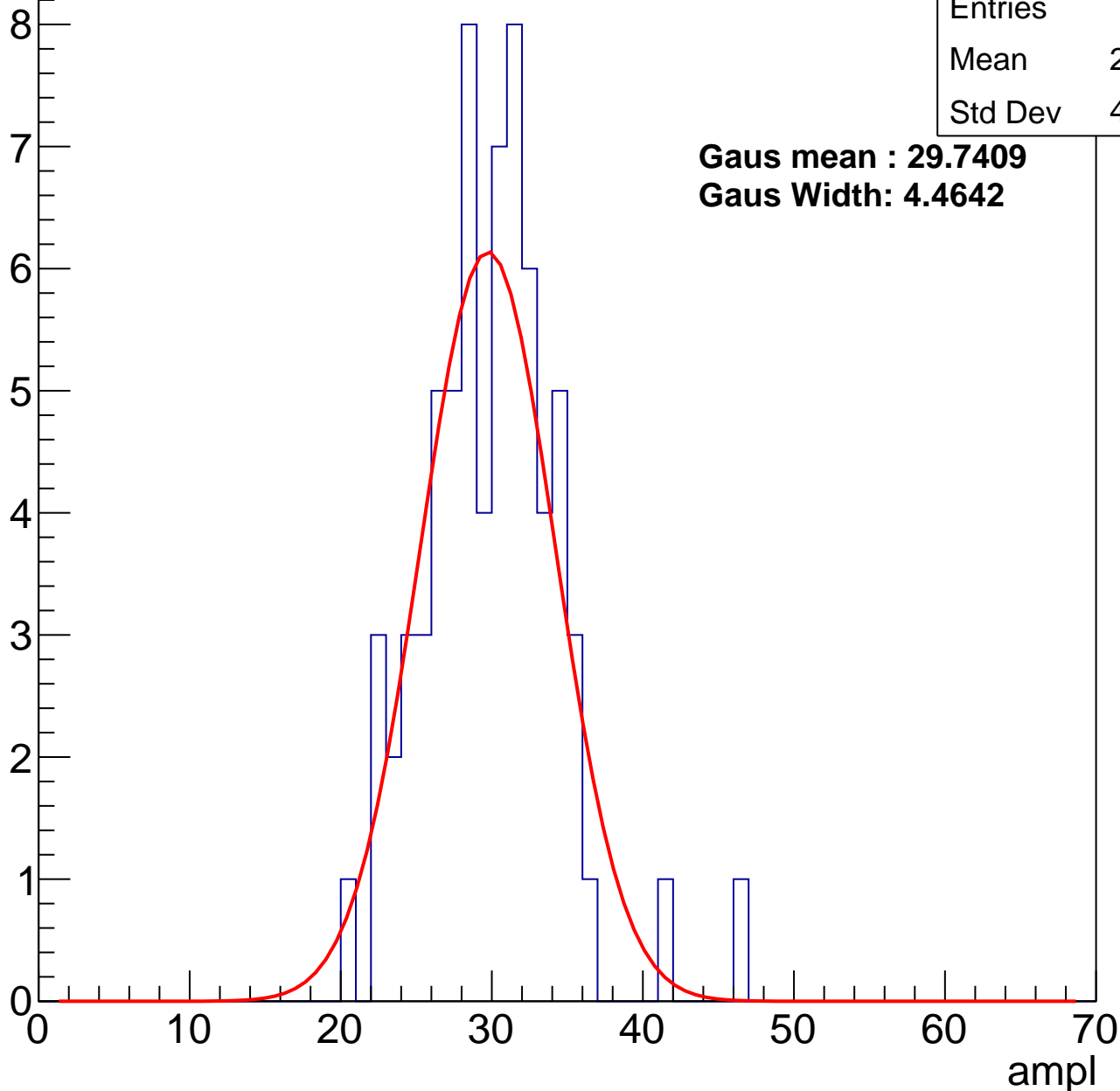
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.49
Std Dev	4.394

Gaus mean : 29.7409

Gaus Width: 4.4642

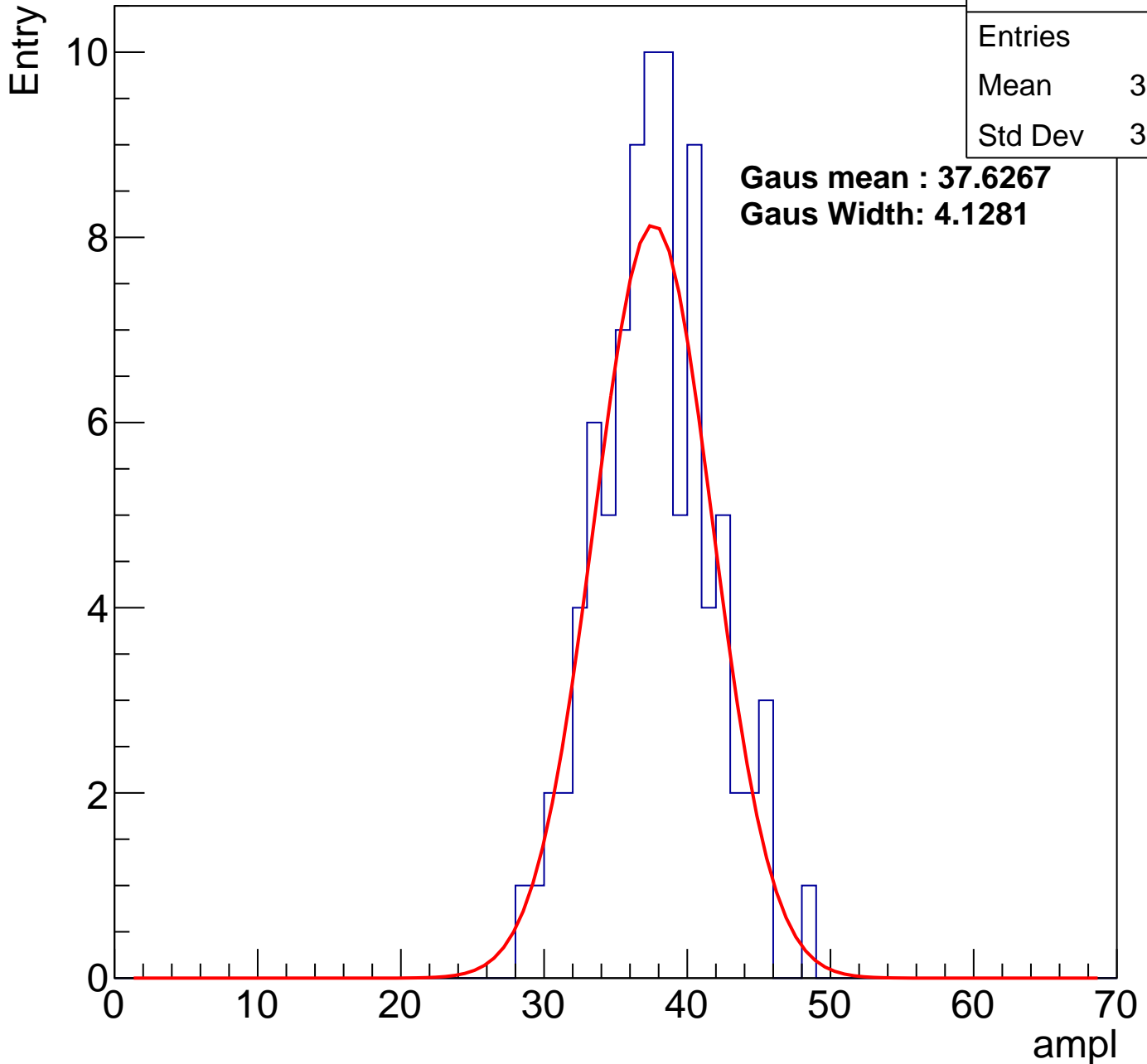


B1L102S, U10-ch50, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	37.27
Std Dev	3.945

Gaus mean : 37.6267
Gaus Width: 4.1281



B1L102S, U10-ch50, adc2

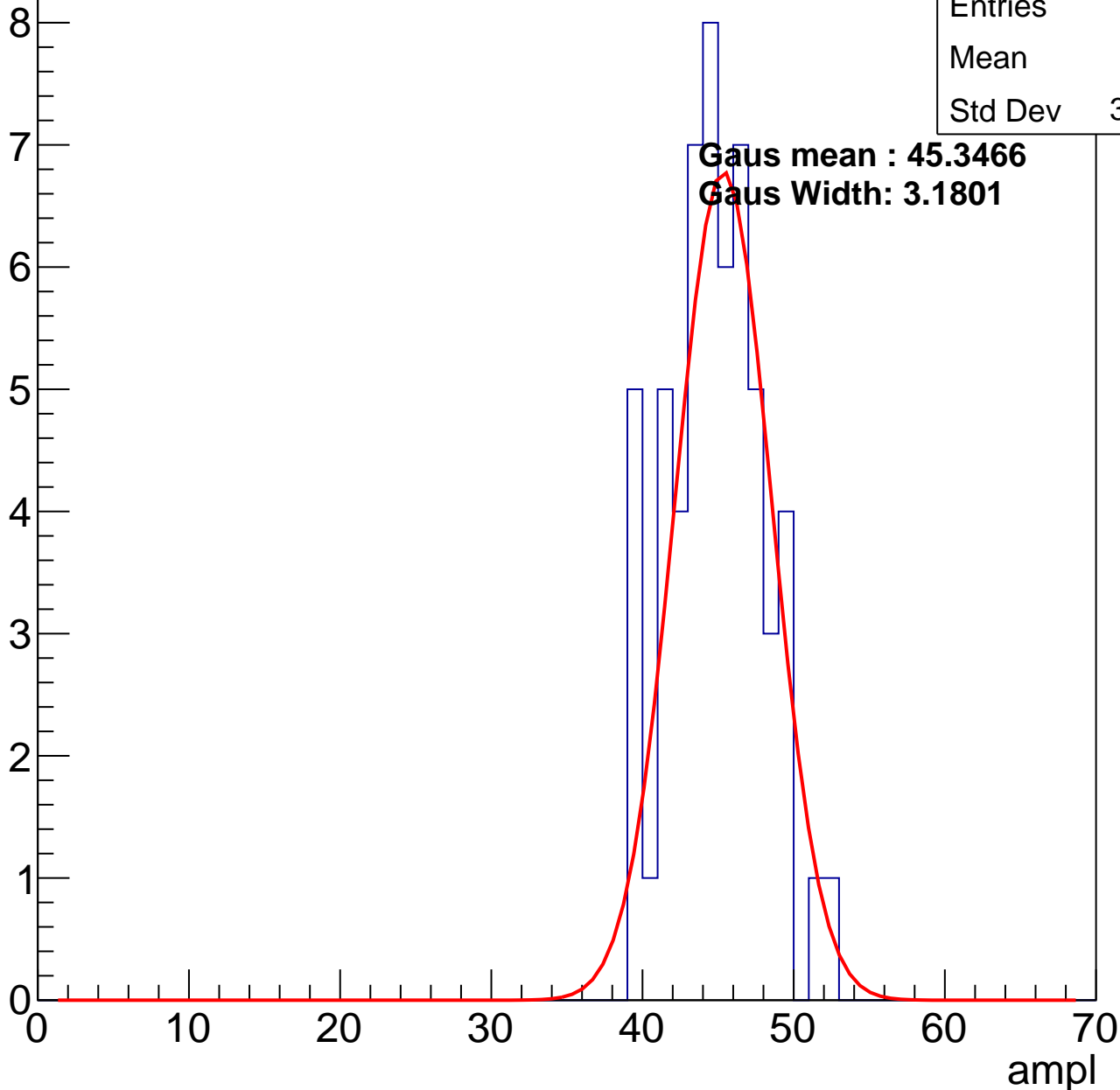
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	44.4
Std Dev	3.077

Gaus mean : 45.3466

Gaus Width: 3.1801

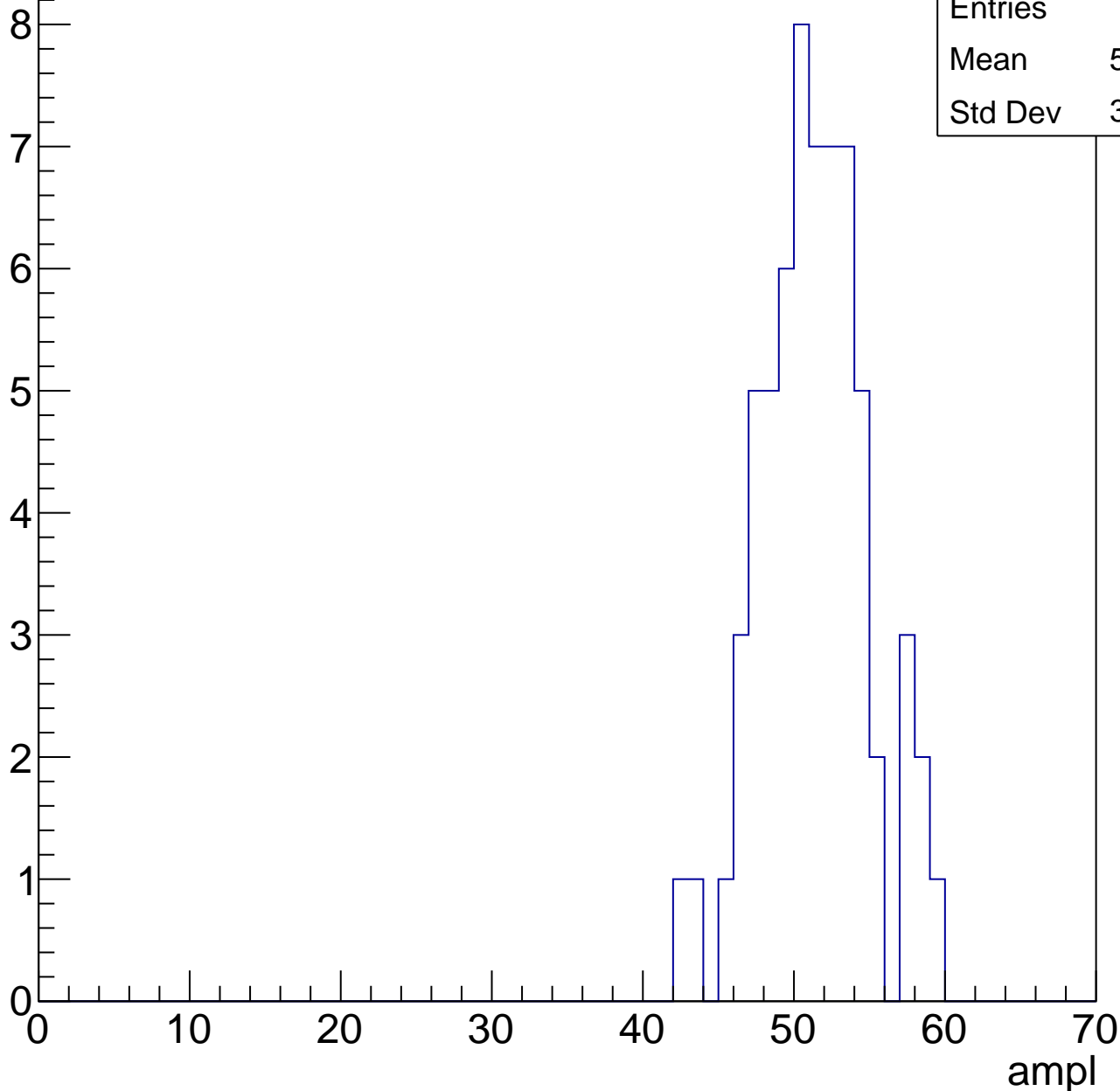


B1L102S, U10-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.86
Std Dev	3.522

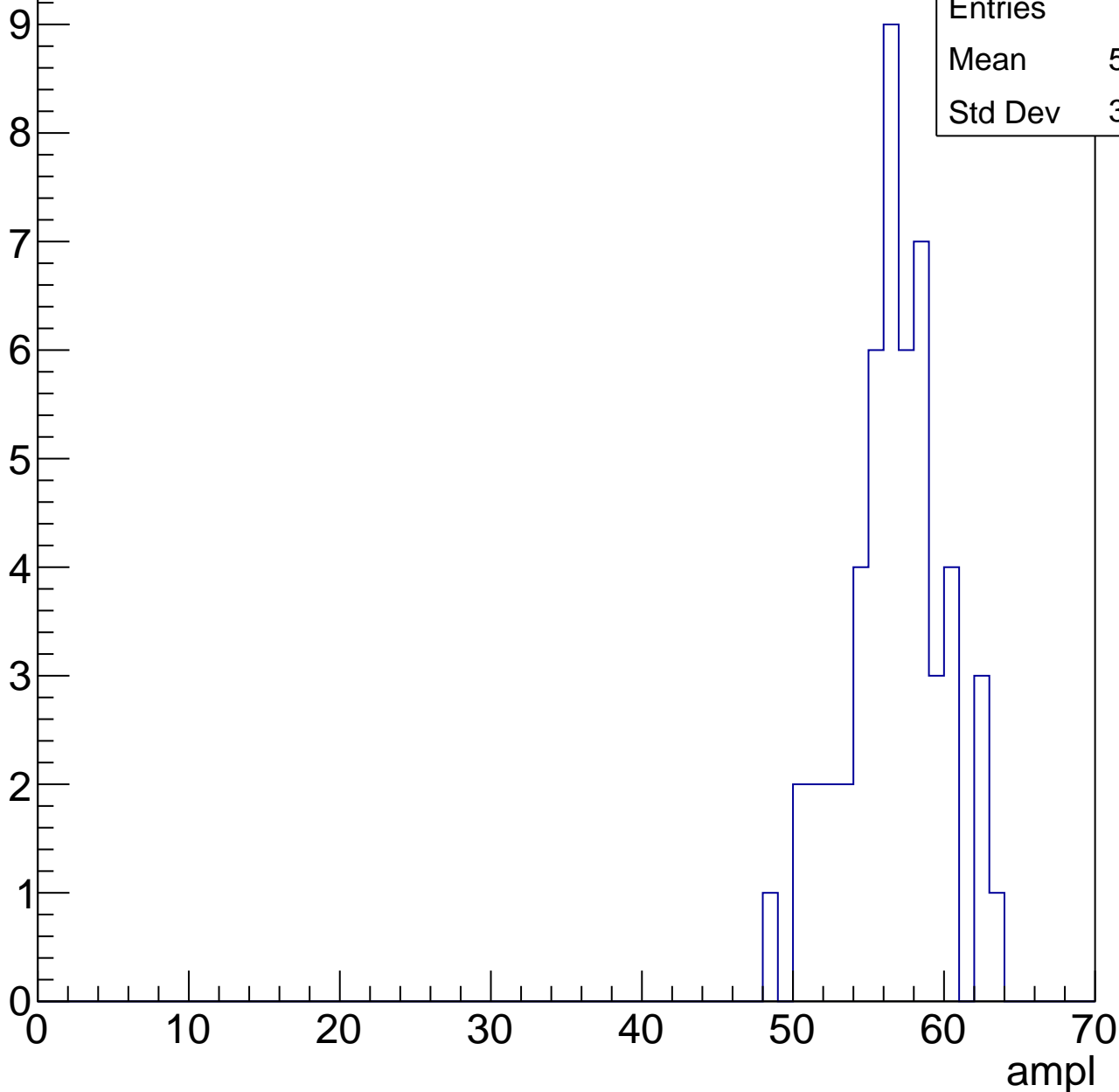


B1L102S, U10-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.23
Std Dev	3.214

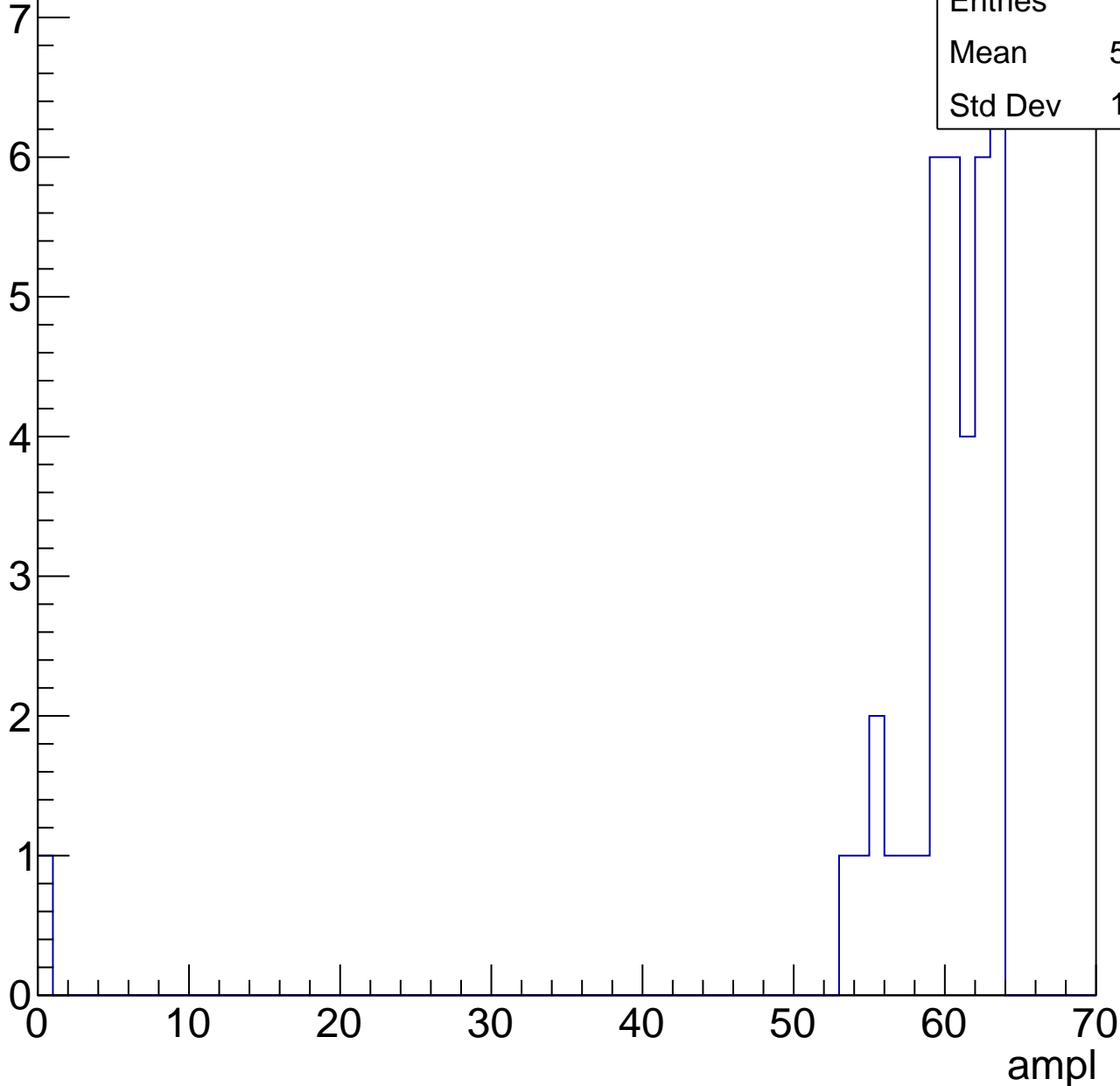


B1L102S, U10-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.35
Std Dev	10.08

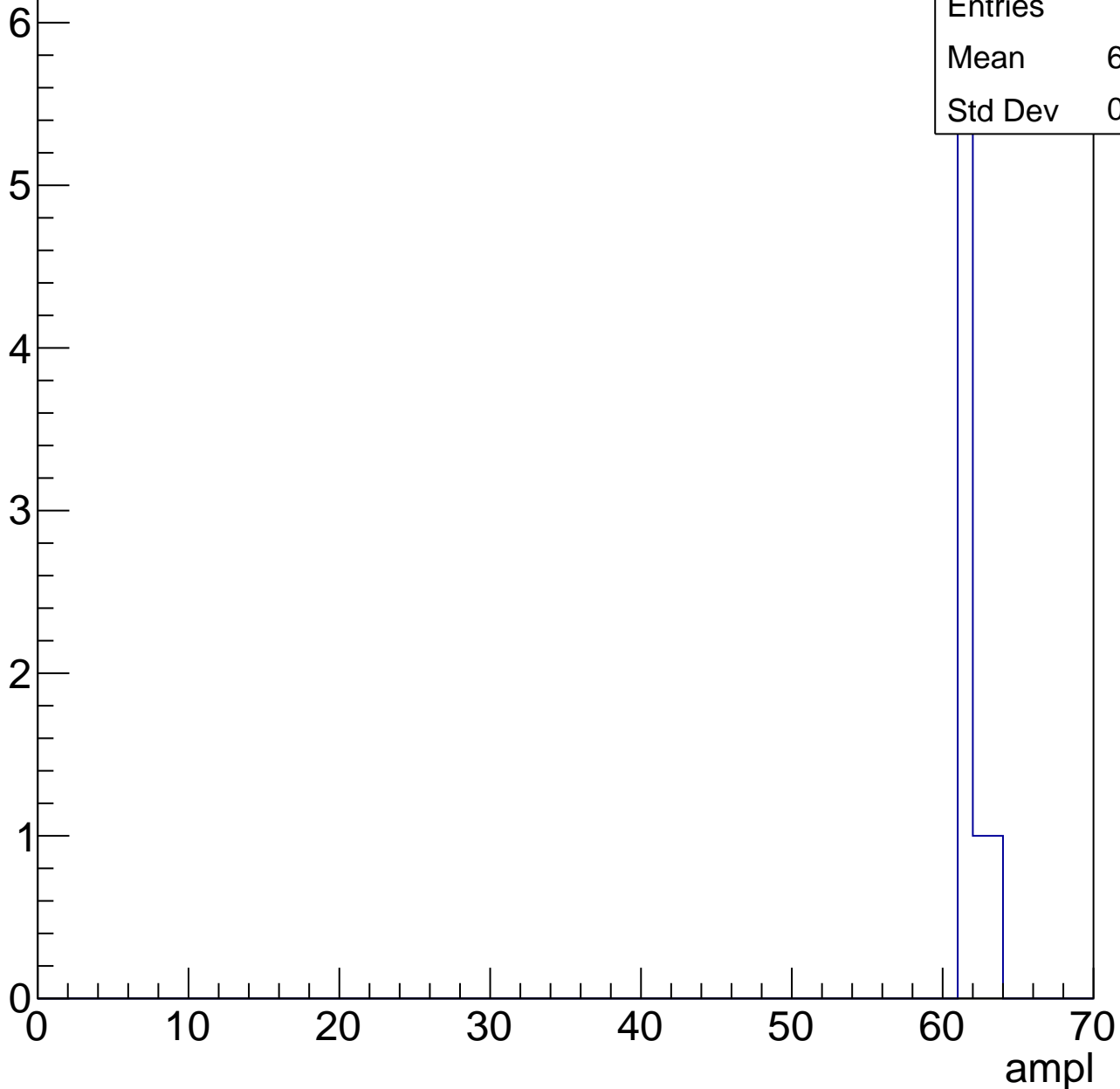


B1L102S, U10-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	61.38
Std Dev	0.696



B1L102S, U10-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch51, adc0

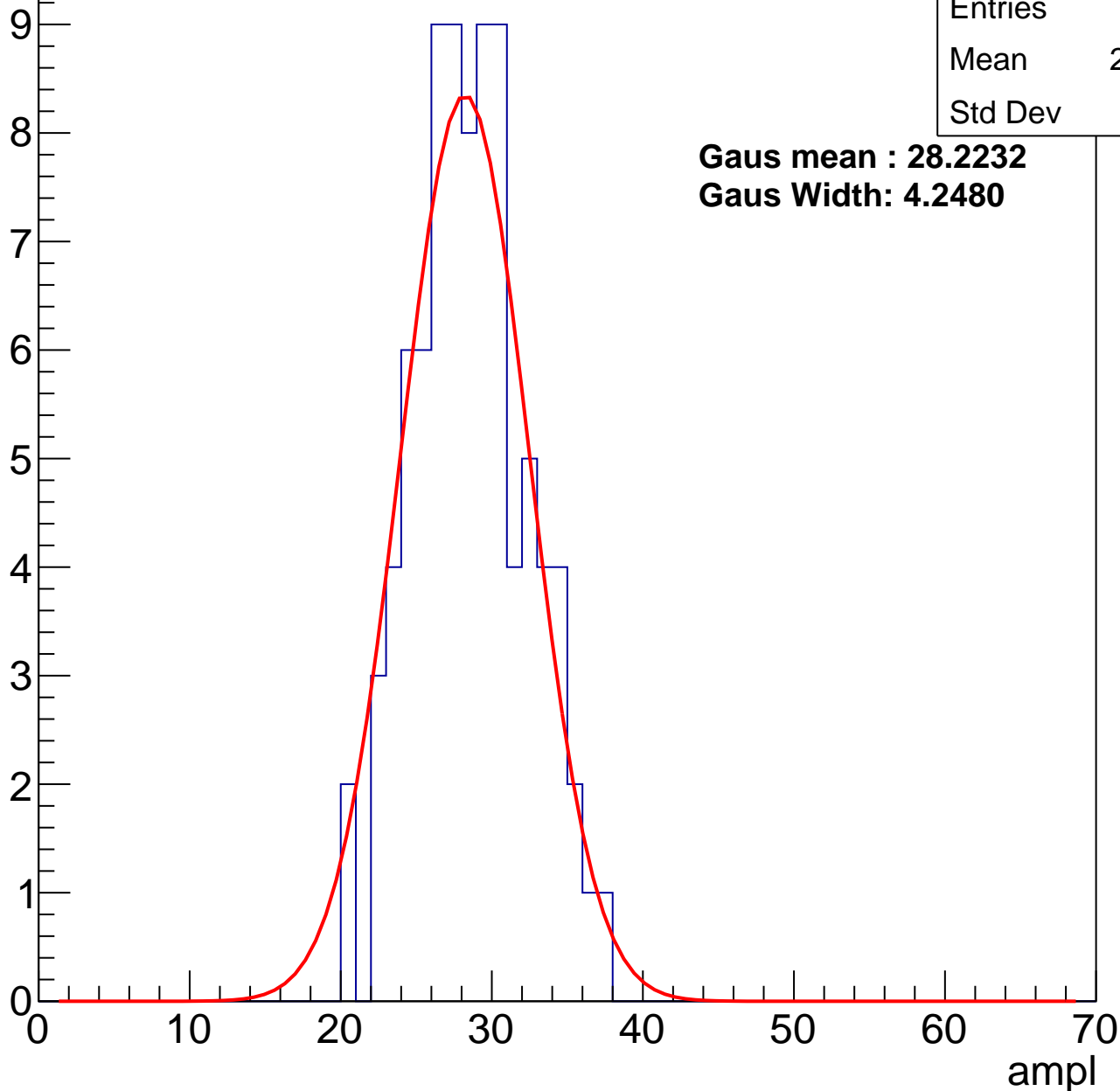
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.13
Std Dev	3.7

Gaus mean : 28.2232

Gaus Width: 4.2480



B1L102S, U10-ch51, adc1

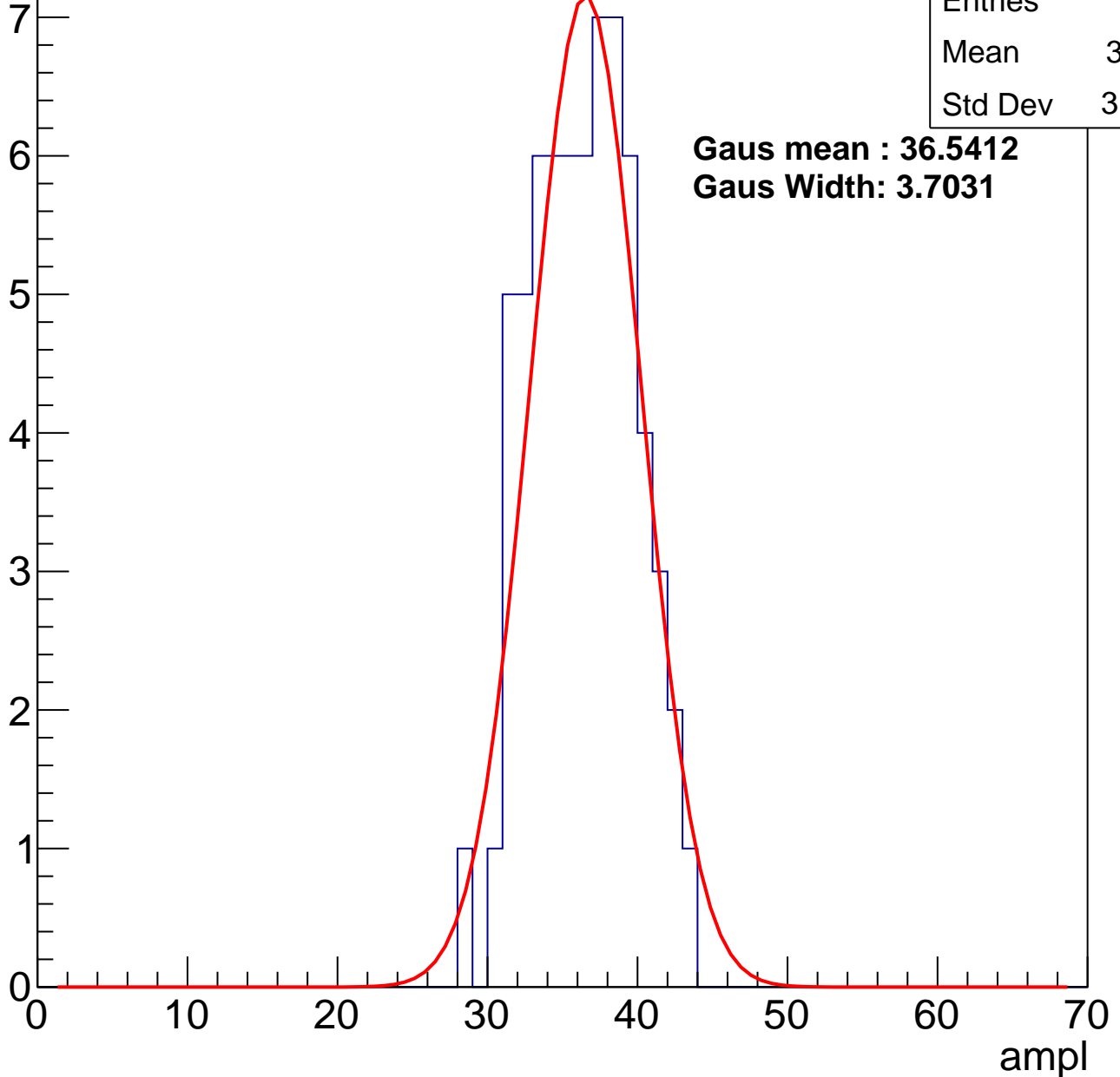
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.91
Std Dev	3.343

Gaus mean : 36.5412

Gaus Width: 3.7031



B1L102S, U10-ch51, adc2

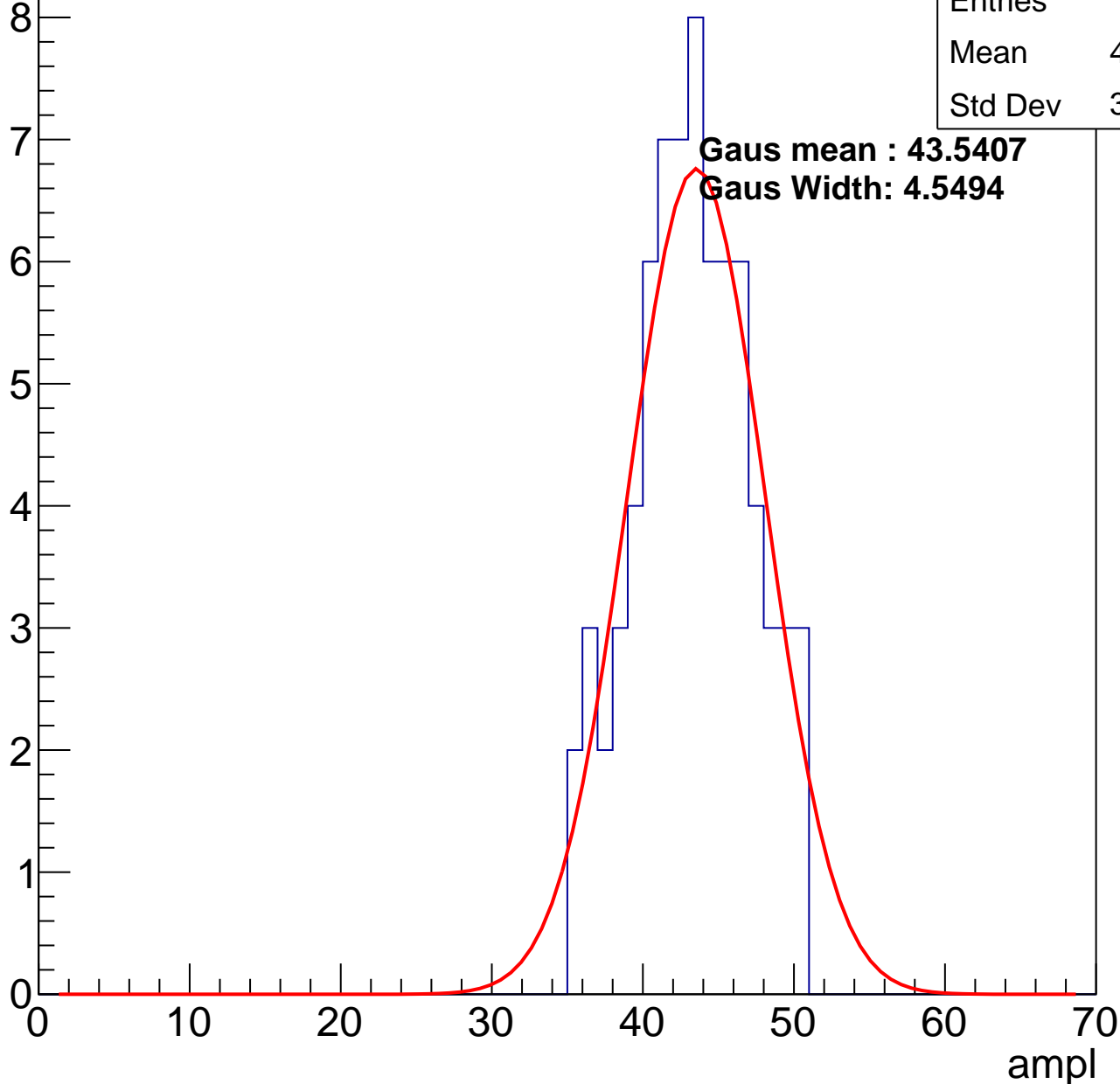
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.82
Std Dev	3.794

Gaus mean : 43.5407

Gaus Width: 4.5494

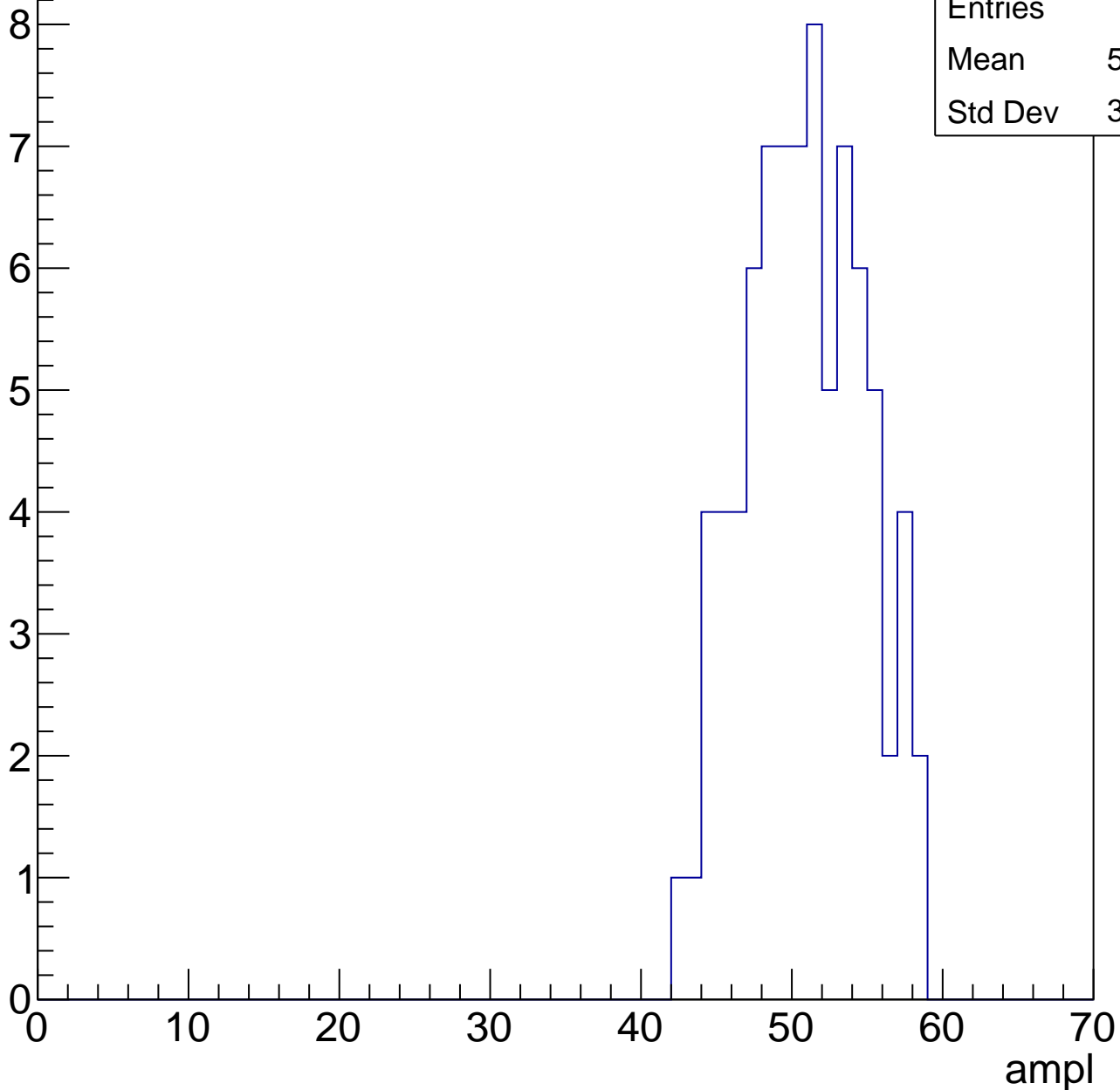


B1L102S, U10-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.38
Std Dev	3.884

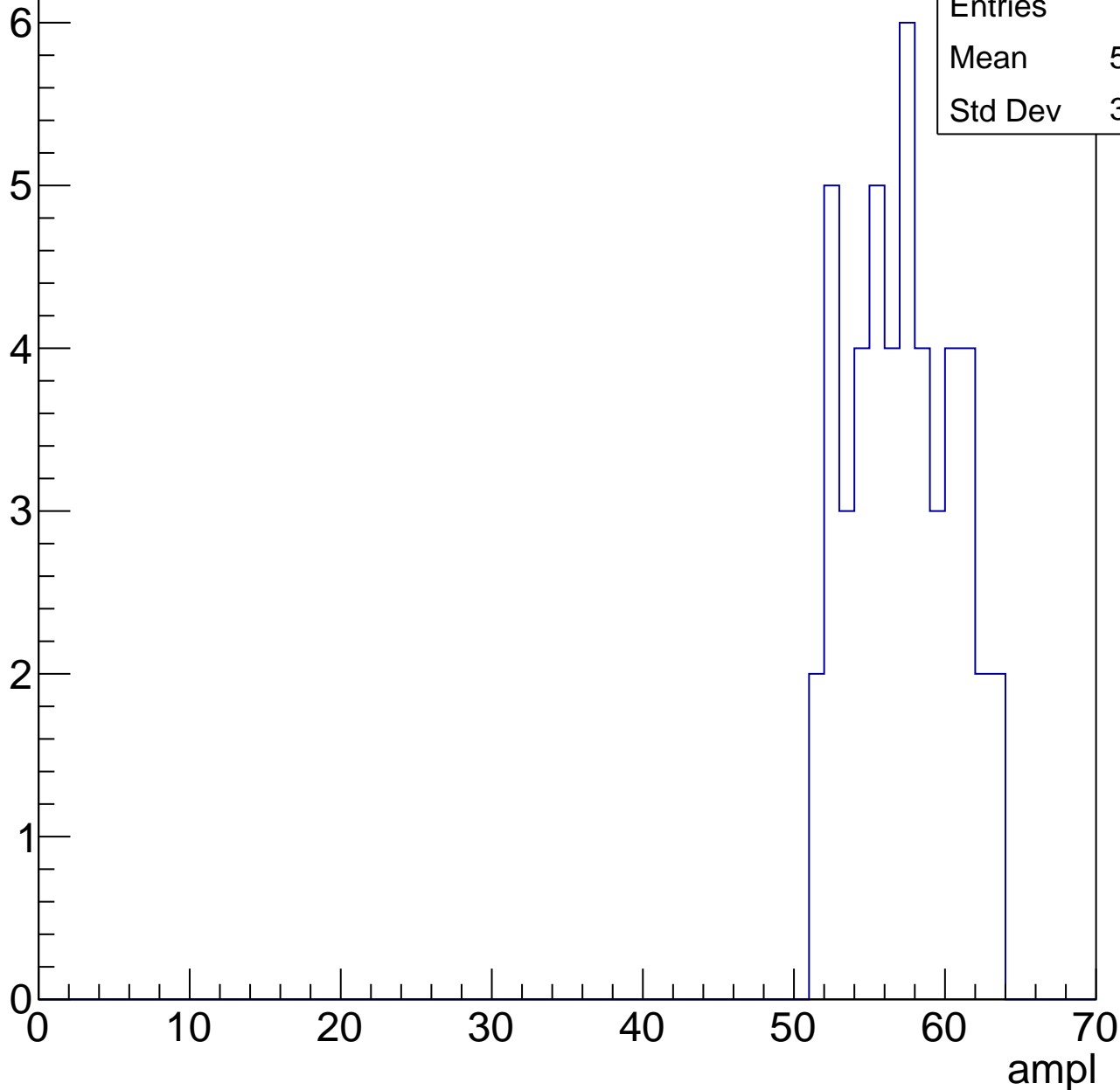


B1L102S, U10-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.69
Std Dev	3.349

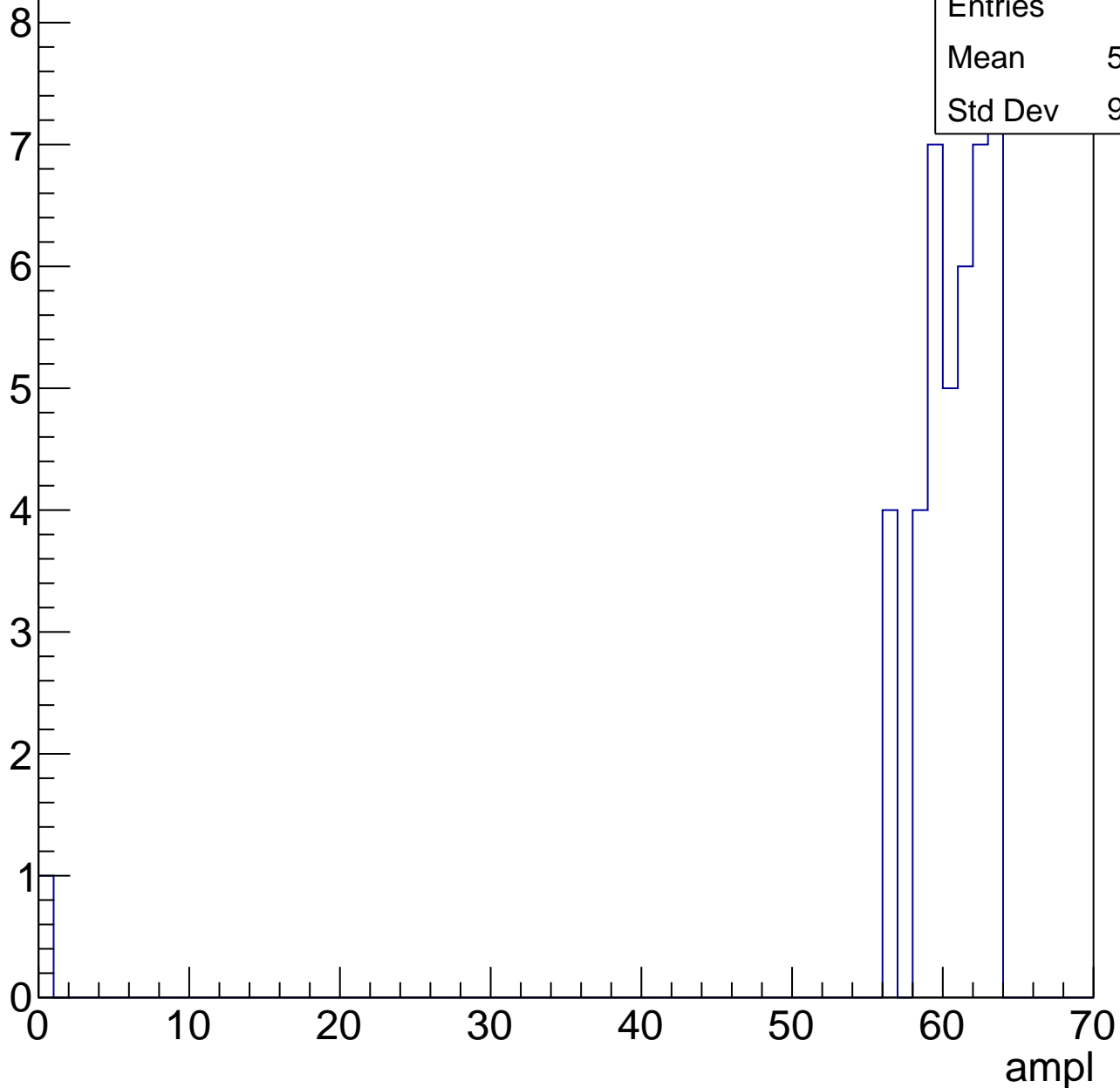


B1L102S, U10-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.88
Std Dev	9.437



B1L102S, U10-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U10-ch52, adc0

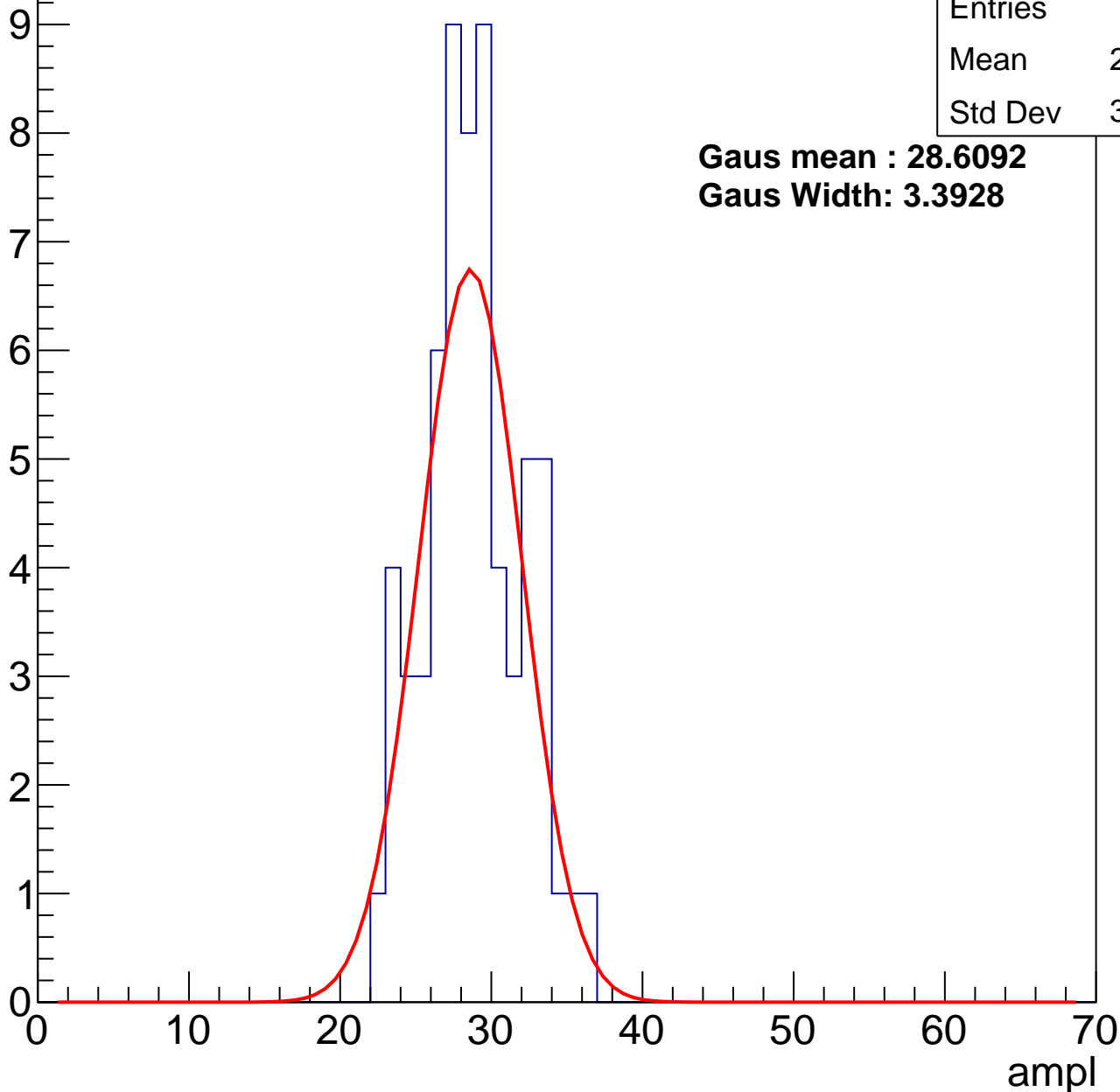
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.38
Std Dev	3.189

Gaus mean : 28.6092

Gaus Width: 3.3928



B1L102S, U10-ch52, adc1

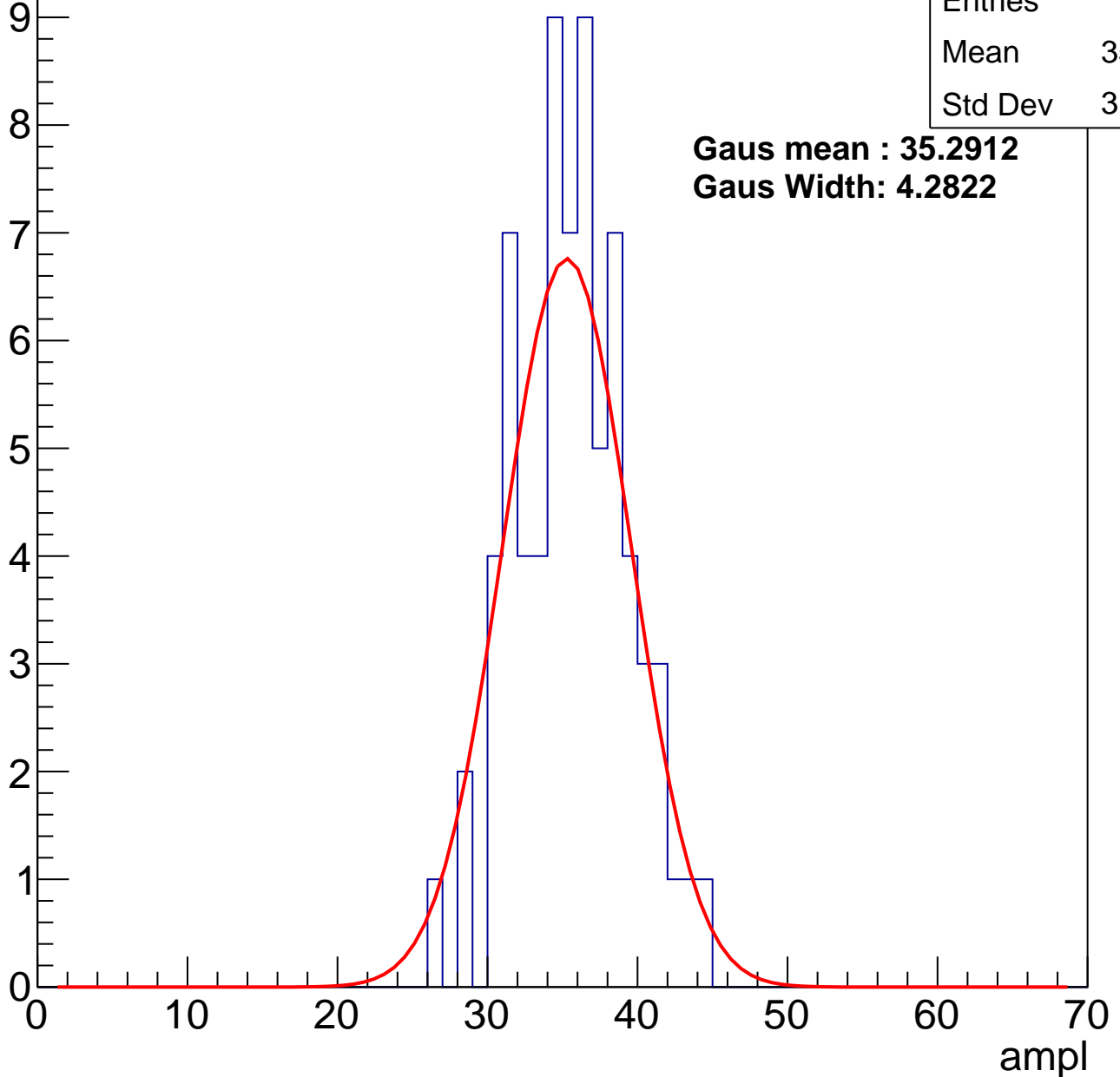
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.18
Std Dev	3.694

Gaus mean : 35.2912

Gaus Width: 4.2822



B1L102S, U10-ch52, adc2

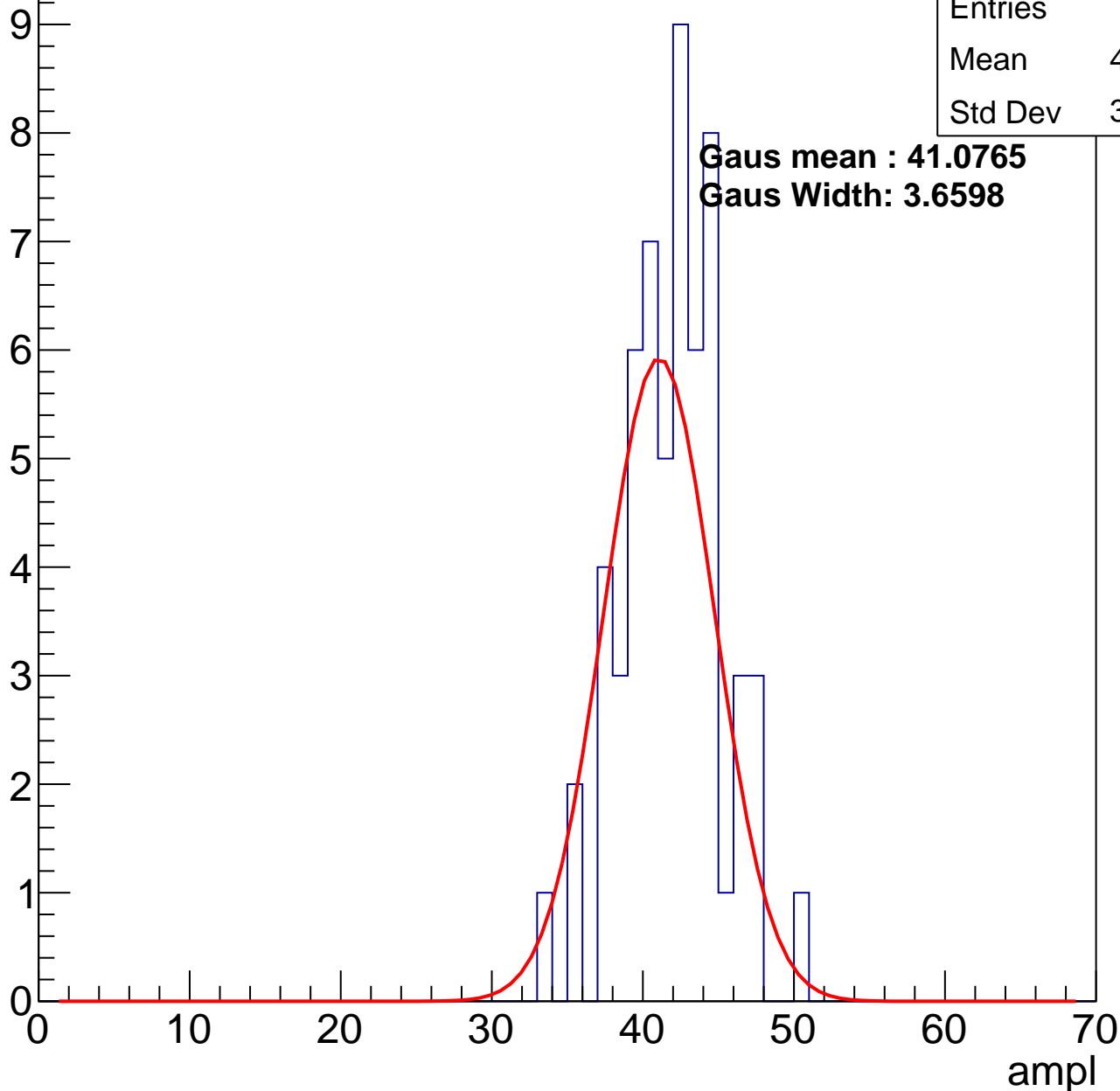
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.46
Std Dev	3.269

Gaus mean : 41.0765

Gaus Width: 3.6598

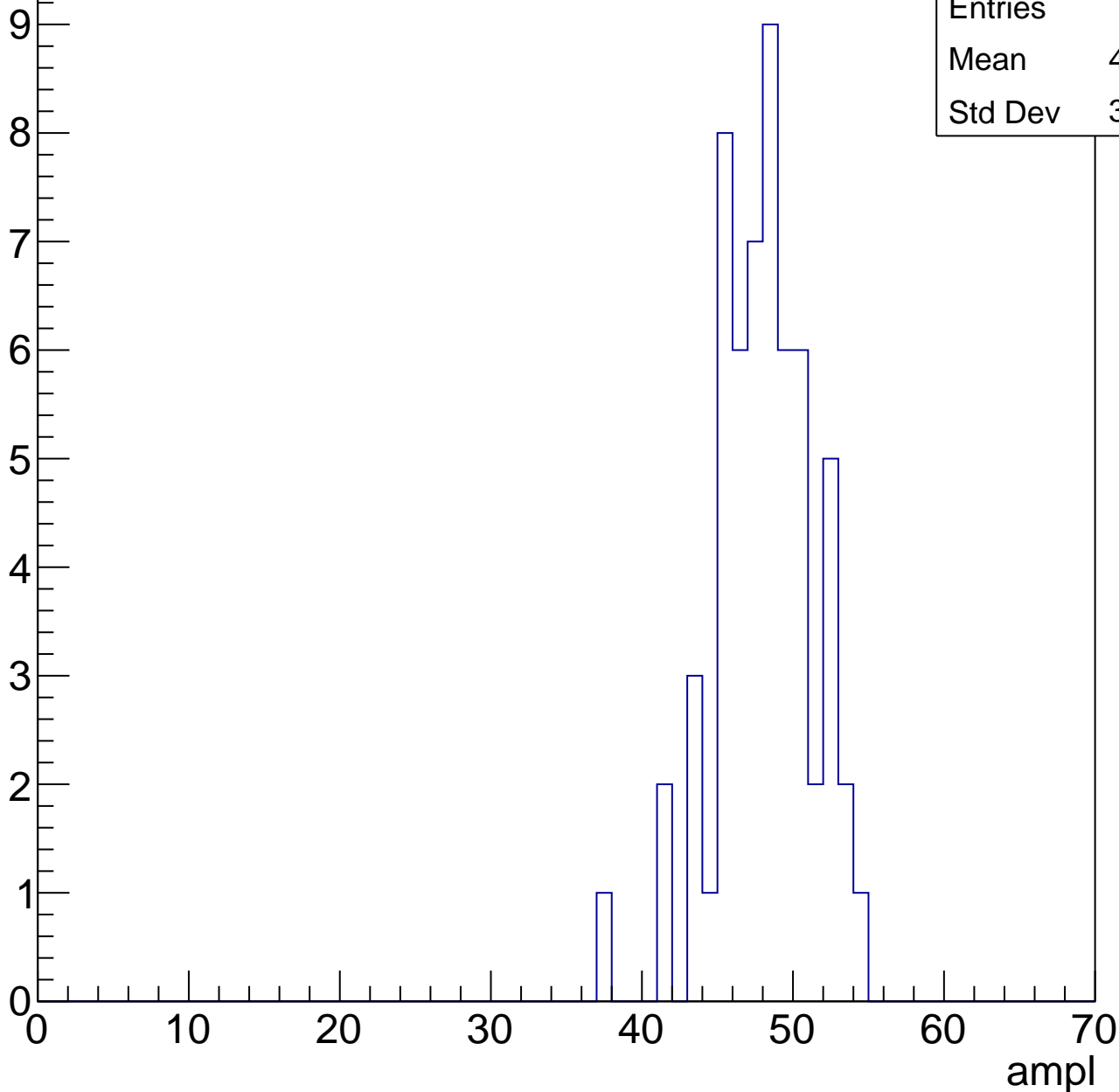


B1L102S, U10-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.54
Std Dev	3.228

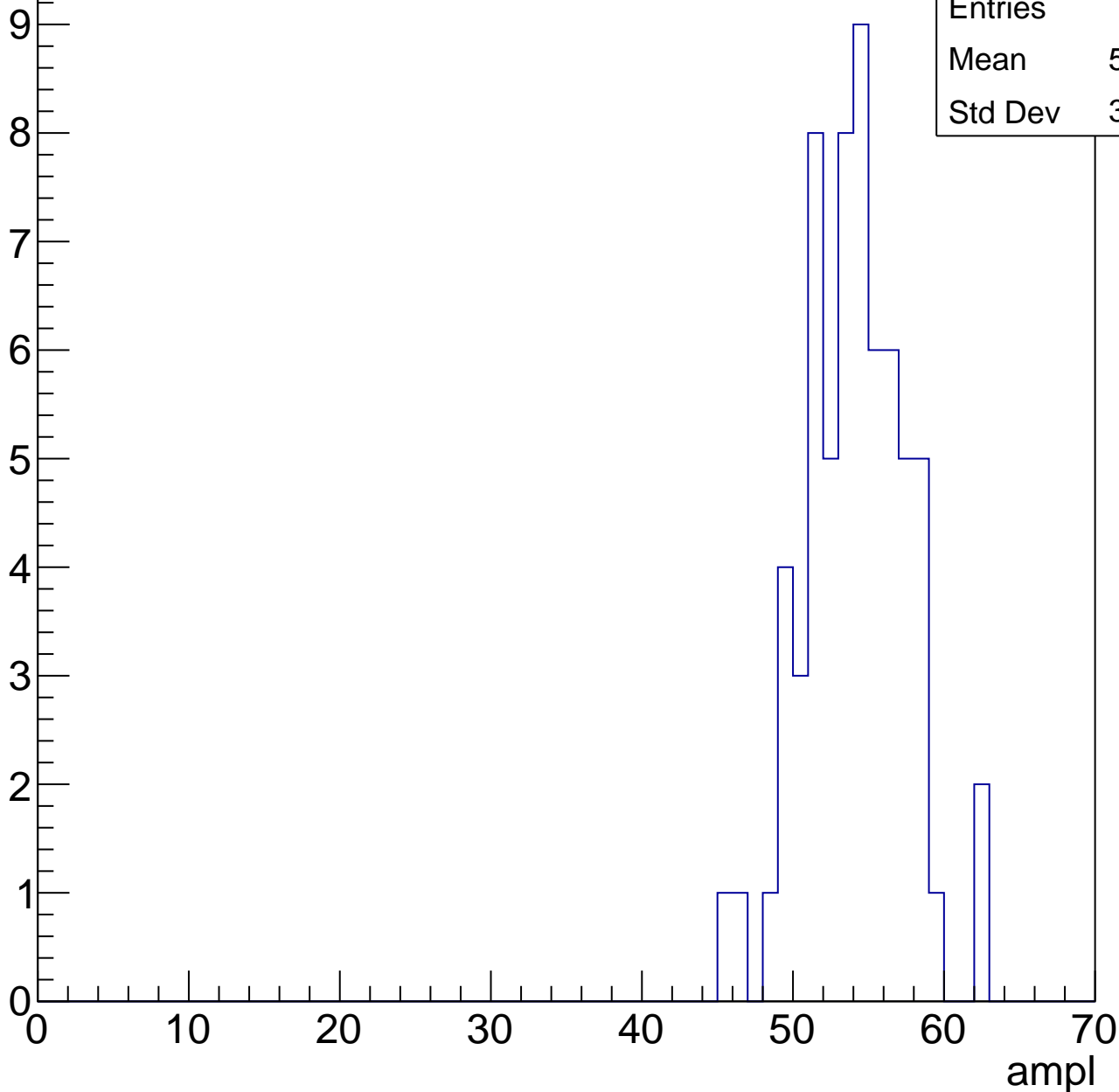


B1L102S, U10-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.65
Std Dev	3.349



B1L102S, U10-ch52, adc5

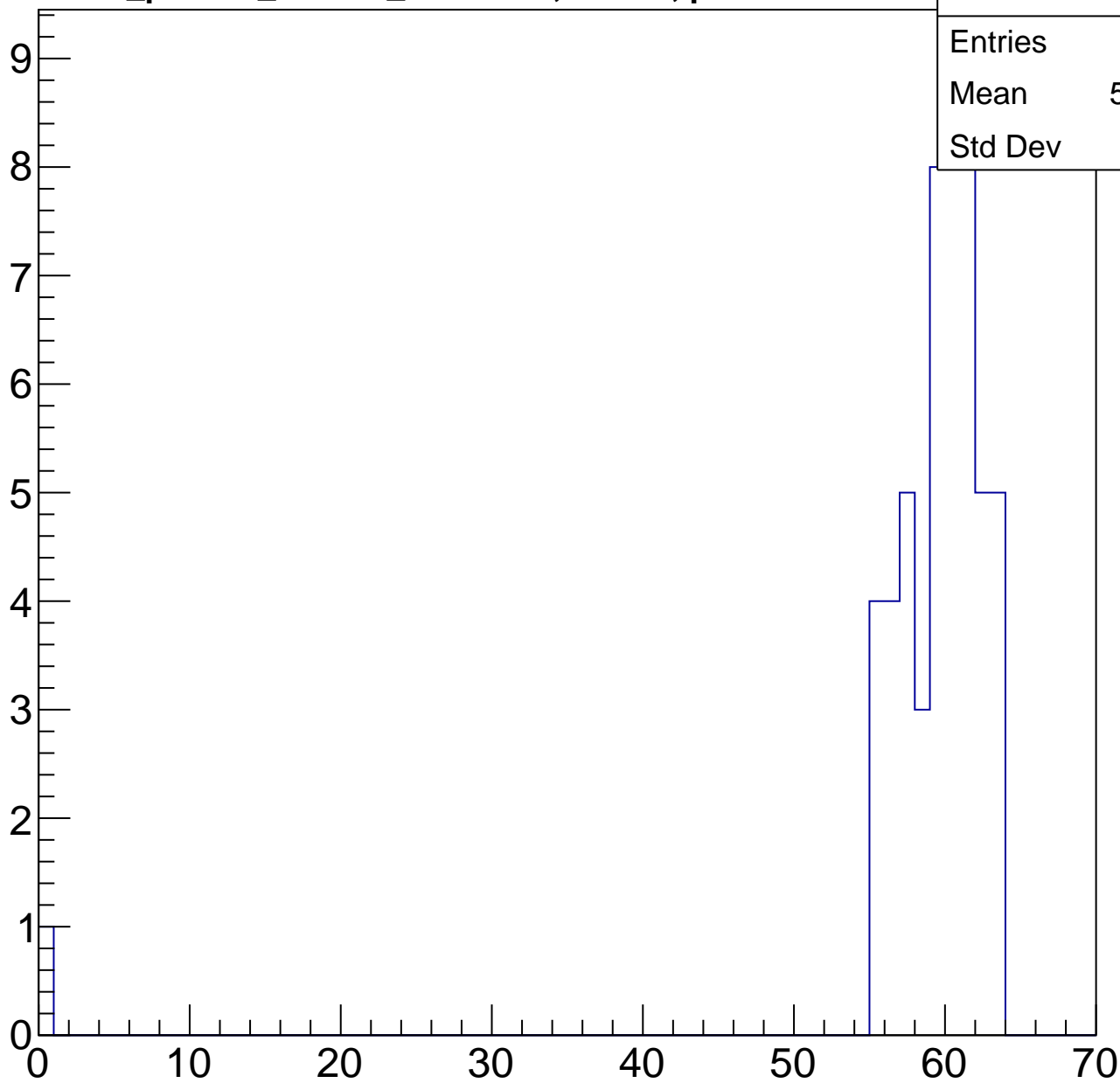
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.23
Std Dev	8.48

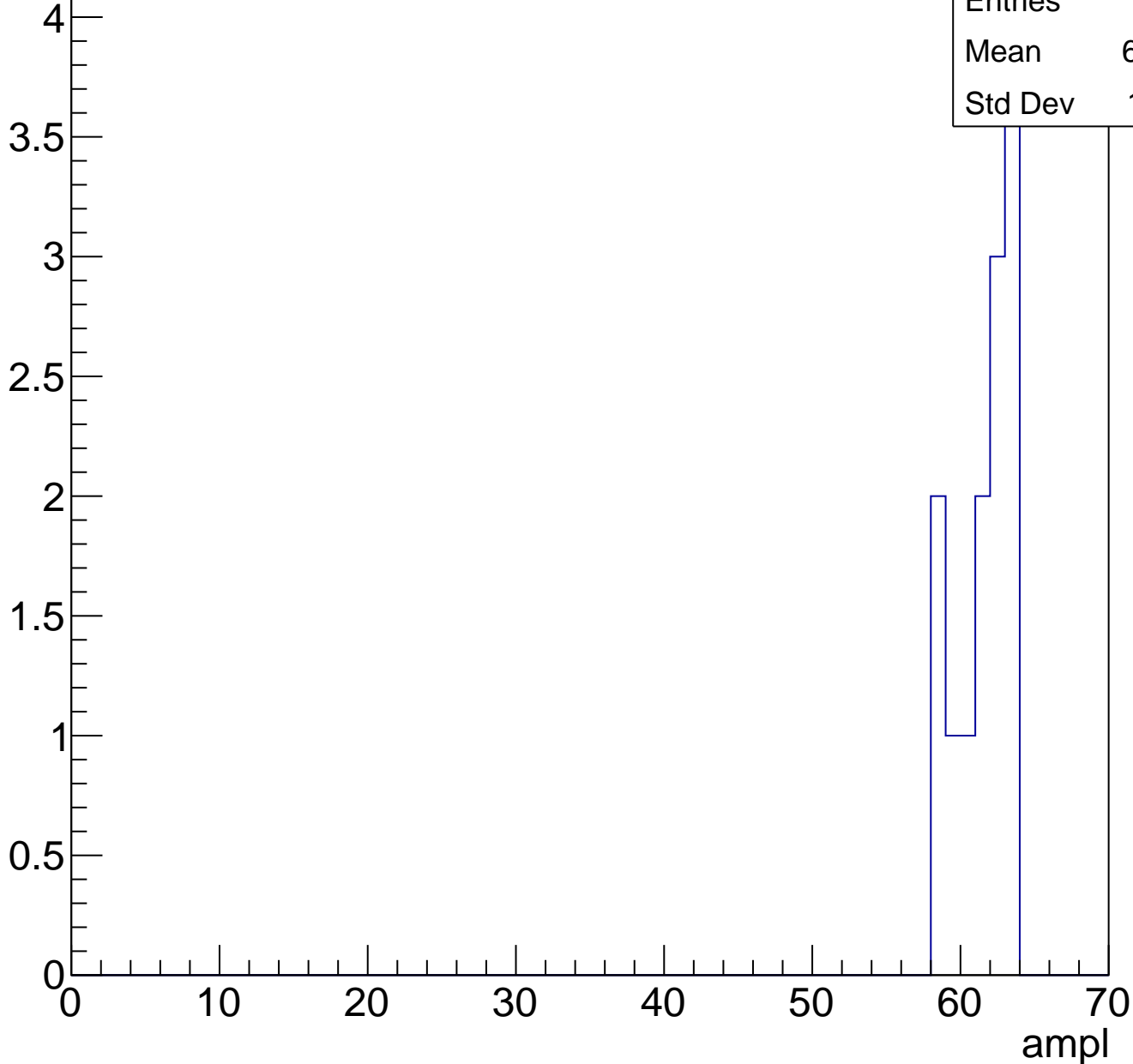
ampl



B1L102S, U10-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch53, adc0

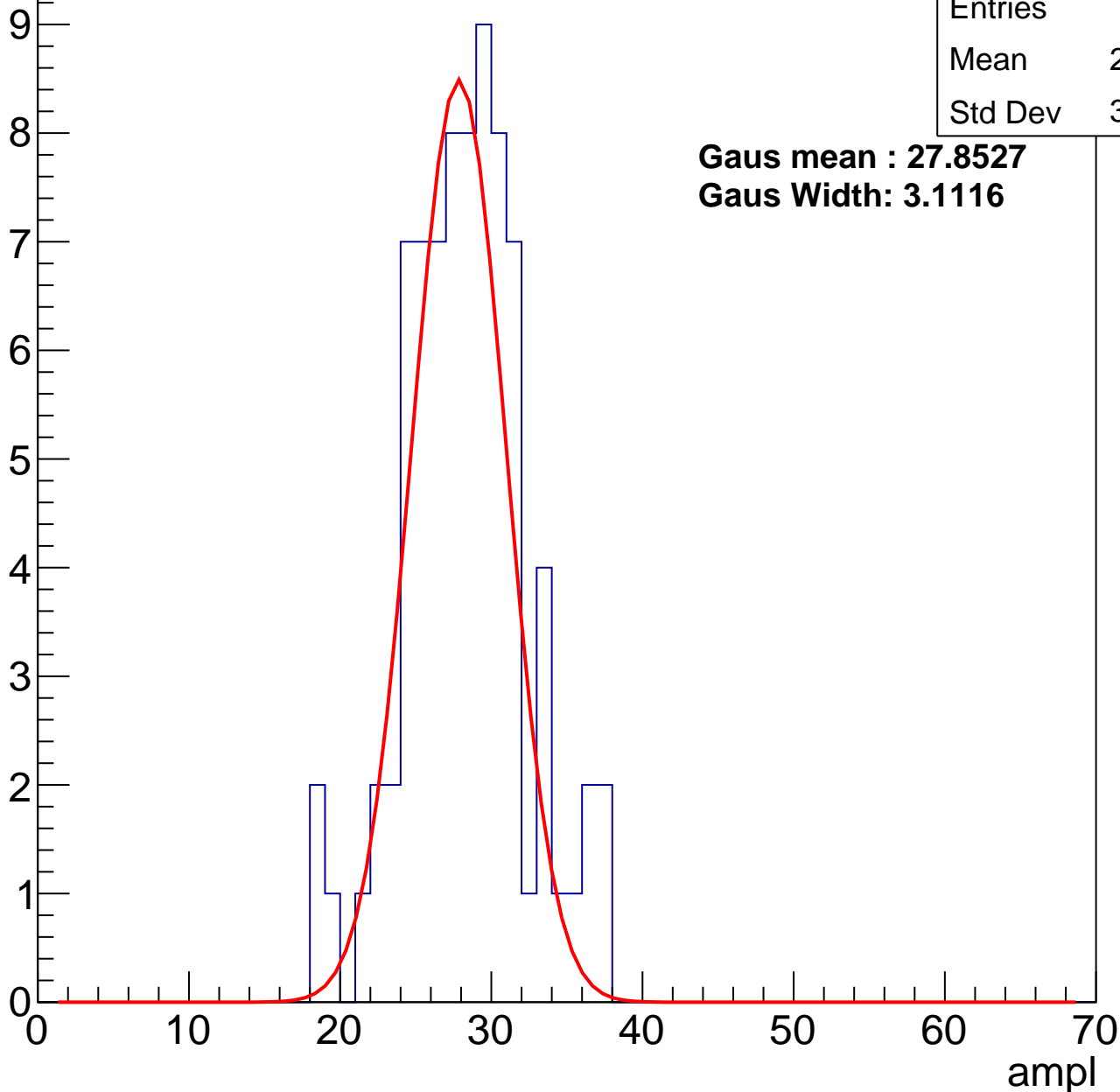
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.85
Std Dev	3.963

Gaus mean : 27.8527

Gaus Width: 3.1116



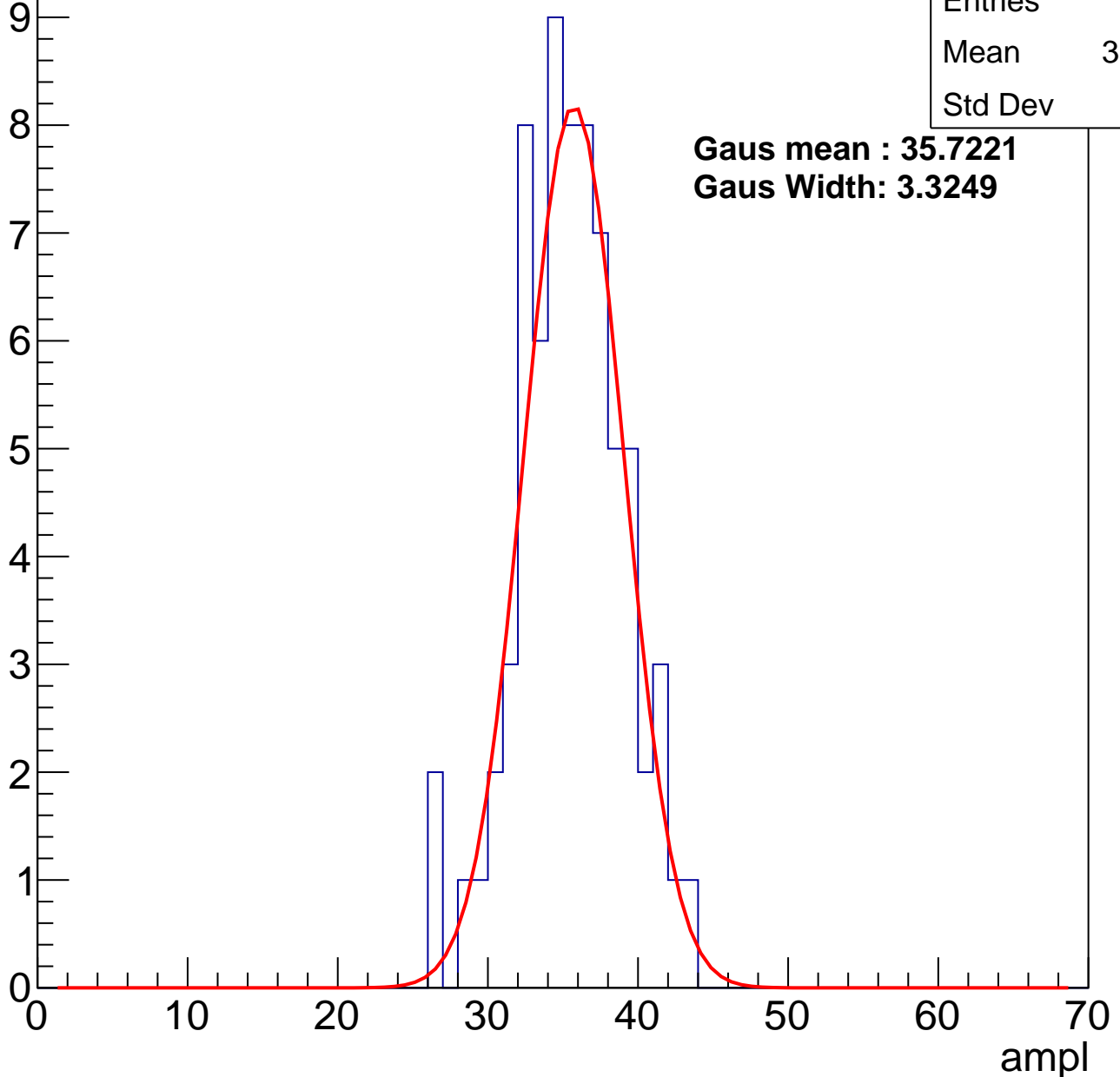
B1L102S, U10-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.03
Std Dev	3.5

Gaus mean : 35.7221
Gaus Width: 3.3249



B1L102S, U10-ch53, adc2

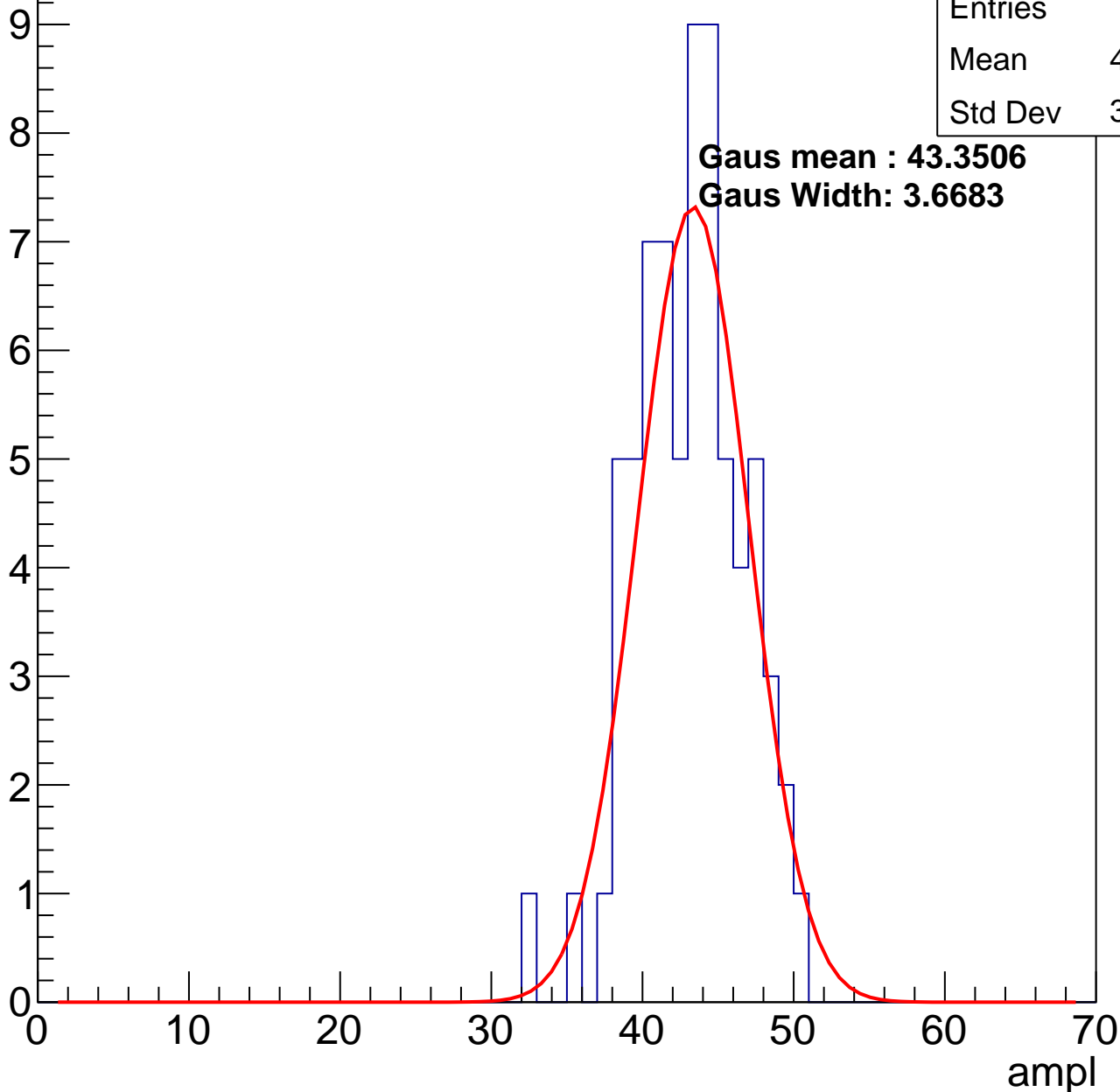
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.64
Std Dev	3.493

Gaus mean : 43.3506

Gaus Width: 3.6683

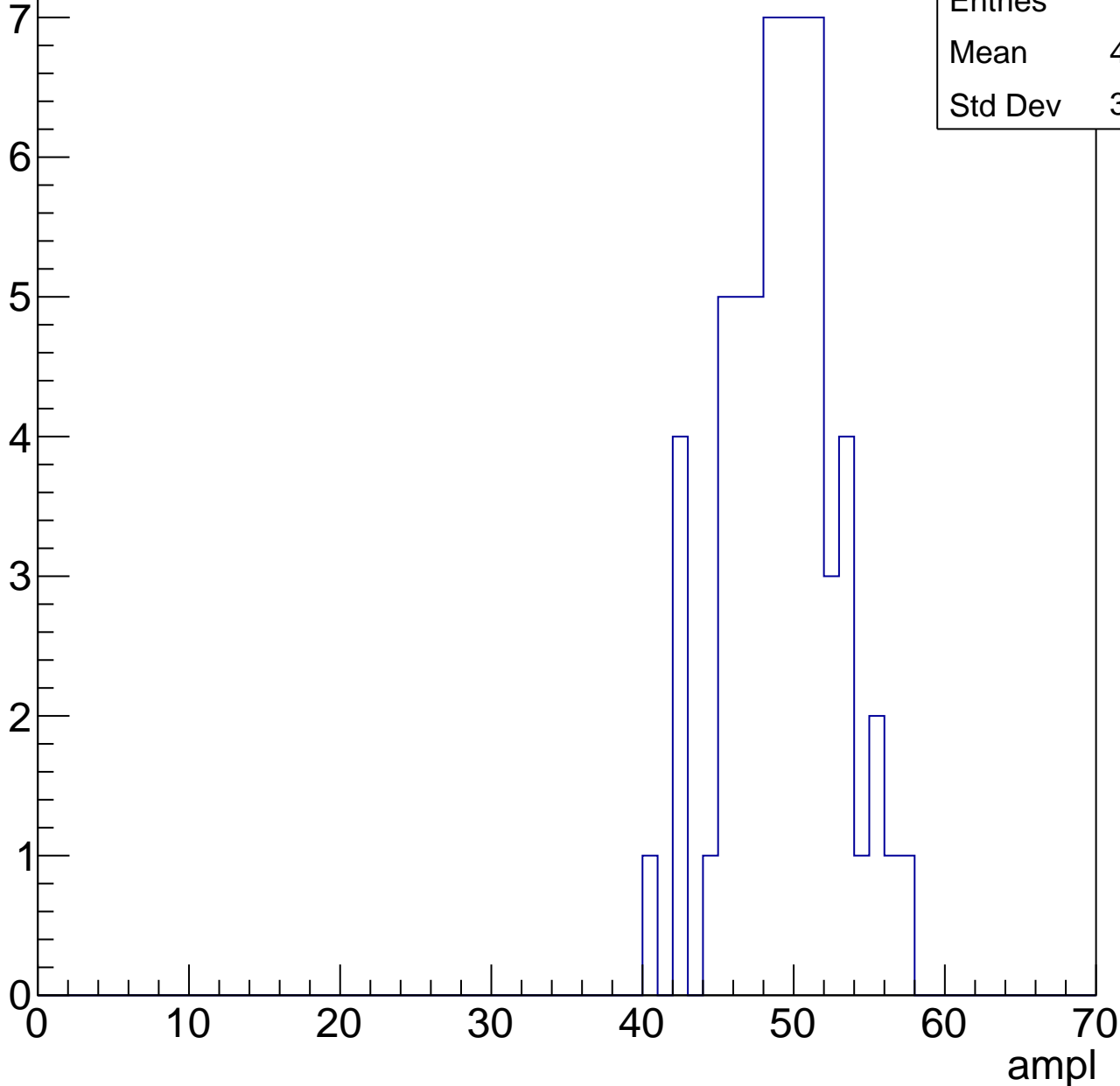


B1L102S, U10-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.74
Std Dev	3.589

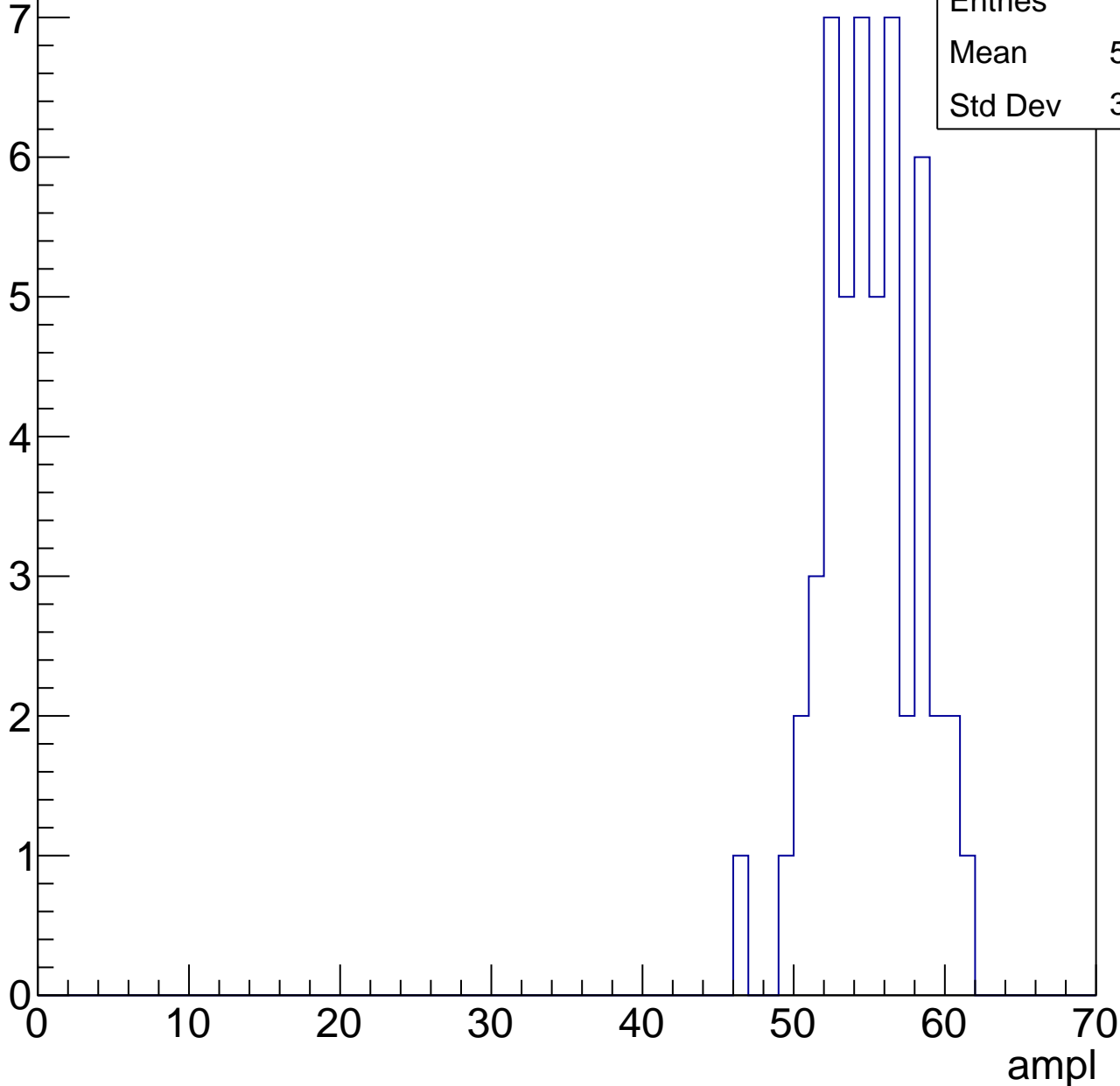


B1L102S, U10-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

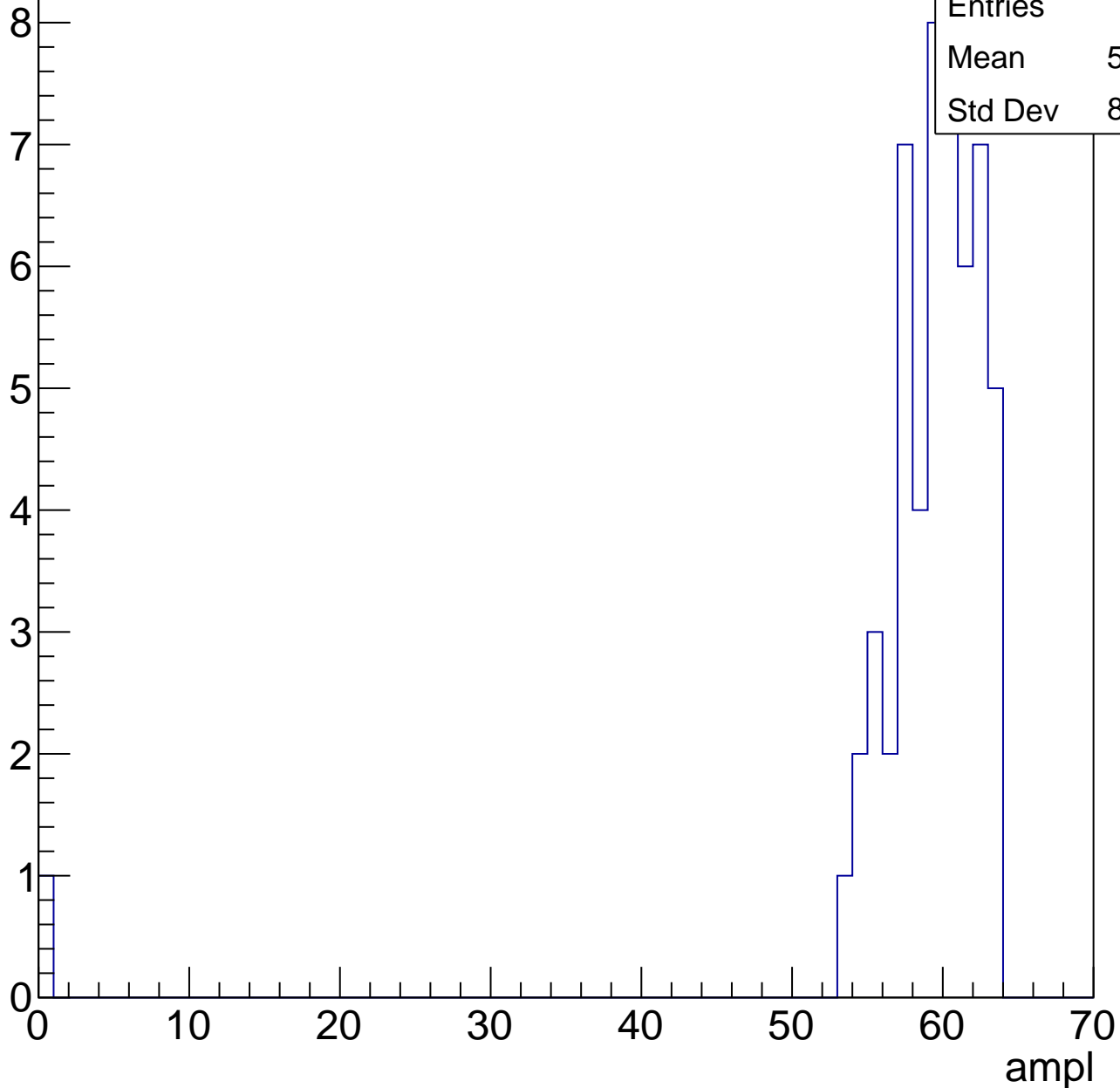
Entries	51
Mean	54.57
Std Dev	3.076



B1L102S, U10-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

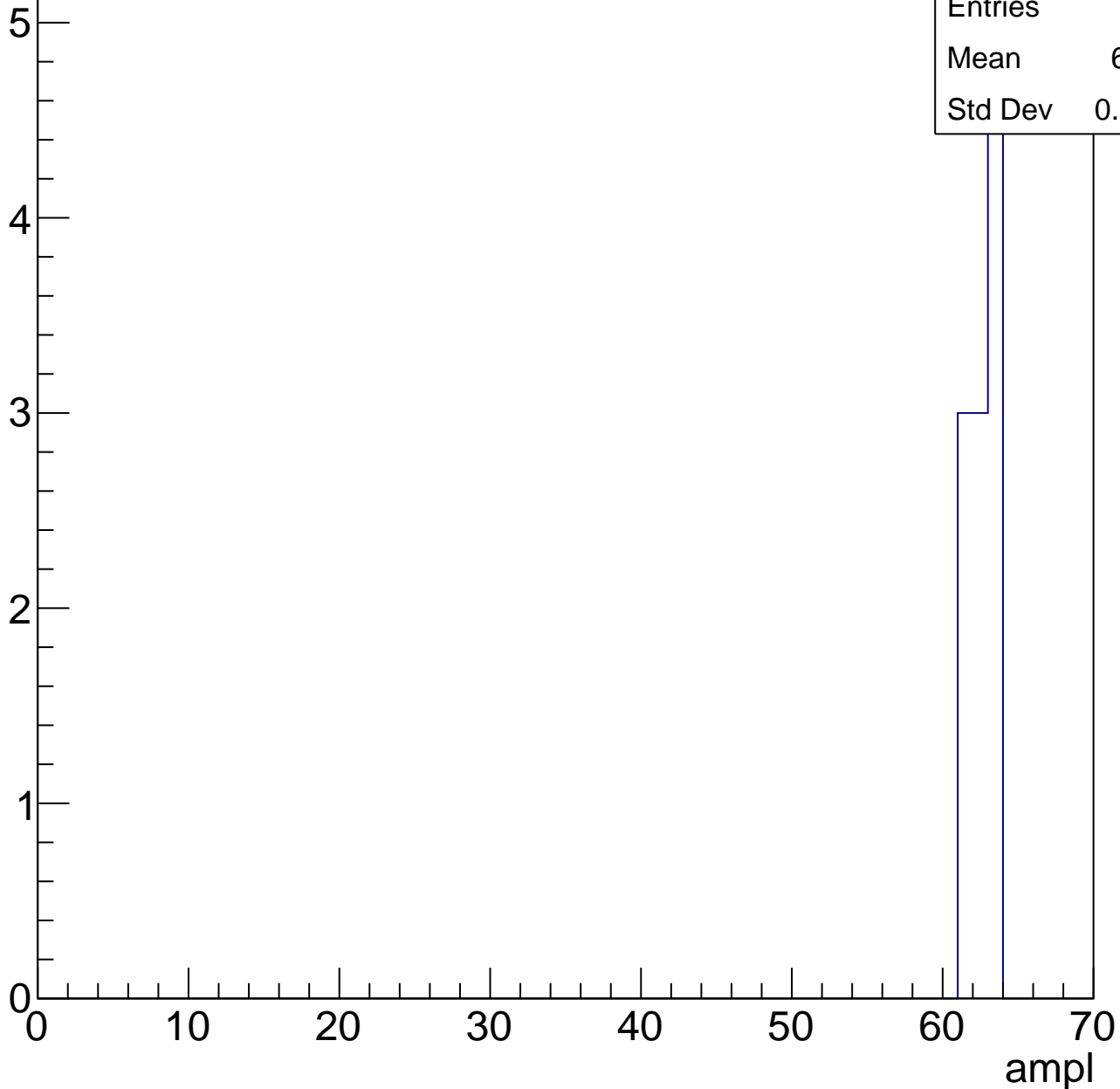


B1L102S, U10-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62.18
Std Dev	0.8332



B1L102S, U10-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch54, adc0

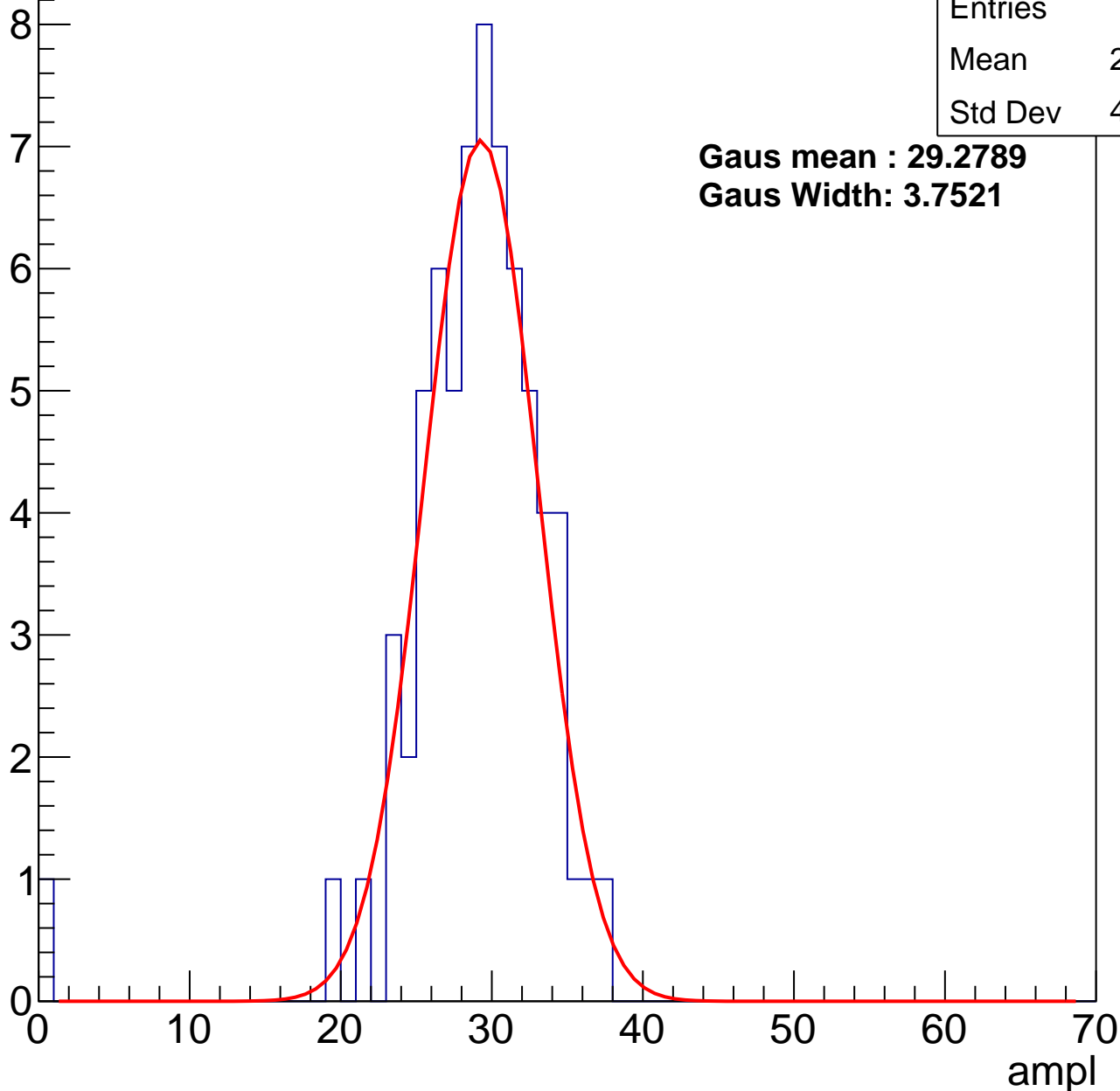
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.43
Std Dev	4.986

Gaus mean : 29.2789

Gaus Width: 3.7521



B1L102S, U10-ch54, adc1

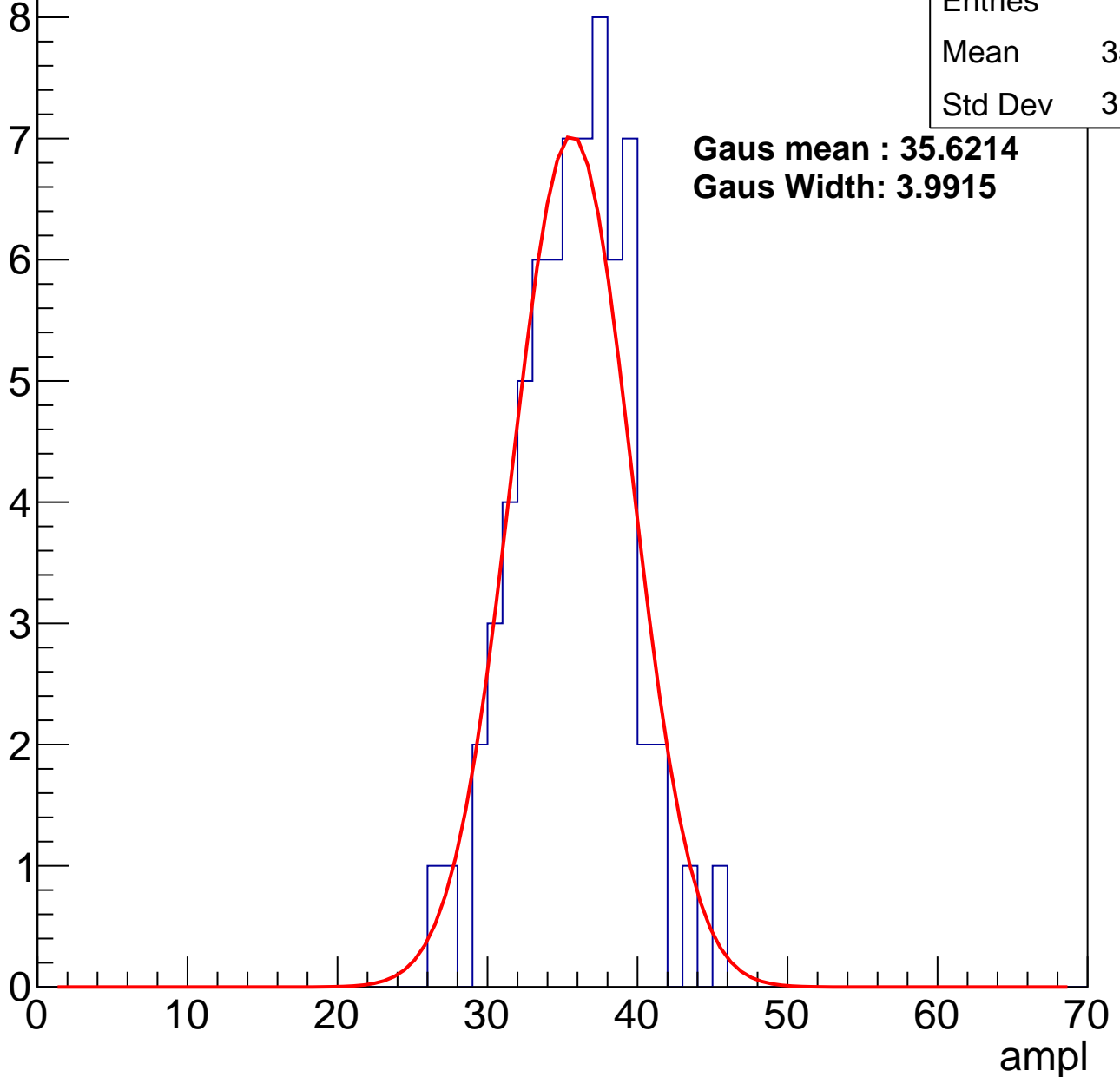
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.23
Std Dev	3.648

Gaus mean : 35.6214

Gaus Width: 3.9915



B1L102S, U10-ch54, adc2

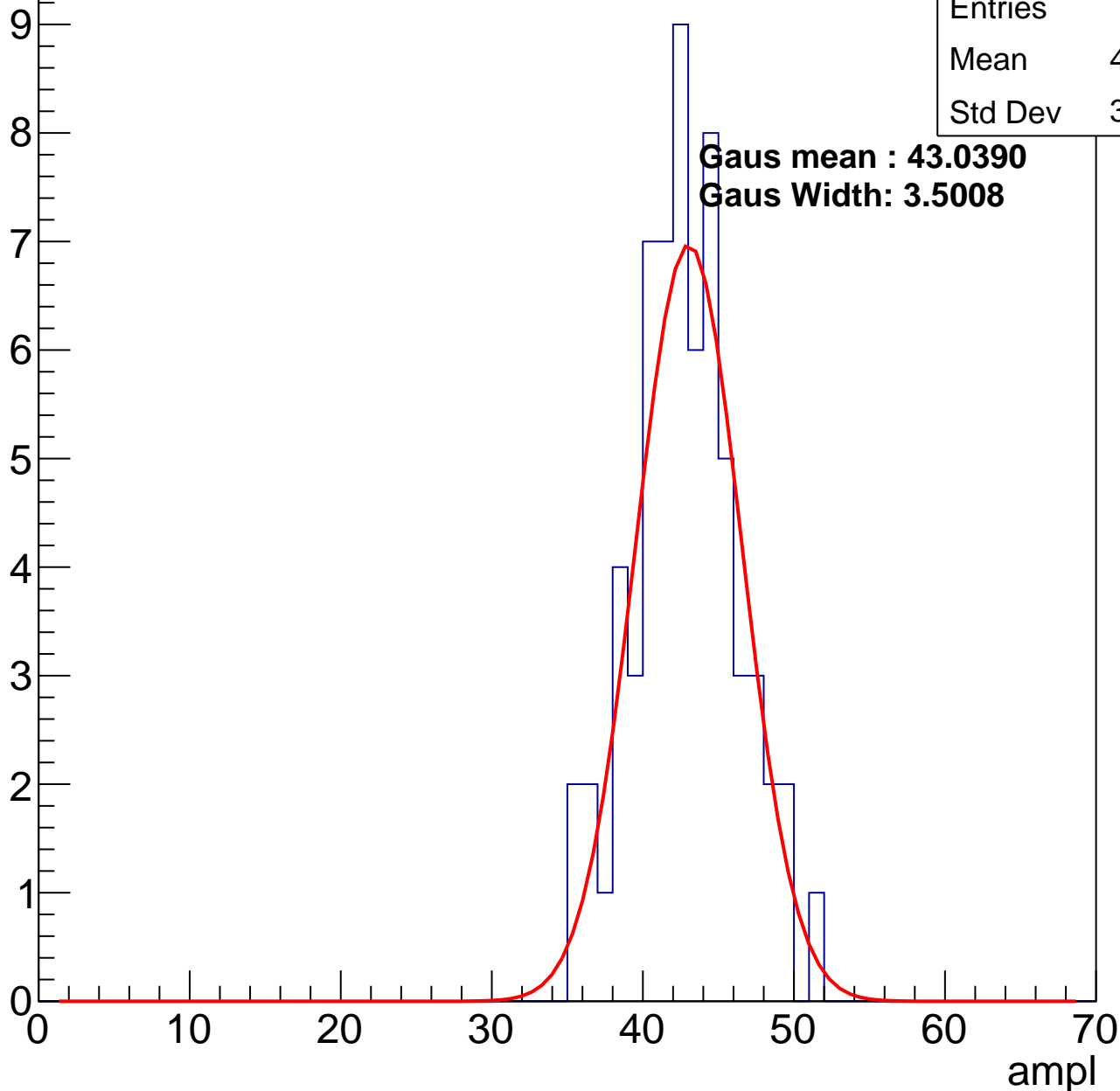
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.34
Std Dev	3.443

Gaus mean : 43.0390

Gaus Width: 3.5008

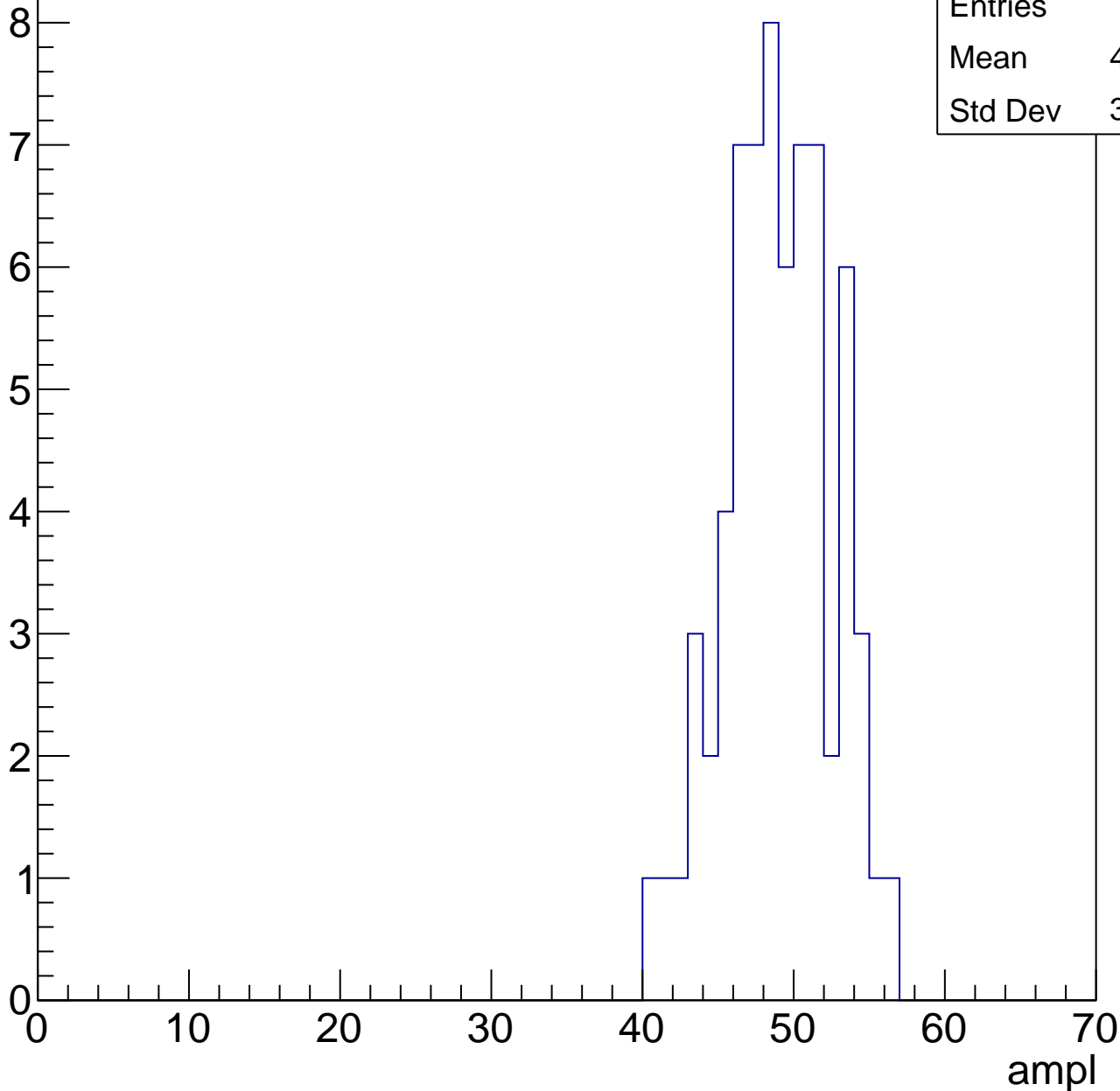


B1L102S, U10-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.52
Std Dev	3.474

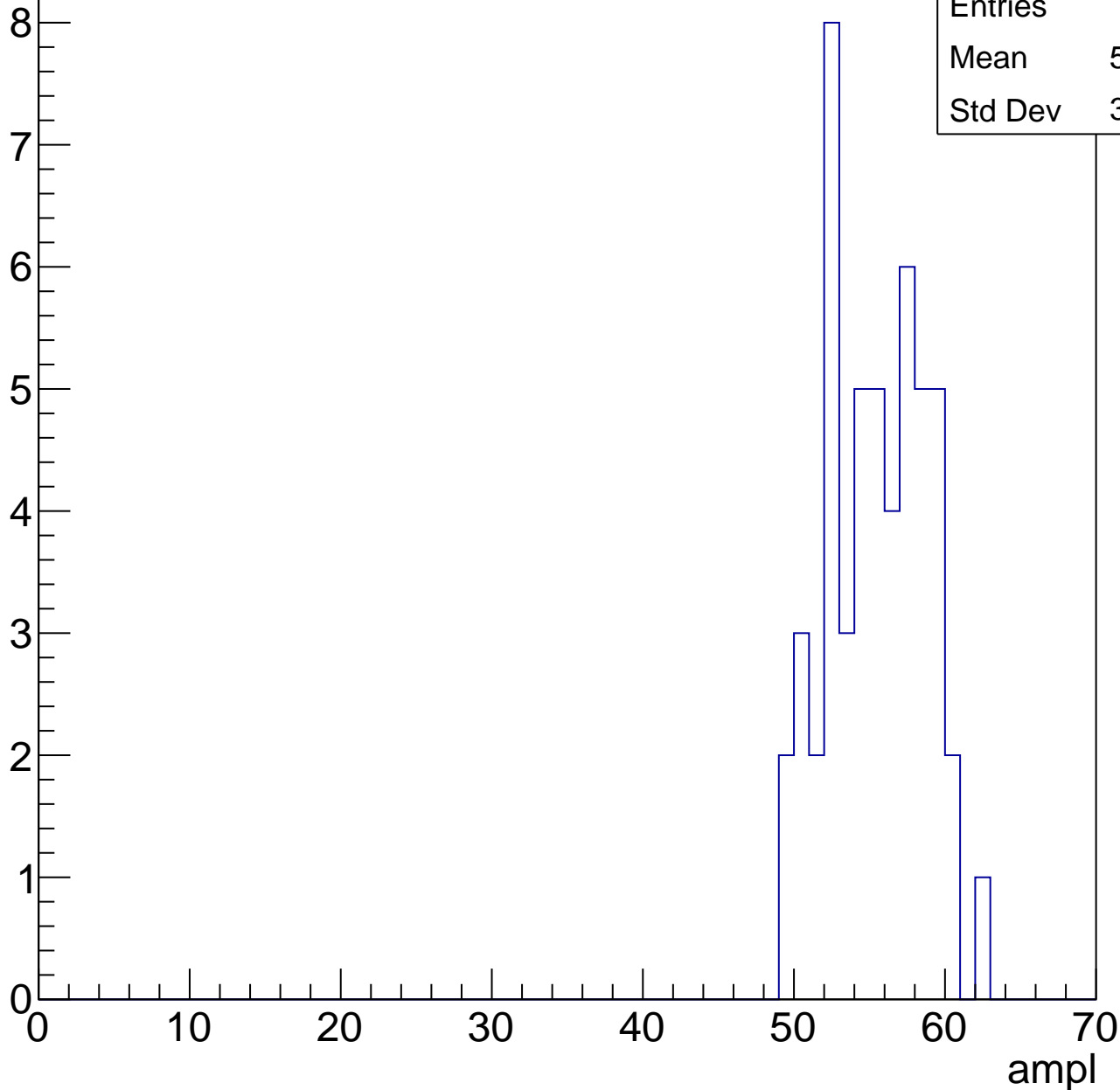


B1L102S, U10-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.96
Std Dev	3.193

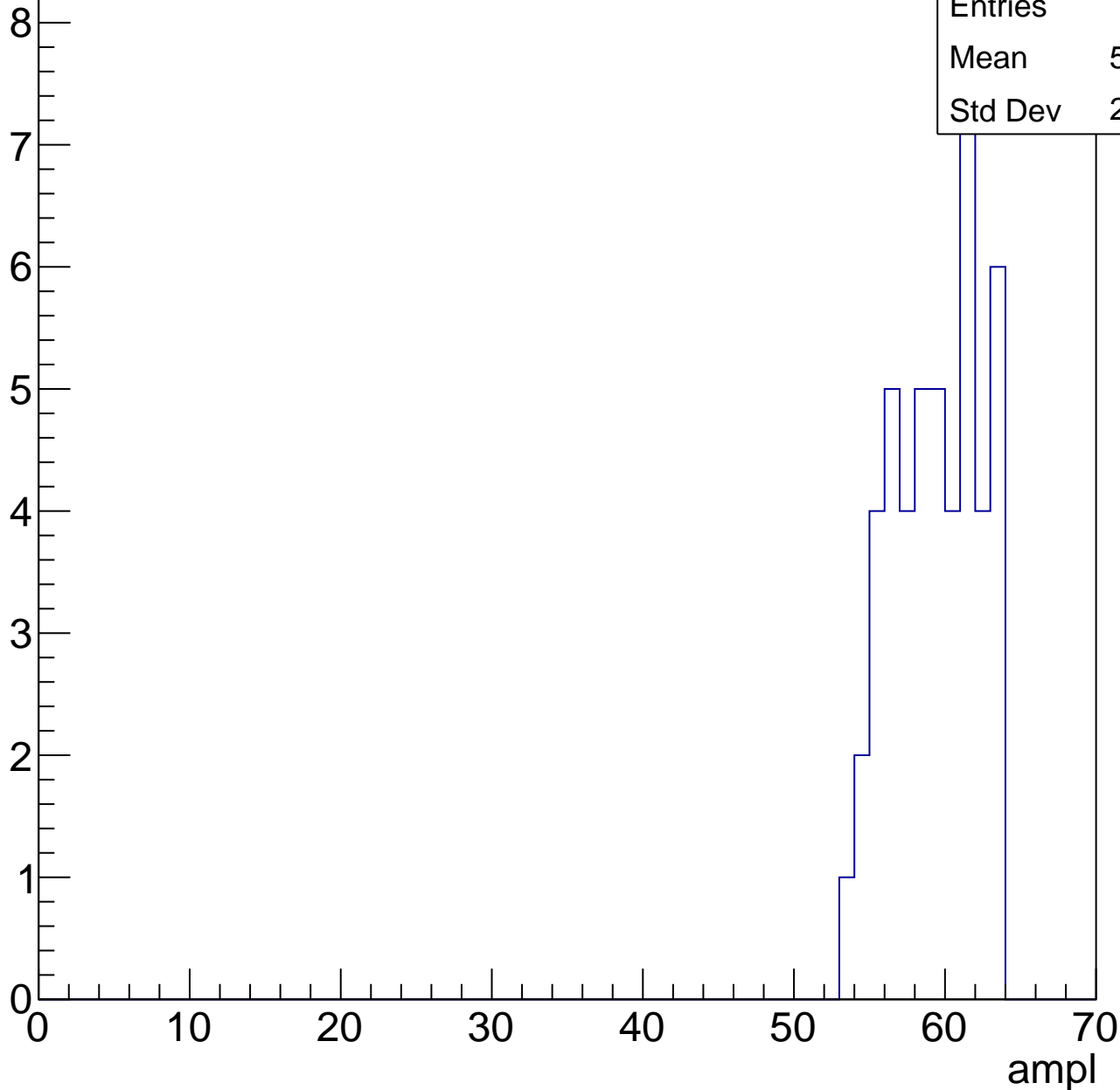


B1L102S, U10-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.92
Std Dev	2.827

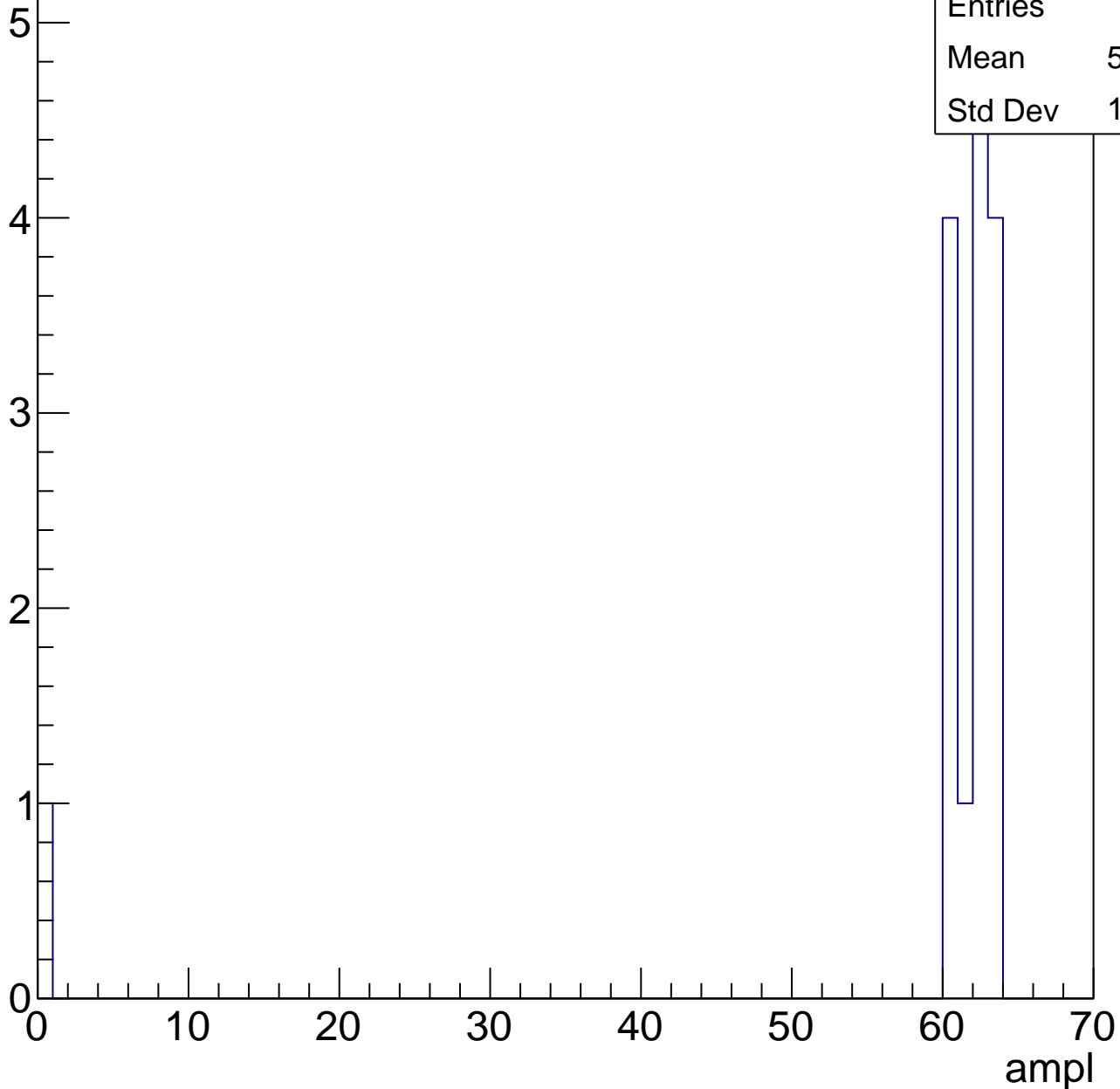


B1L102S, U10-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

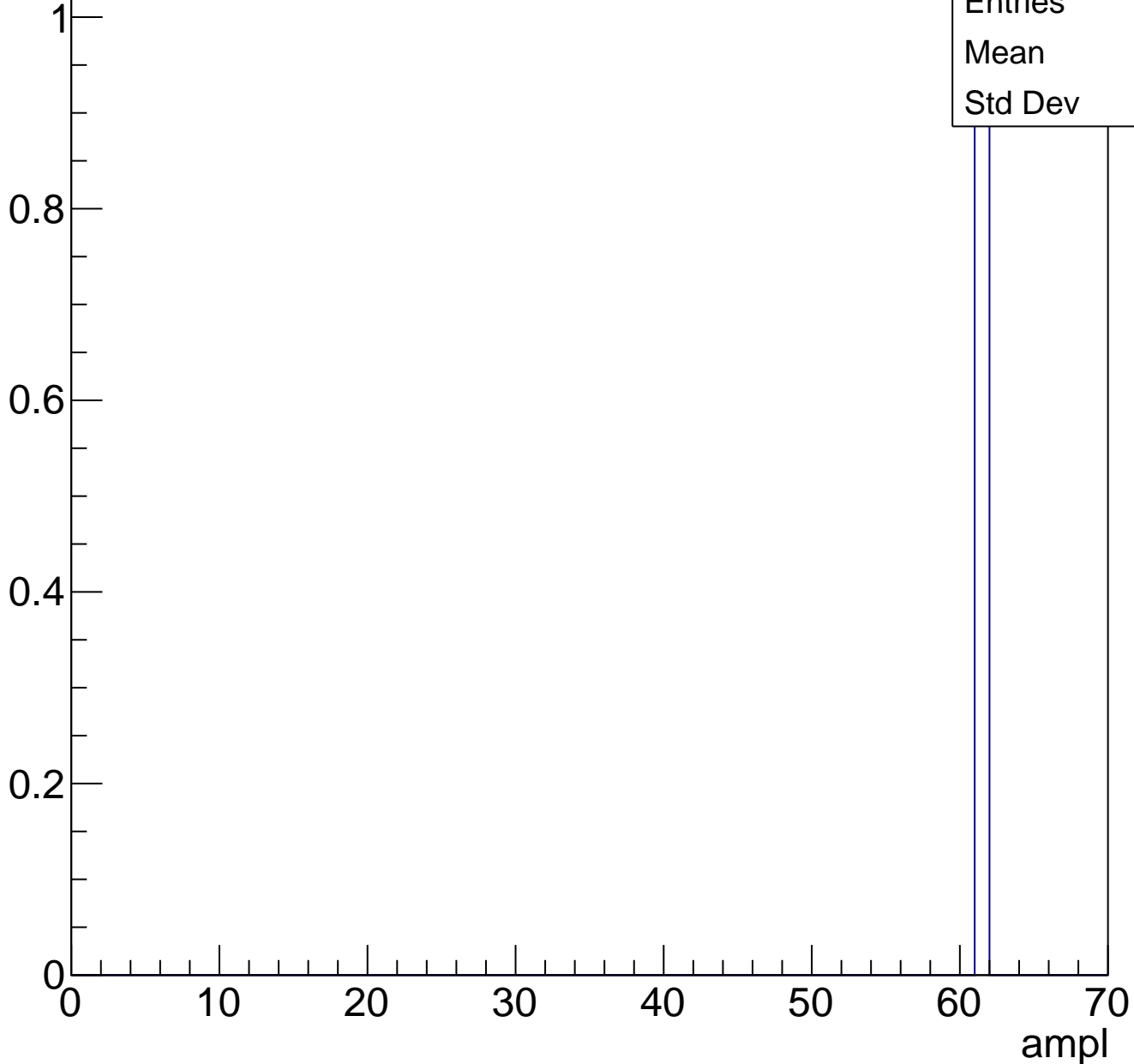
Entries	15
Mean	57.53
Std Dev	15.42



B1L102S, U10-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch55, adc0

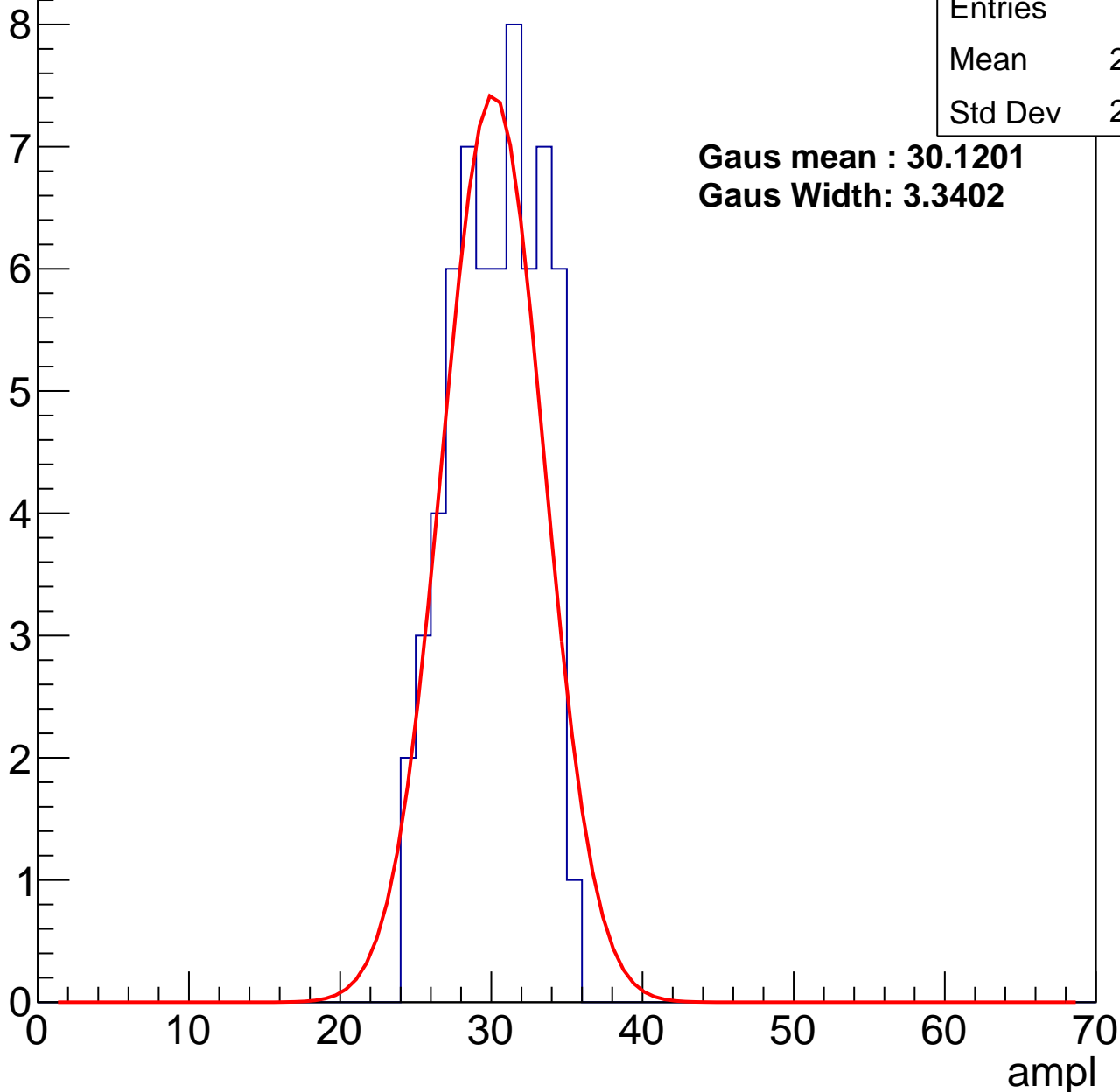
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.82
Std Dev	2.877

Gaus mean : 30.1201

Gaus Width: 3.3402



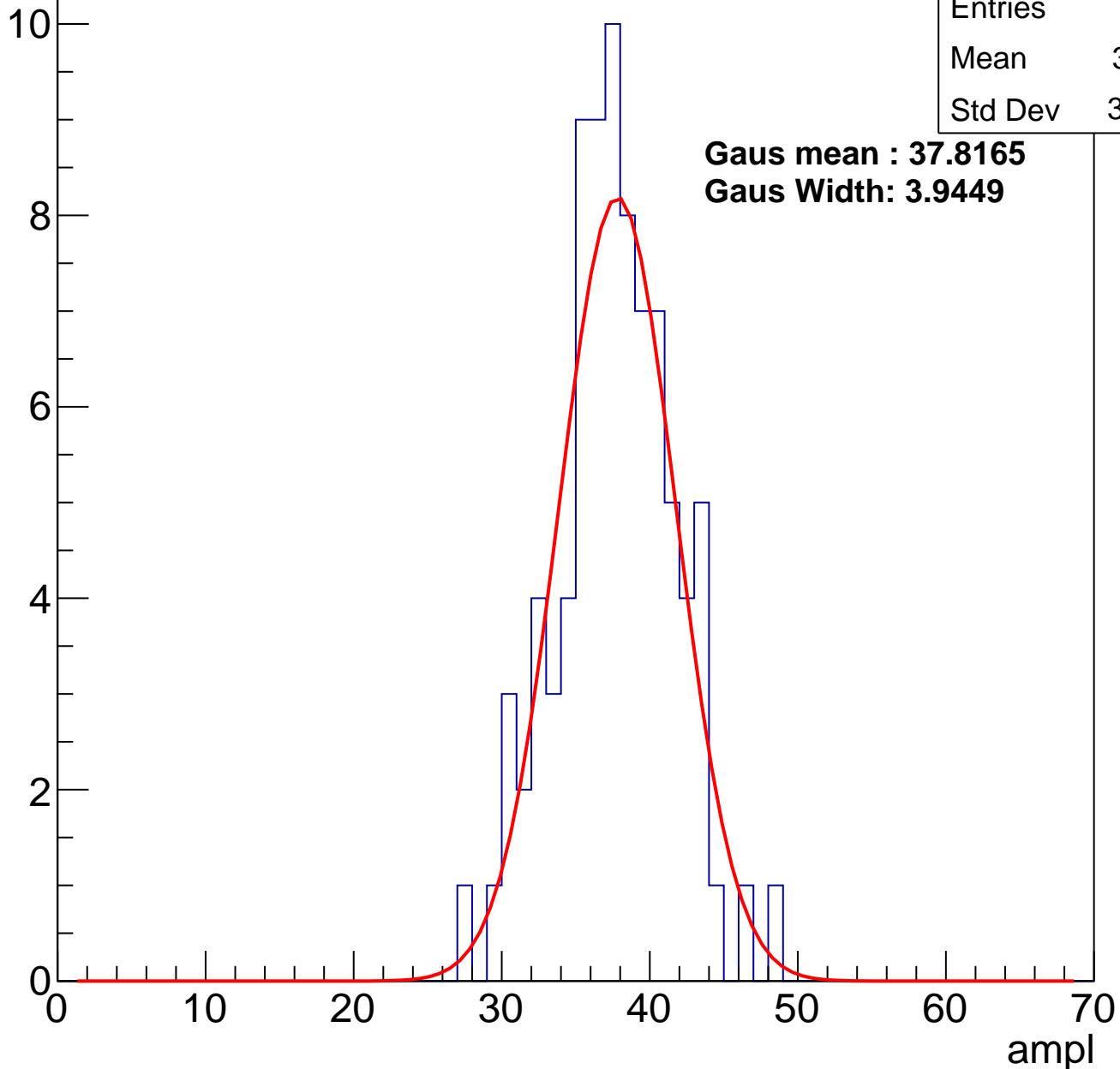
B1L102S, U10-ch55, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	37.21
Std Dev	3.938

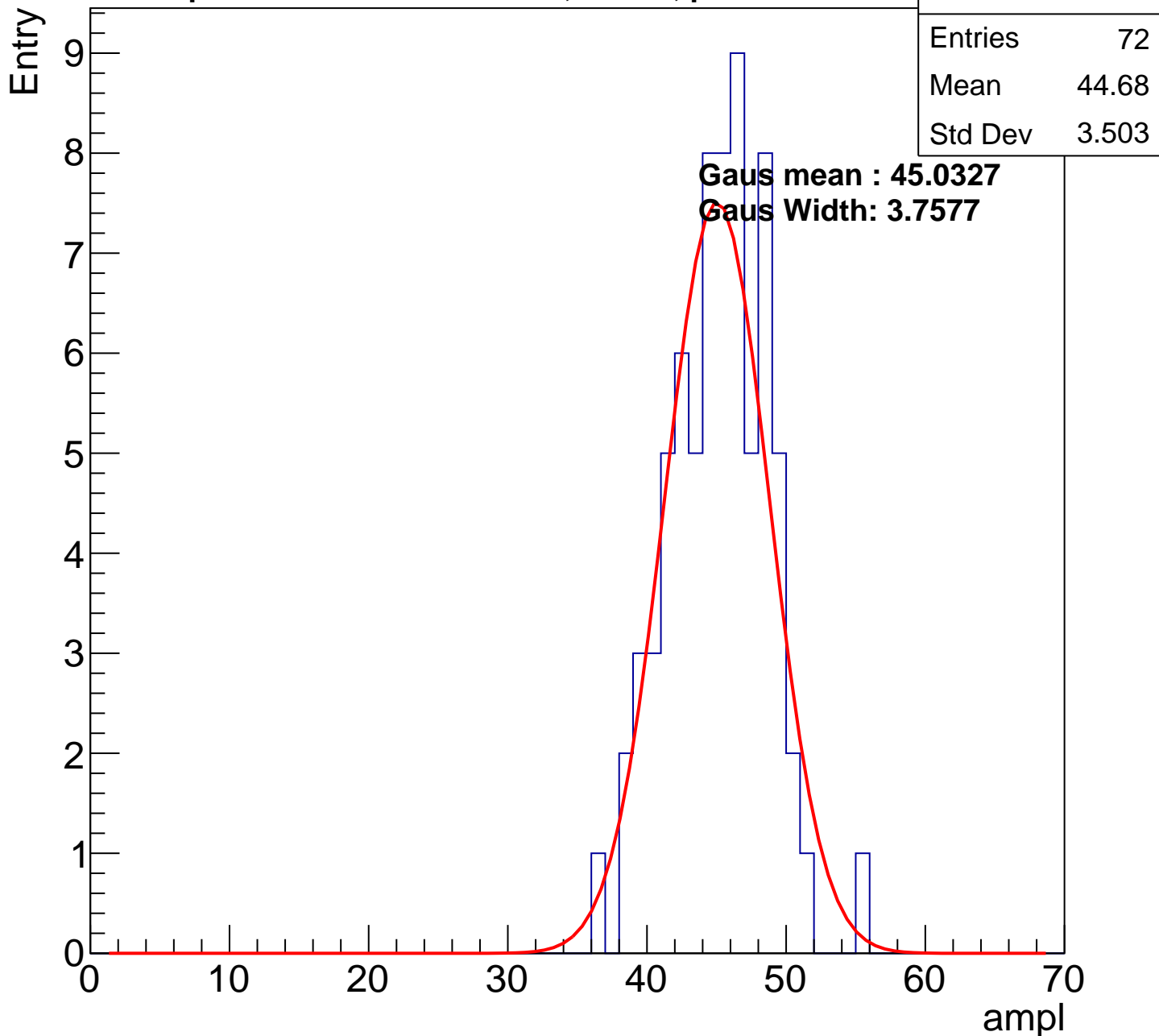
Gaus mean : 37.8165
Gaus Width: 3.9449

Entry



B1L102S, U10-ch55, adc2

calib_packv5_042523_0143.root, FC#11, port A2

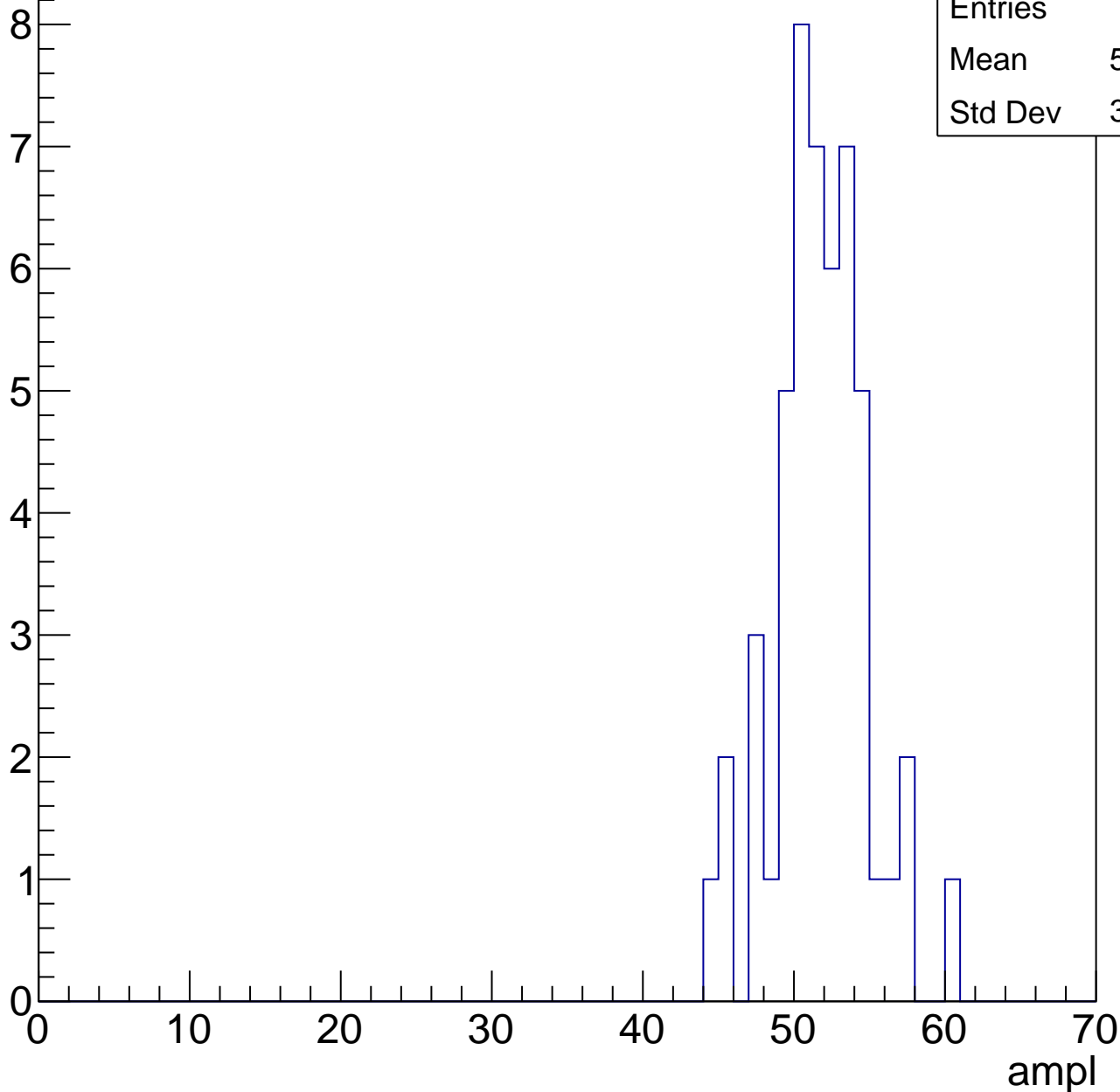


B1L102S, U10-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

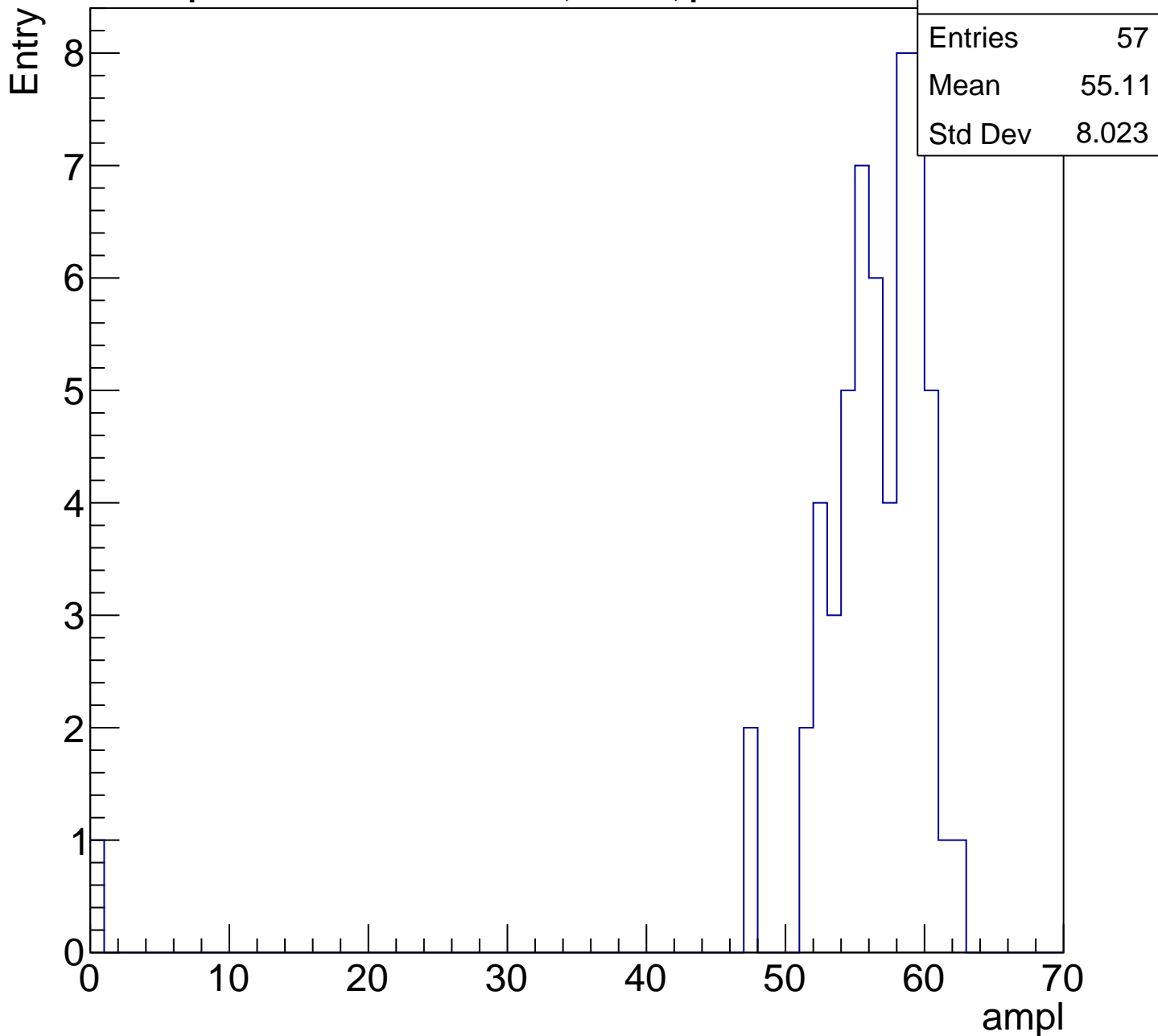
Entry

Entries	50
Mean	51.26
Std Dev	3.084



B1L102S, U10-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U10-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	44
Mean	60.5
Std Dev	2.35

B1L102S, U10-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch56, adc0

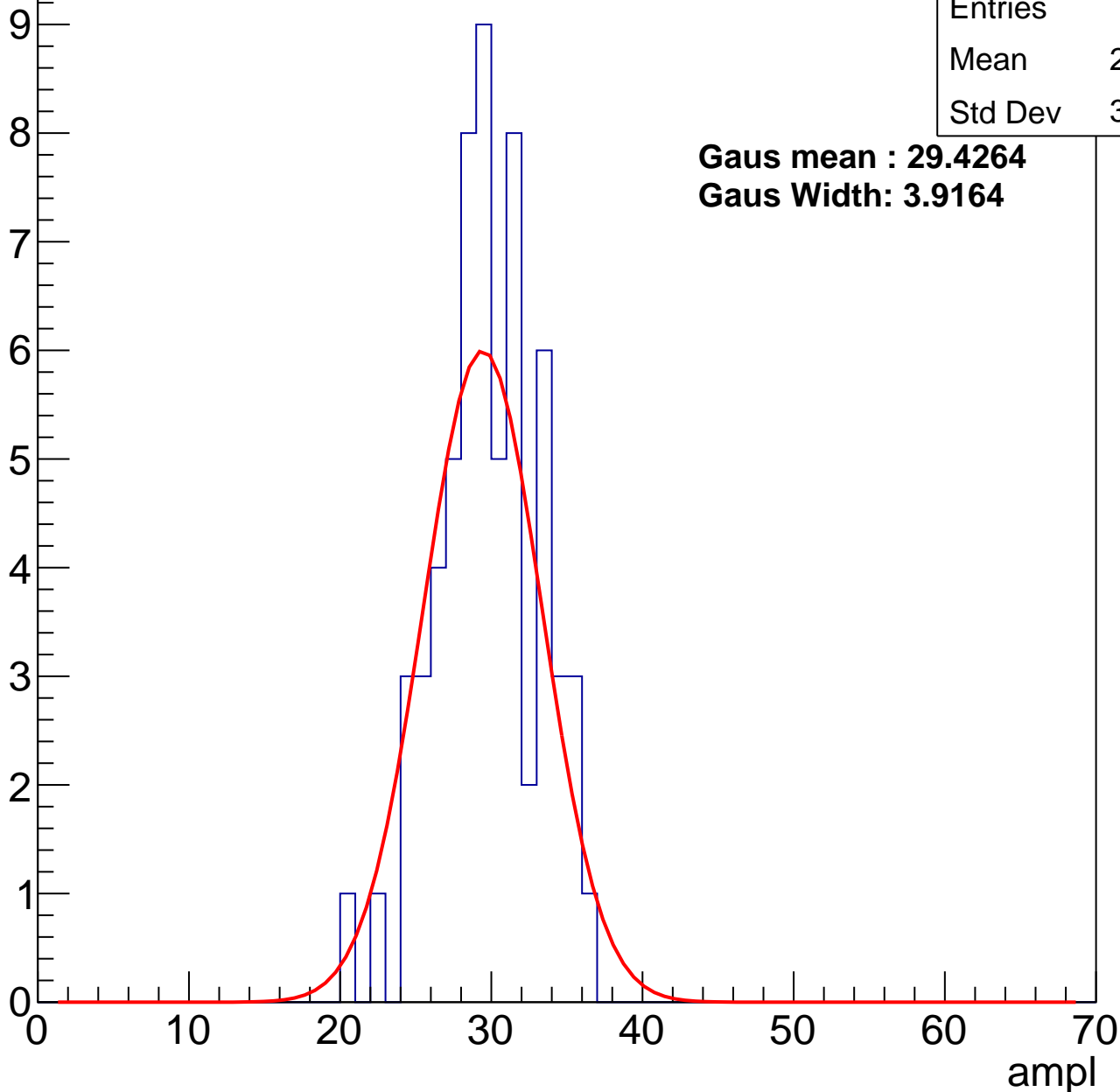
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.29
Std Dev	3.348

Gaus mean : 29.4264

Gaus Width: 3.9164



B1L102S, U10-ch56, adc1

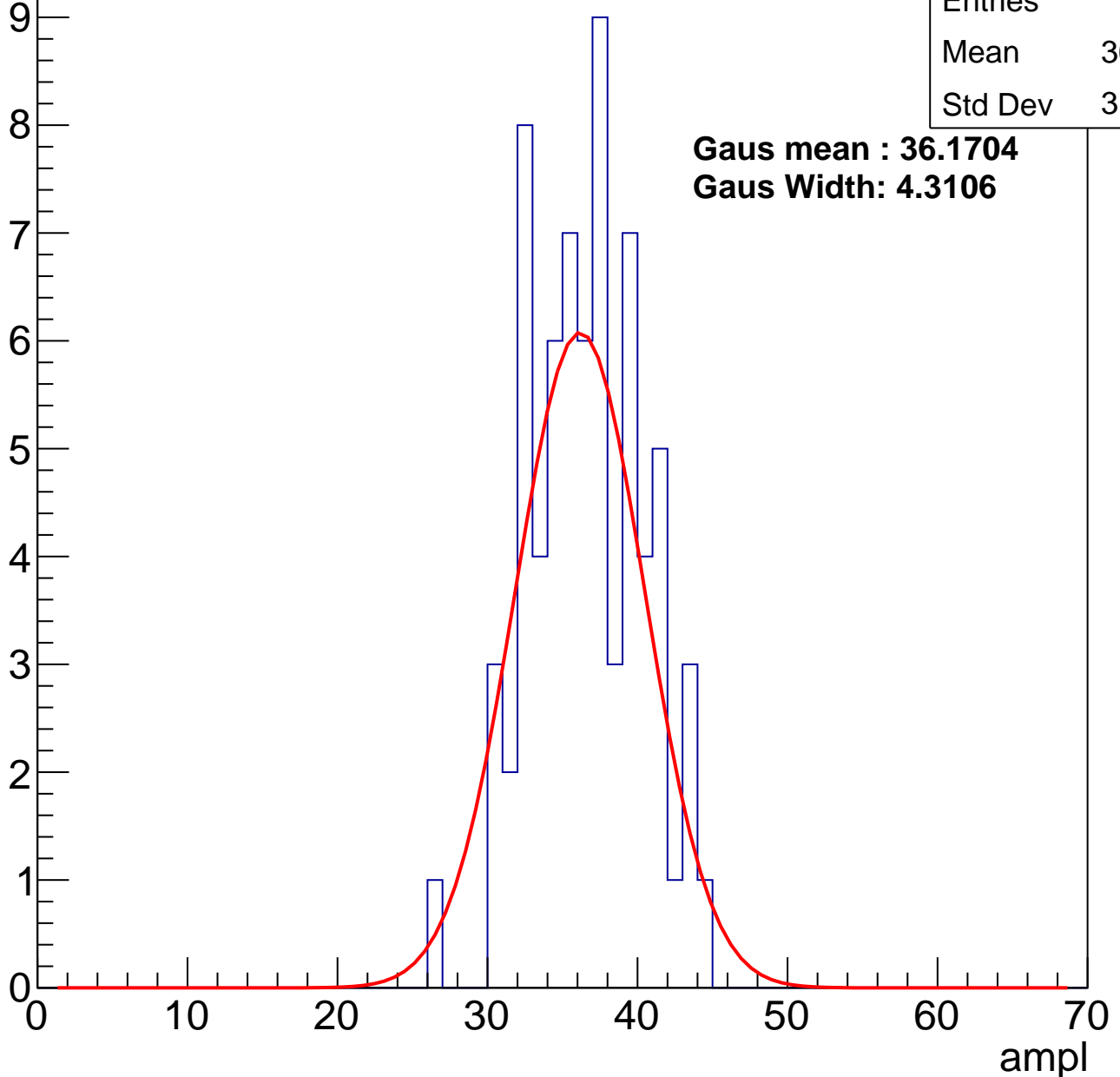
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.16
Std Dev	3.725

Gaus mean : 36.1704

Gaus Width: 4.3106



B1L102S, U10-ch56, adc2

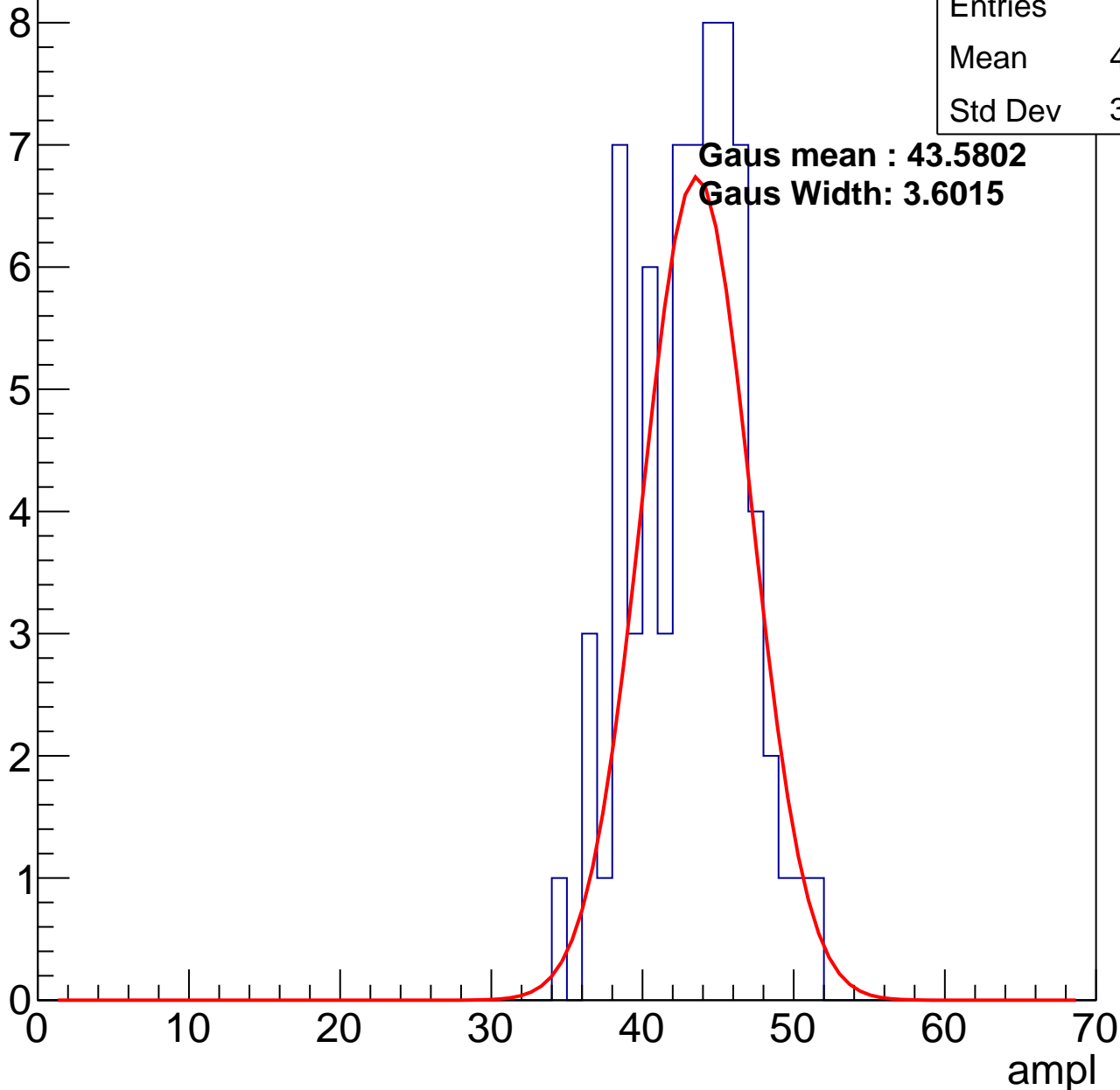
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.69
Std Dev	3.624

Gaus mean : 43.5802

Gaus Width: 3.6015

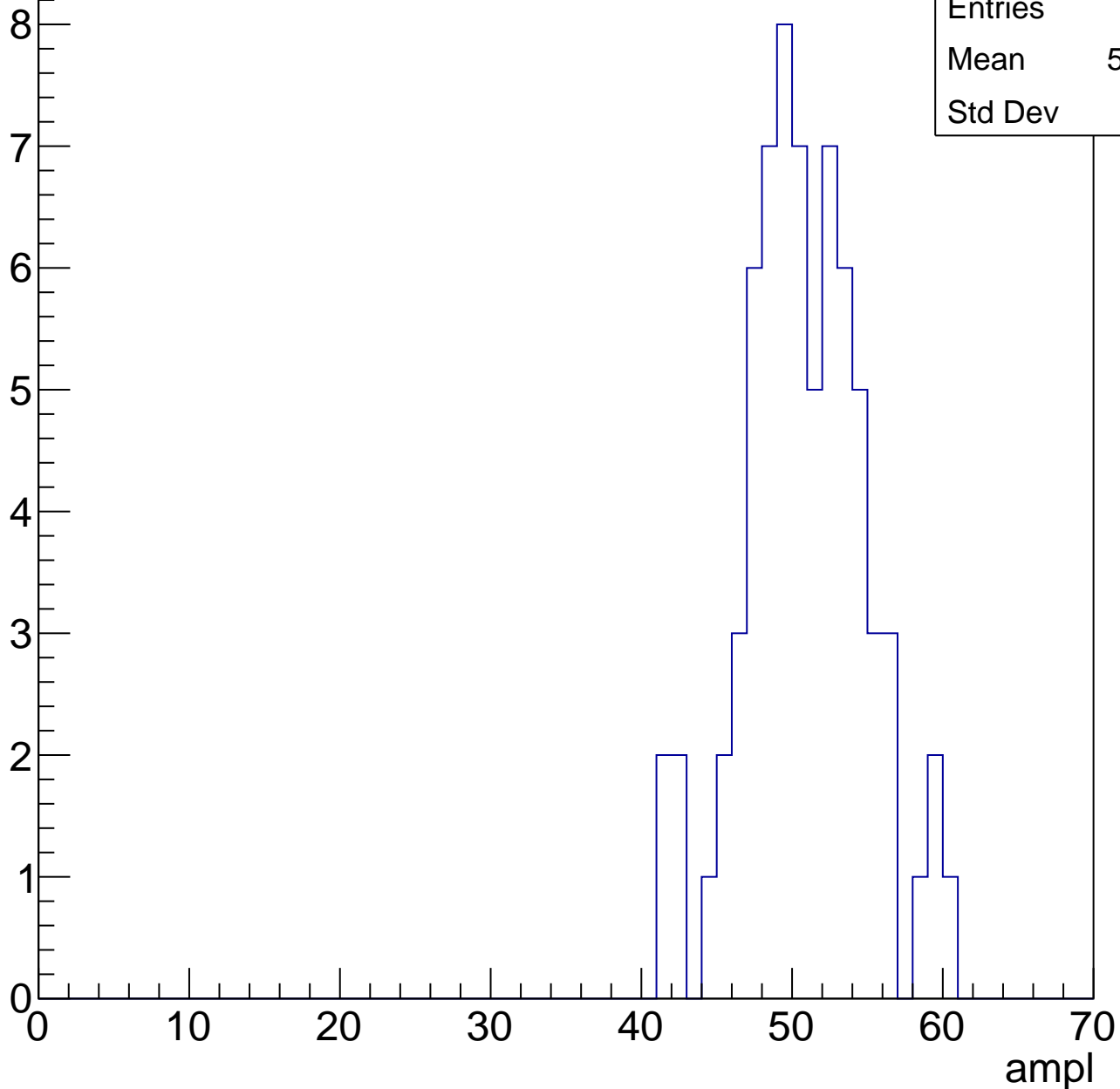


B1L102S, U10-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.34
Std Dev	4.08

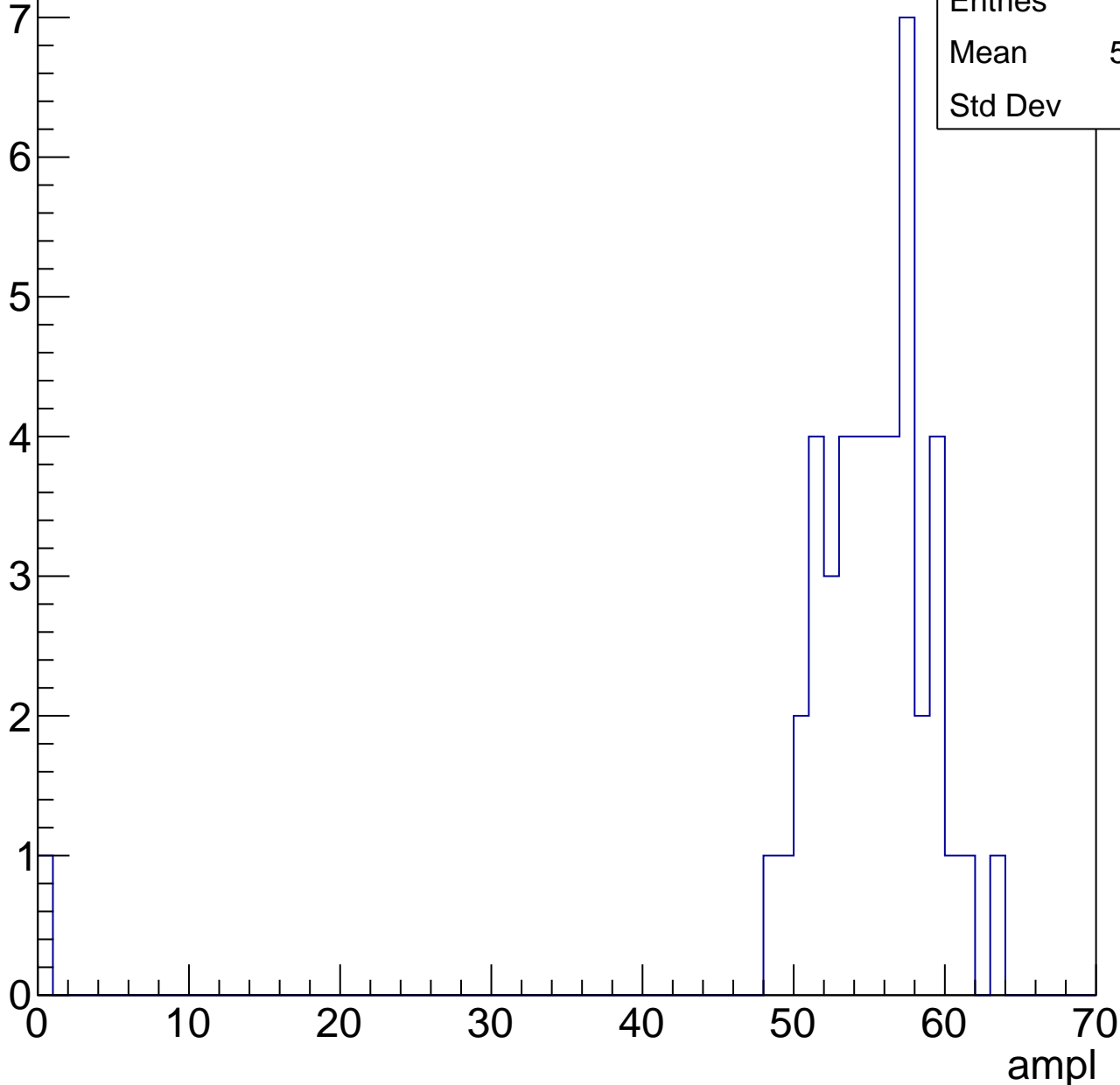


B1L102S, U10-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	53.73
Std Dev	8.84

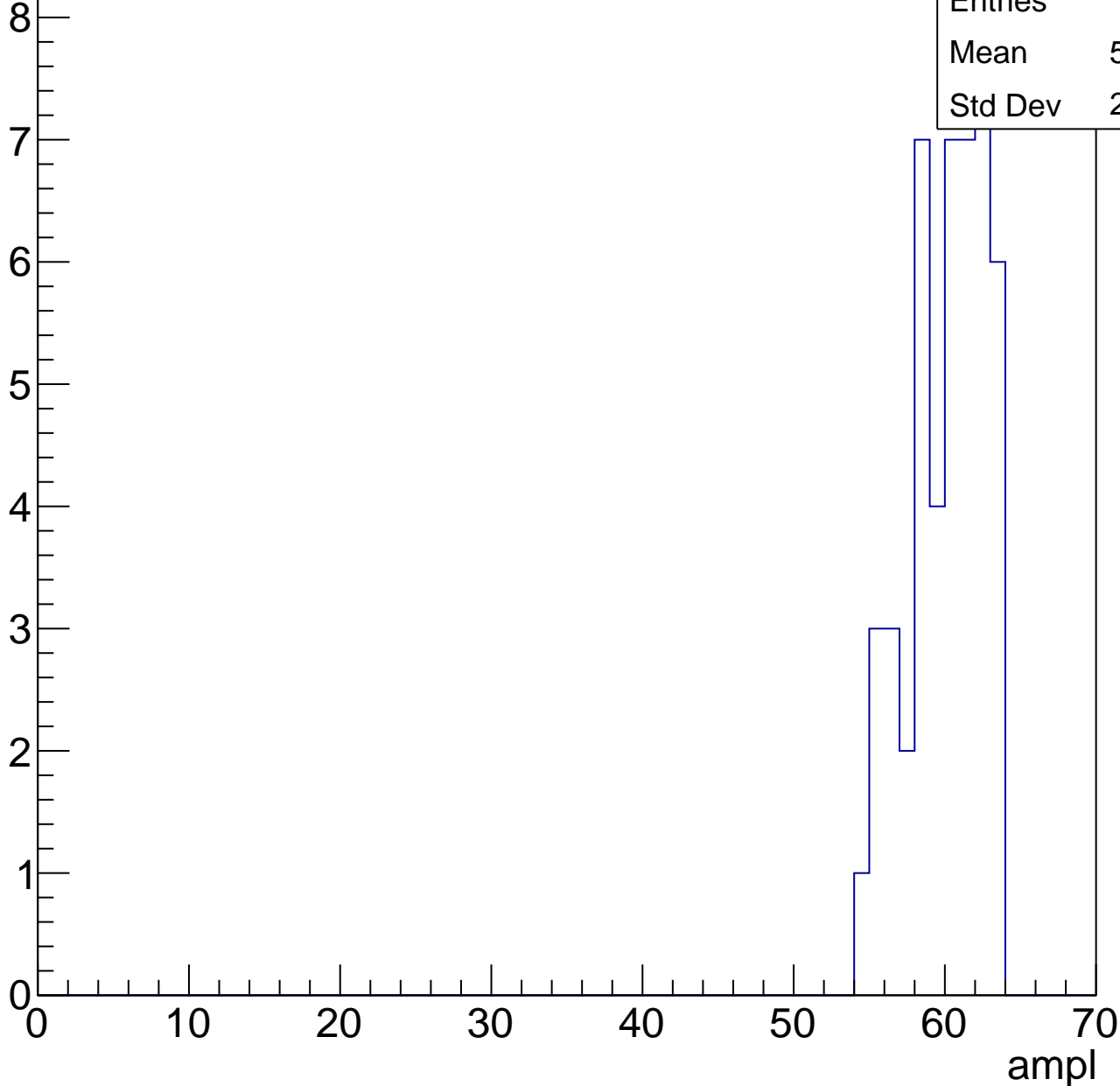


B1L102S, U10-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59.67
Std Dev	2.486



B1L102S, U10-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

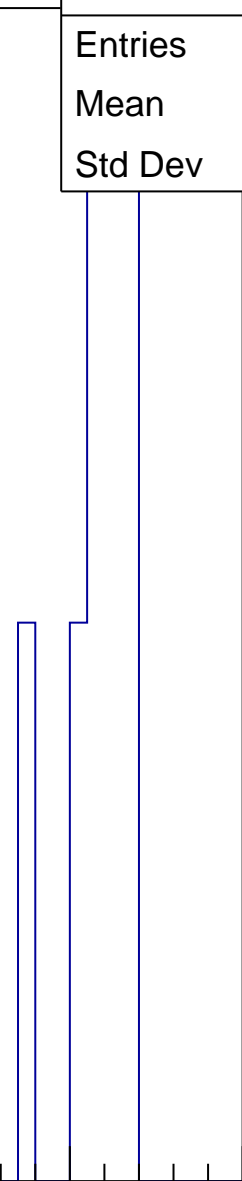
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.12
Std Dev	1.833

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch57, adc0

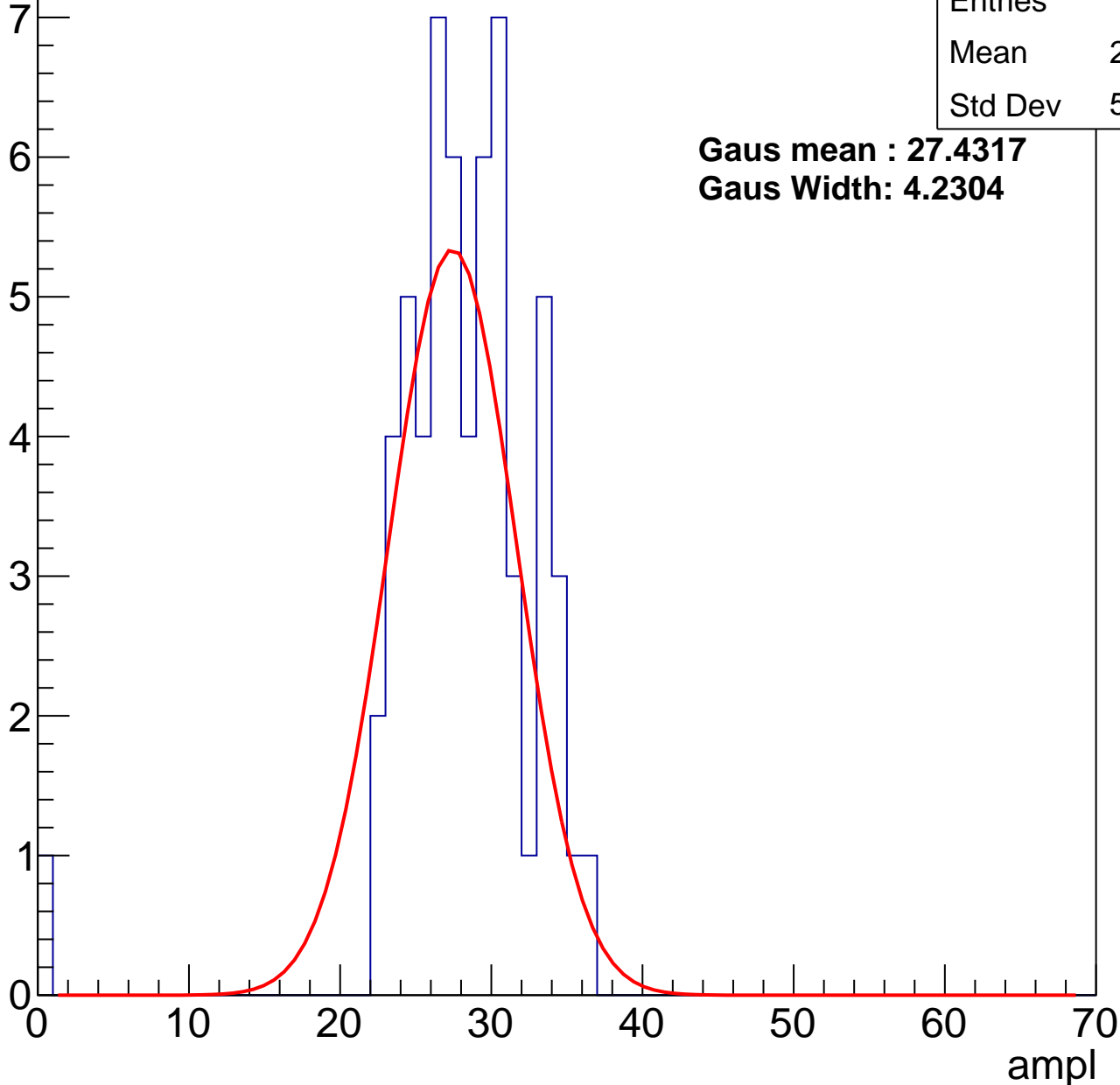
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.65
Std Dev	5.036

Gaus mean : 27.4317

Gaus Width: 4.2304



B1L102S, U10-ch57, adc1

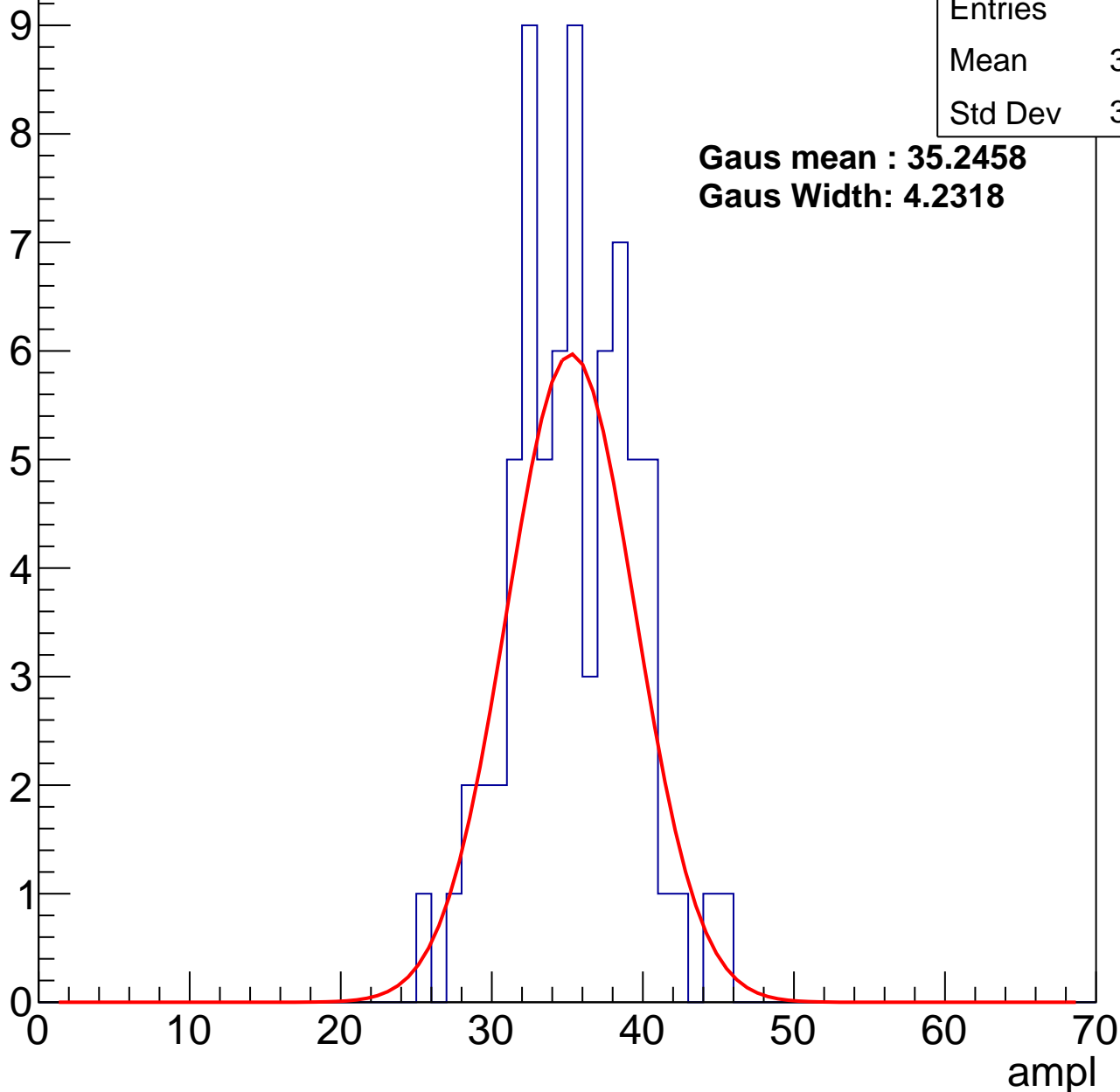
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.94
Std Dev	3.965

Gaus mean : 35.2458

Gaus Width: 4.2318



B1L102S, U10-ch57, adc2

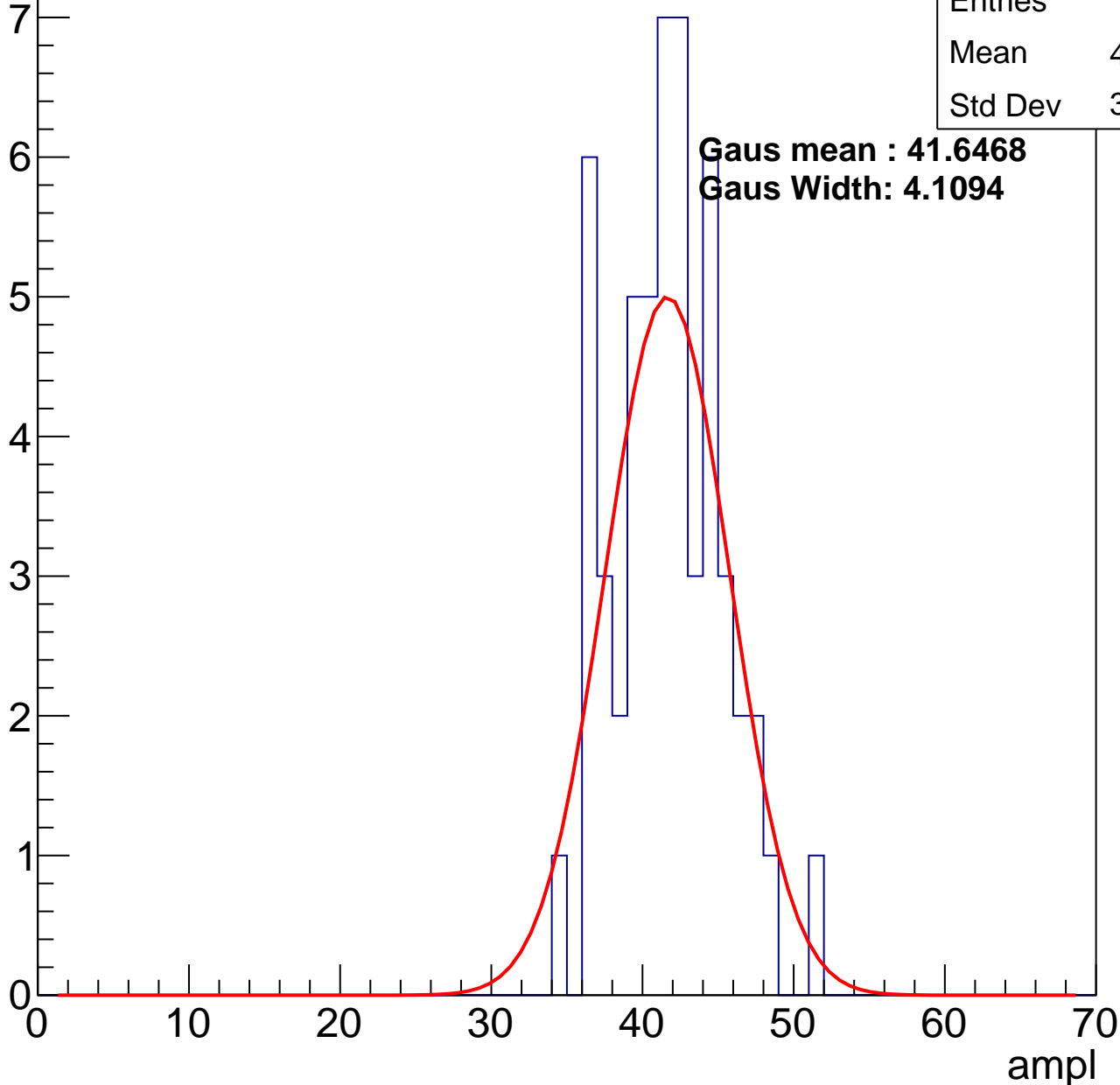
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.22
Std Dev	3.547

Gaus mean : 41.6468

Gaus Width: 4.1094

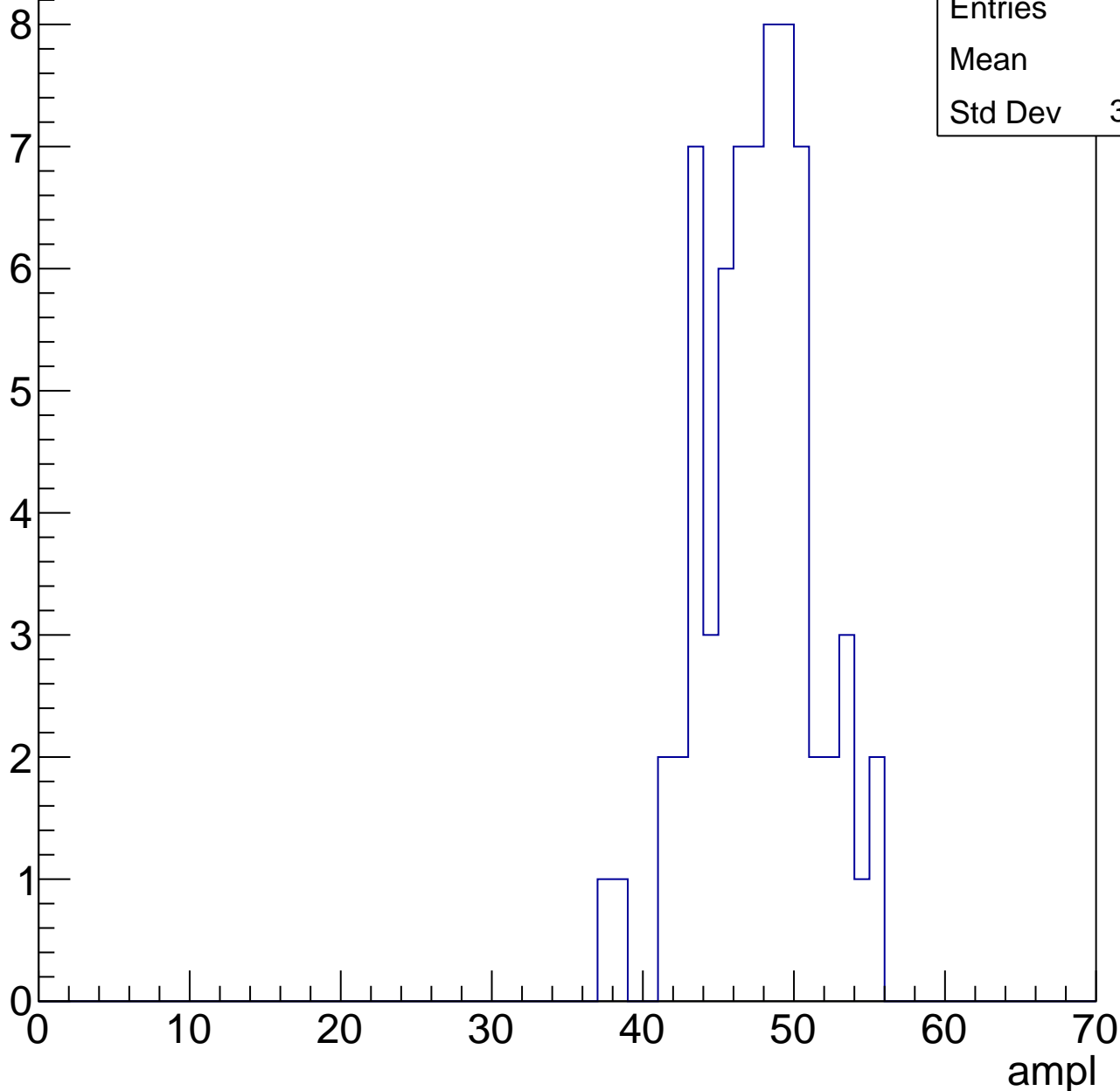


B1L102S, U10-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

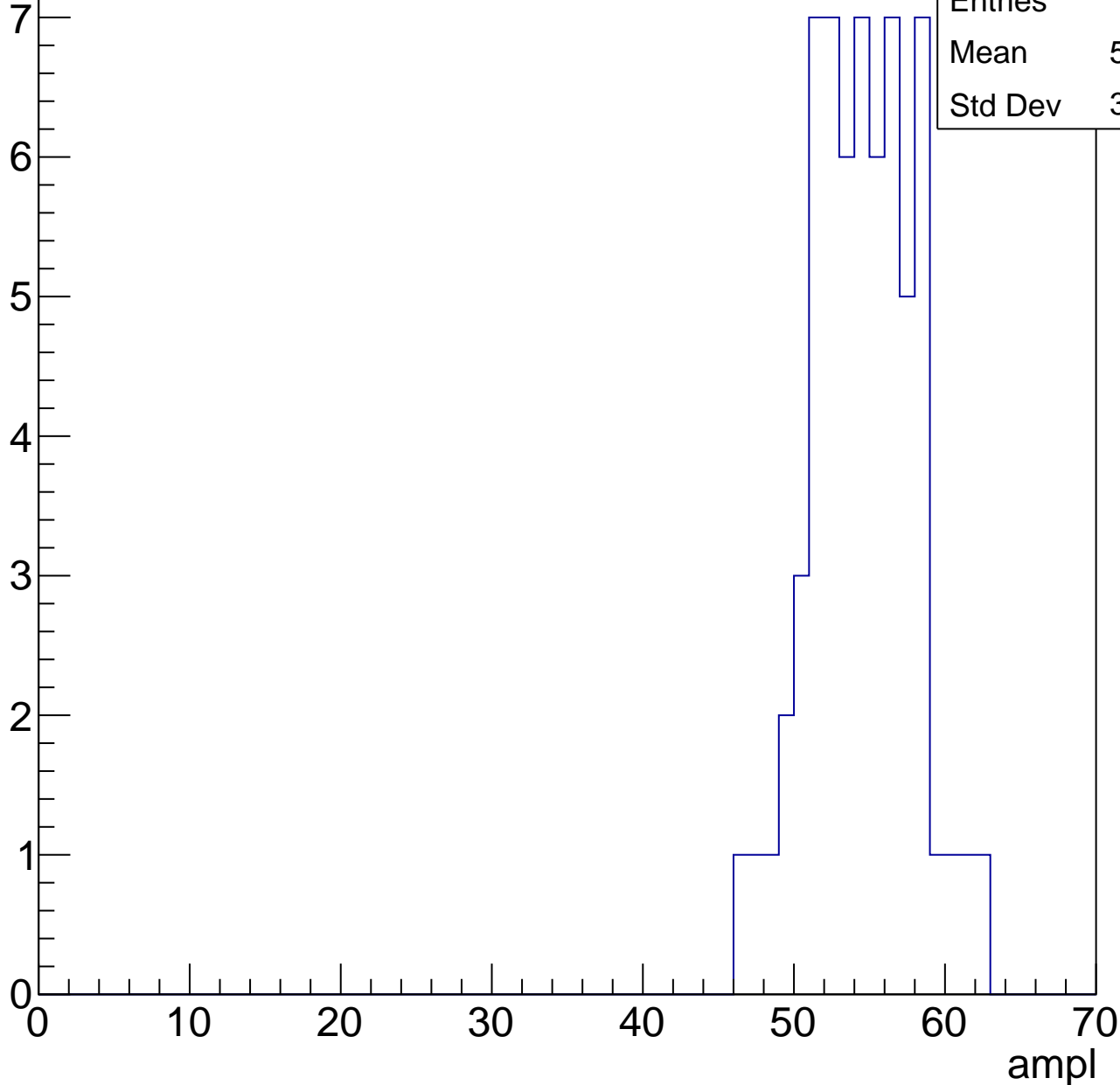
Entries	69
Mean	47.1
Std Dev	3.695



B1L102S, U10-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



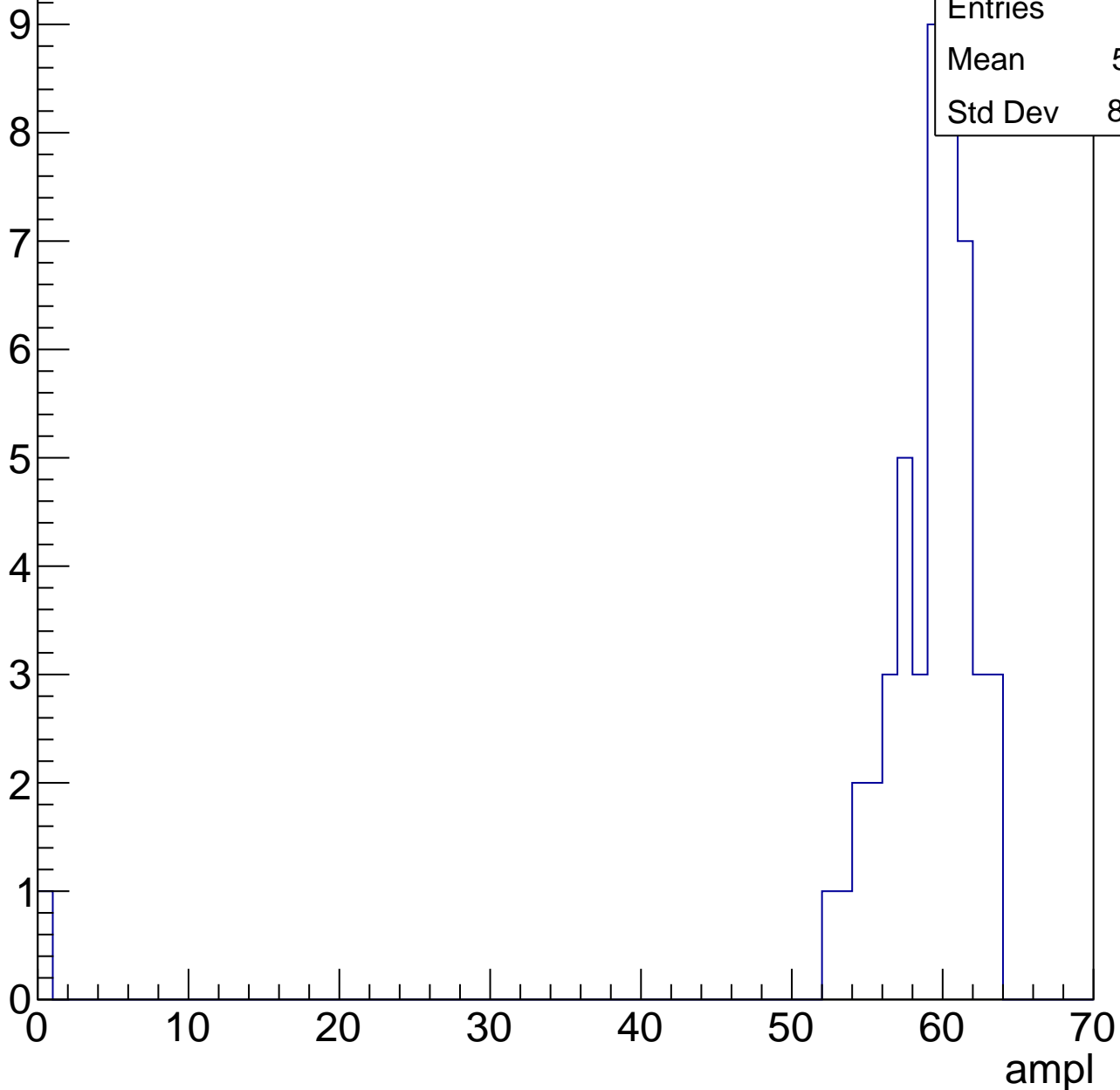
Entries	64
Mean	54.08
Std Dev	3.327

B1L102S, U10-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.61
Std Dev	8.708

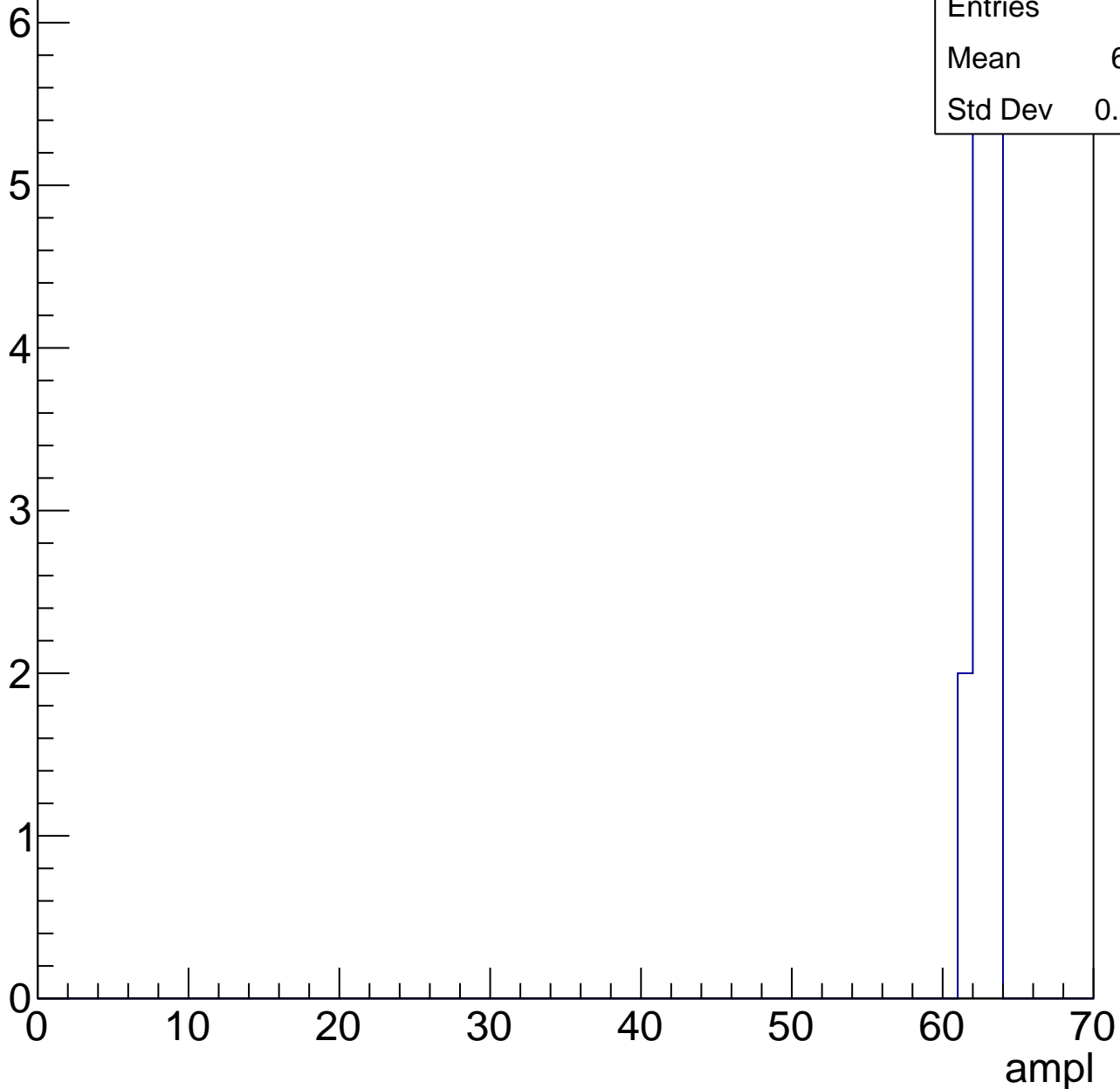


B1L102S, U10-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	62.29
Std Dev	0.6999



B1L102S, U10-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch58, adc0

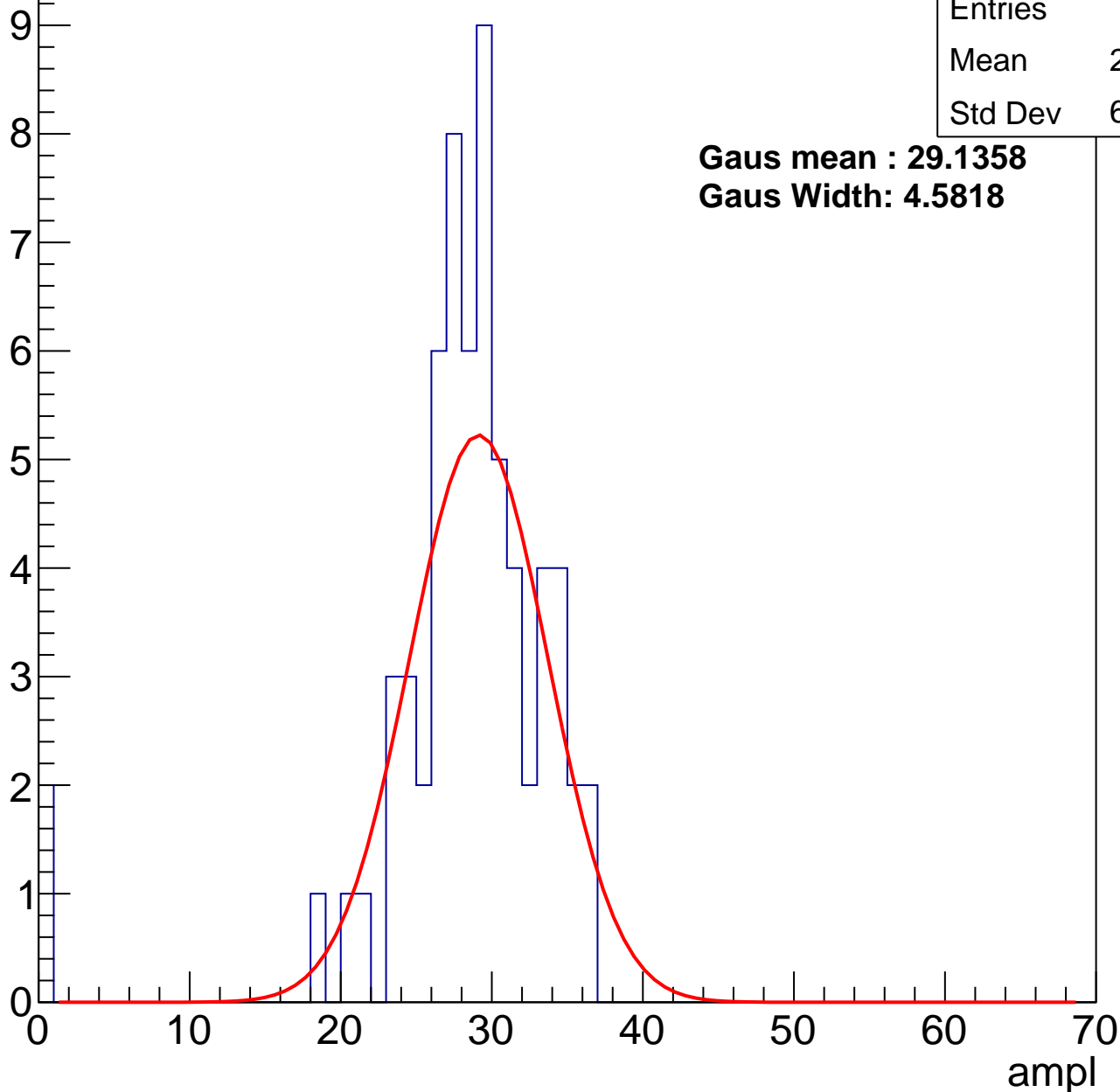
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.68
Std Dev	6.219

Gaus mean : 29.1358

Gaus Width: 4.5818



B1L102S, U10-ch58, adc1

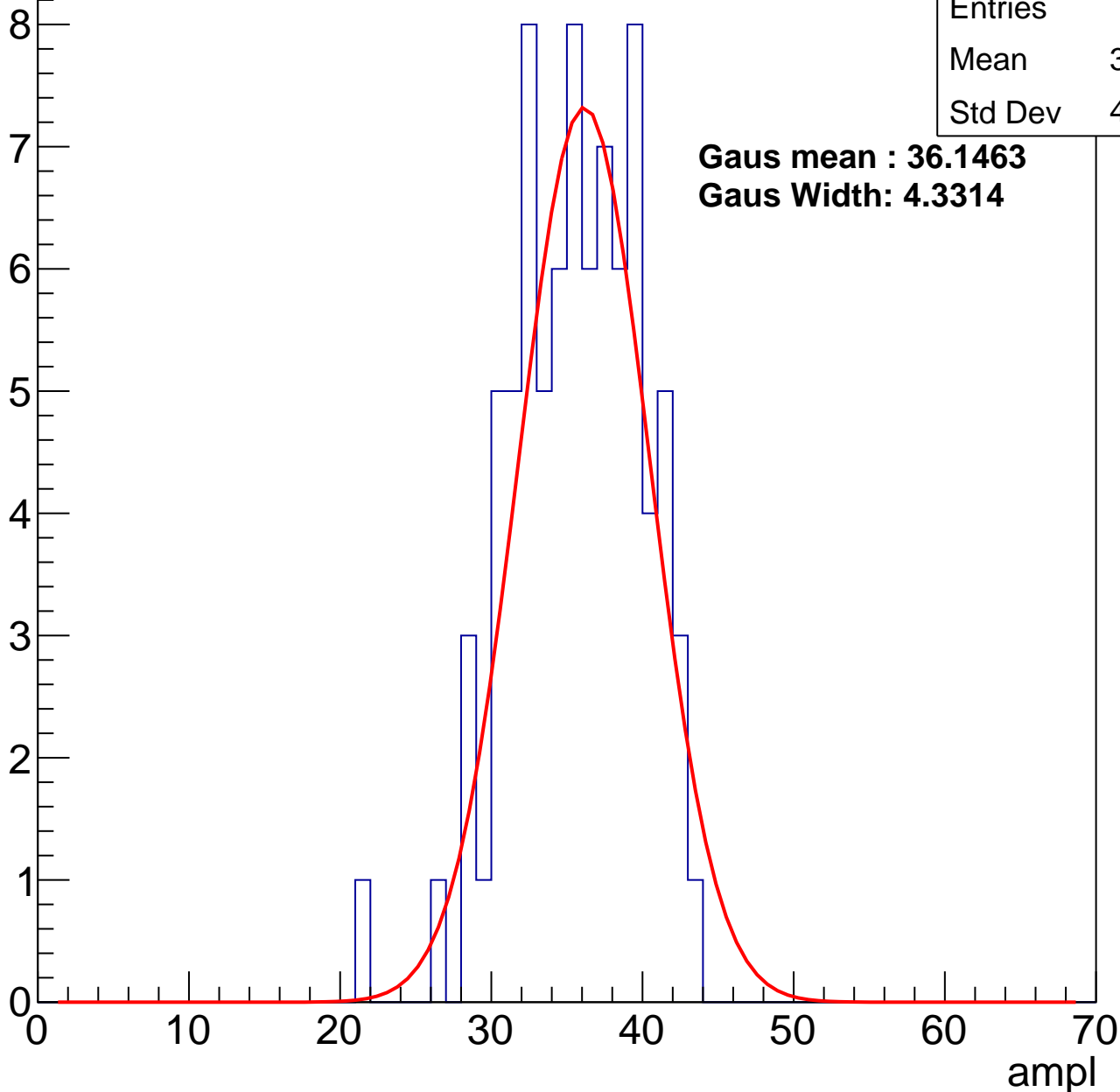
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	35.17
Std Dev	4.205

Gaus mean : 36.1463

Gaus Width: 4.3314



B1L102S, U10-ch58, adc2

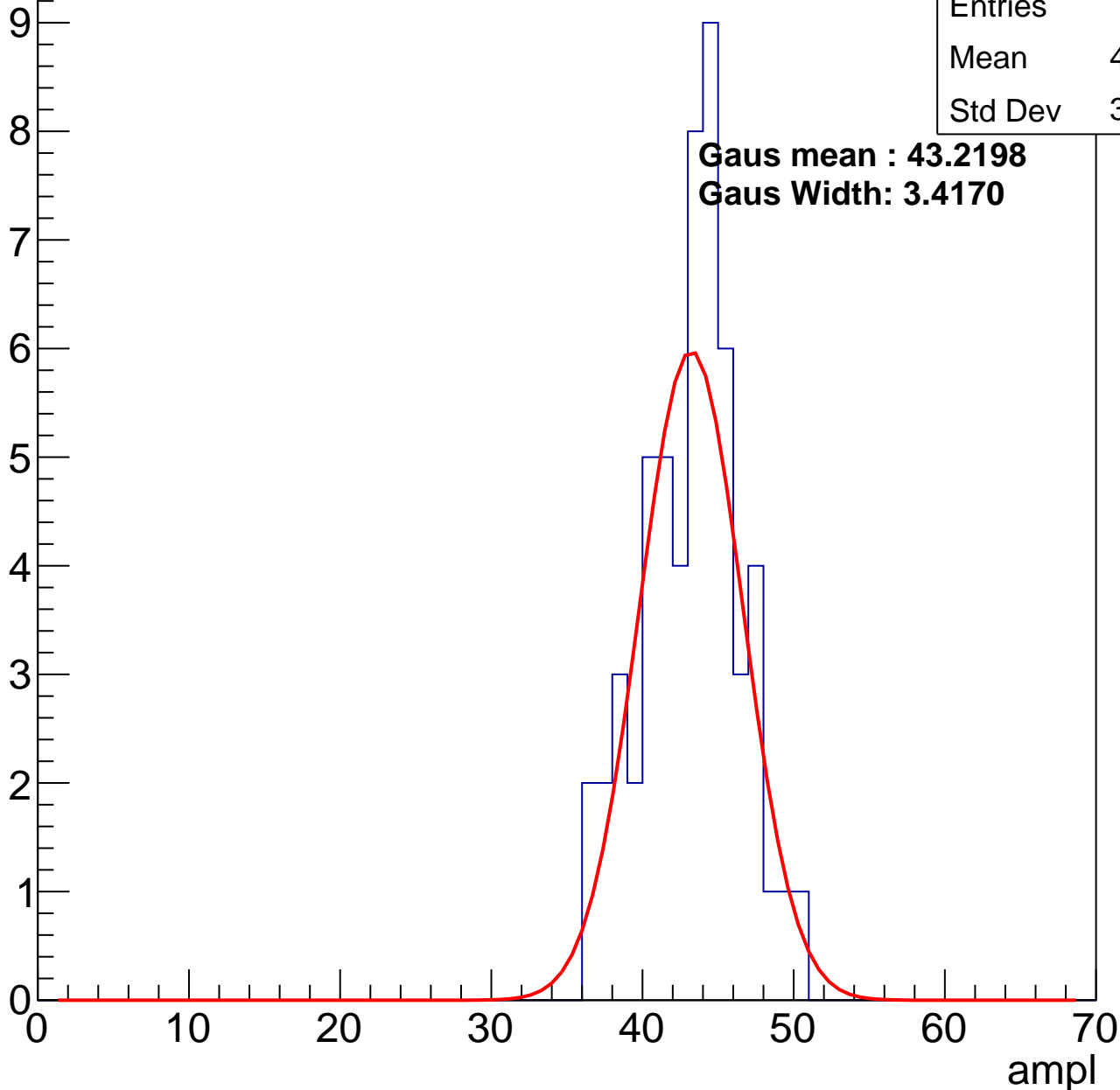
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.75
Std Dev	3.209

Gaus mean : 43.2198

Gaus Width: 3.4170

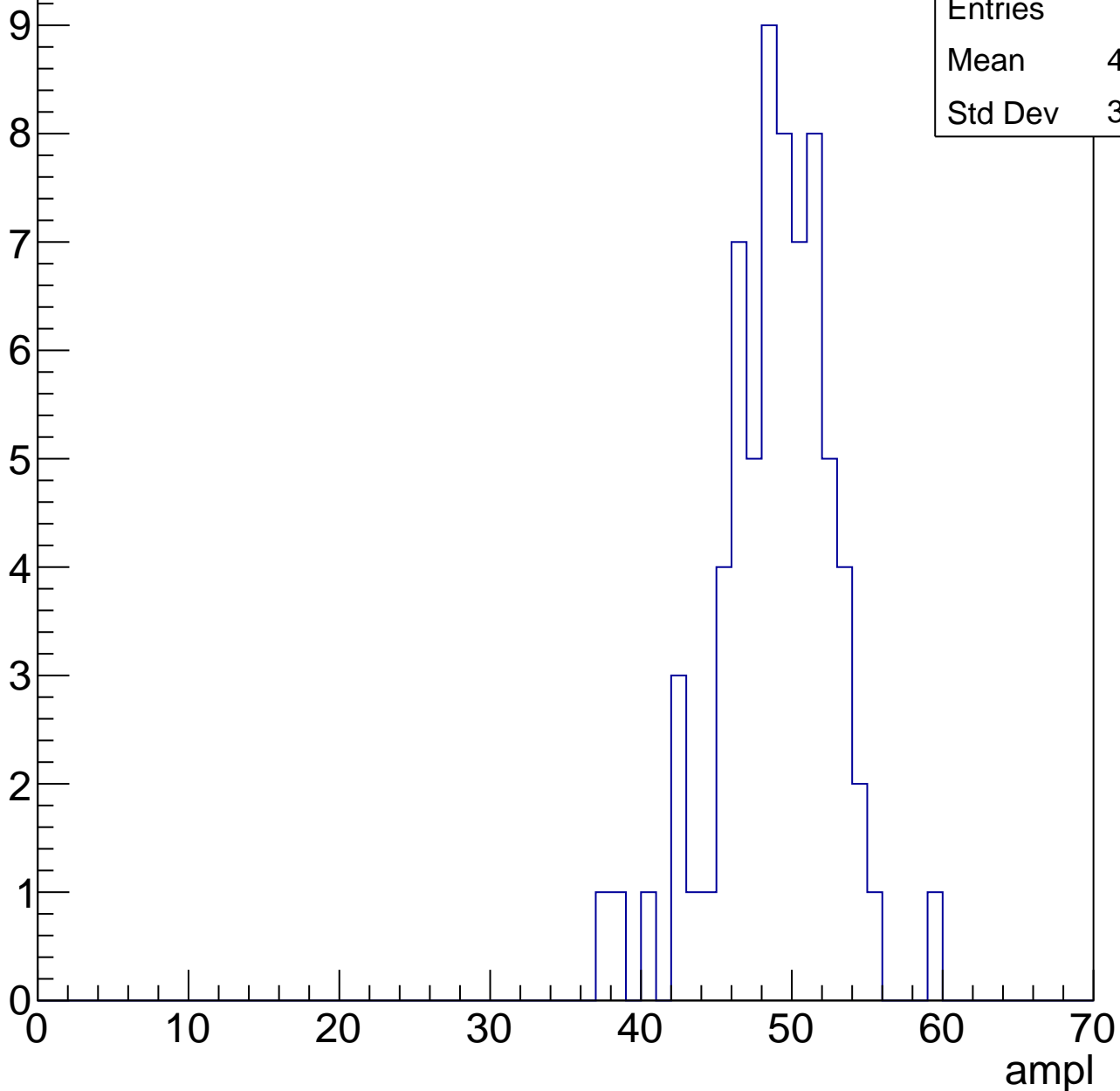


B1L102S, U10-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

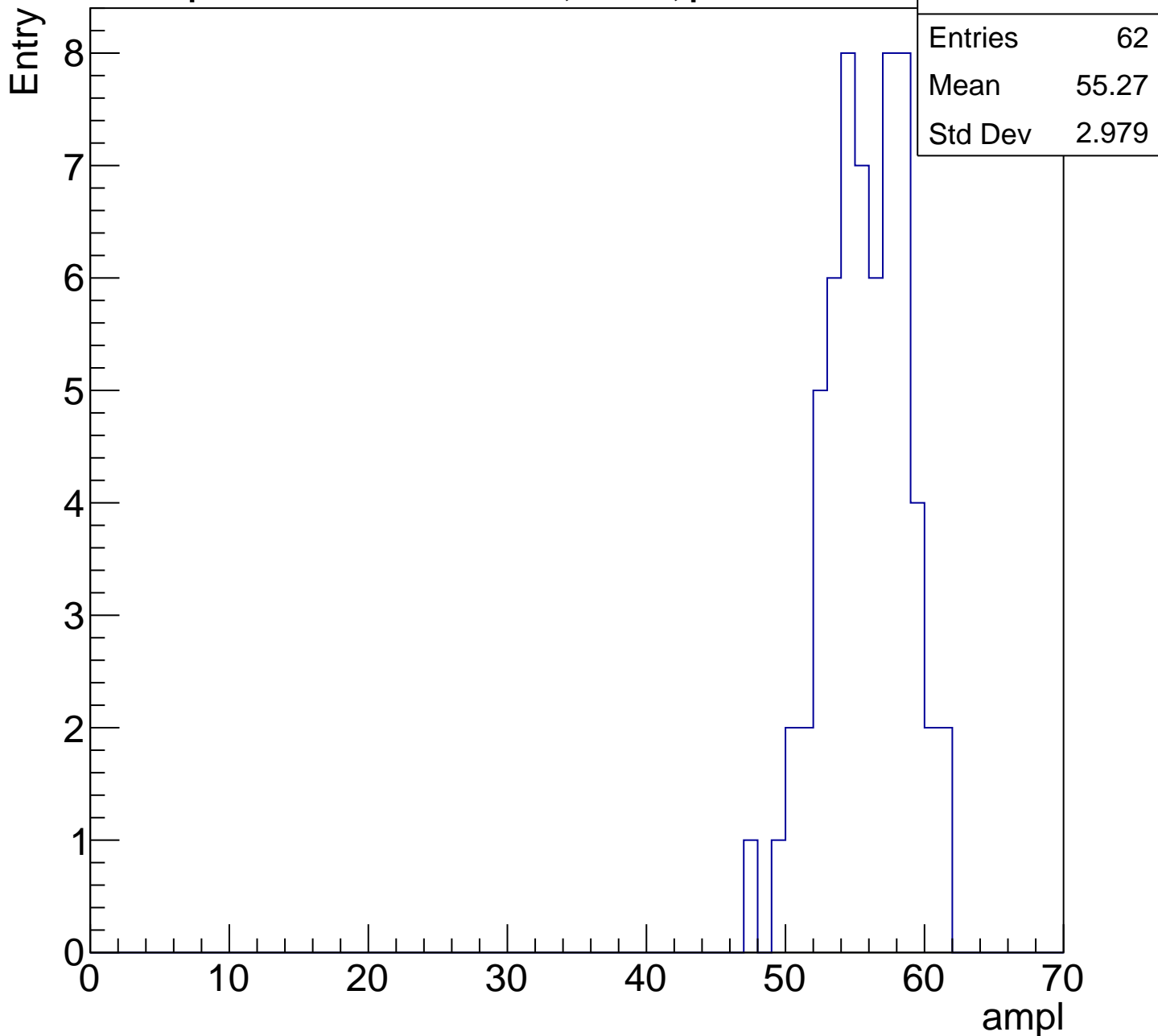
Entry

Entries	69
Mean	48.42
Std Dev	3.846



B1L102S, U10-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

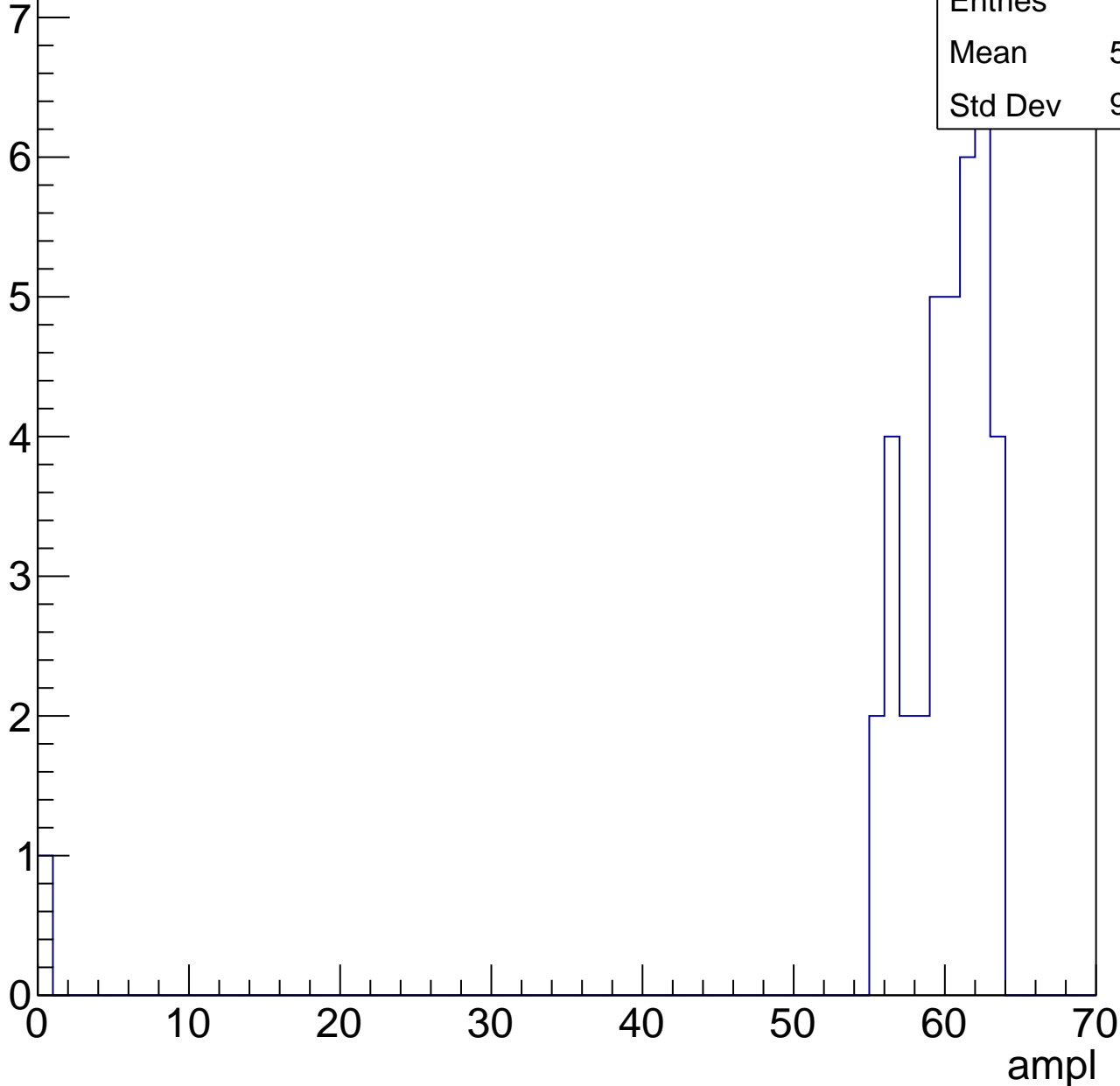


B1L102S, U10-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

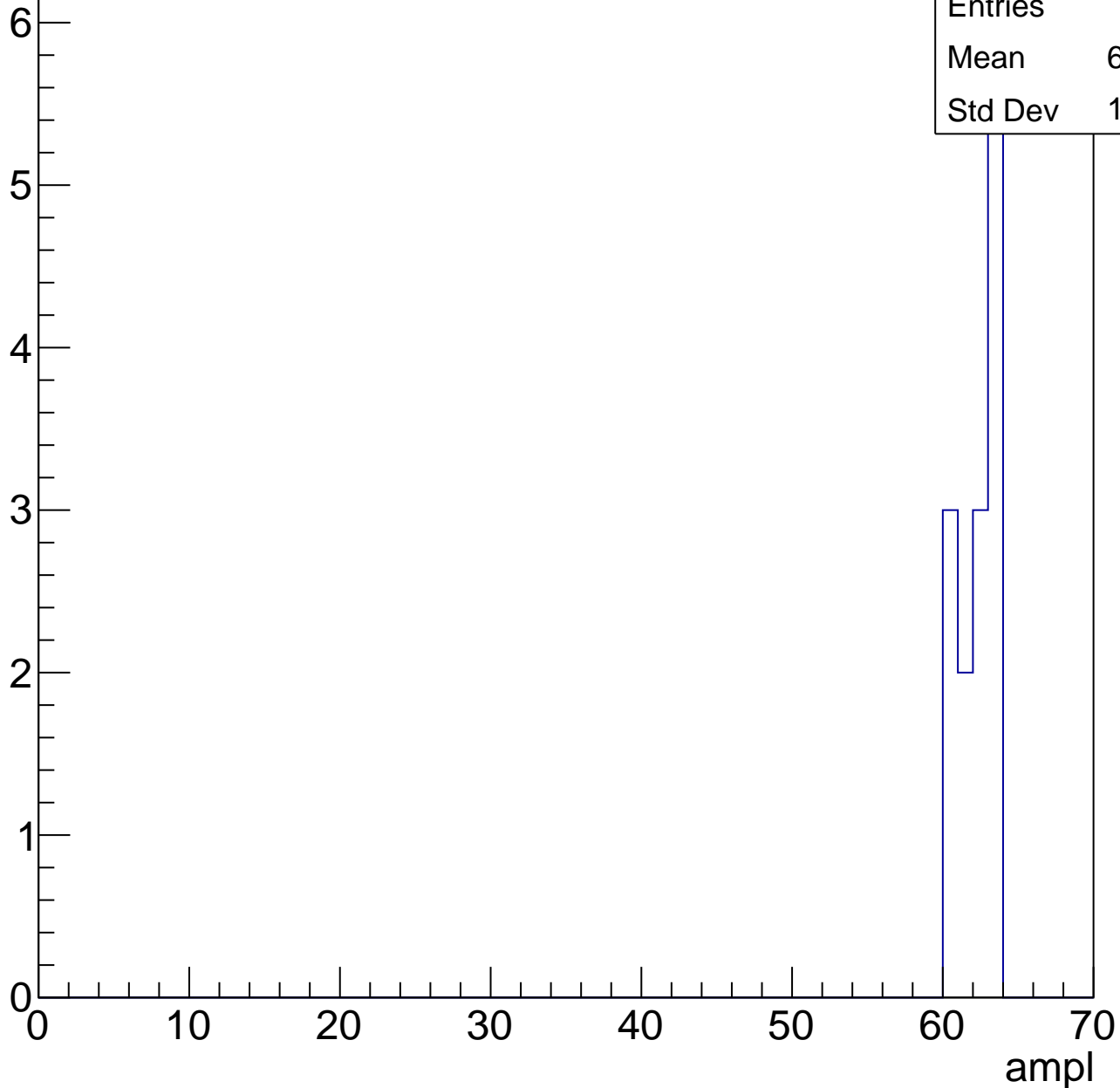
Entries	38
Mean	58.18
Std Dev	9.854



B1L102S, U10-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch59, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	28.39
Std Dev	4.556

Gaus mean : 29.2154

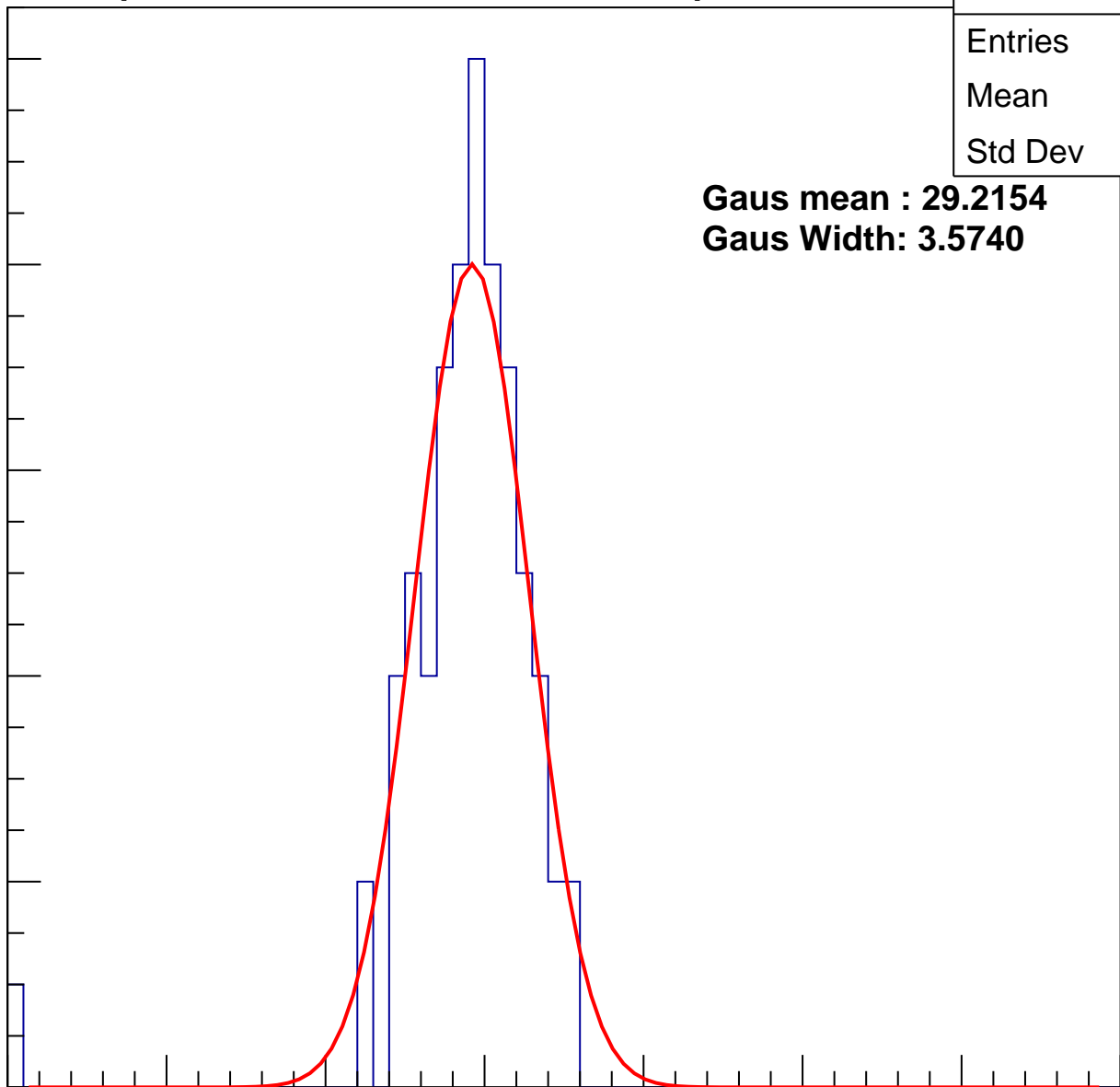
Gaus Width: 3.5740

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch59, adc1

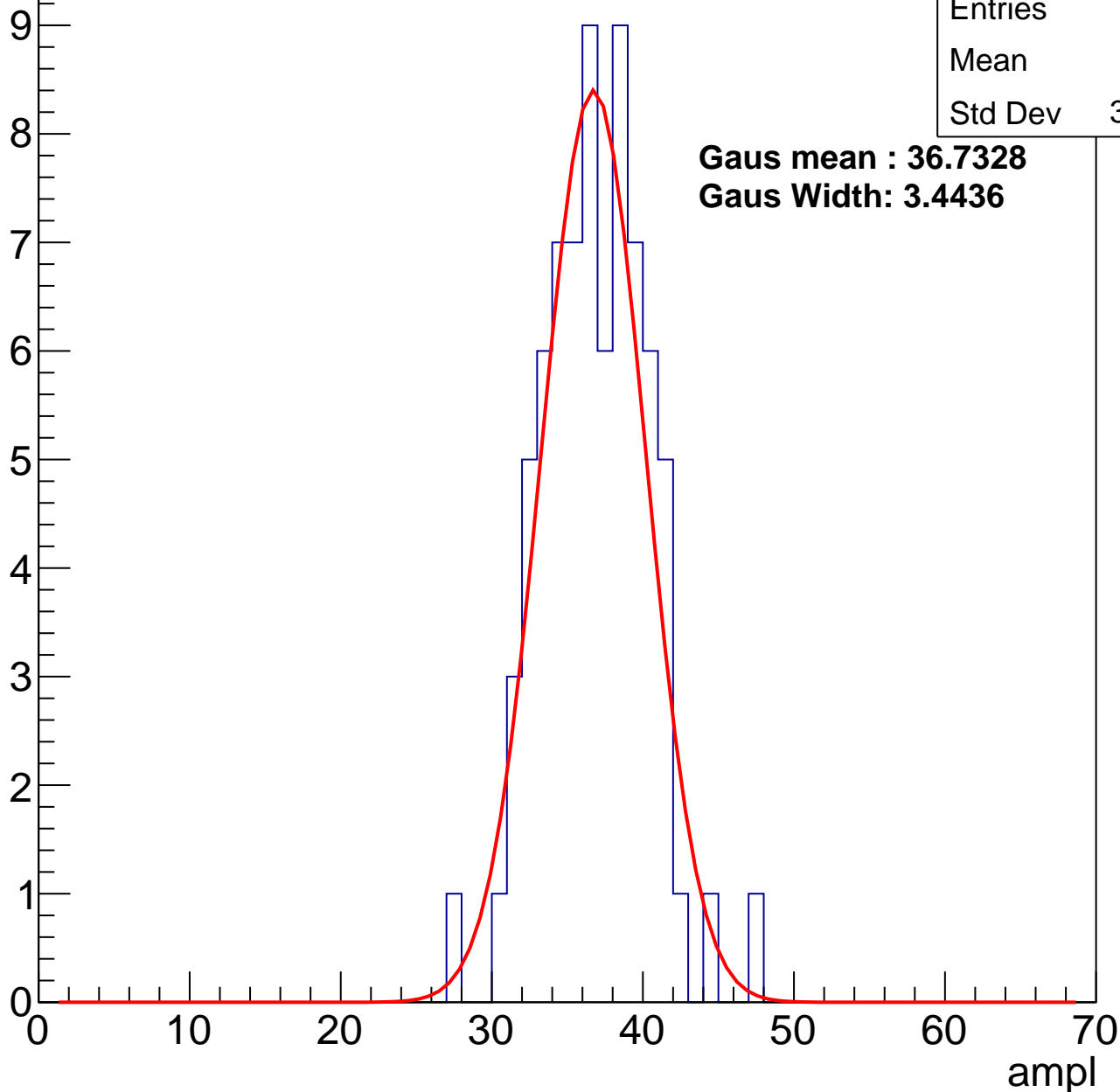
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.4
Std Dev	3.456

Gaus mean : 36.7328

Gaus Width: 3.4436



B1L102S, U10-ch59, adc2

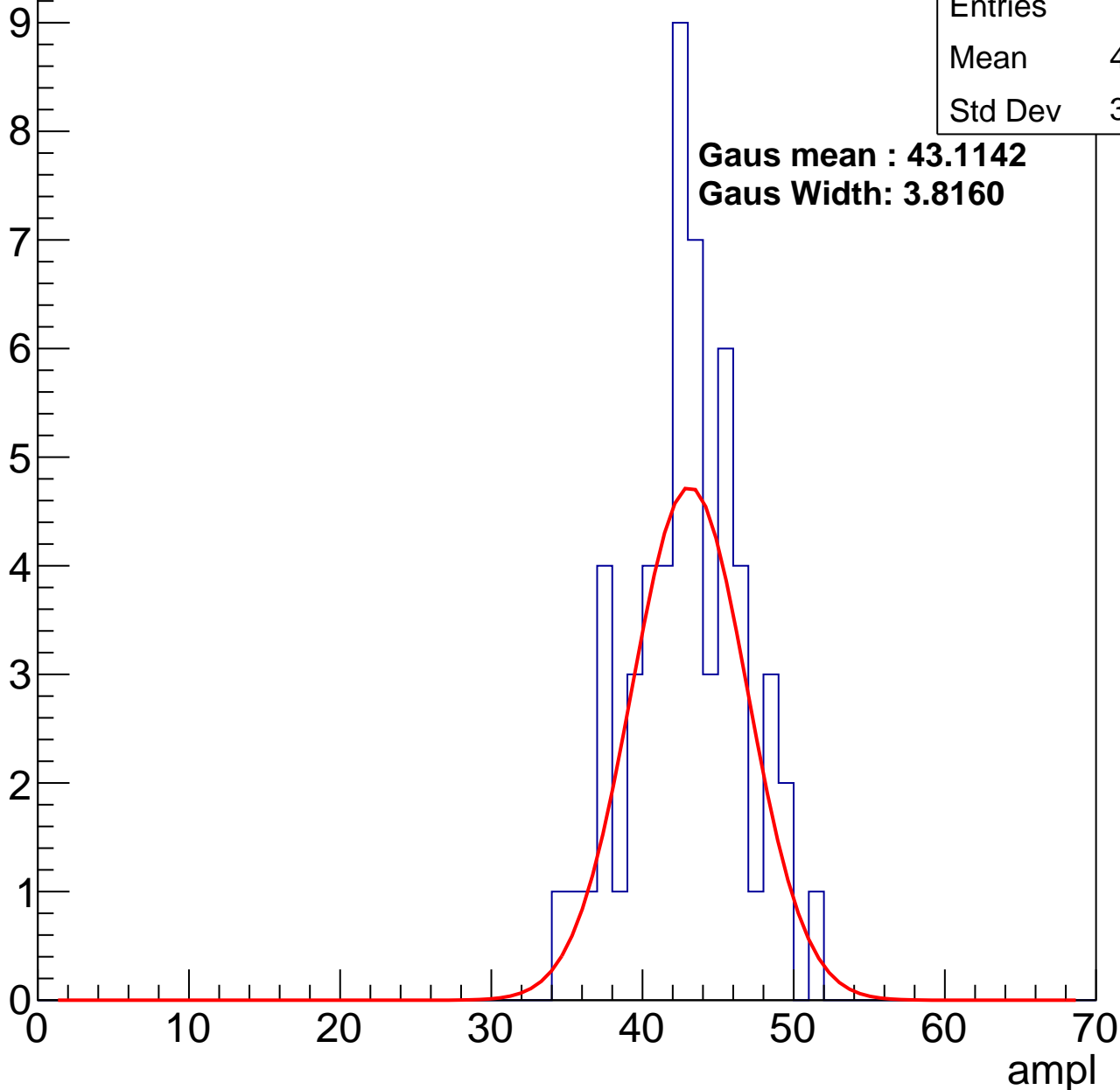
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.49
Std Dev	3.687

Gaus mean : 43.1142

Gaus Width: 3.8160

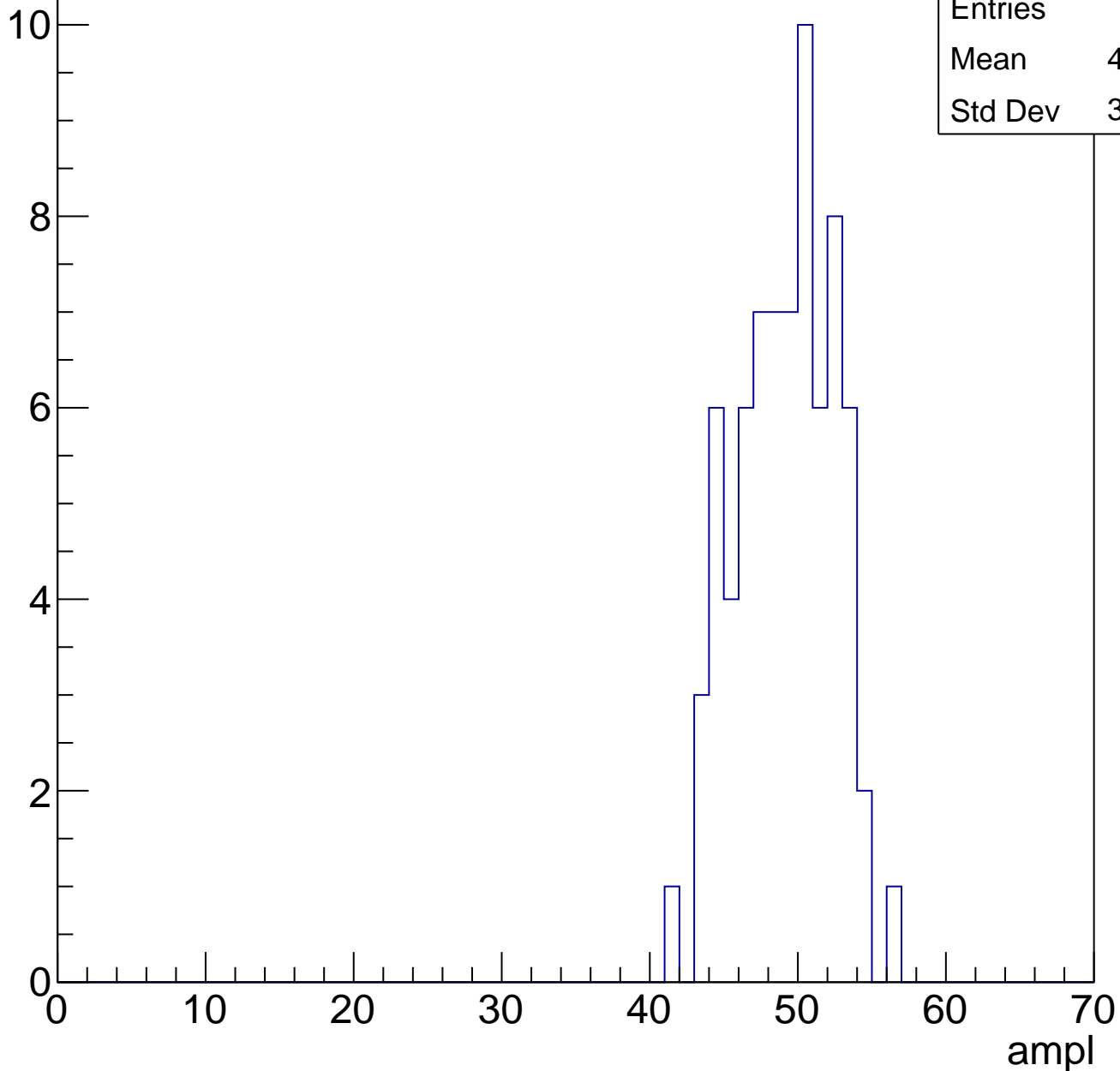


B1L102S, U10-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	48.68
Std Dev	3.222

Entry

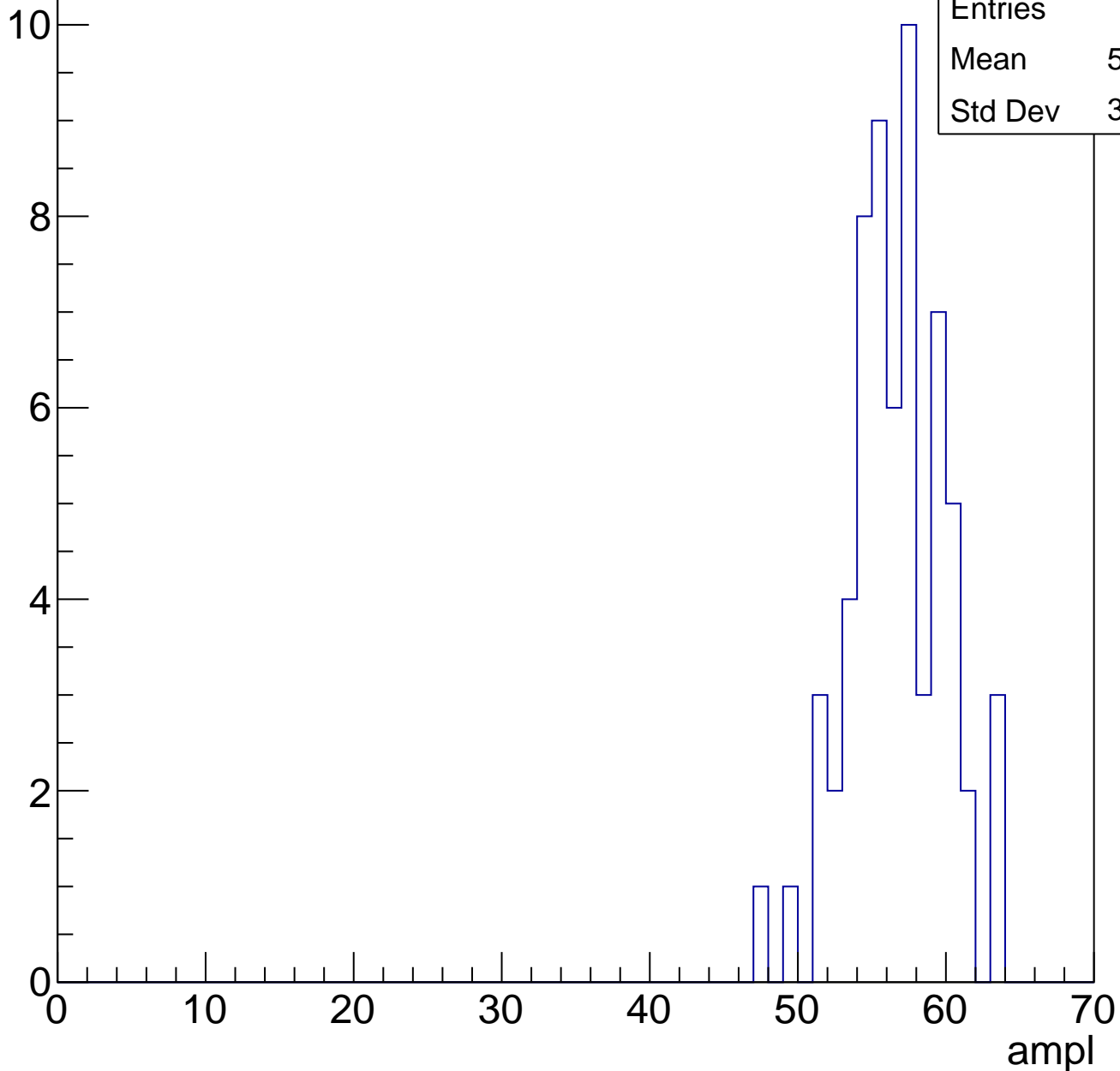


B1L102S, U10-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	56.19
Std Dev	3.245

Entry

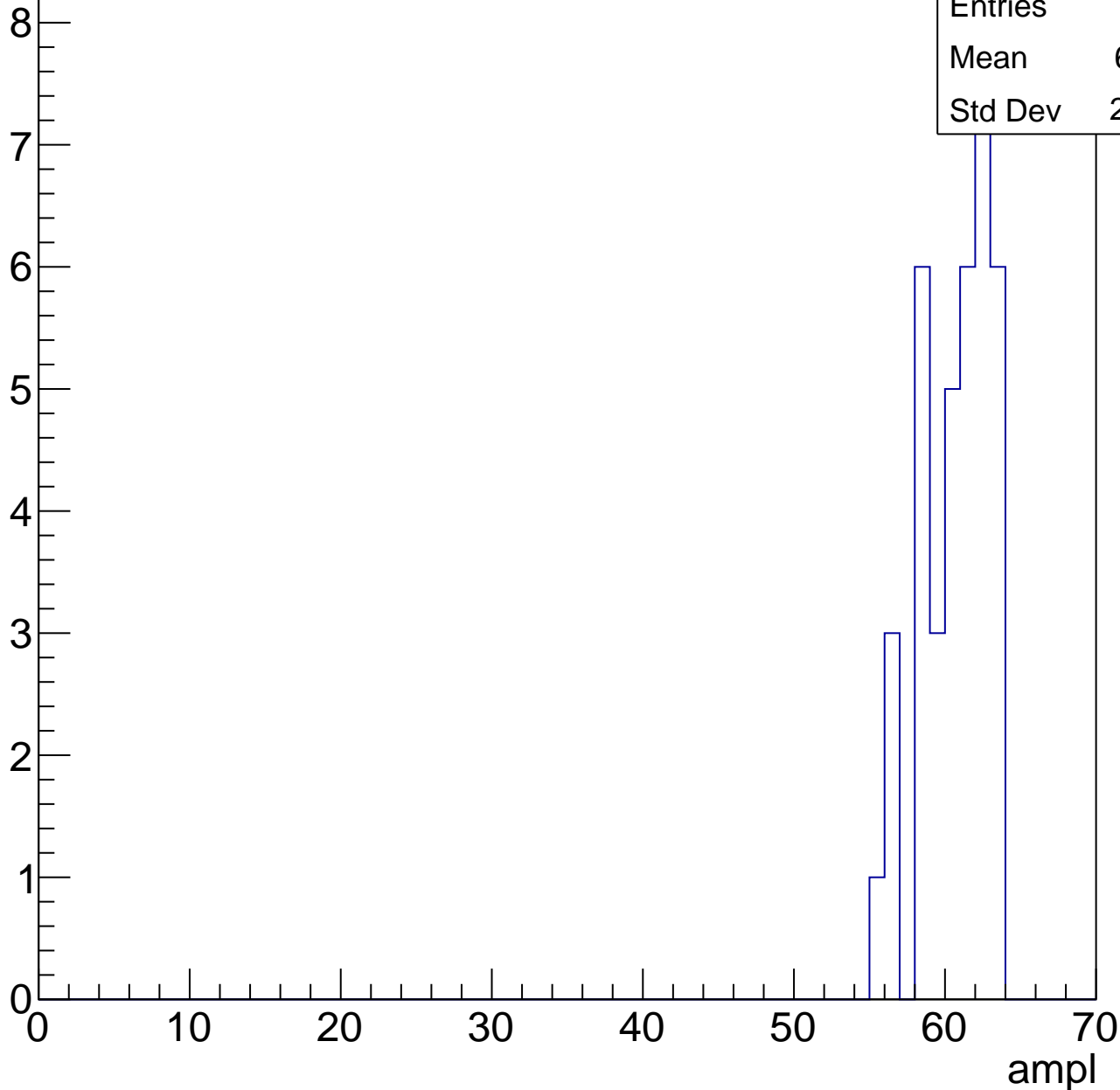


B1L102S, U10-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

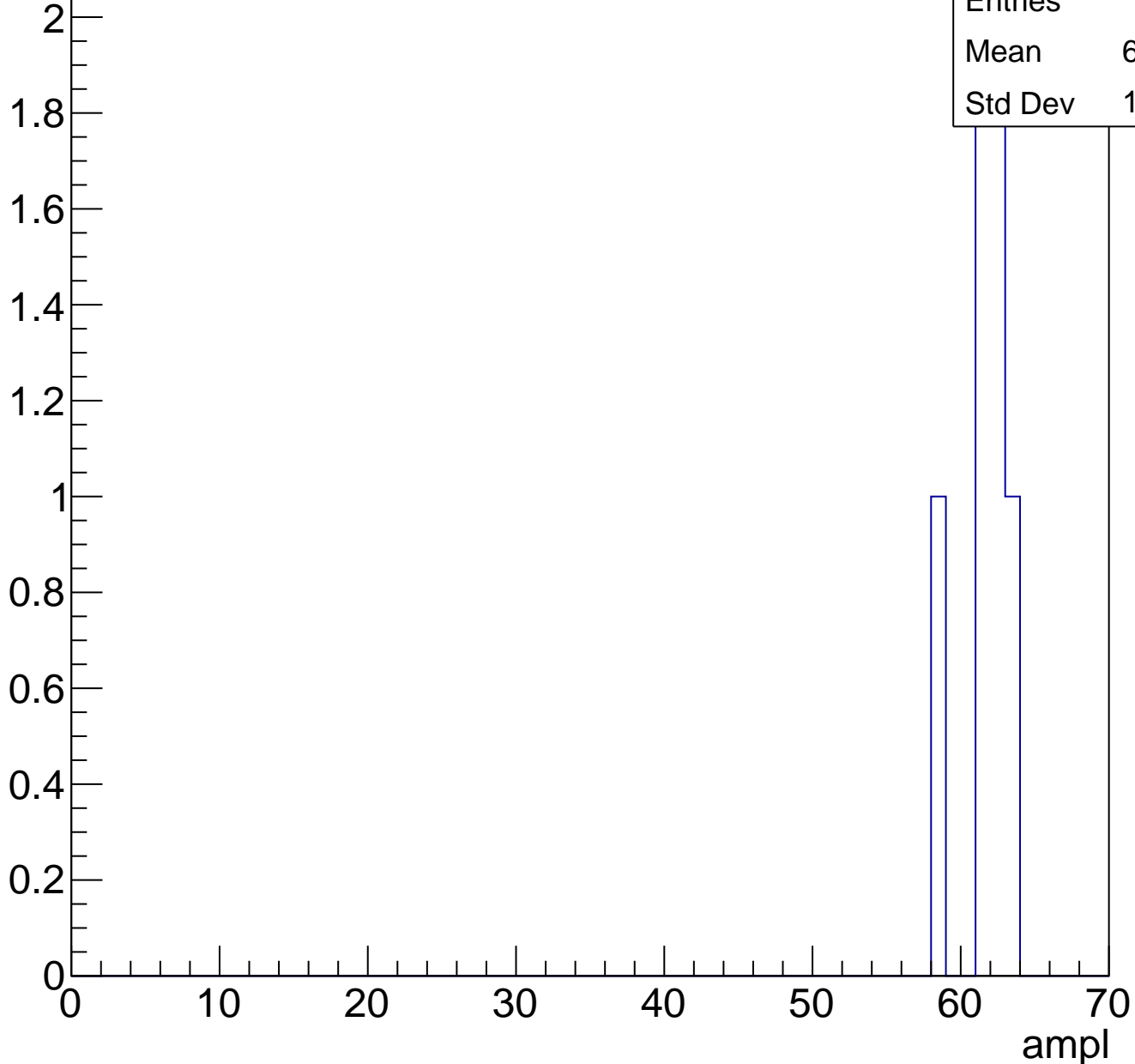
Entries	38
Mean	60.21
Std Dev	2.238



B1L102S, U10-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch60, adc0

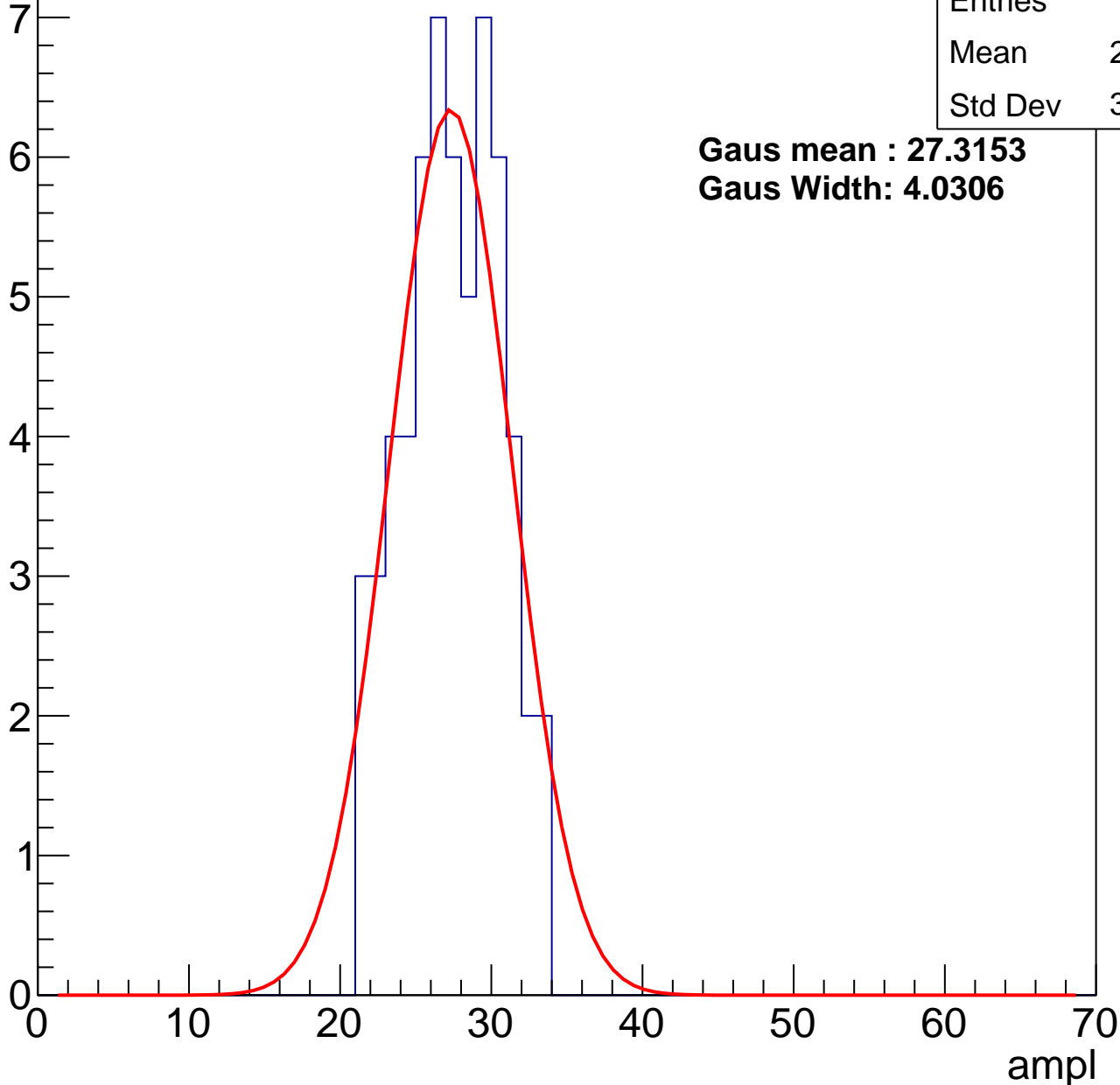
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	26.92
Std Dev	3.153

Gaus mean : 27.3153

Gaus Width: 4.0306



B1L102S, U10-ch60, adc1

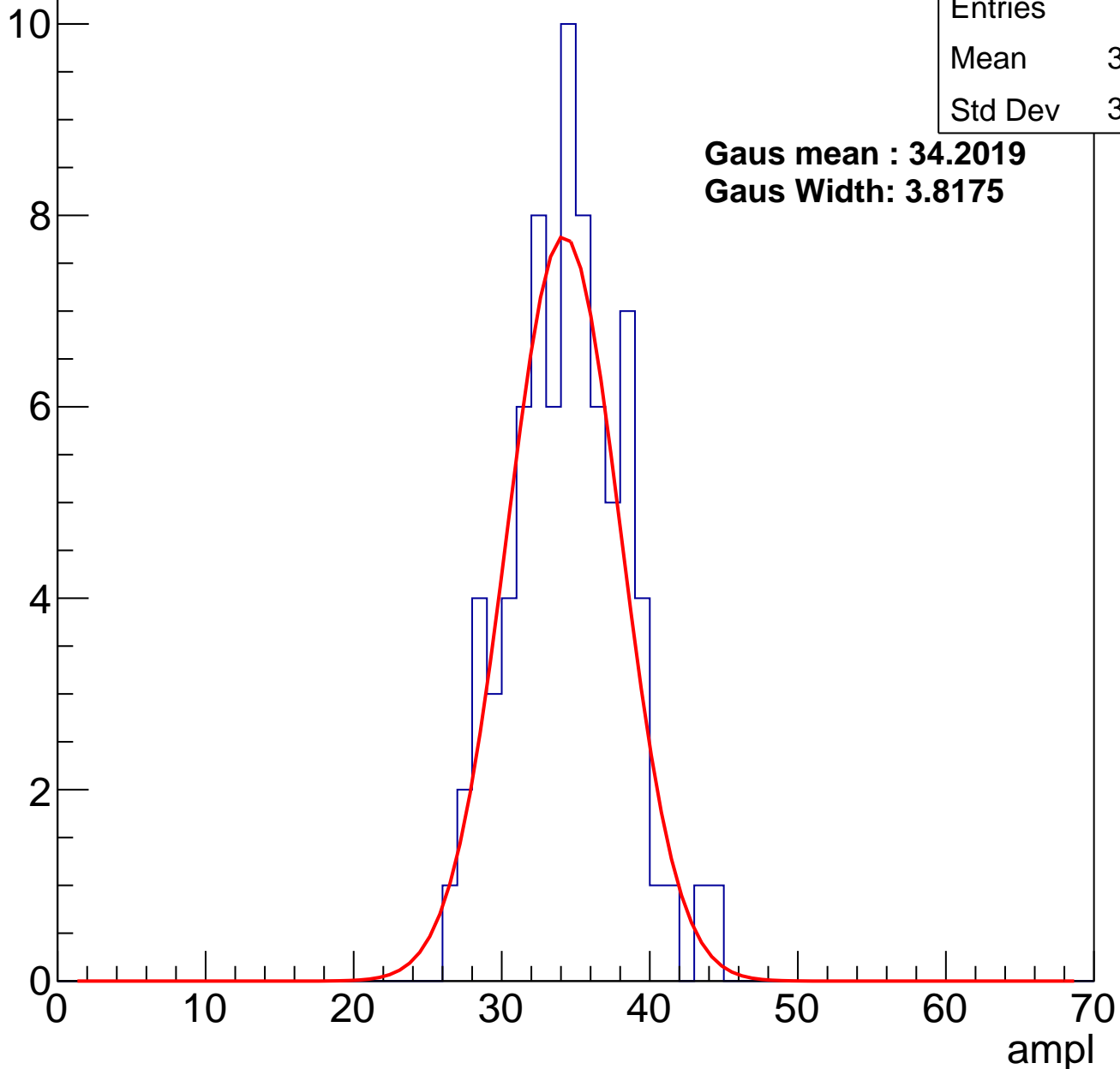
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	33.97
Std Dev	3.742

Gaus mean : 34.2019

Gaus Width: 3.8175

Entry



B1L102S, U10-ch60, adc2

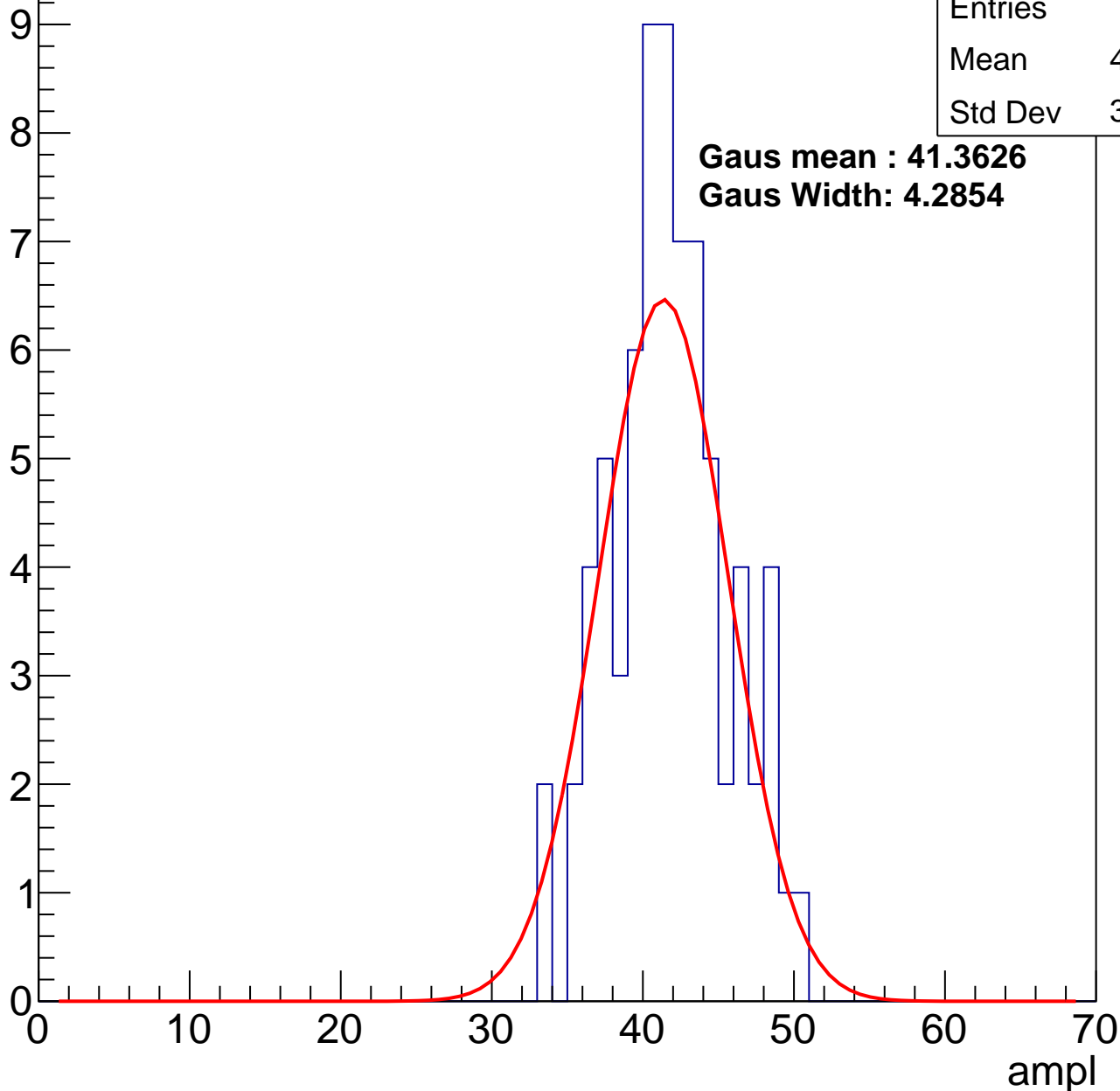
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.32
Std Dev	3.807

Gaus mean : 41.3626

Gaus Width: 4.2854

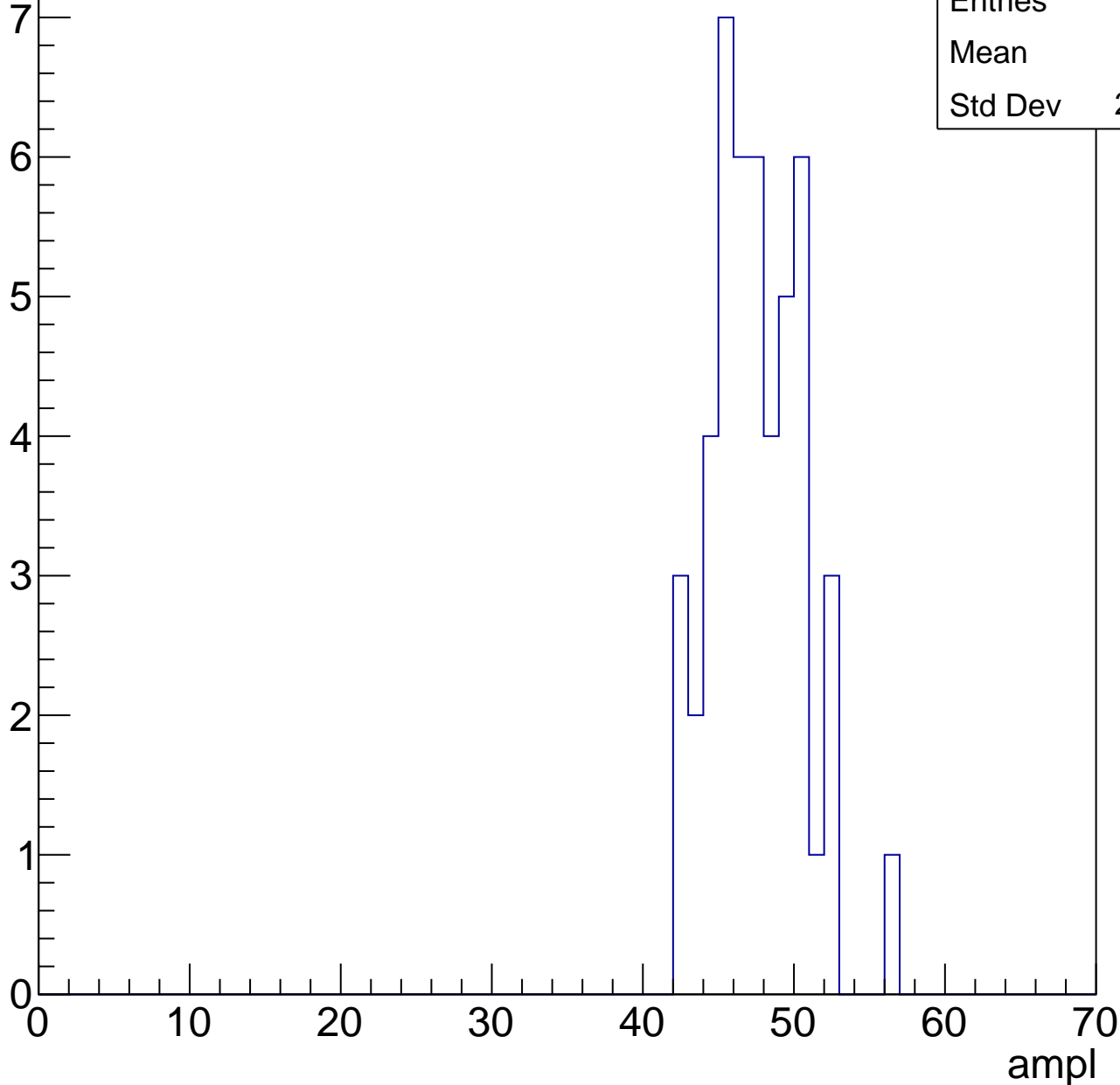


B1L102S, U10-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	47.1
Std Dev	2.981

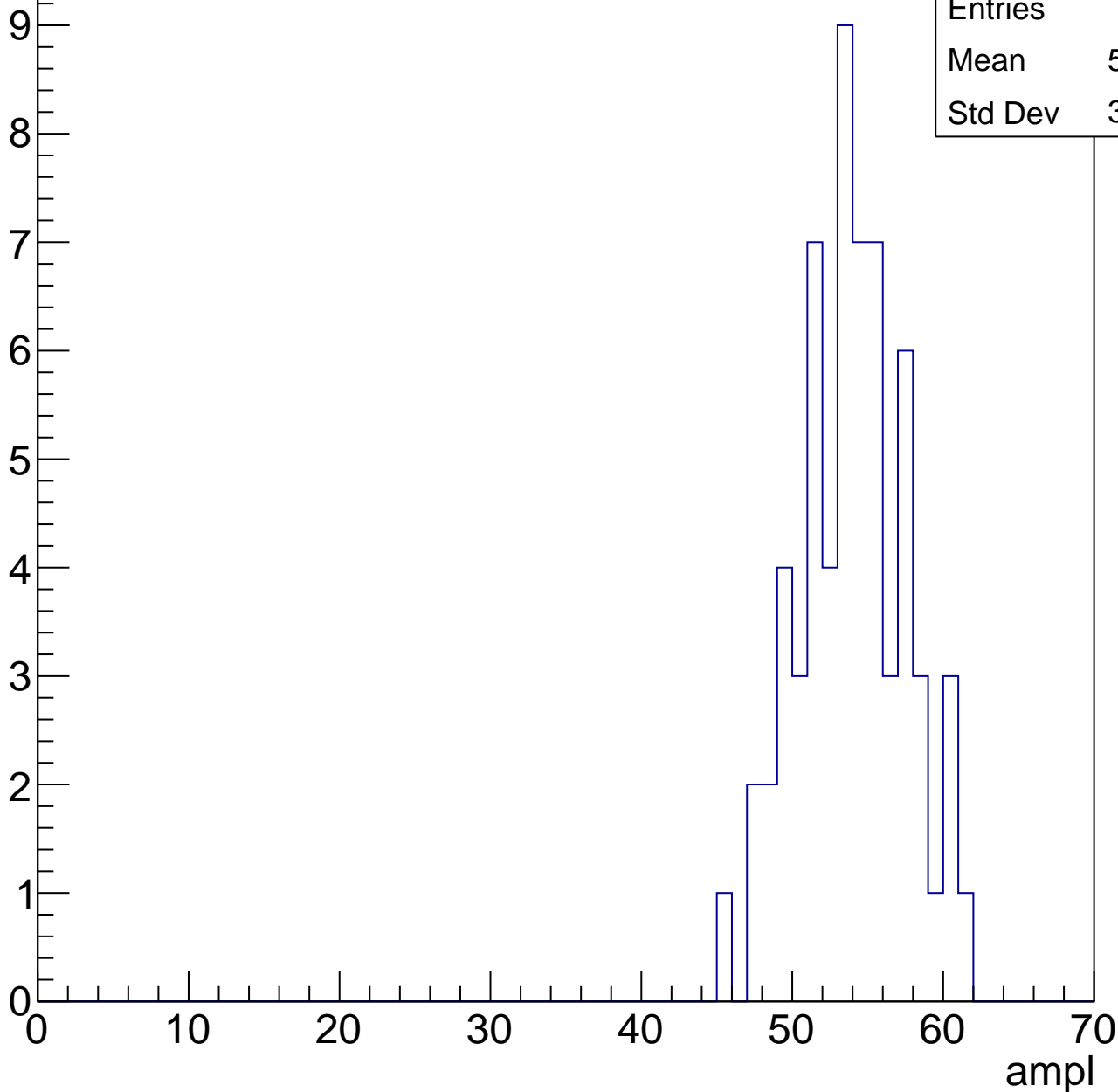


B1L102S, U10-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.49
Std Dev	3.505

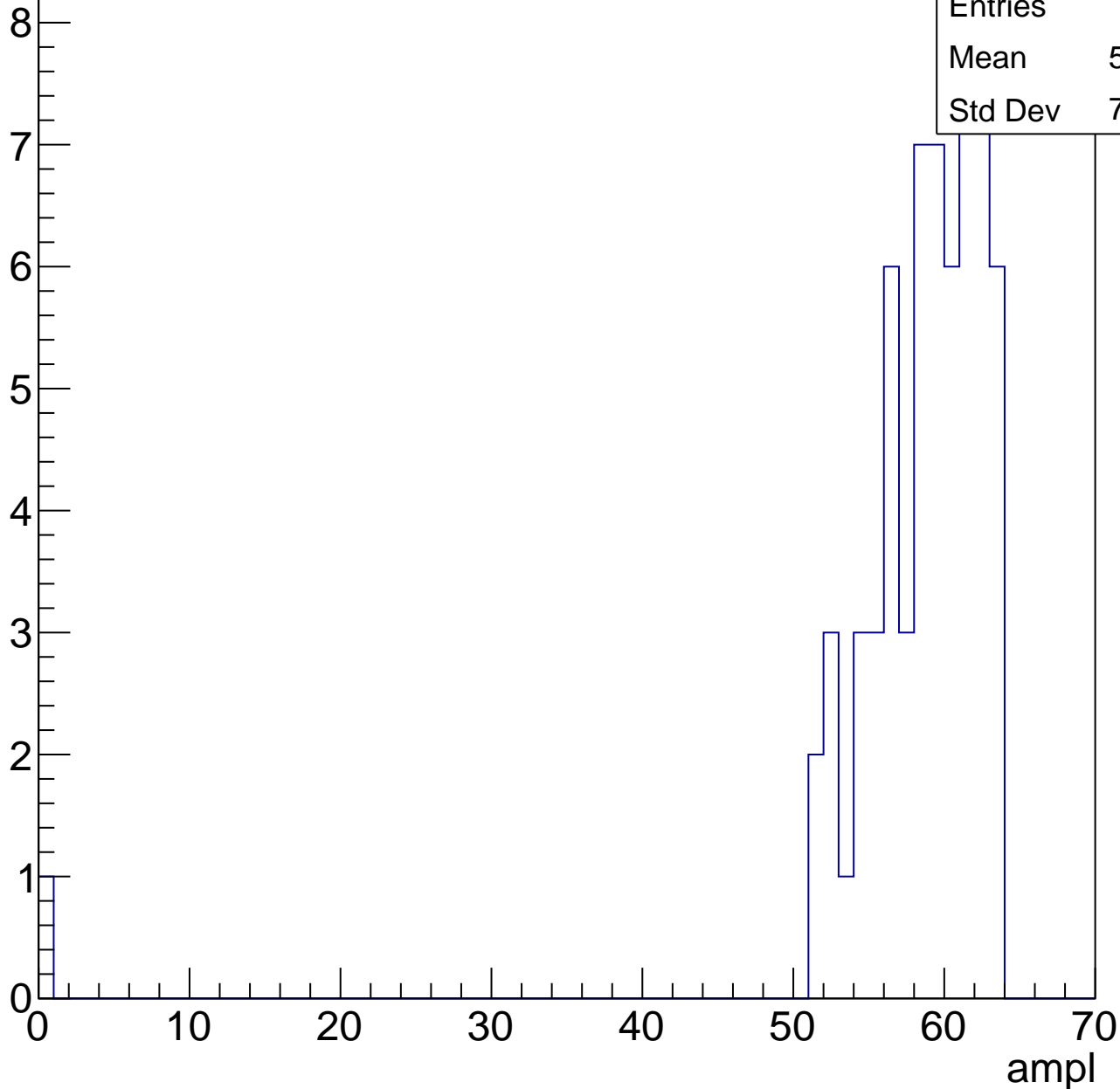


B1L102S, U10-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	57.59
Std Dev	7.972



B1L102S, U10-ch60, adc6

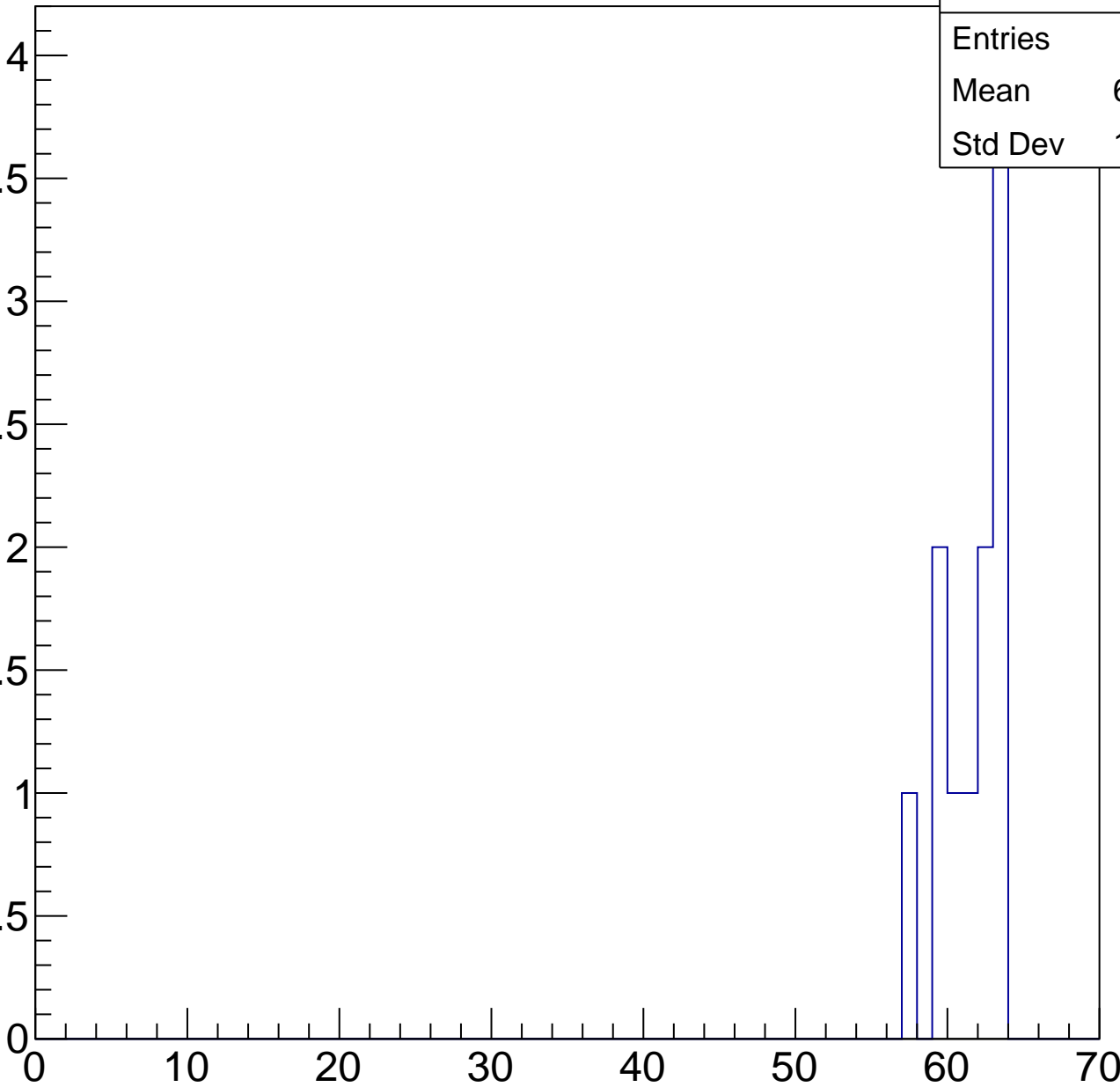
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	61.09
Std Dev	1.975

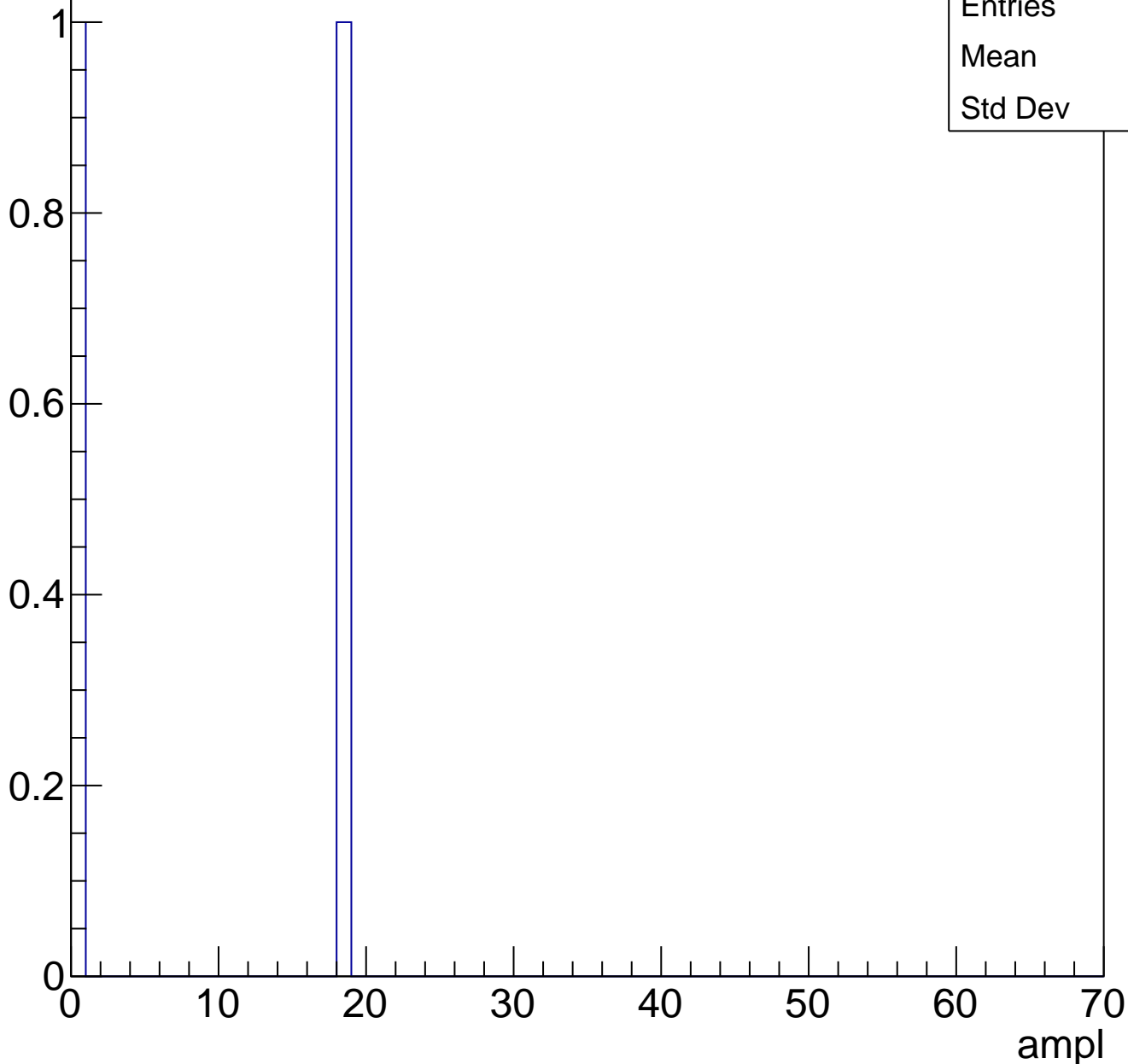
ampl



B1L102S, U10-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9
Std Dev	9

B1L102S, U10-ch61, adc0

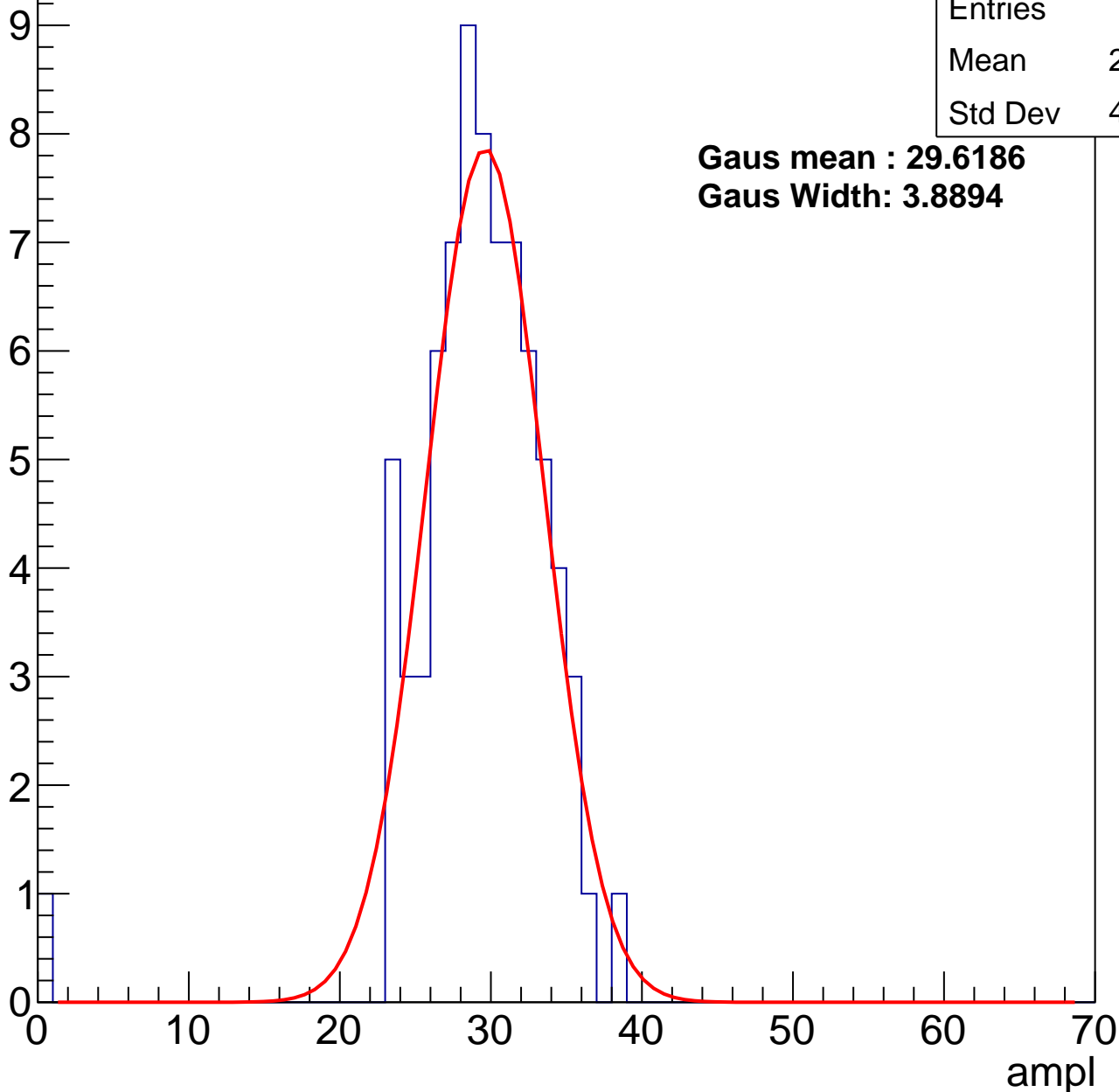
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.82
Std Dev	4.784

Gaus mean : 29.6186

Gaus Width: 3.8894

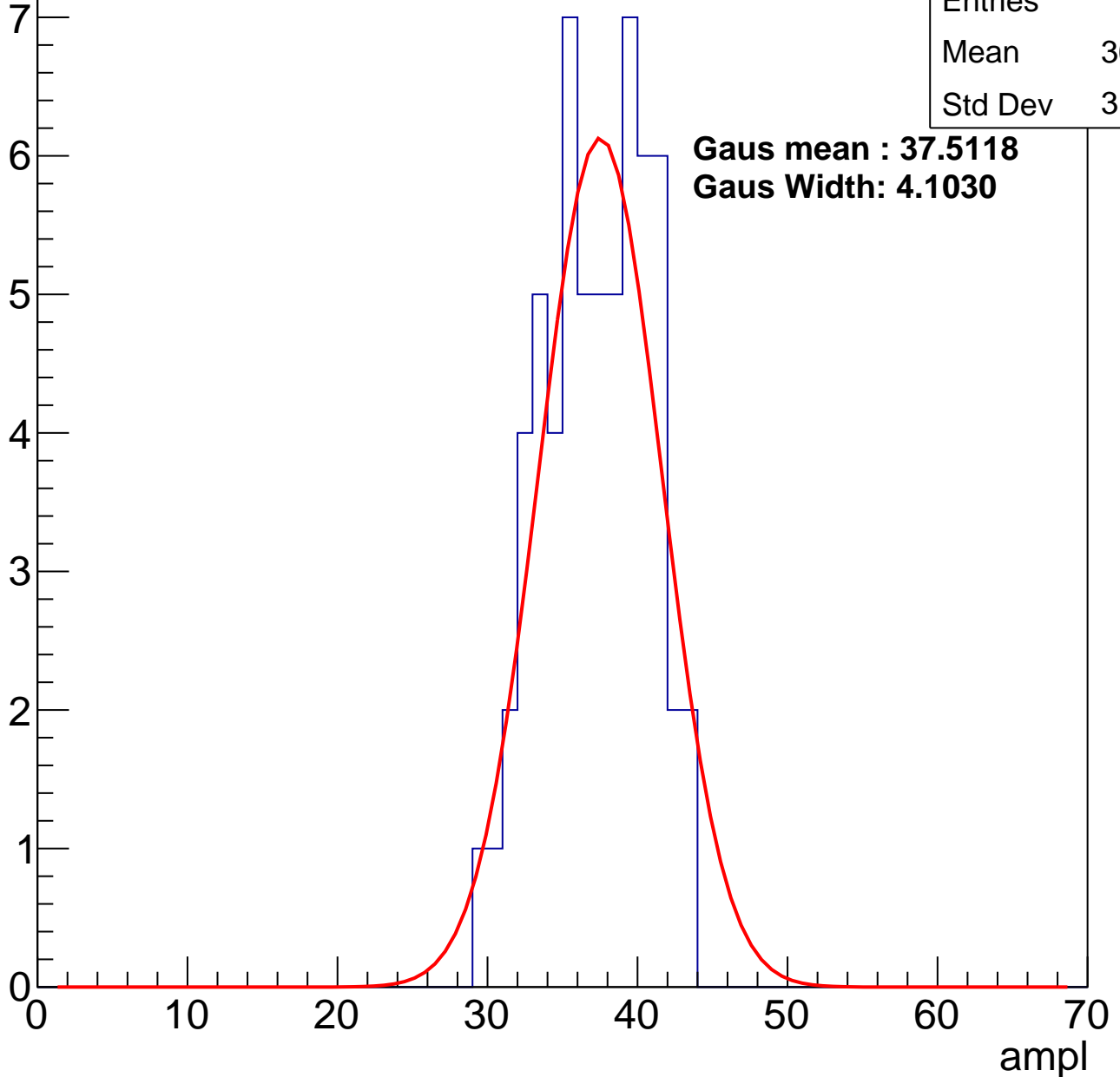


B1L102S, U10-ch61, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.76
Std Dev	3.444



B1L102S, U10-ch61, adc2

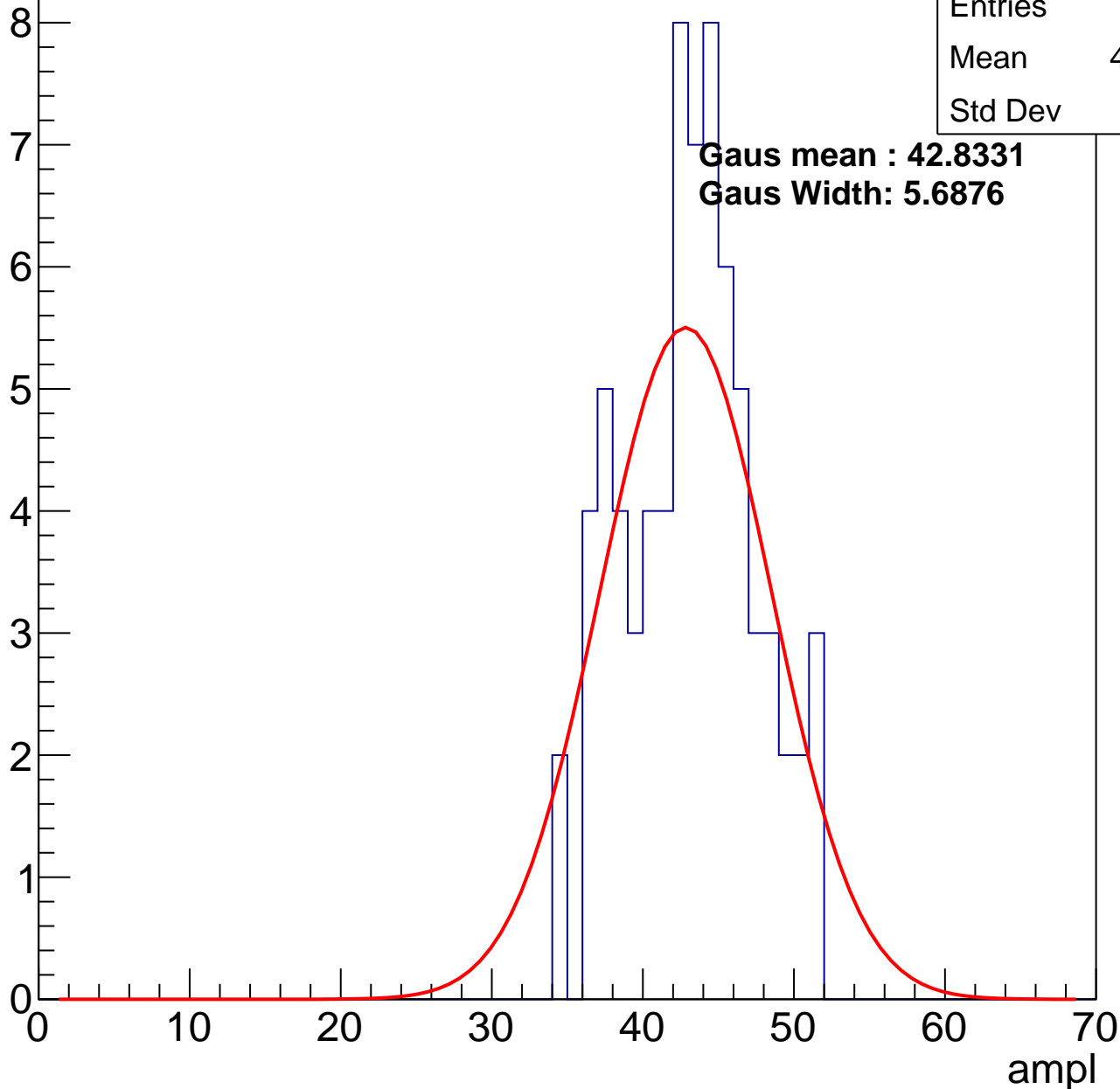
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.67
Std Dev	4.22

Gaus mean : 42.8331

Gaus Width: 5.6876

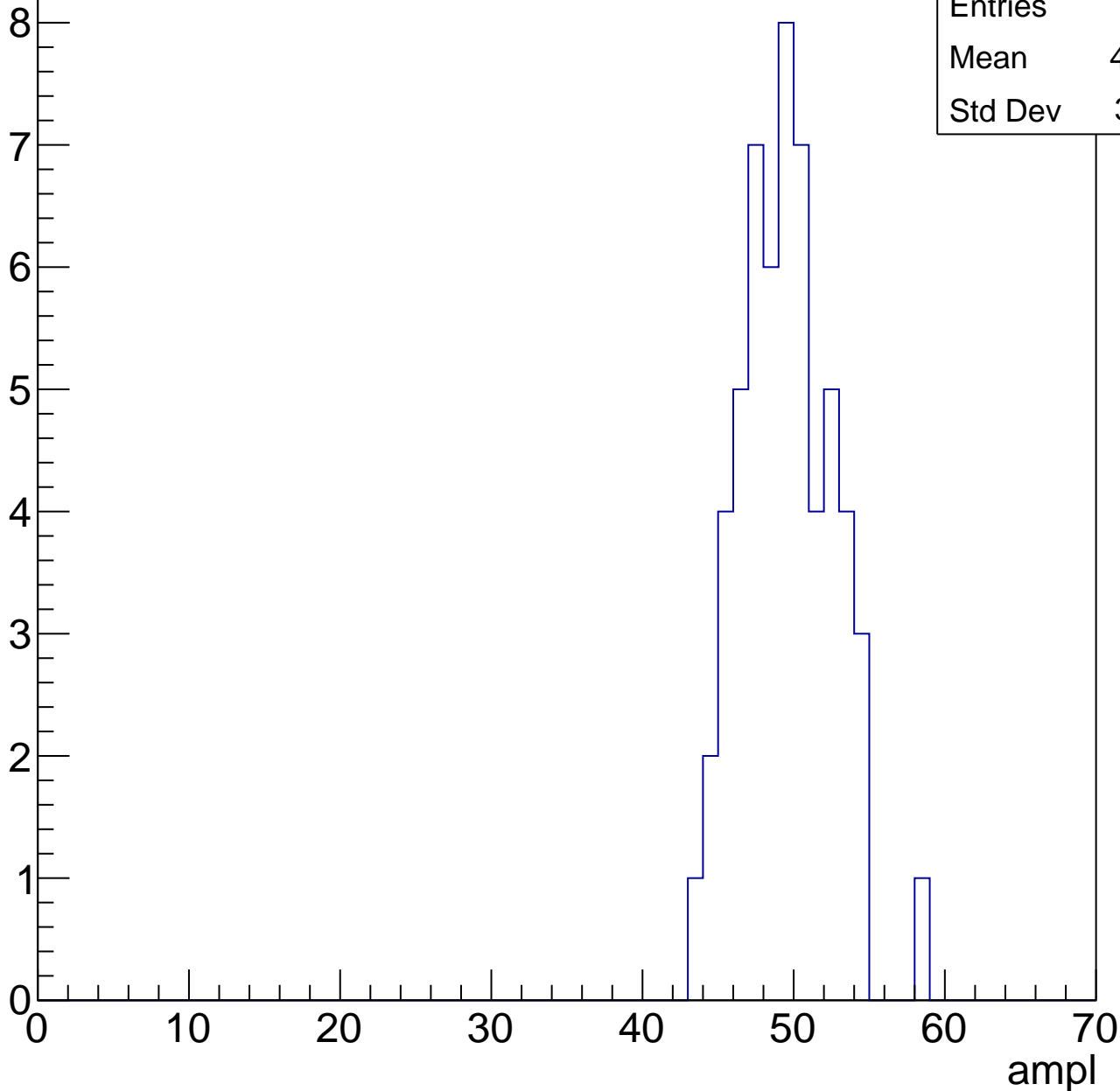


B1L102S, U10-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.05
Std Dev	3.011

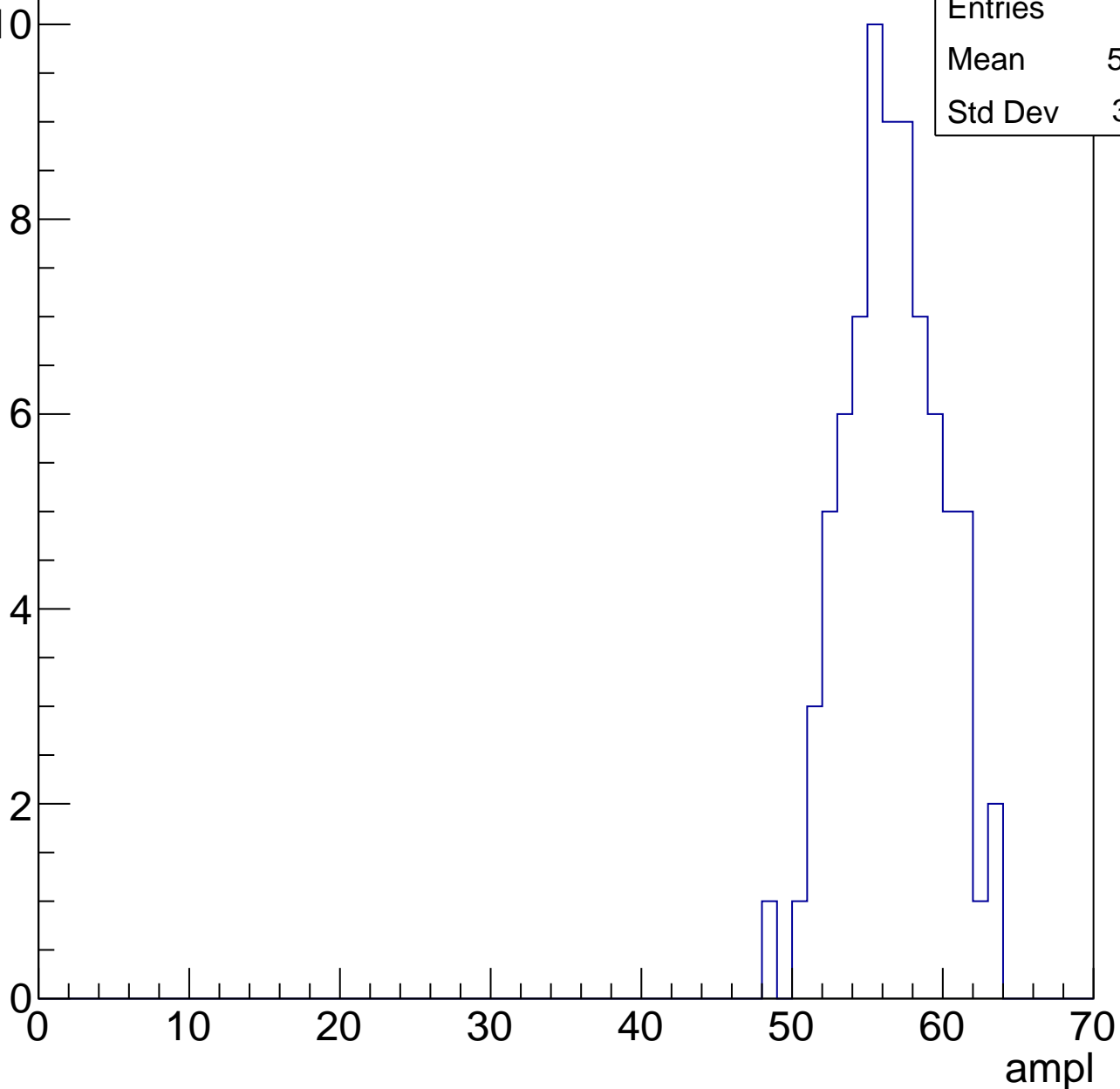


B1L102S, U10-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	56.19
Std Dev	3.171

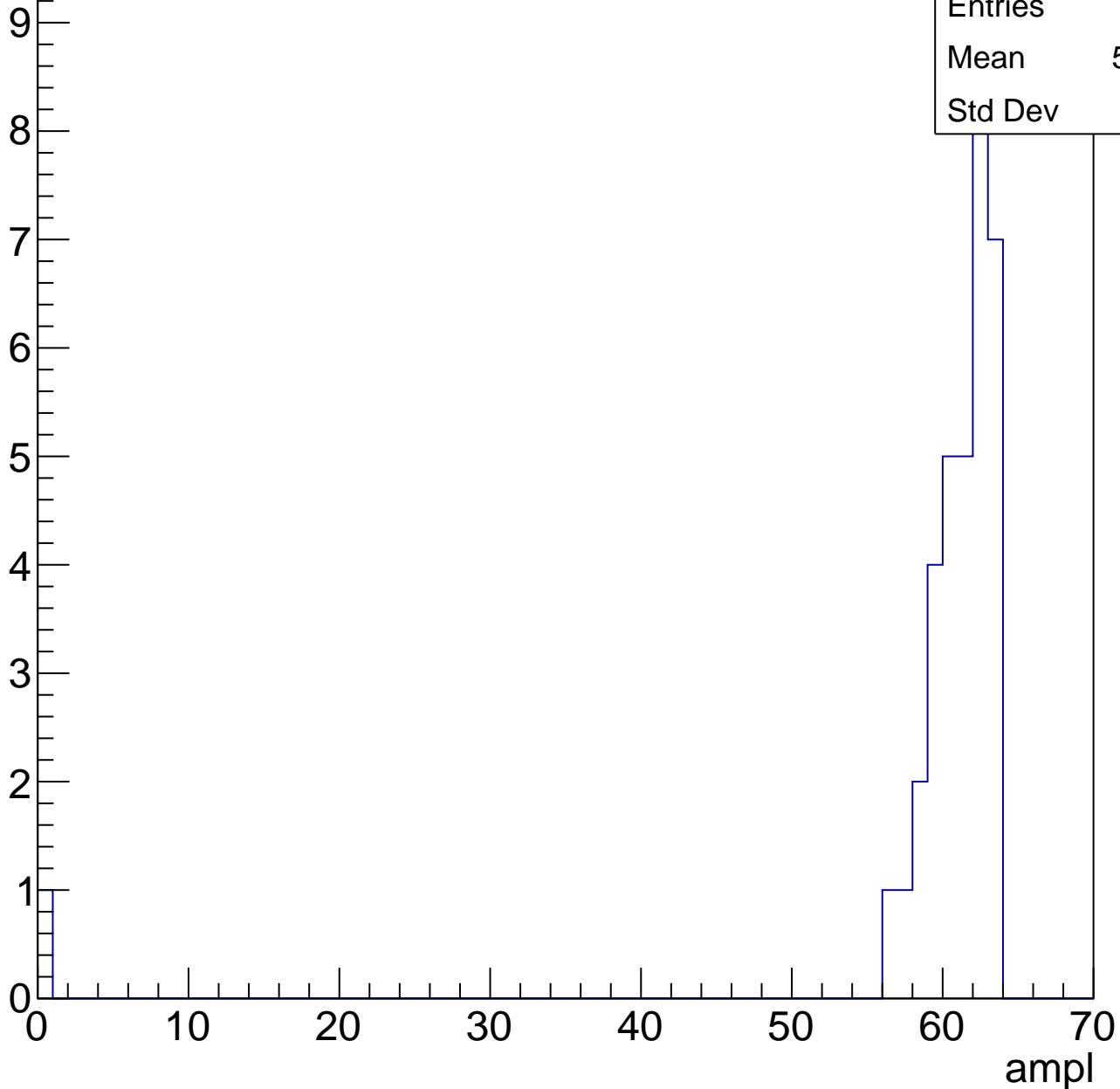


B1L102S, U10-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.11
Std Dev	10.3



B1L102S, U10-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L102S, U10-ch62, adc0

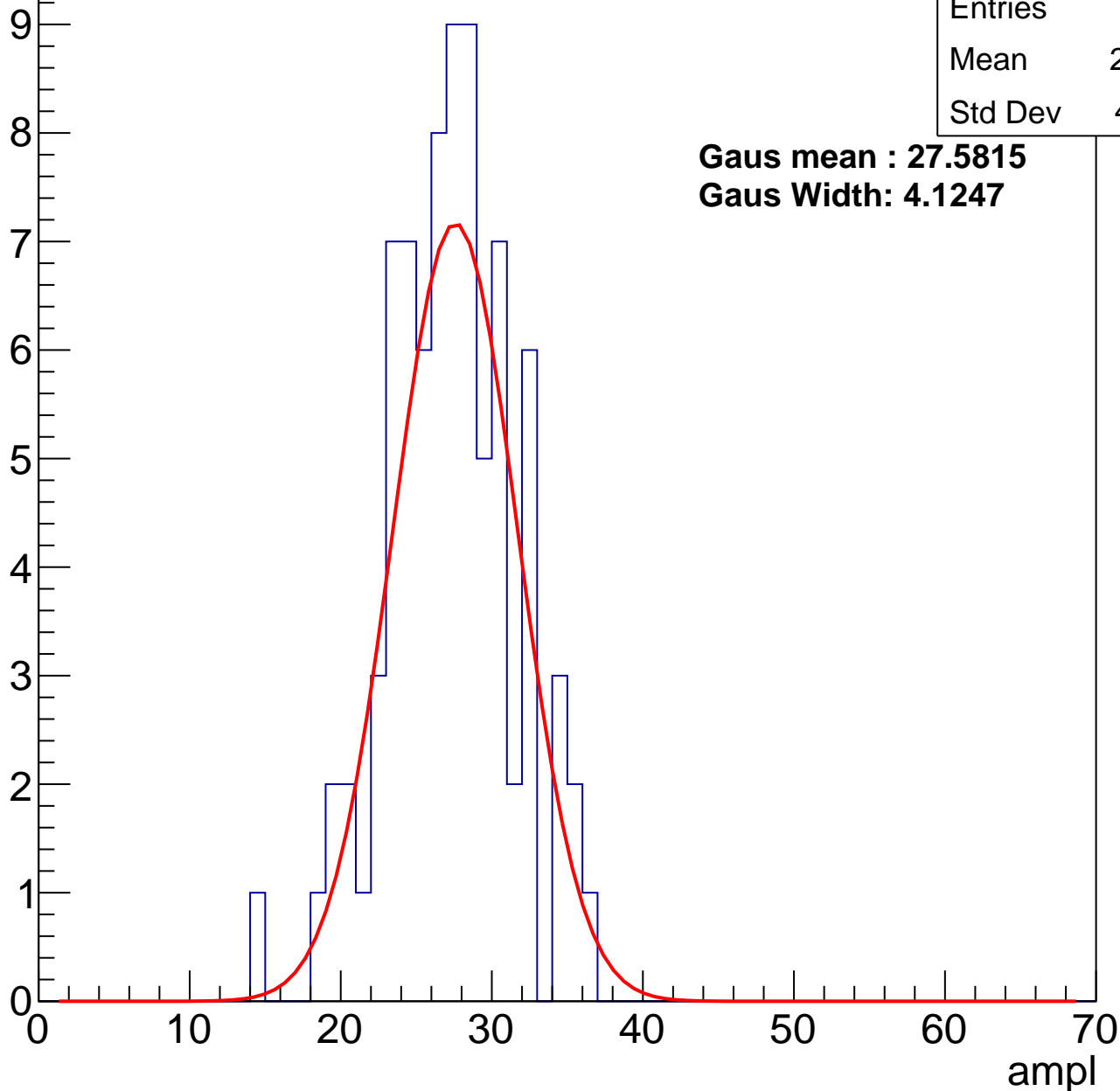
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	26.78
Std Dev	4.161

Gaus mean : 27.5815

Gaus Width: 4.1247



B1L102S, U10-ch62, adc1

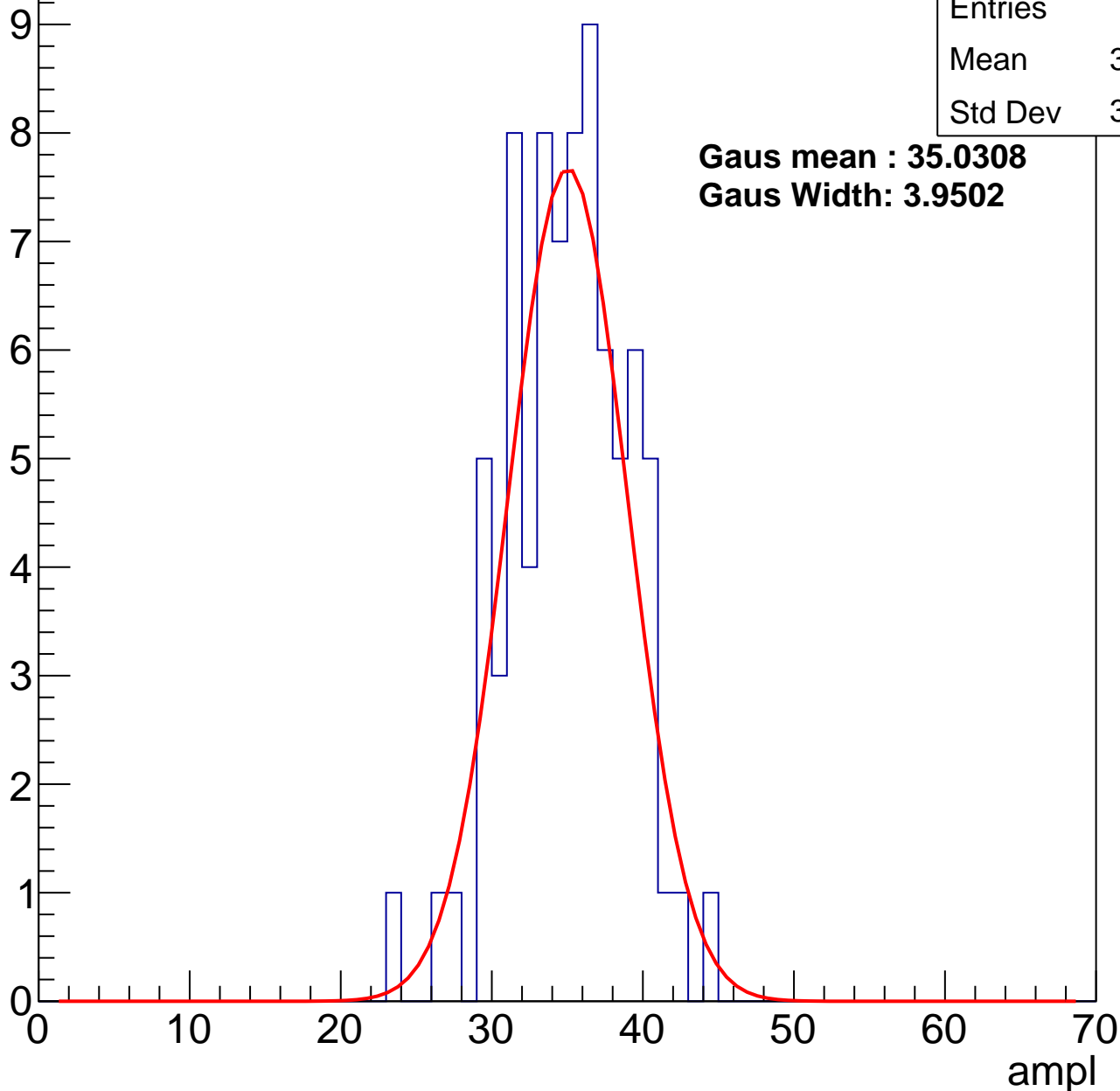
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.58
Std Dev	3.869

Gaus mean : 35.0308

Gaus Width: 3.9502



B1L102S, U10-ch62, adc2

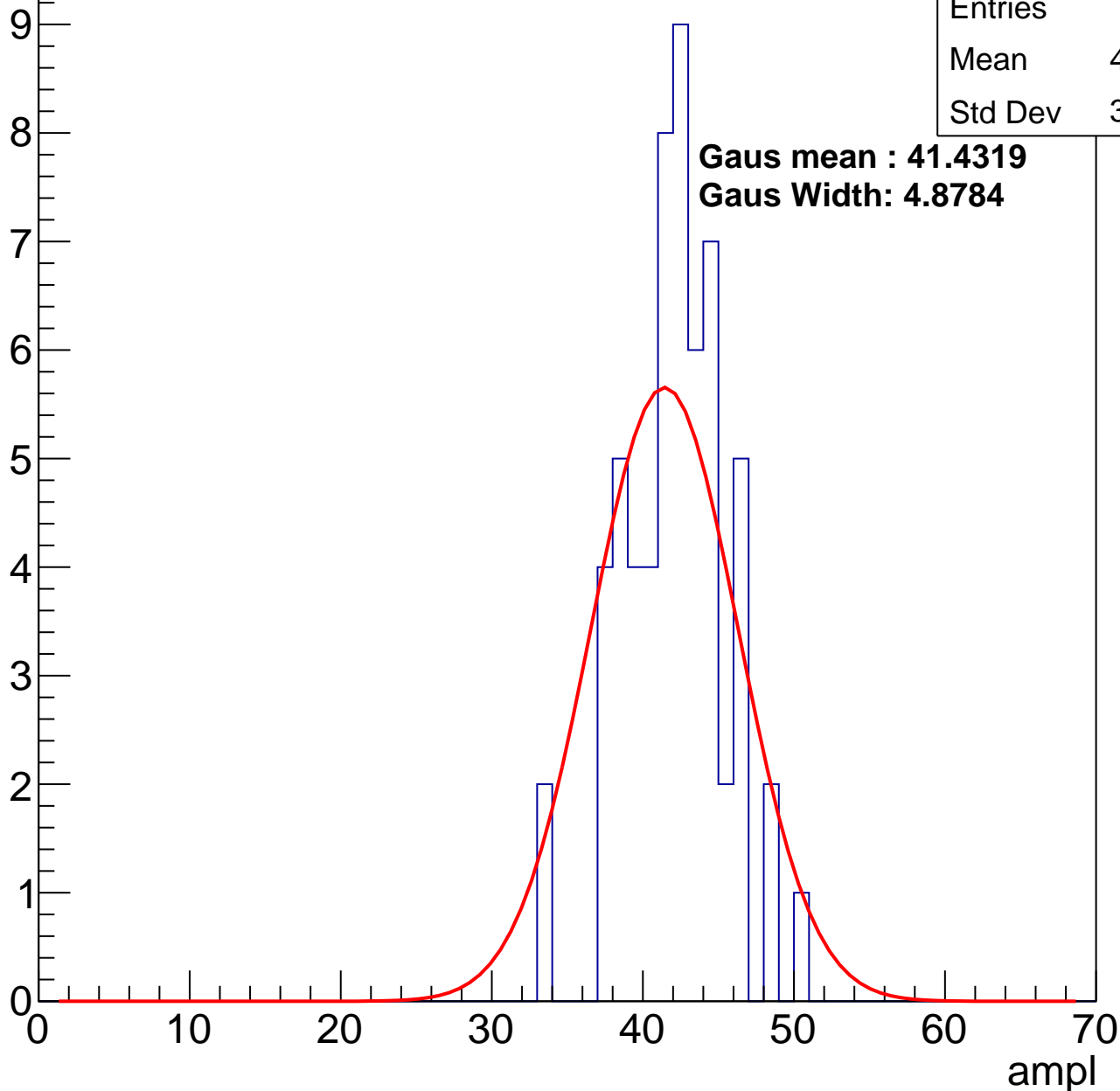
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.66
Std Dev	3.353

Gaus mean : 41.4319

Gaus Width: 4.8784

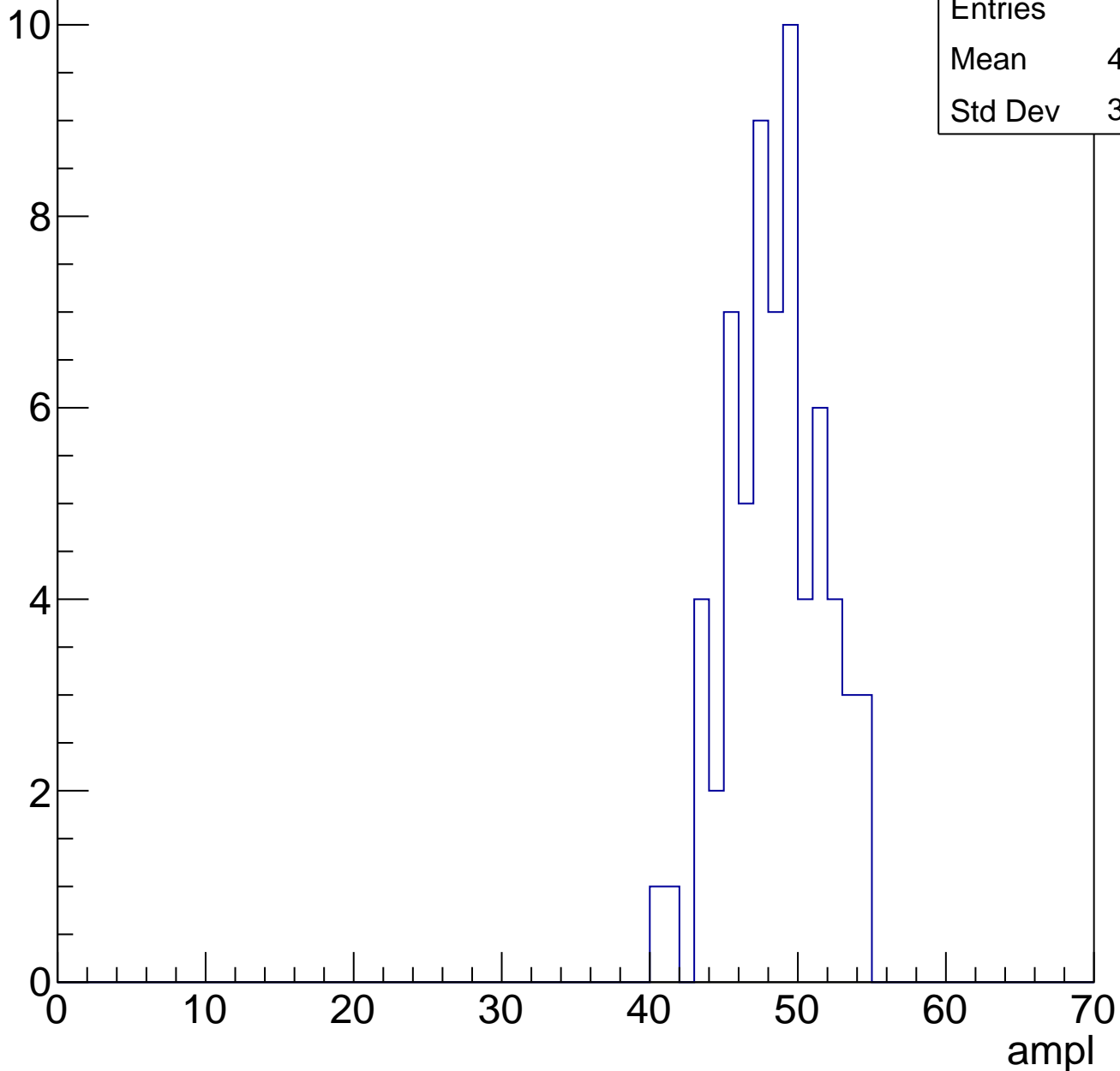


B1L102S, U10-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.03
Std Dev	3.172



B1L102S, U10-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

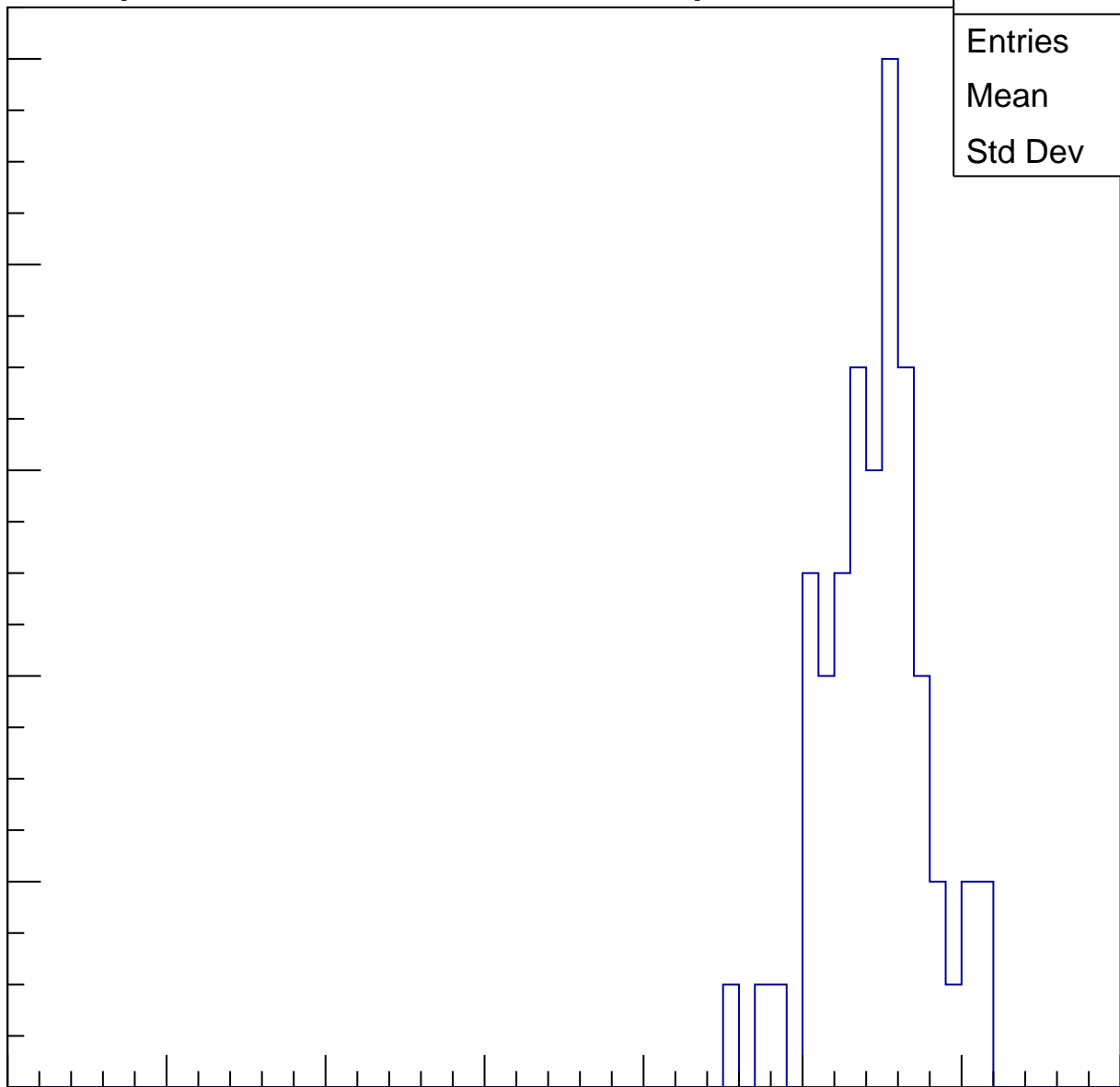
Entries	58
Mean	54.07
Std Dev	3.242

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

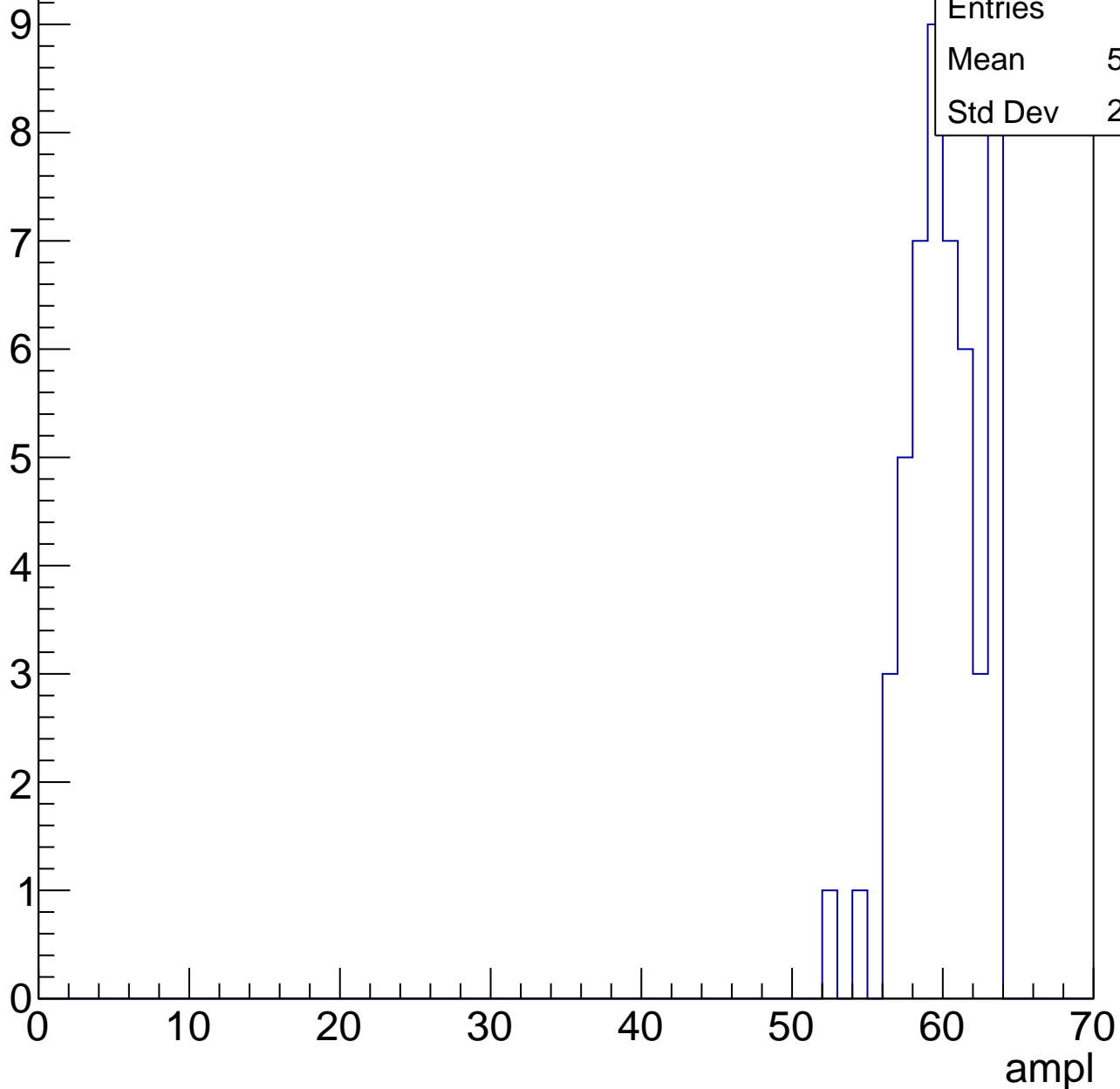
ampl



B1L102S, U10-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

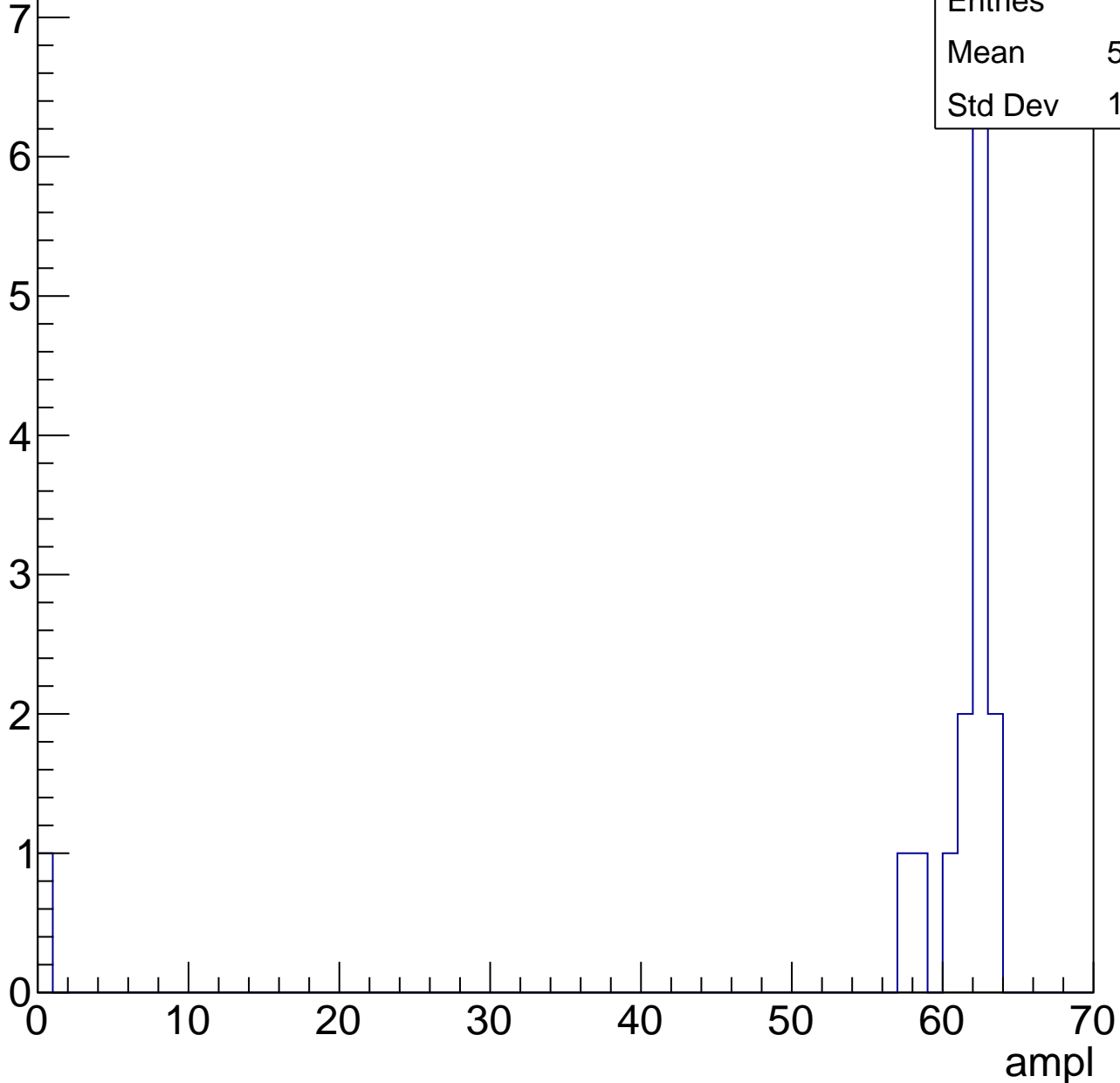


B1L102S, U10-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.13
Std Dev	15.36



B1L102S, U10-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch63, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	29.86
Std Dev	5.017

Gaus mean : 30.4008

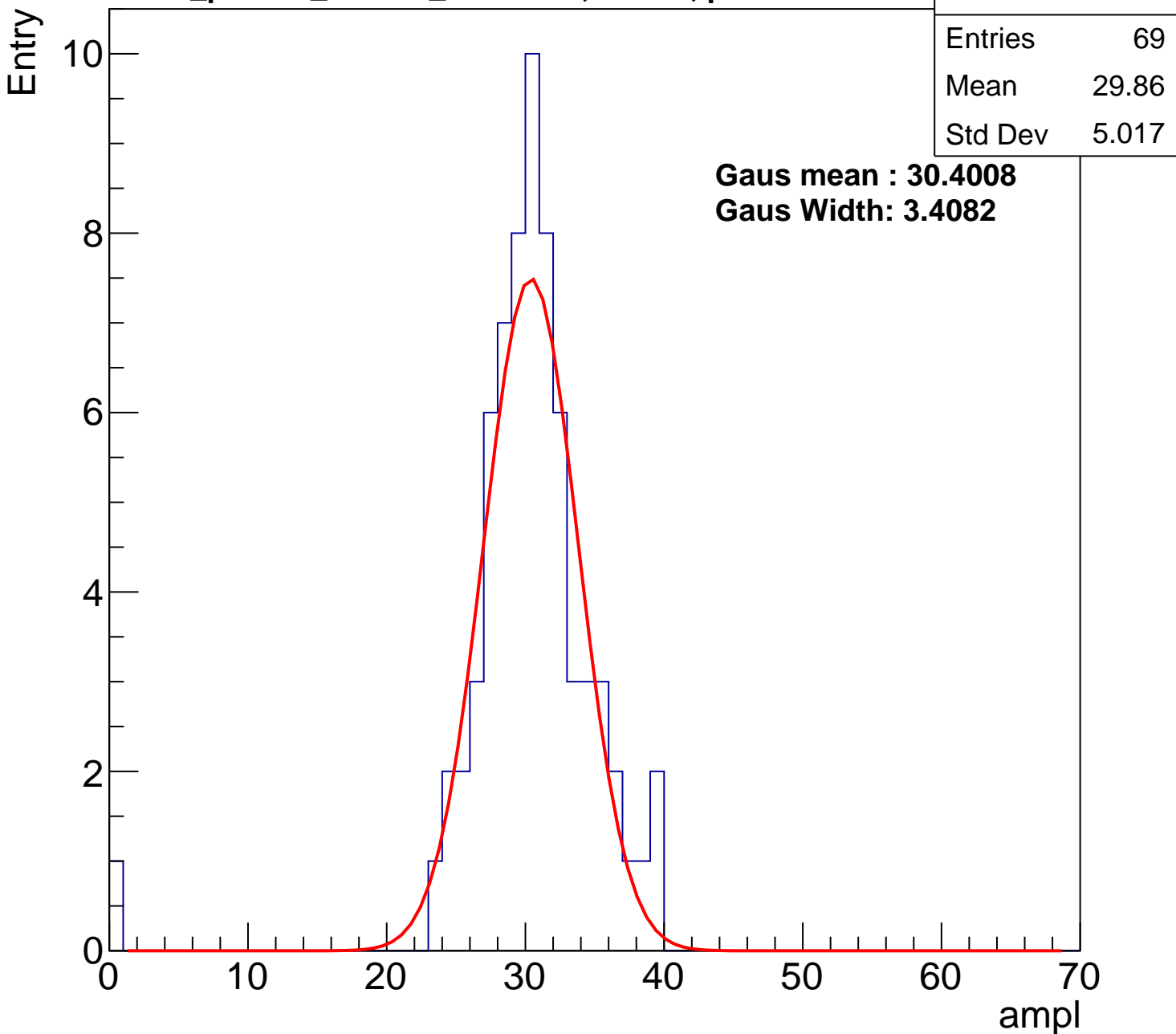
Gaus Width: 3.4082

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch63, adc1

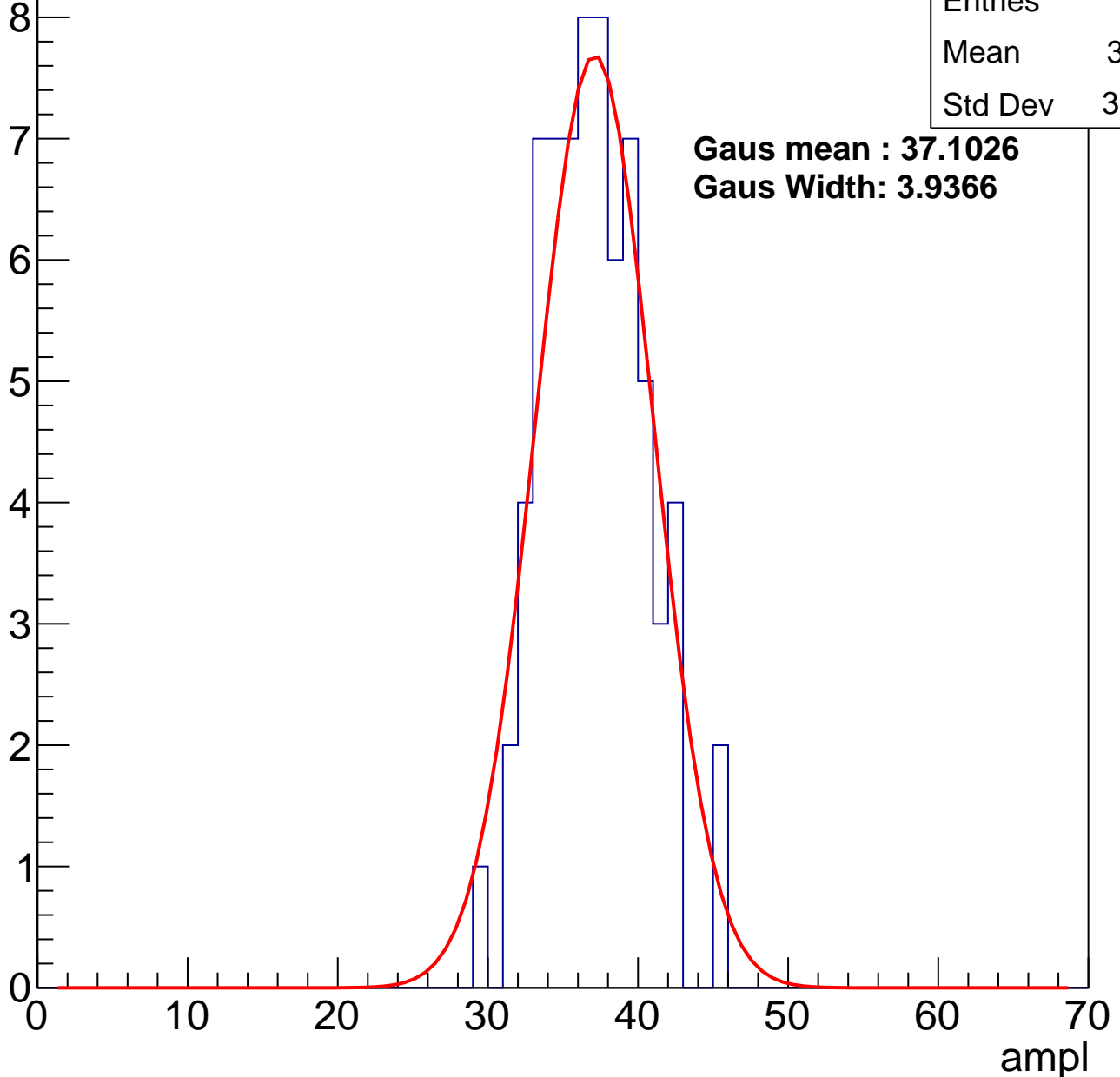
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.61
Std Dev	3.338

Gaus mean : 37.1026

Gaus Width: 3.9366



B1L102S, U10-ch63, adc2

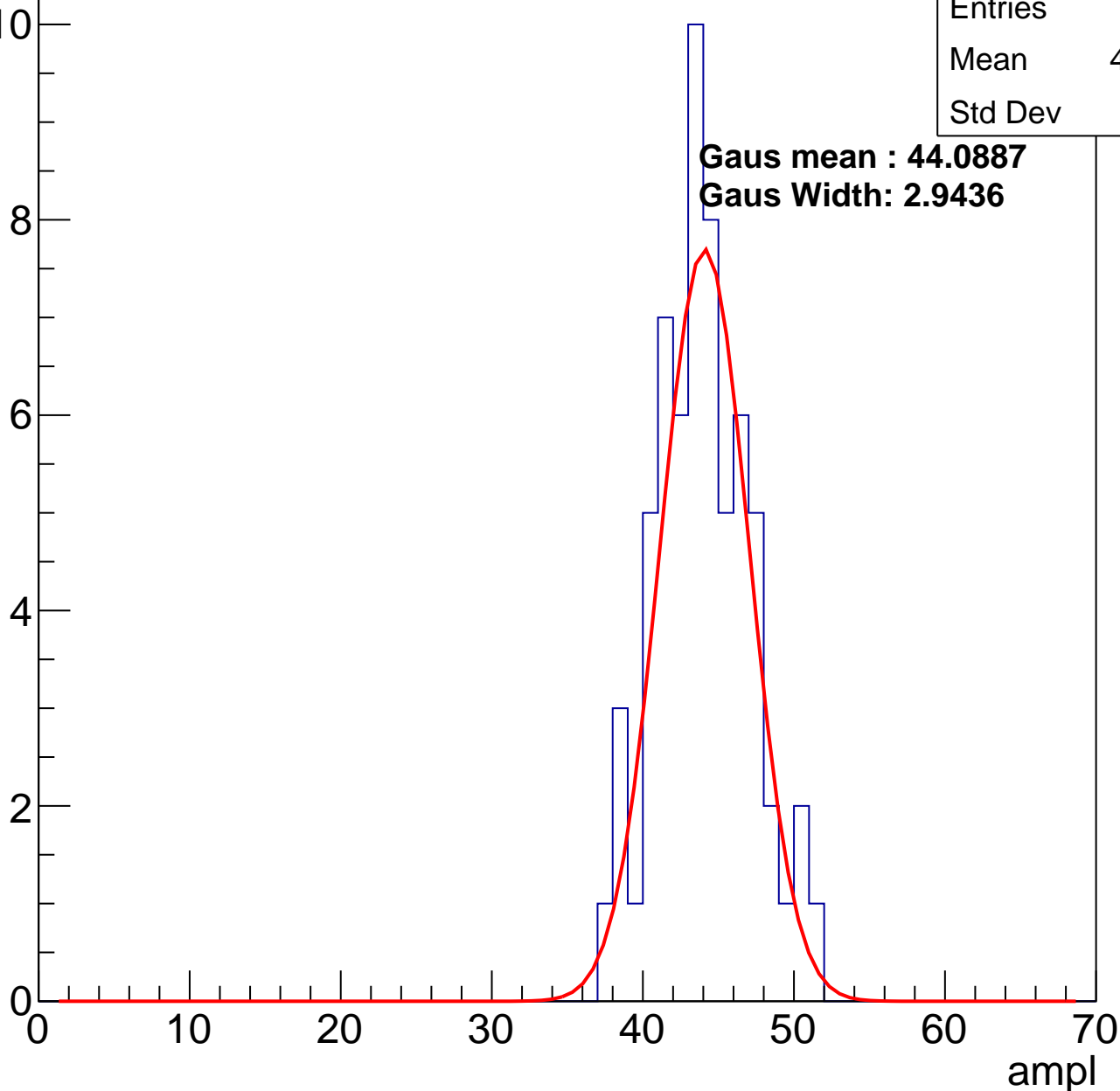
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.54
Std Dev	3.08

Gaus mean : 44.0887

Gaus Width: 2.9436

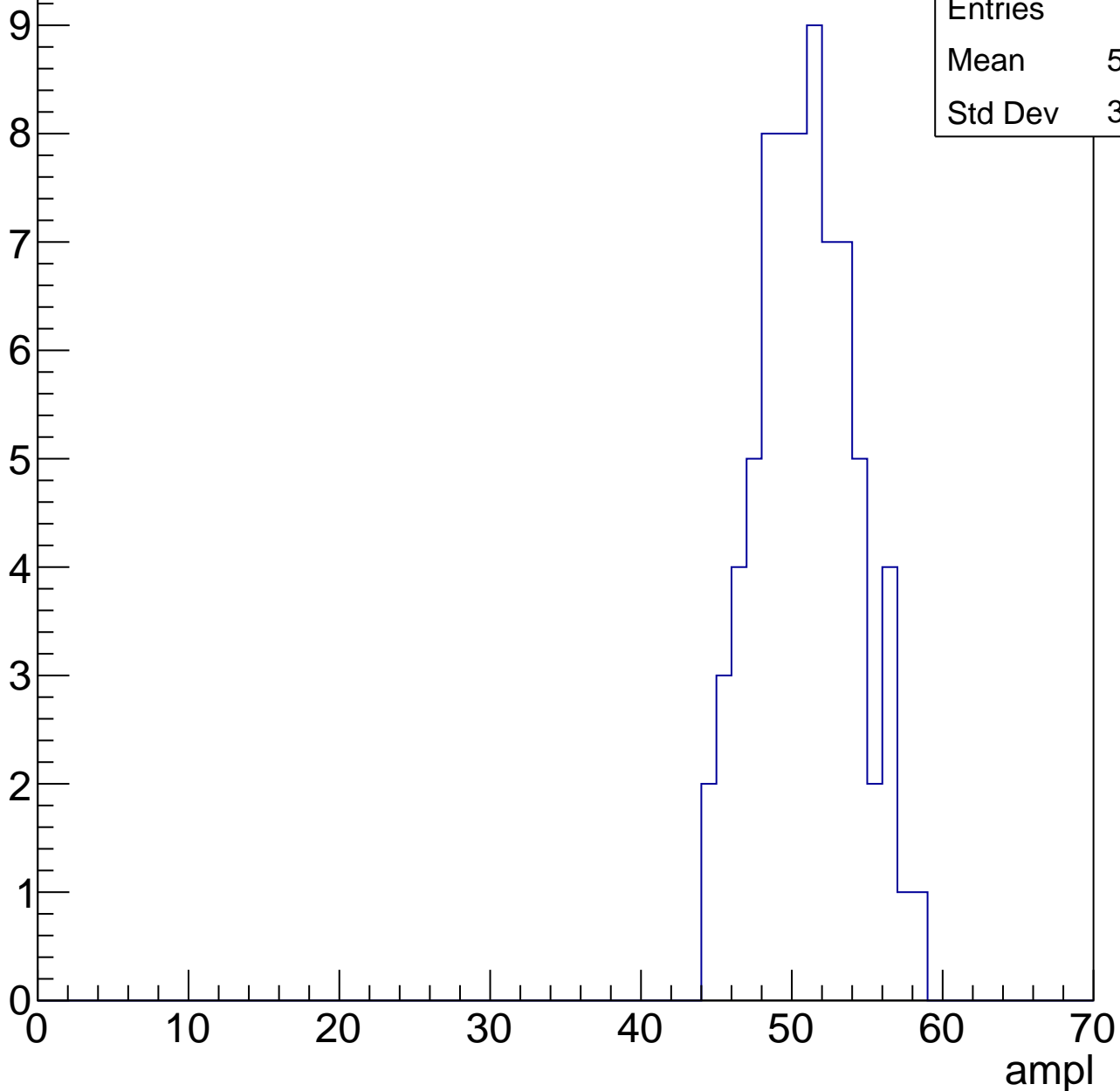


B1L102S, U10-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

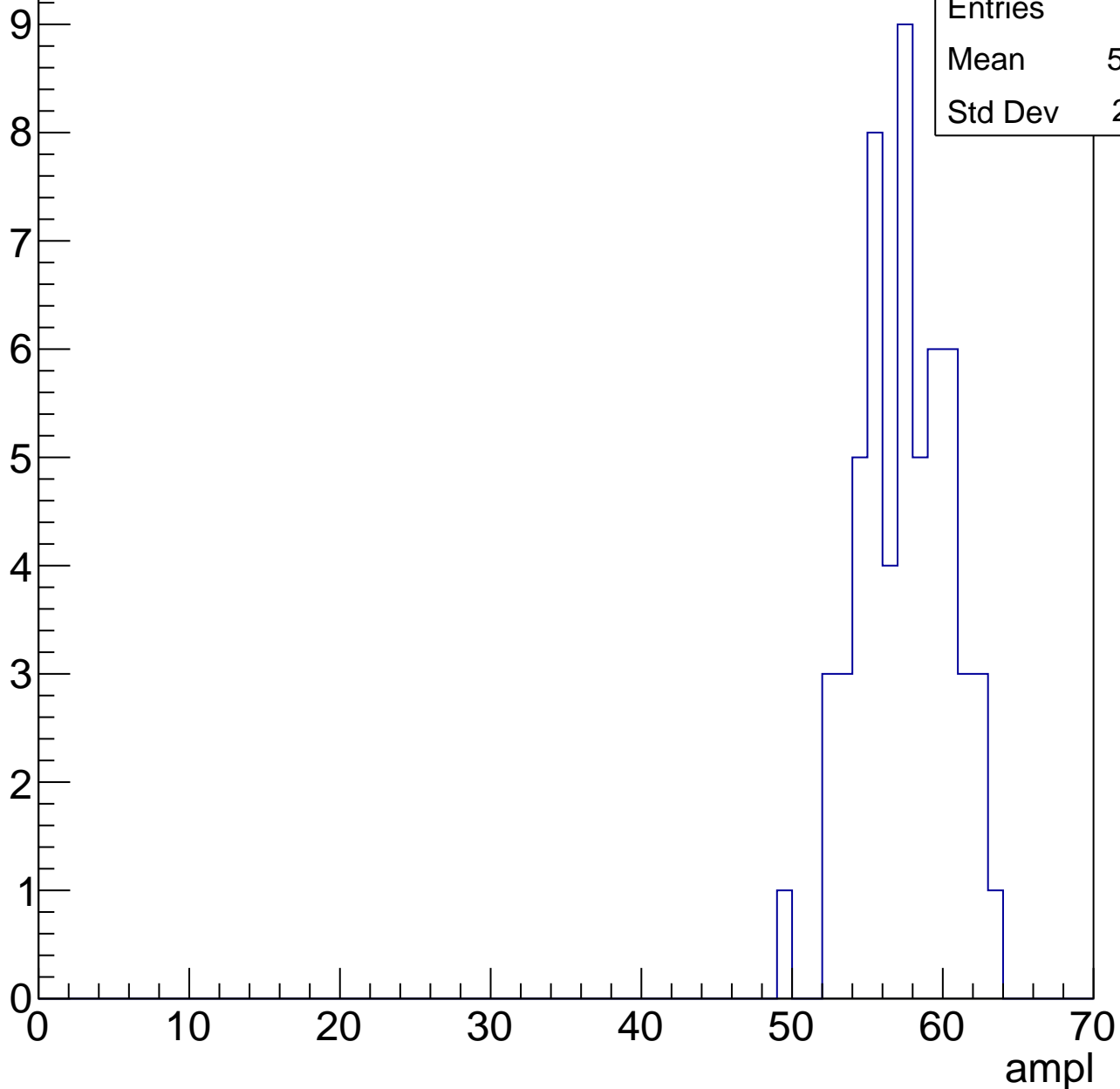
Entries	74
Mean	50.42
Std Dev	3.226



B1L102S, U10-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

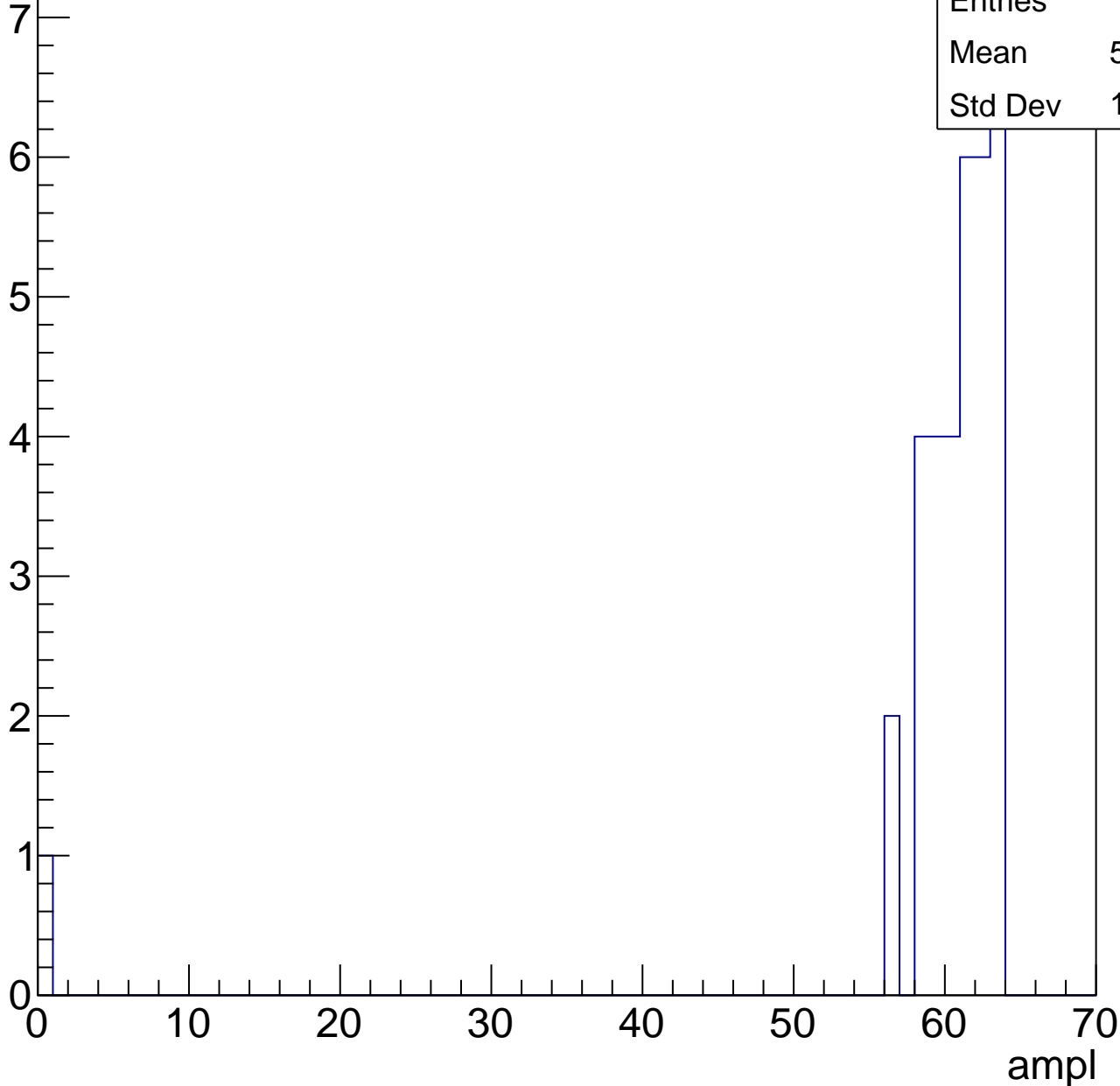


B1L102S, U10-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	58.79
Std Dev	10.43



B1L102S, U10-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L102S, U10-ch64, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	103
Mean	29.66
Std Dev	4.551

Gaus mean : 29.6384

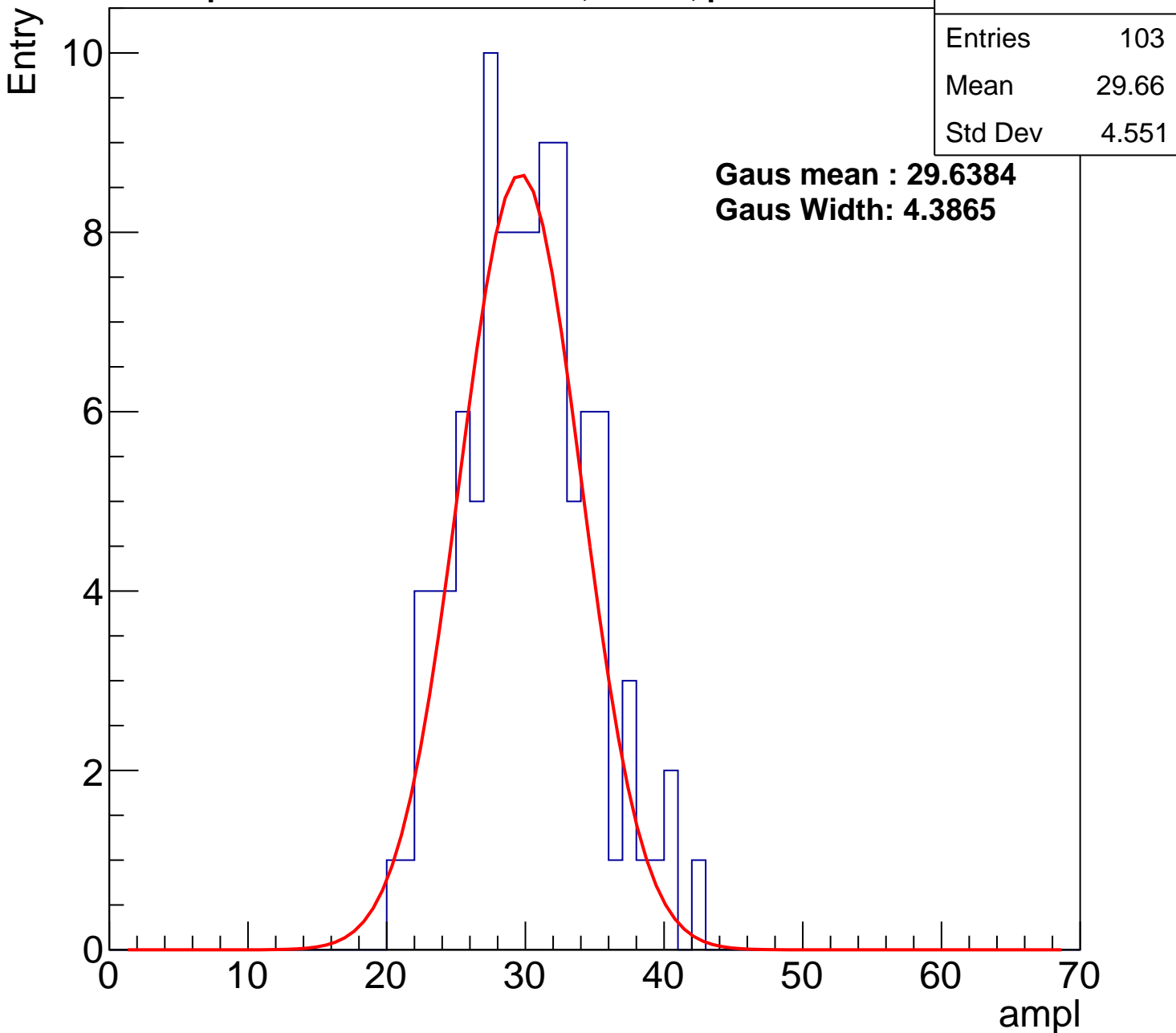
Gaus Width: 4.3865

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



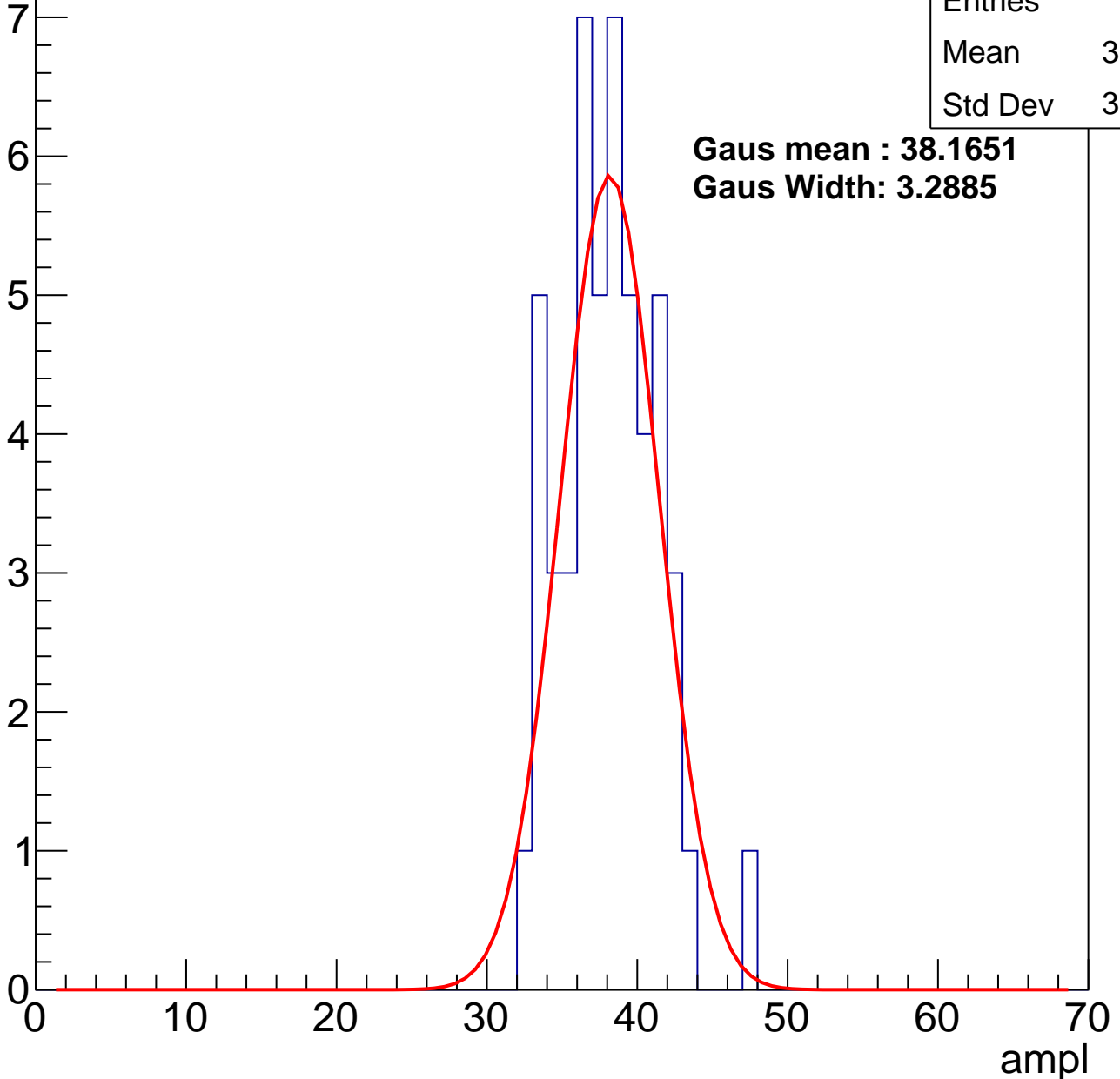
B1L102S, U10-ch64, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	37.66
Std Dev	3.109

Gaus mean : 38.1651
Gaus Width: 3.2885



B1L102S, U10-ch64, adc2

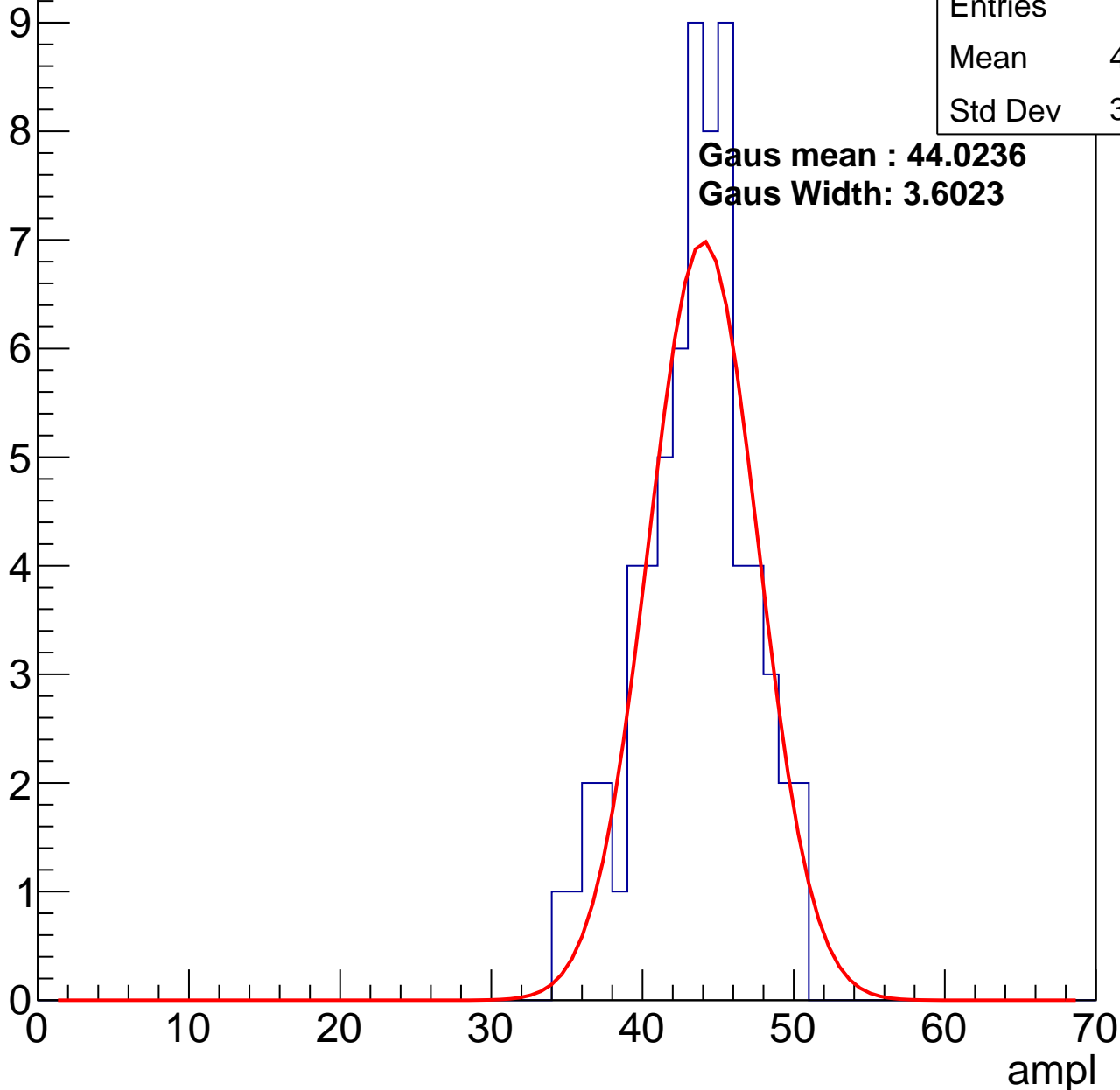
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.04
Std Dev	3.572

Gaus mean : 44.0236

Gaus Width: 3.6023

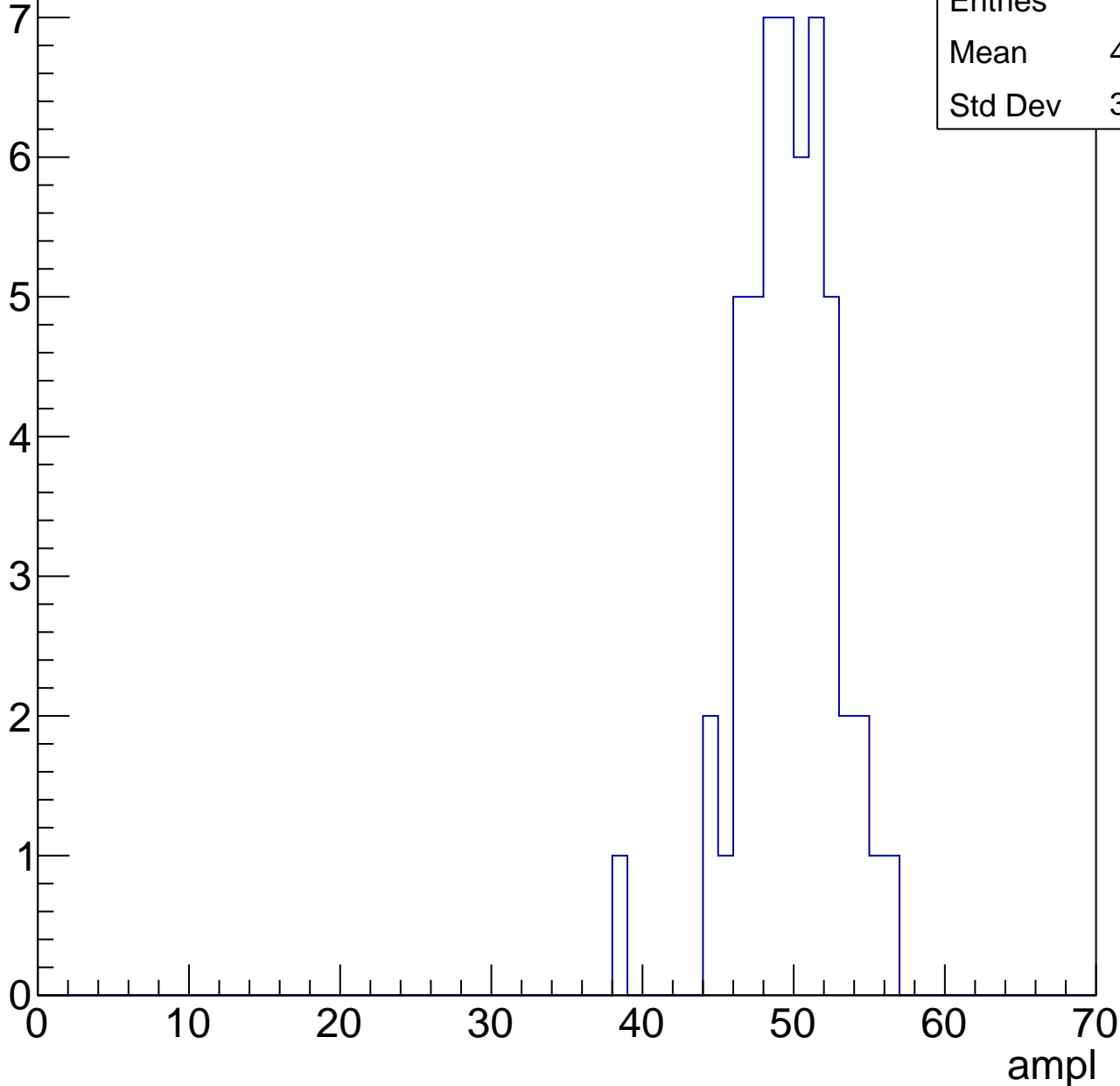


B1L102S, U10-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.17
Std Dev	3.112

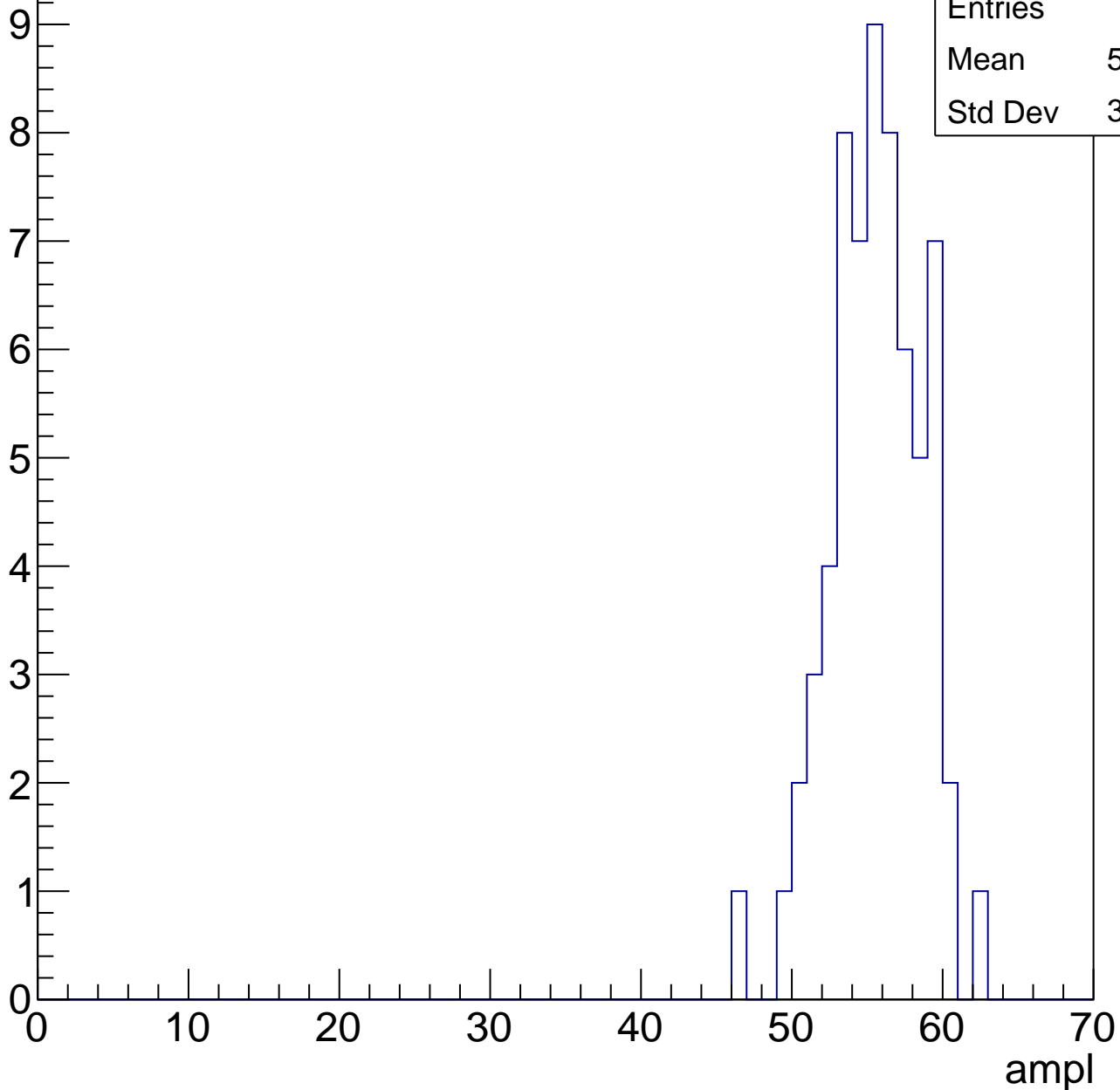


B1L102S, U10-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

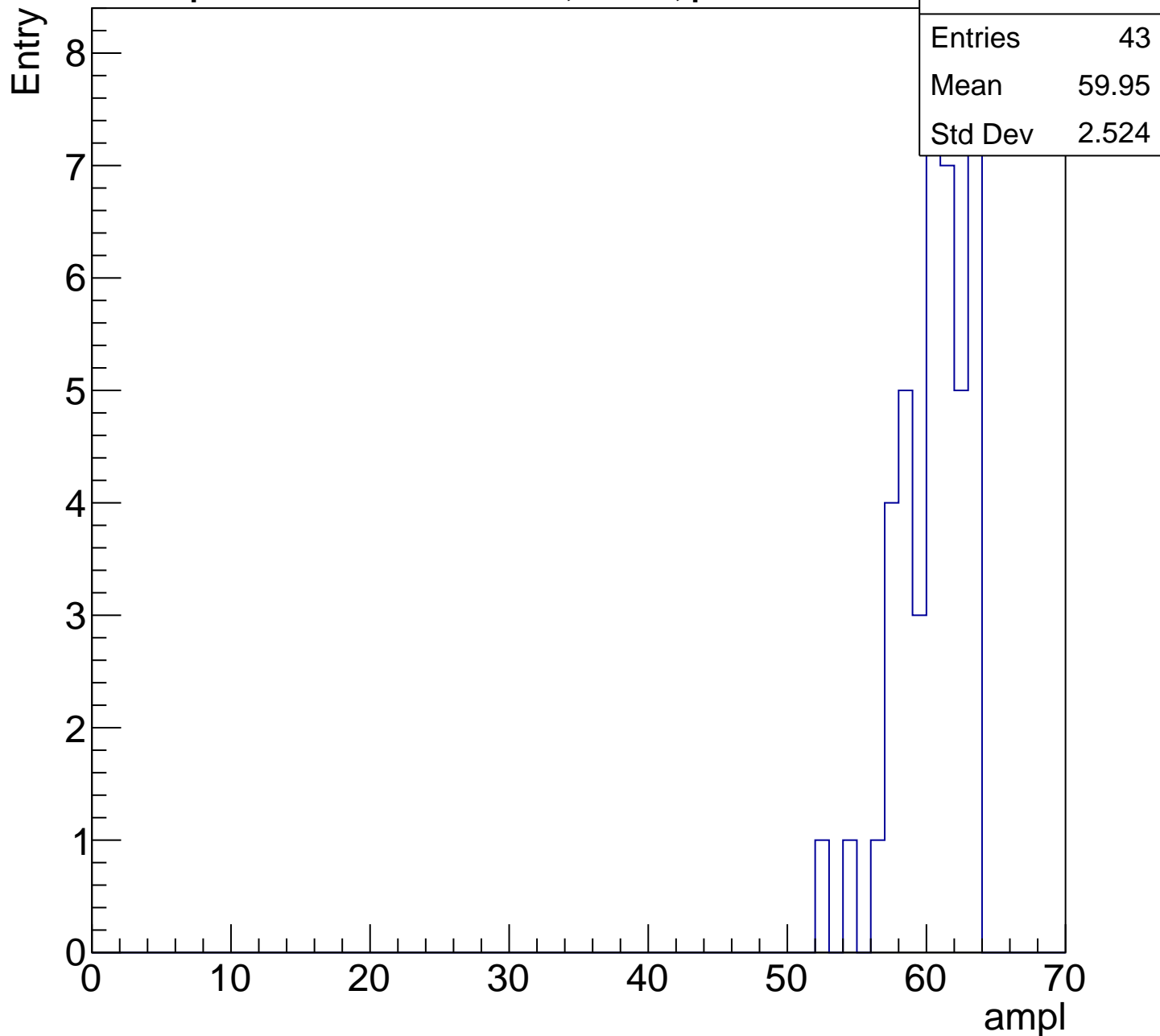
Entry

Entries	64
Mean	55.12
Std Dev	3.003



B1L102S, U10-ch64, adc5

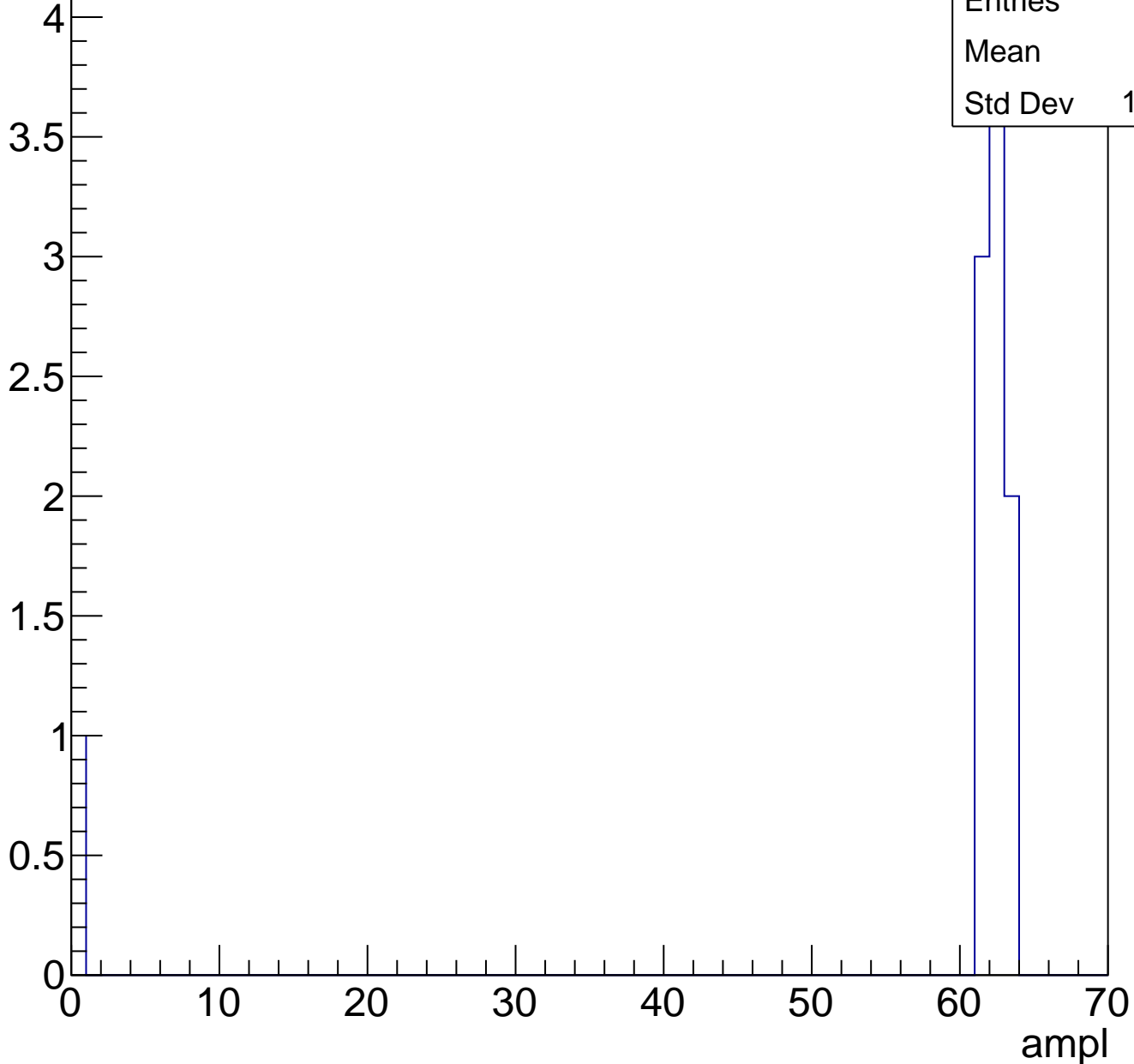
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U10-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	55.7
Std Dev	18.58

B1L102S, U10-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch65, adc0

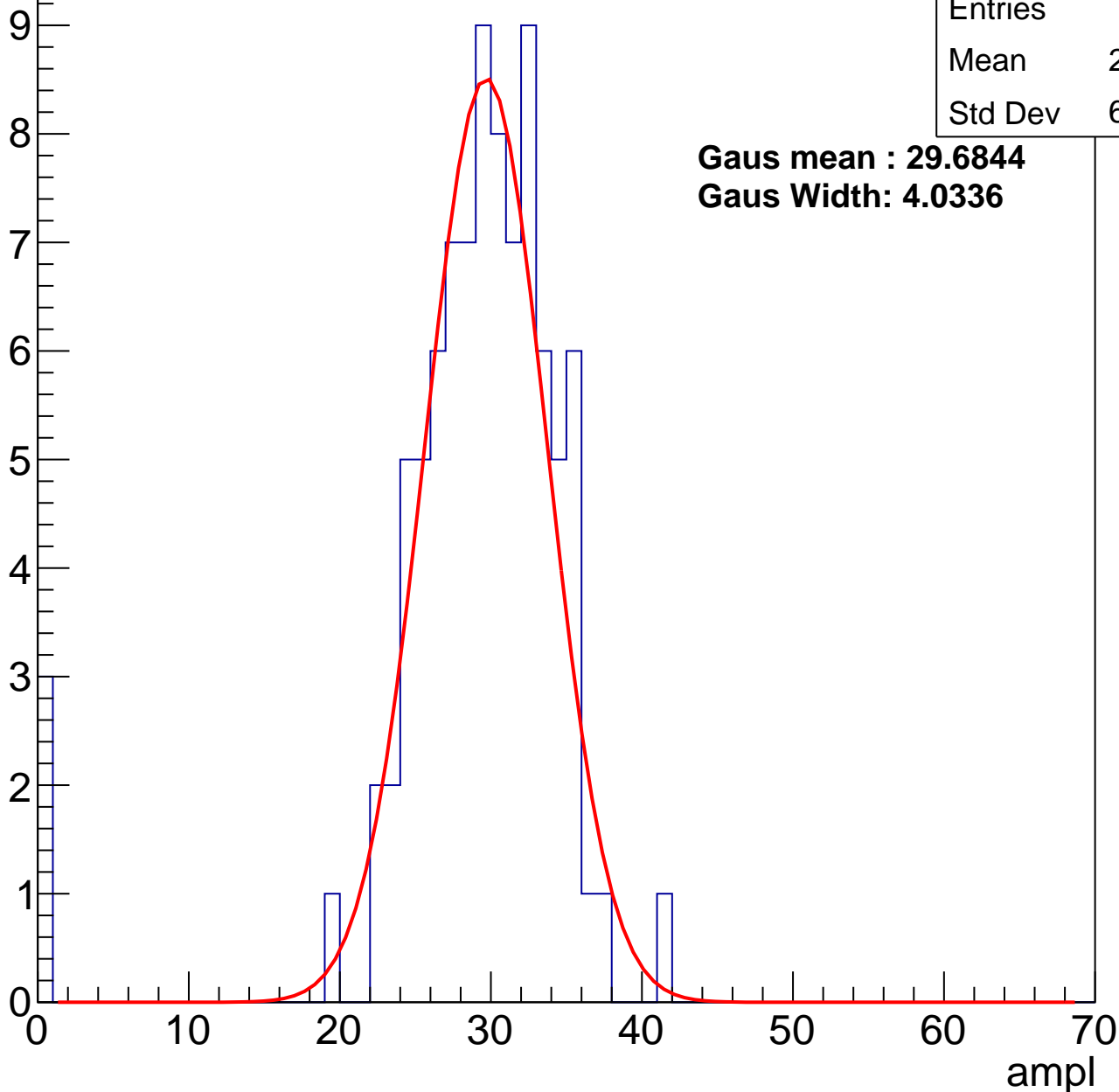
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	91
Mean	28.49
Std Dev	6.525

Gaus mean : 29.6844

Gaus Width: 4.0336



B1L102S, U10-ch65, adc1

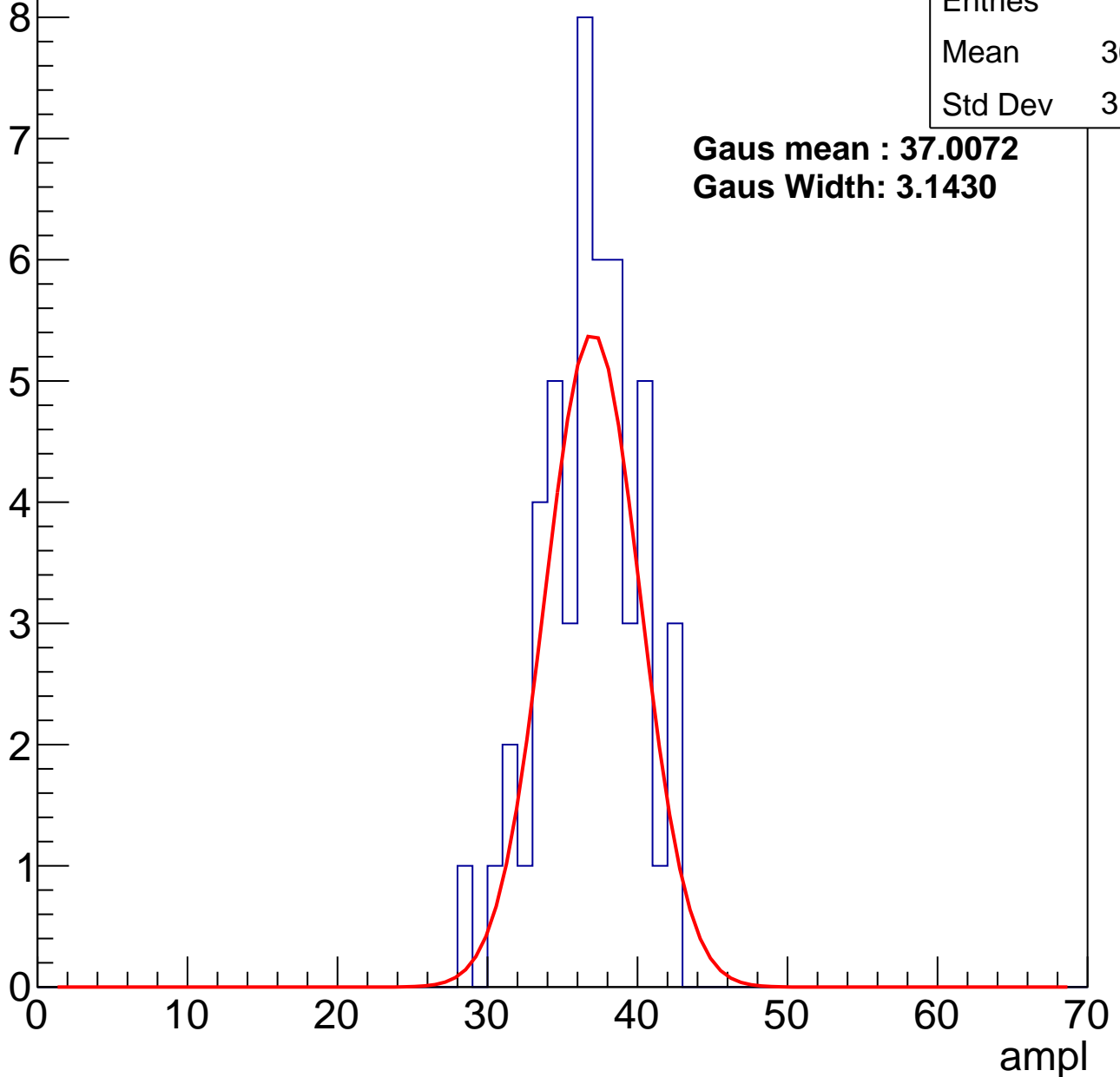
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	36.35
Std Dev	3.159

Gaus mean : 37.0072

Gaus Width: 3.1430



B1L102S, U10-ch65, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	43.56
Std Dev	3.92

Gaus mean : 43.9219

Gaus Width: 4.1779

10

8

6

4

2

0

0

10

20

30

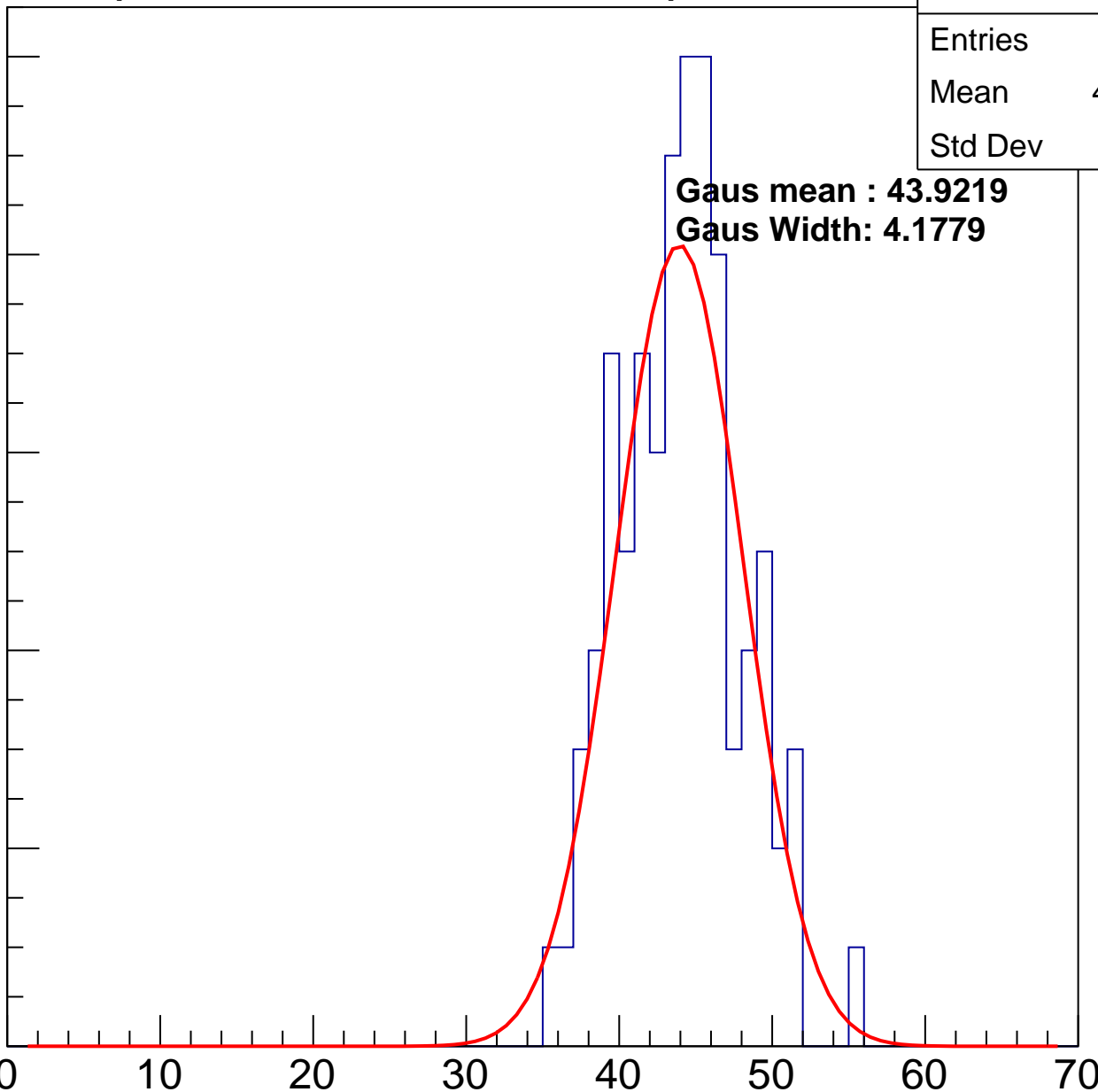
40

50

60

70

ampl

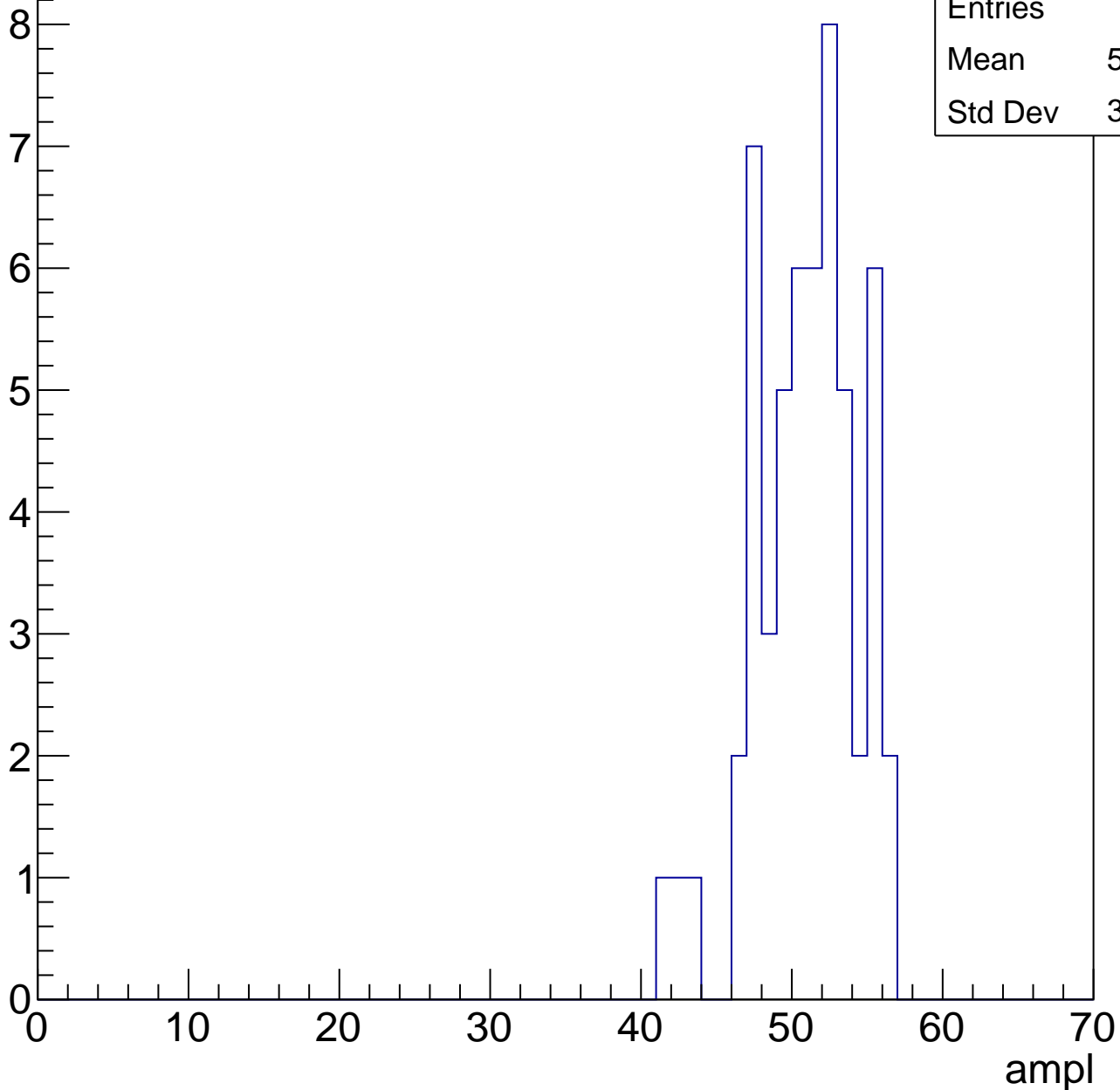


B1L102S, U10-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.42
Std Dev	3.394

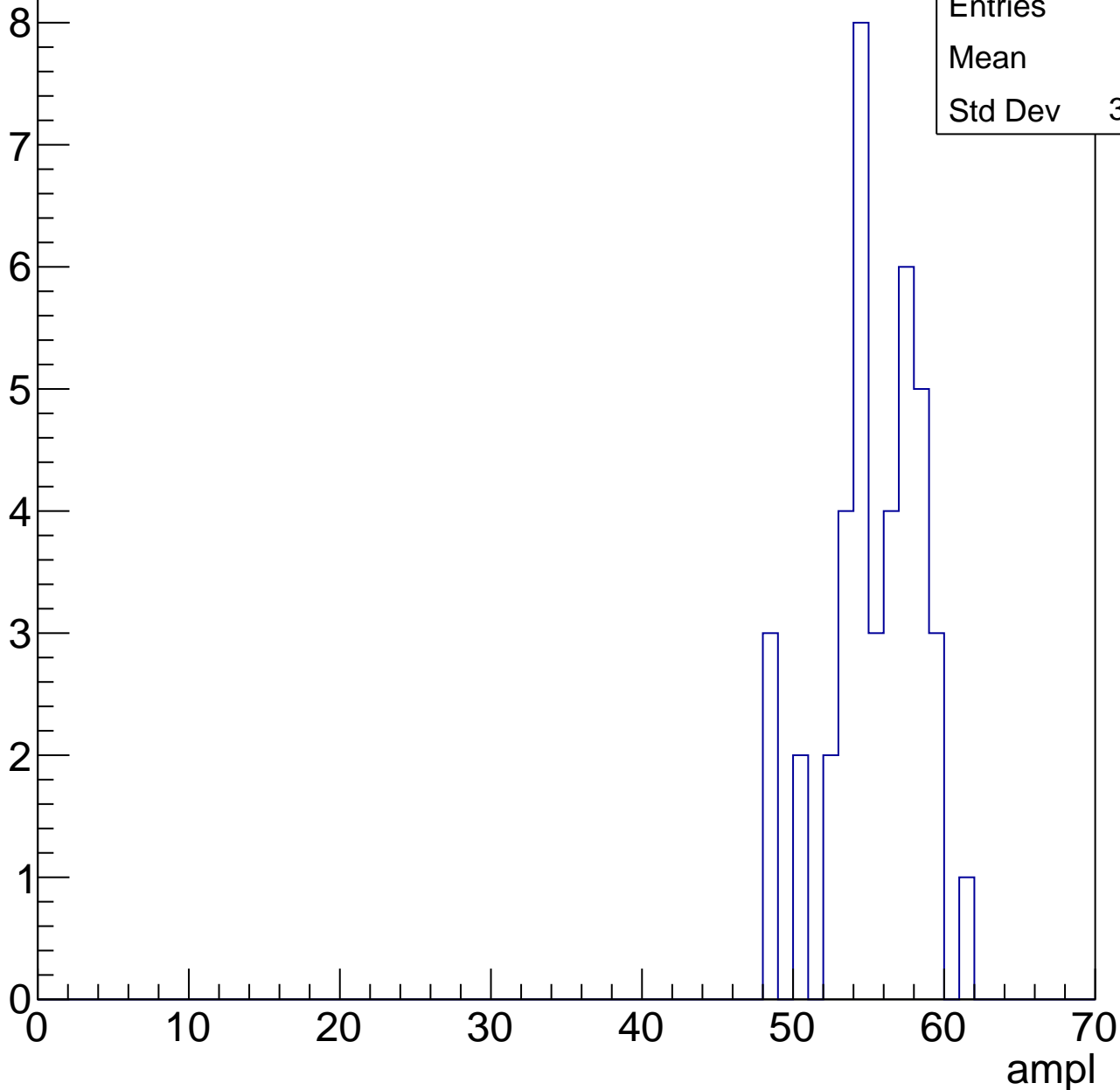


B1L102S, U10-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	54.9
Std Dev	3.106

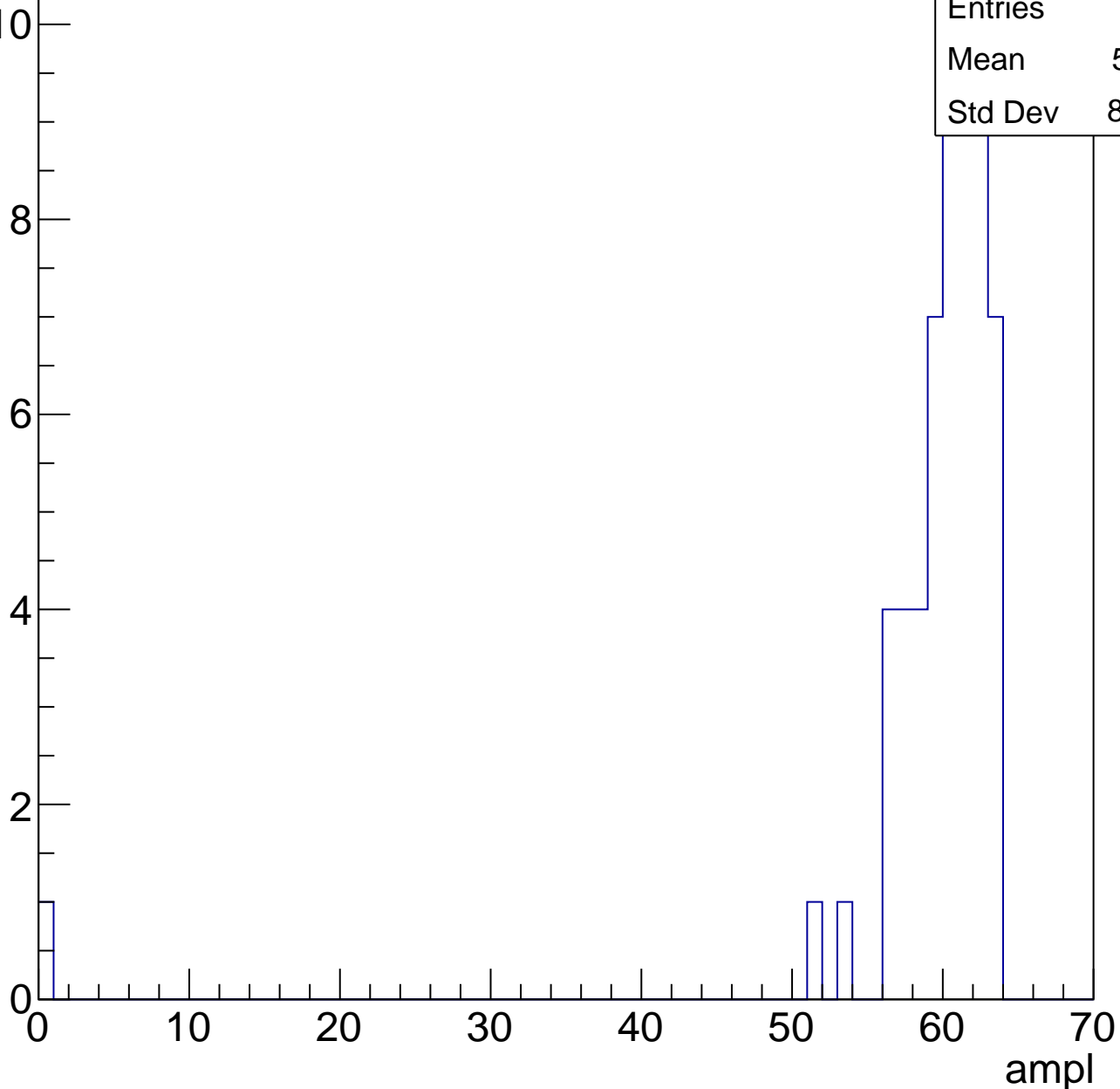


B1L102S, U10-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

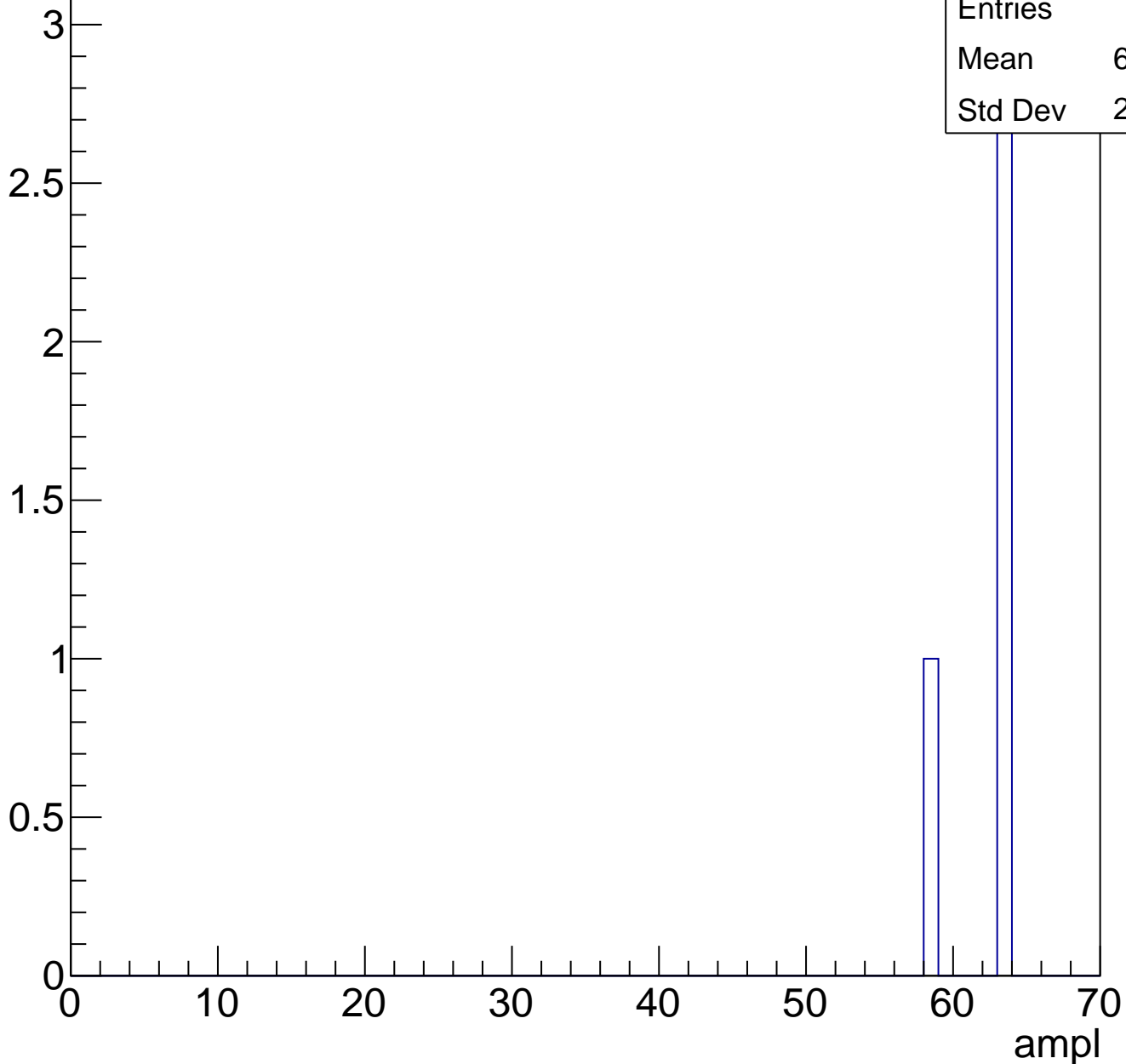
Entries	58
Mean	58.81
Std Dev	8.182



B1L102S, U10-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch66, adc0

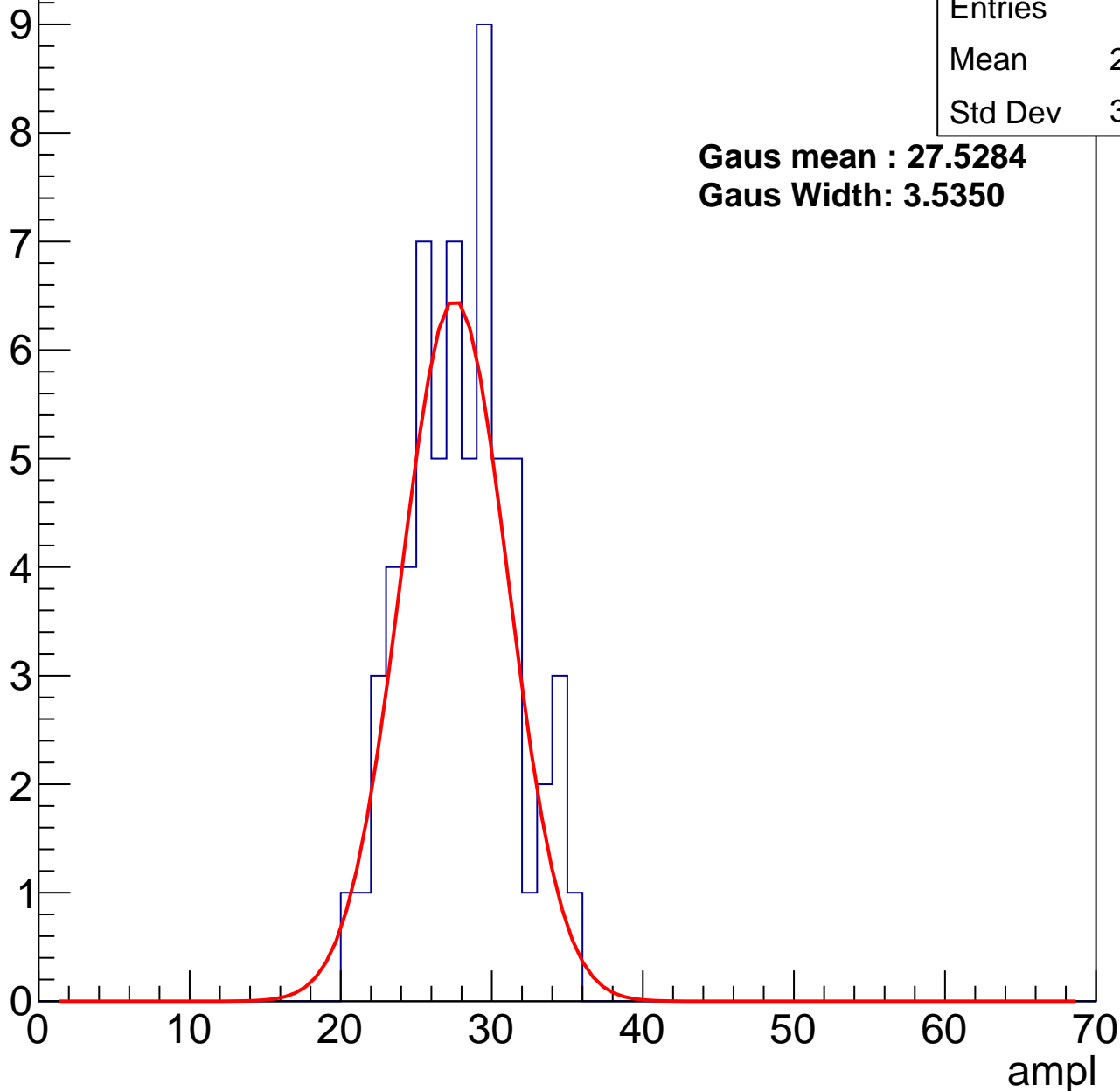
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.46
Std Dev	3.459

Gaus mean : 27.5284

Gaus Width: 3.5350



B1L102S, U10-ch66, adc1

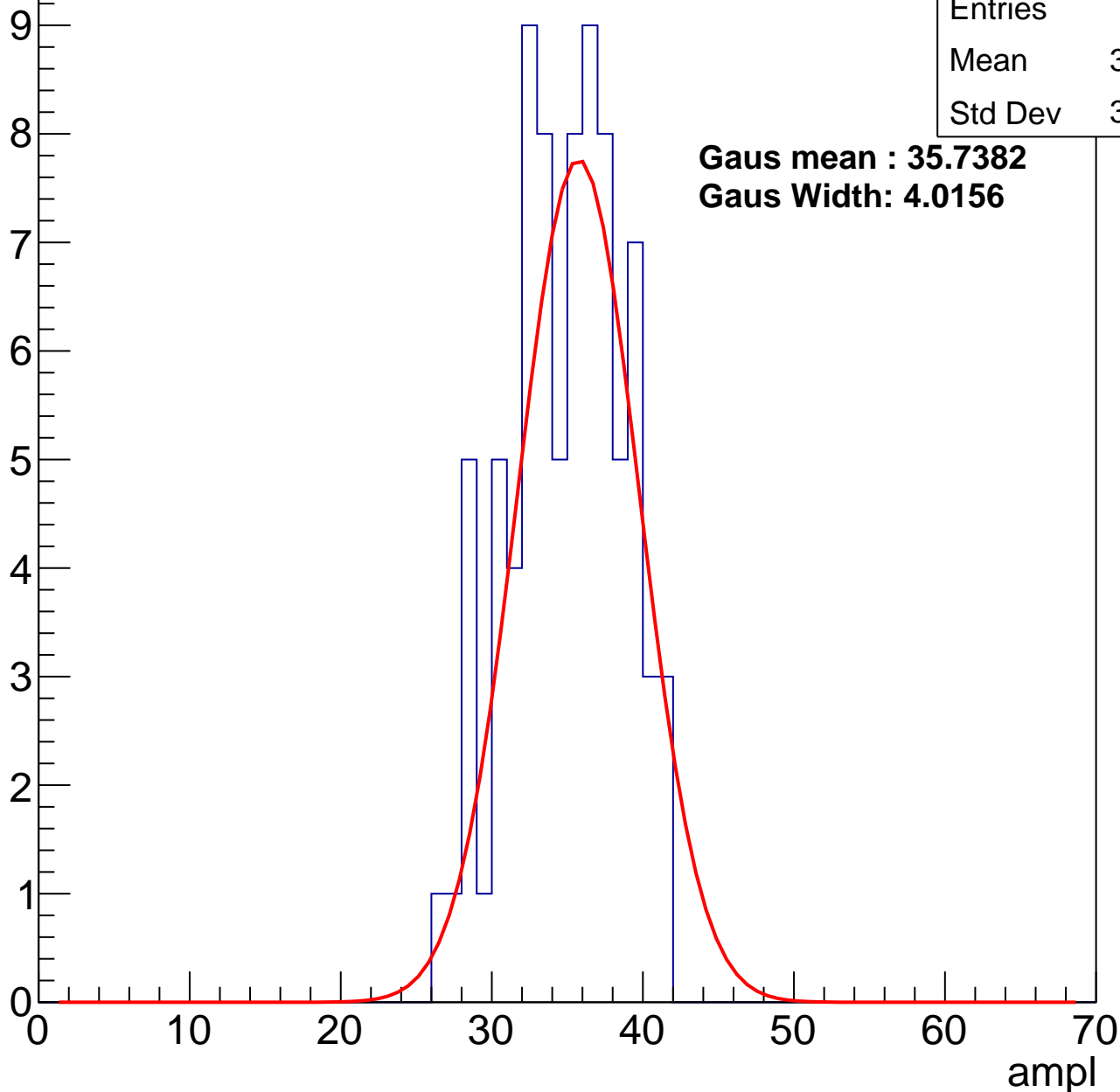
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.44
Std Dev	3.643

Gaus mean : 35.7382

Gaus Width: 4.0156



B1L102S, U10-ch66, adc2

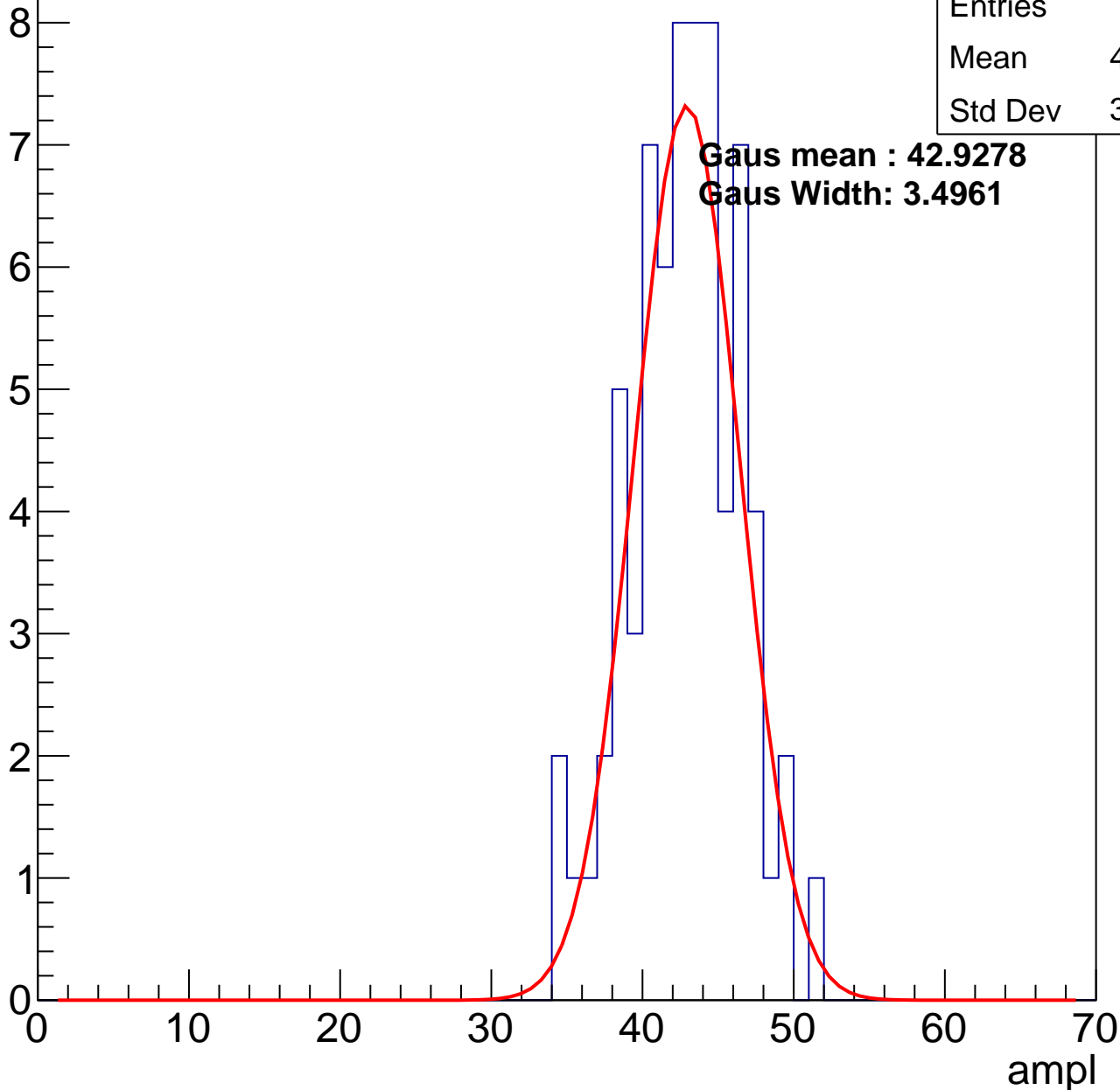
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.36
Std Dev	3.582

Gaus mean : 42.9278

Gaus Width: 3.4961

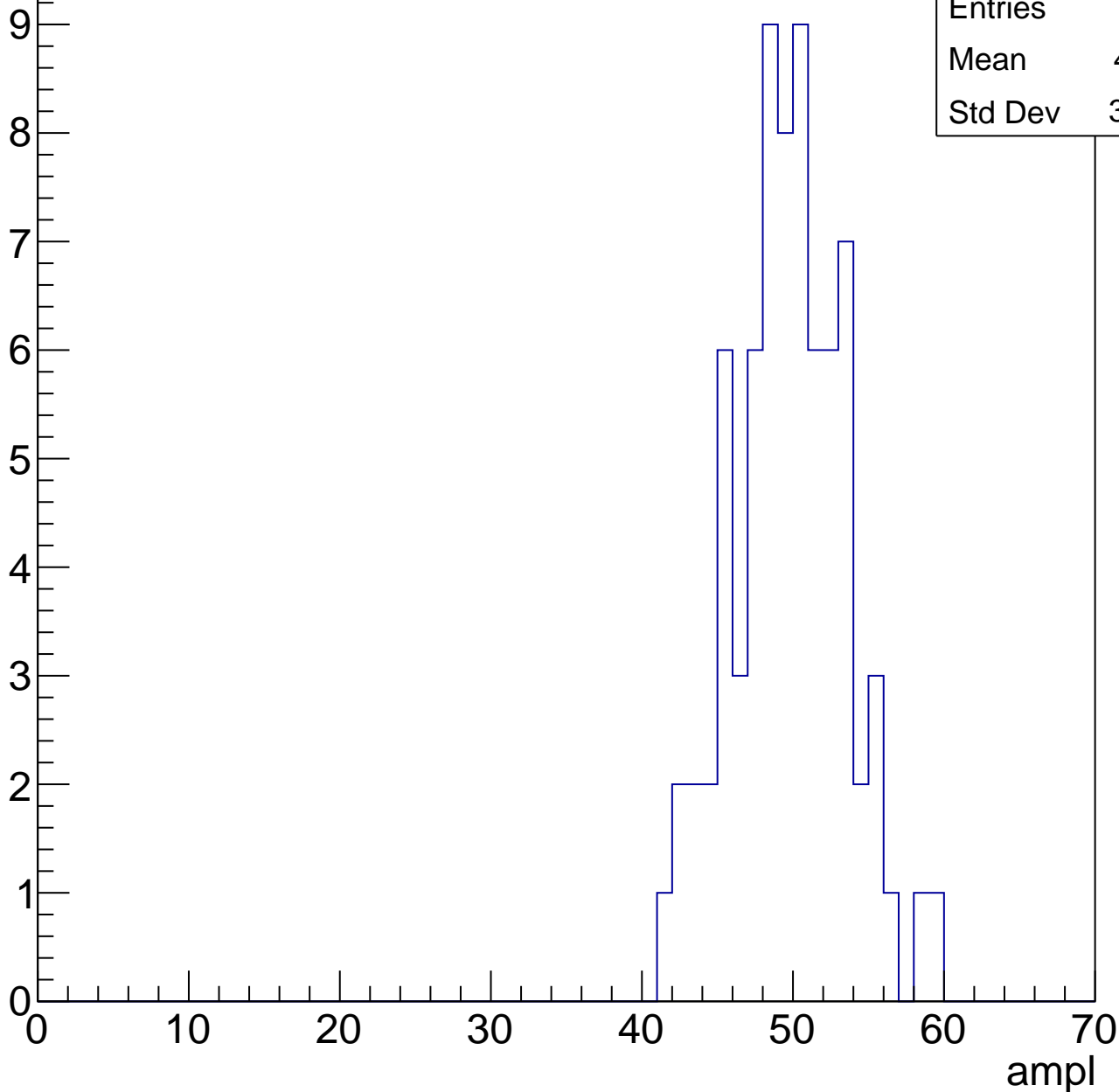


B1L102S, U10-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	49.31
Std Dev	3.688

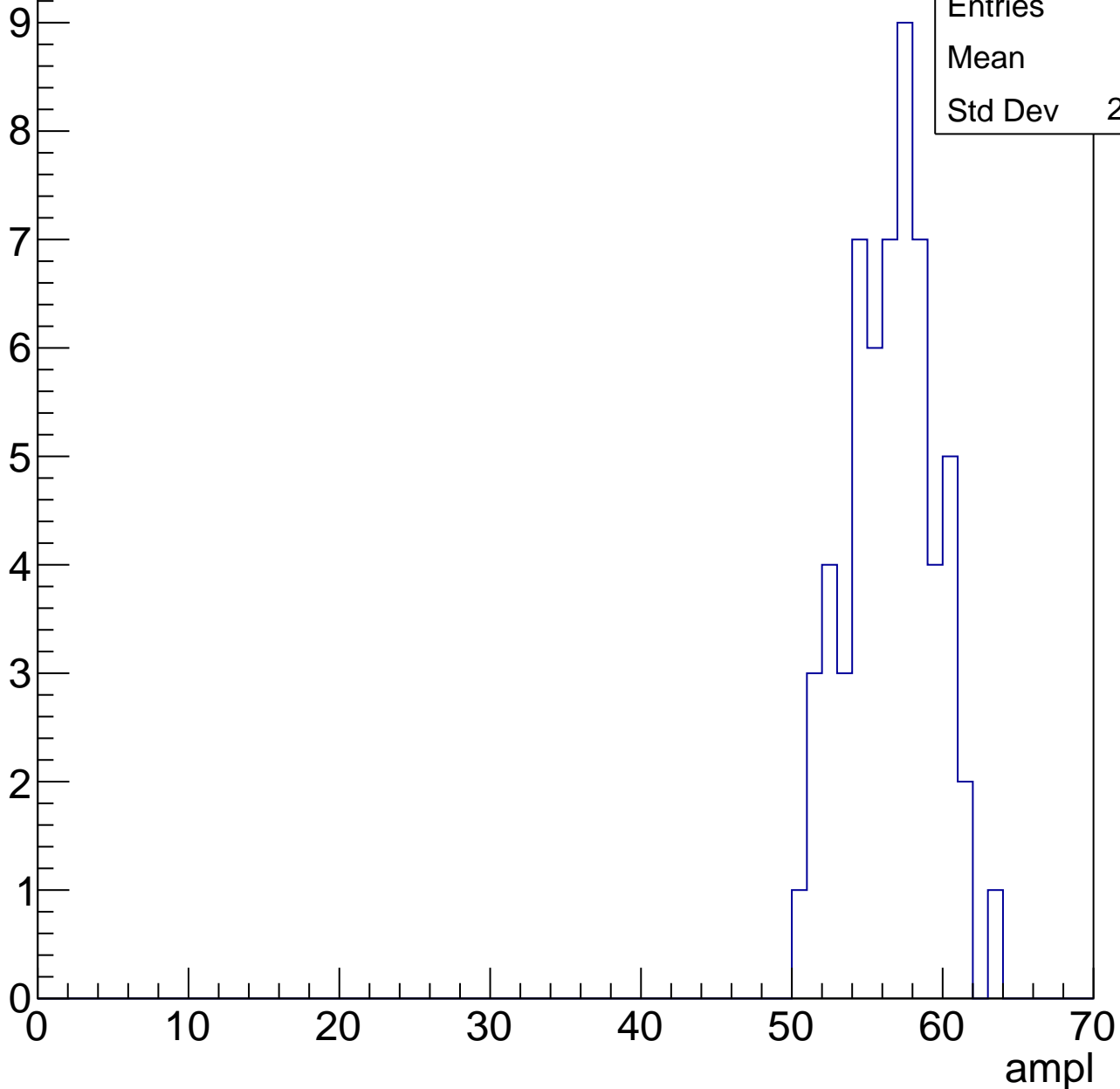


B1L102S, U10-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

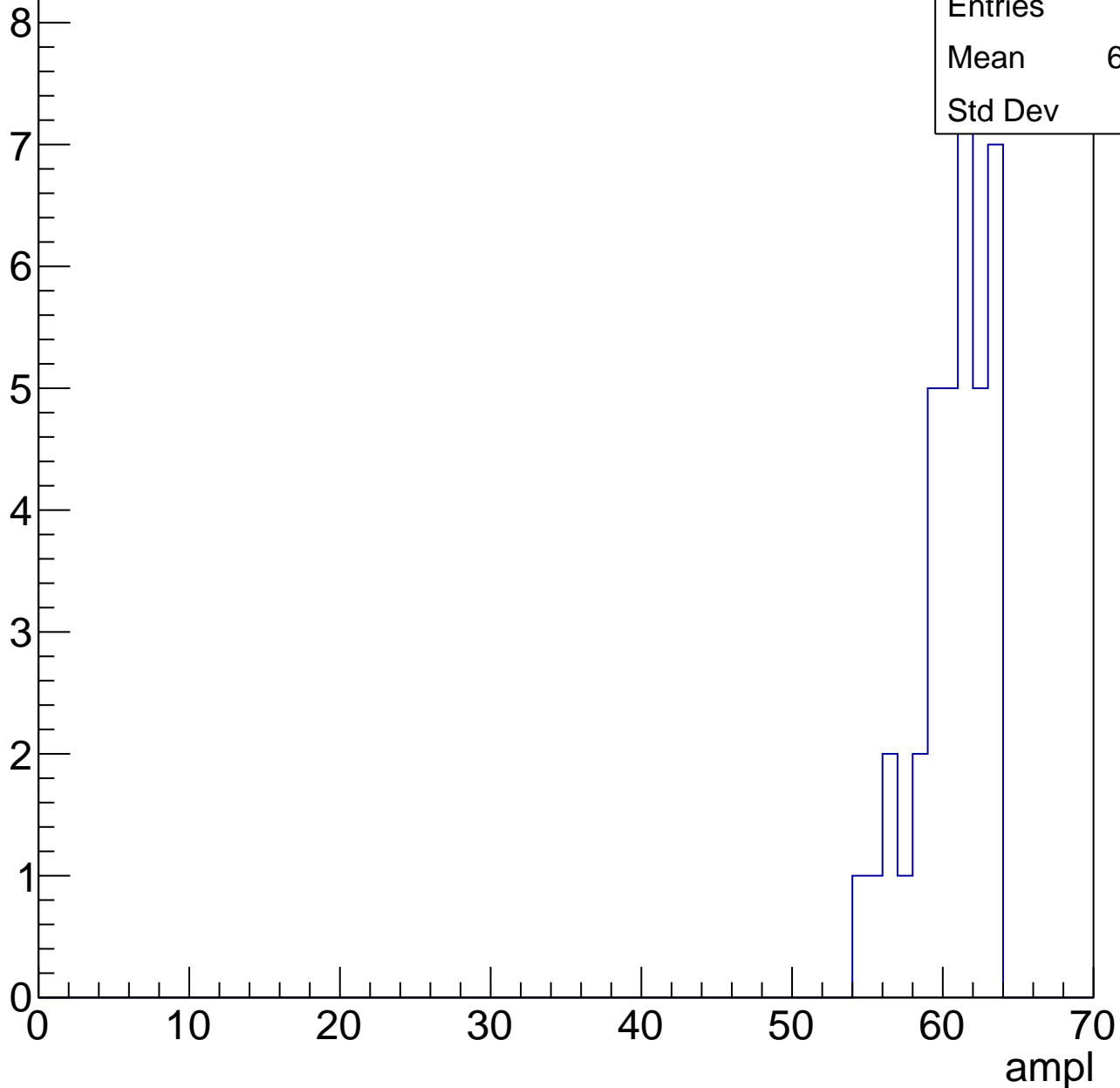
Entries	59
Mean	56.1
Std Dev	2.874



B1L102S, U10-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



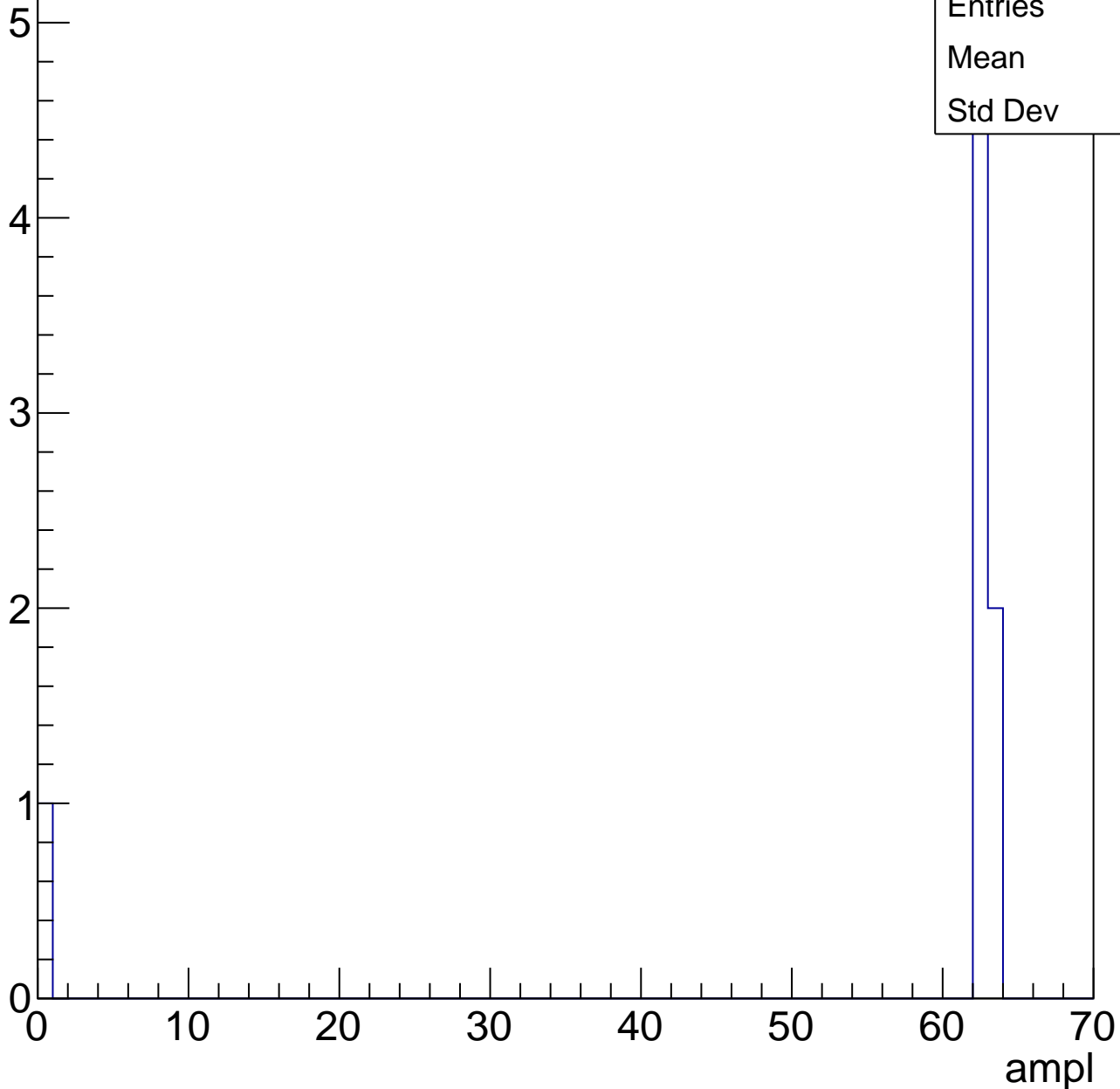
Entries	37
Mean	60.22
Std Dev	2.35

B1L102S, U10-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.5
Std Dev	20.6



B1L102S, U10-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch67, adc0

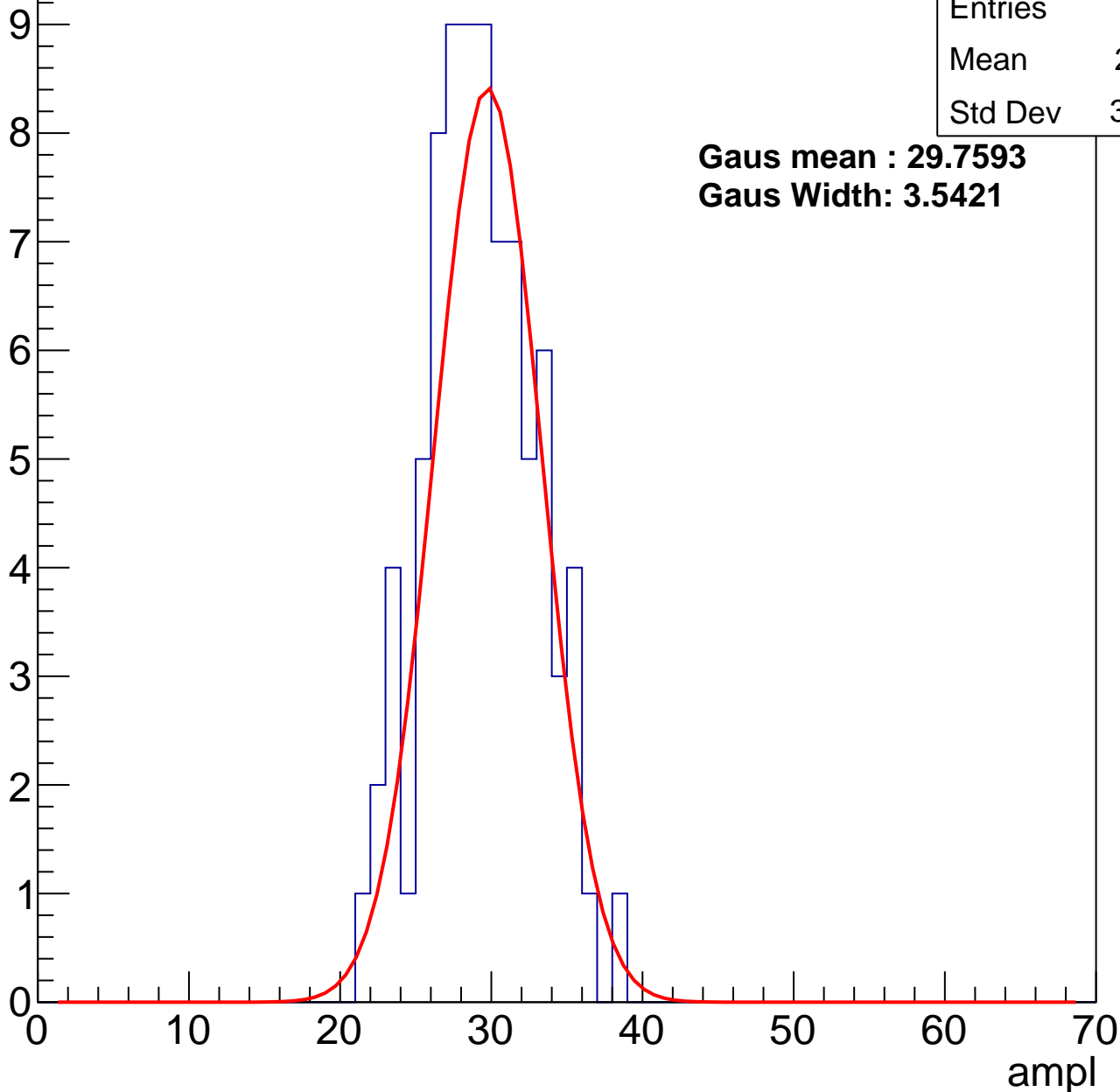
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.91
Std Dev	3.582

Gaus mean : 29.7593

Gaus Width: 3.5421



B1L102S, U10-ch67, adc1

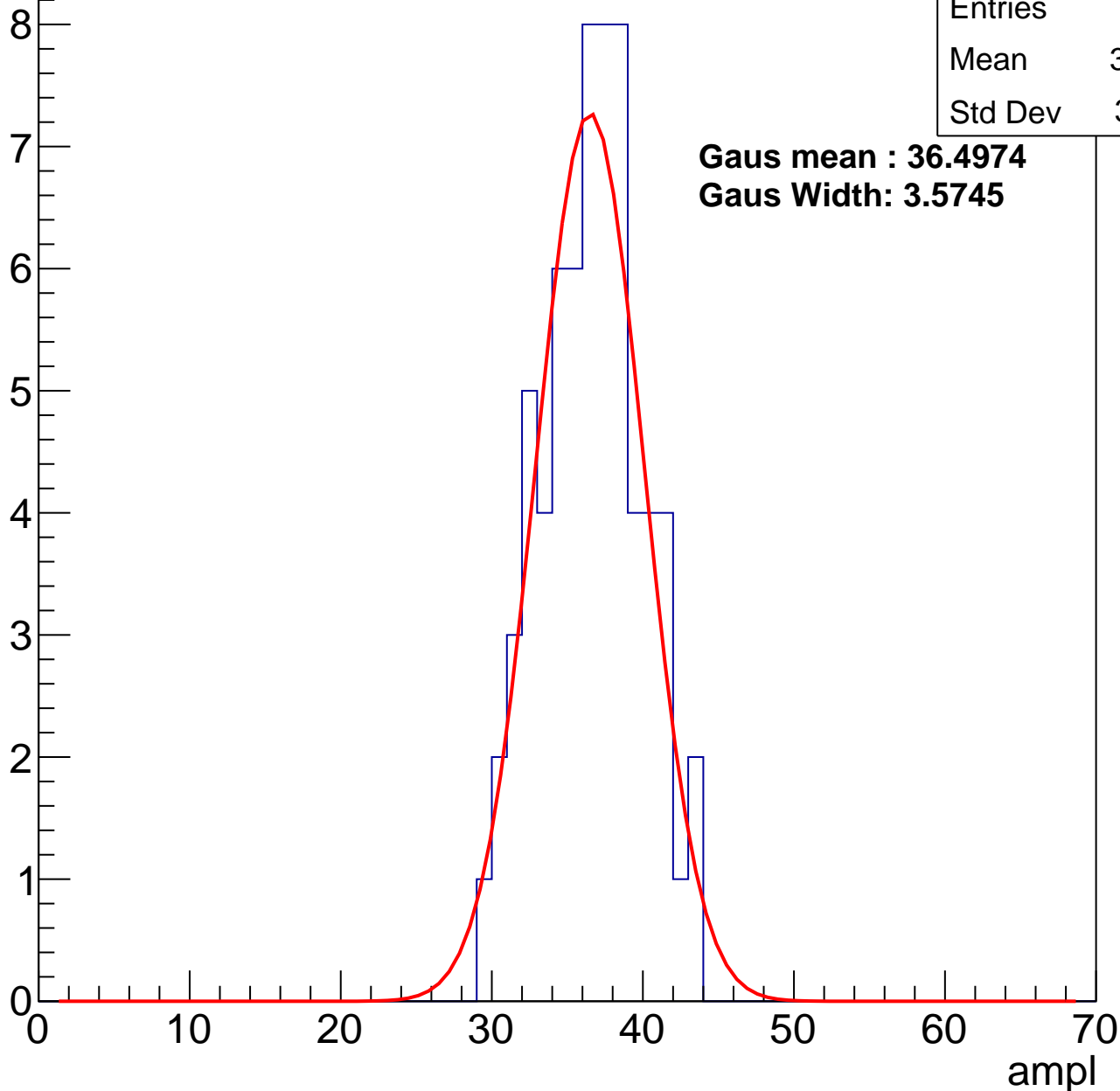
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.12
Std Dev	3.291

Gaus mean : 36.4974

Gaus Width: 3.5745



B1L102S, U10-ch67, adc2

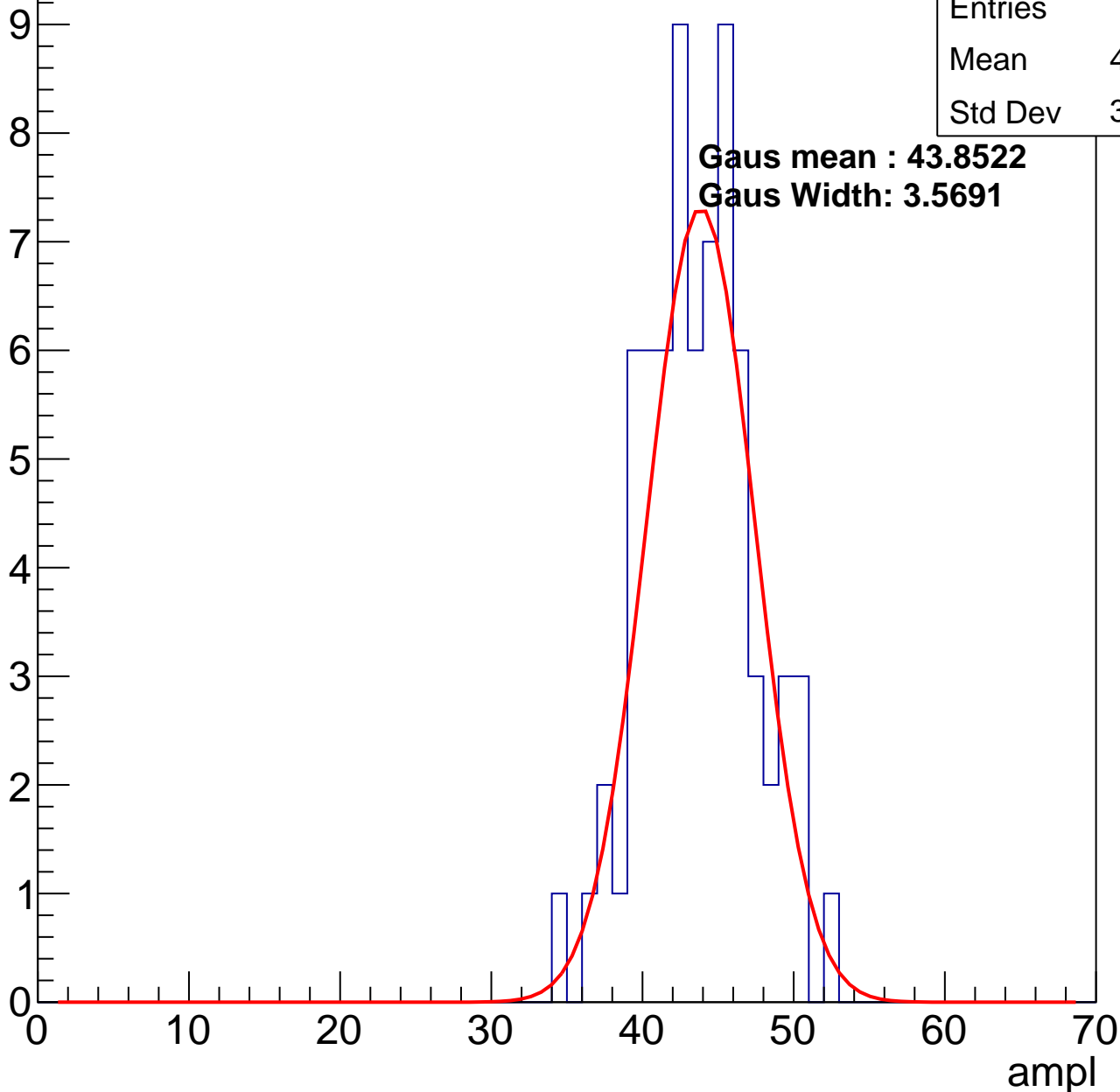
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.24
Std Dev	3.607

Gaus mean : 43.8522

Gaus Width: 3.5691

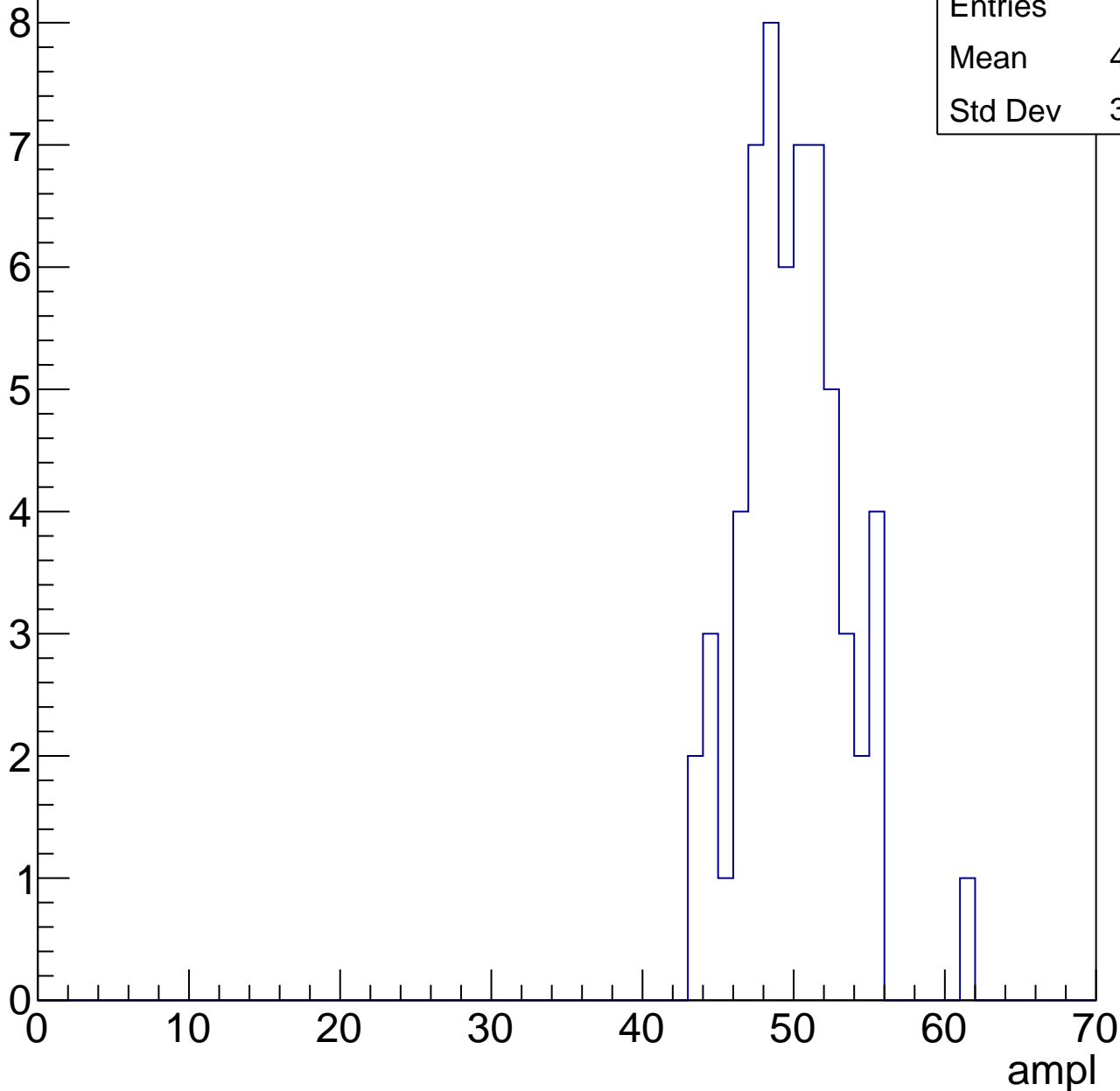


B1L102S, U10-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.48
Std Dev	3.384

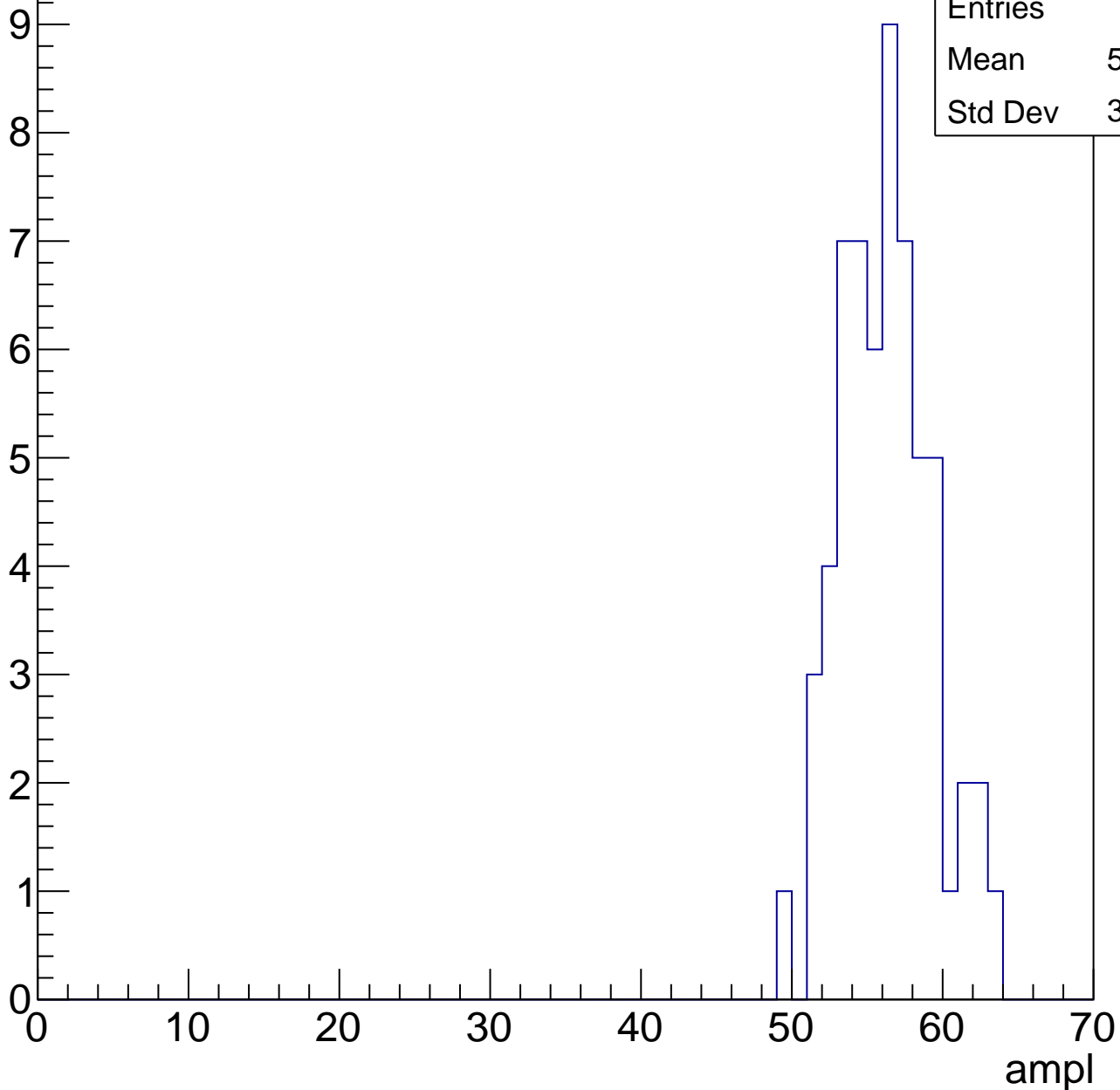


B1L102S, U10-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.77
Std Dev	3.002

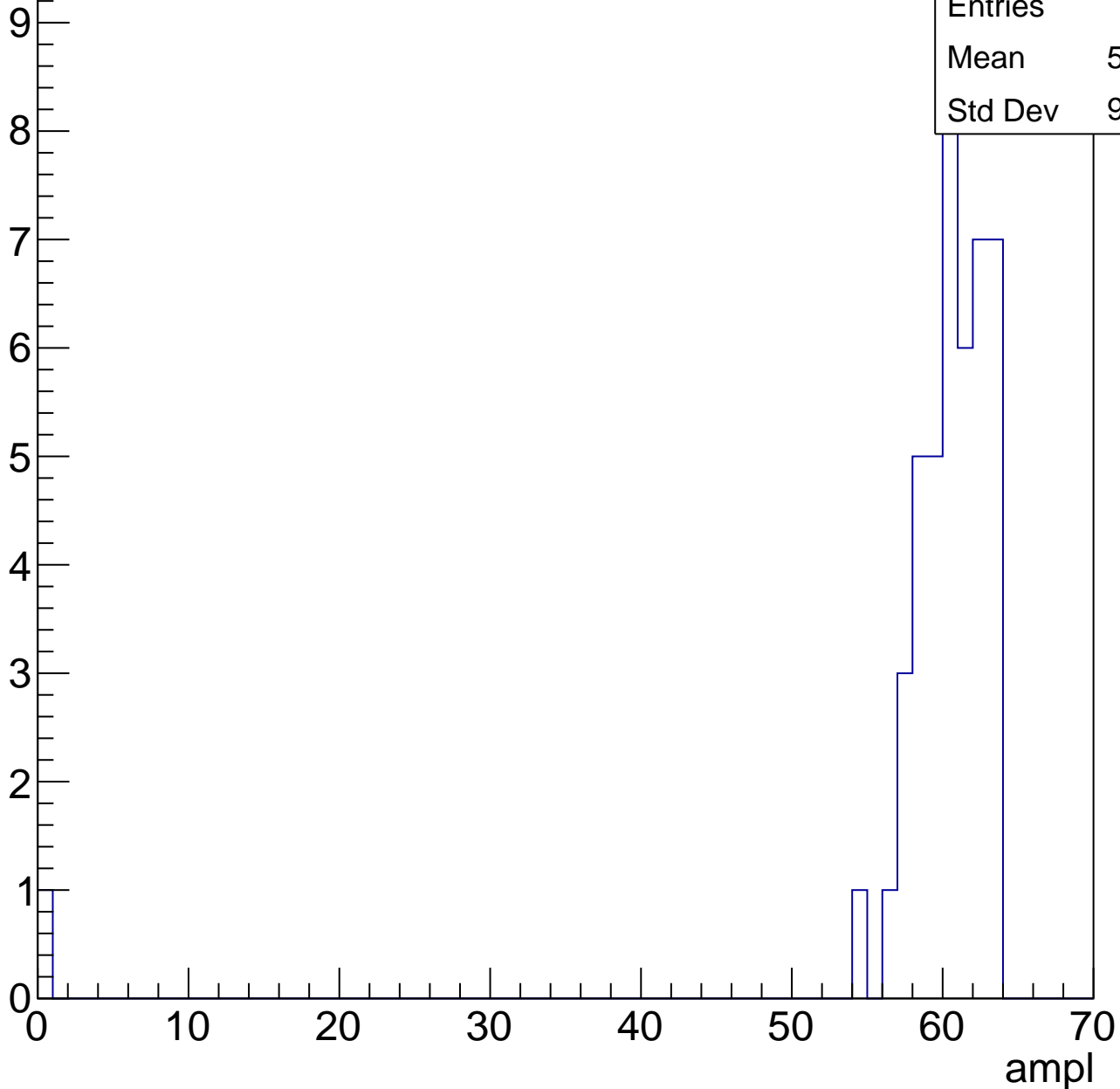


B1L102S, U10-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.82
Std Dev	9.115



B1L102S, U10-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B1L102S, U10-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch68, adc0

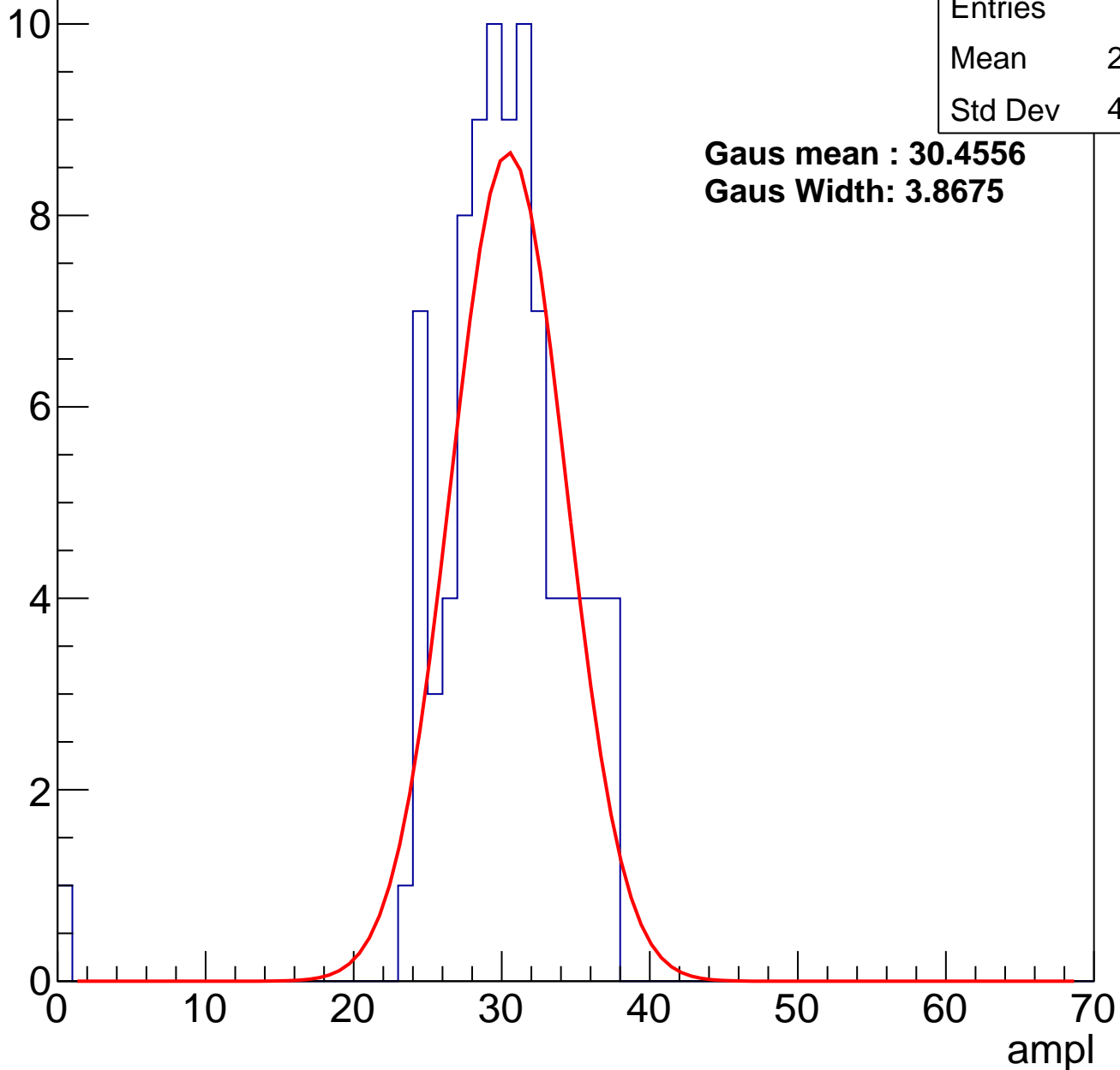
calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	29.57
Std Dev	4.764

Gaus mean : 30.4556

Gaus Width: 3.8675

Entry



B1L102S, U10-ch68, adc1

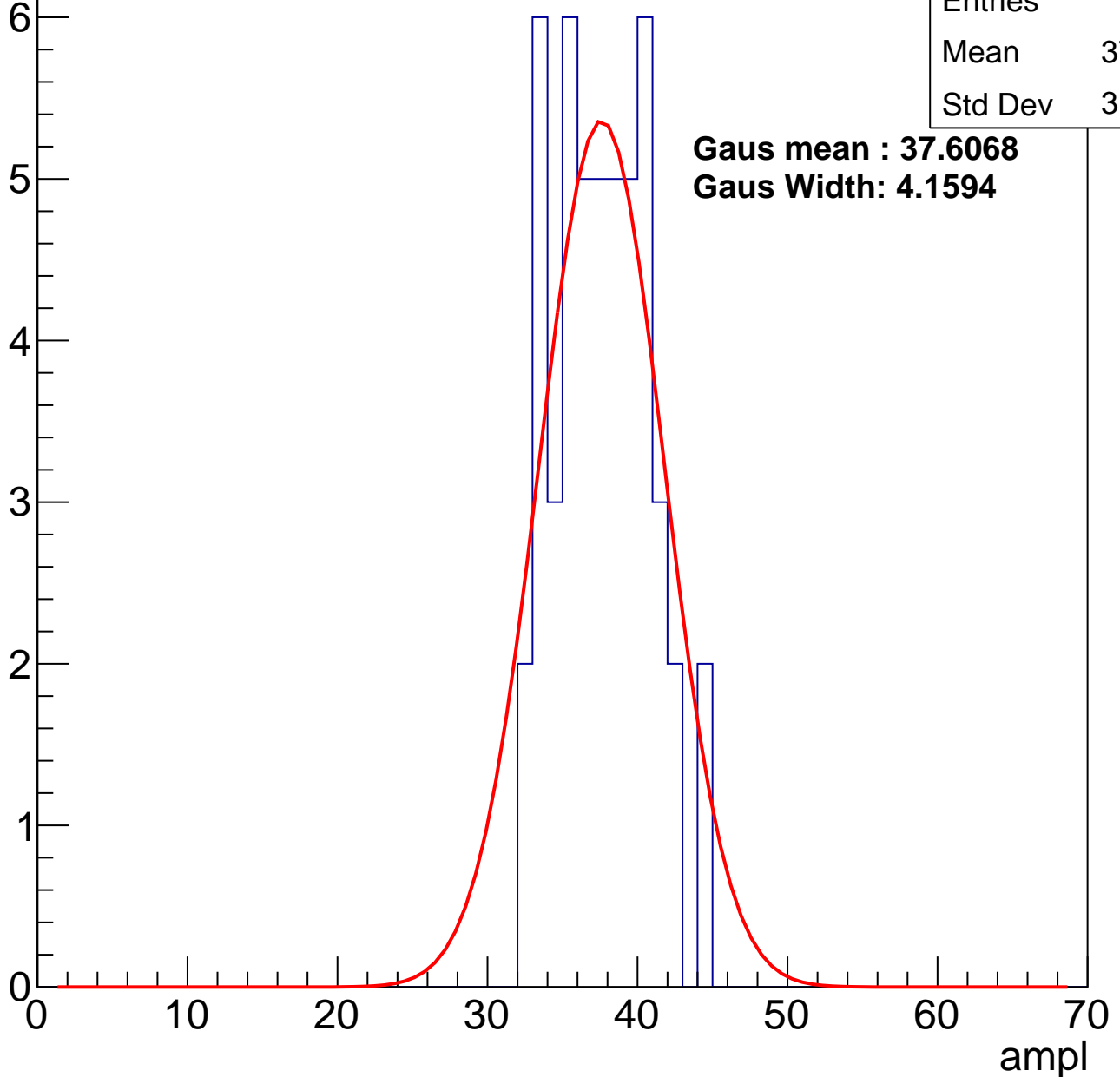
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	37.18
Std Dev	3.083

Gaus mean : 37.6068

Gaus Width: 4.1594



B1L102S, U10-ch68, adc2

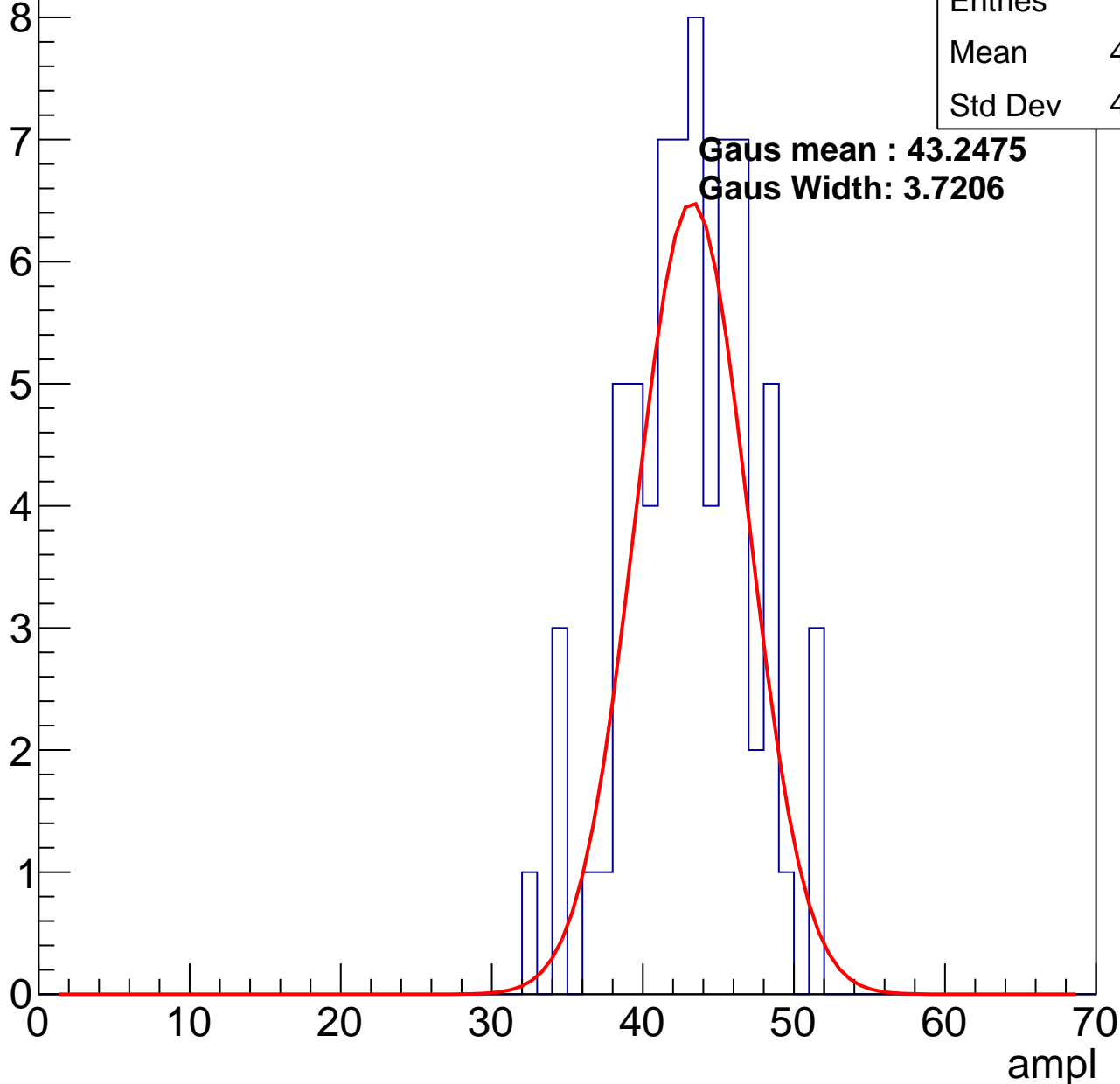
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.62
Std Dev	4.102

Gaus mean : 43.2475

Gaus Width: 3.7206

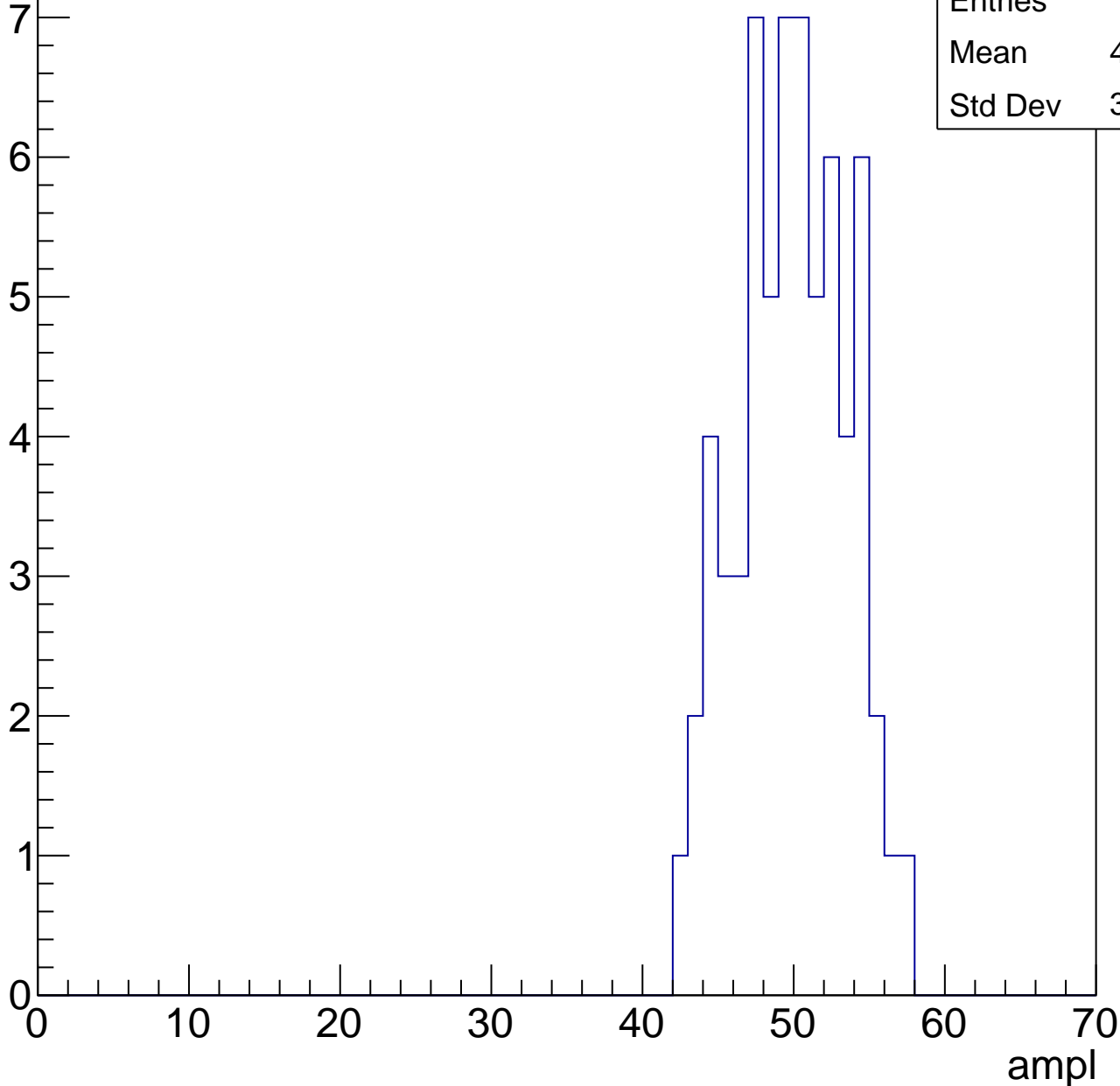


B1L102S, U10-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.45
Std Dev	3.526

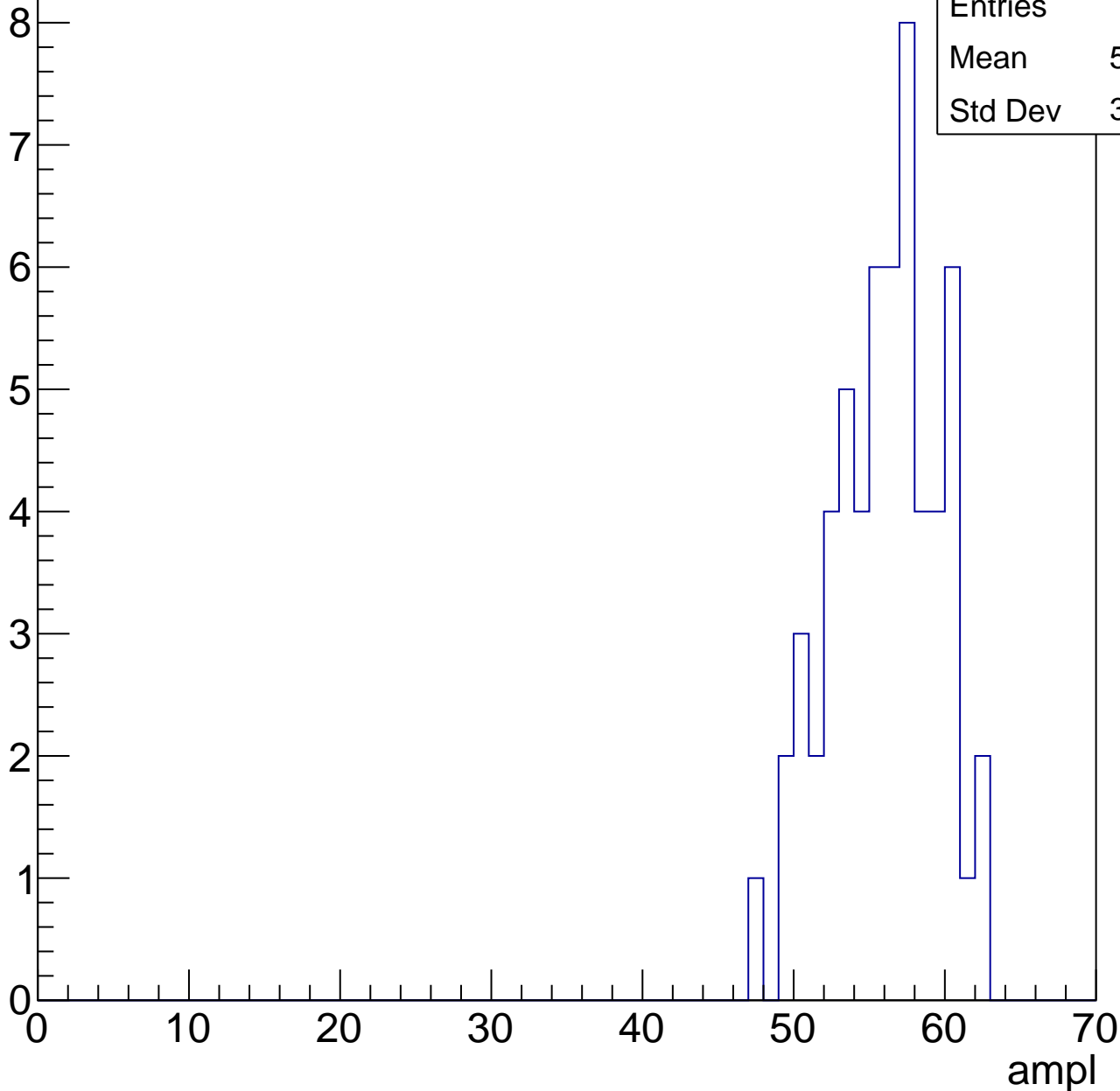


B1L102S, U10-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.53
Std Dev	3.495

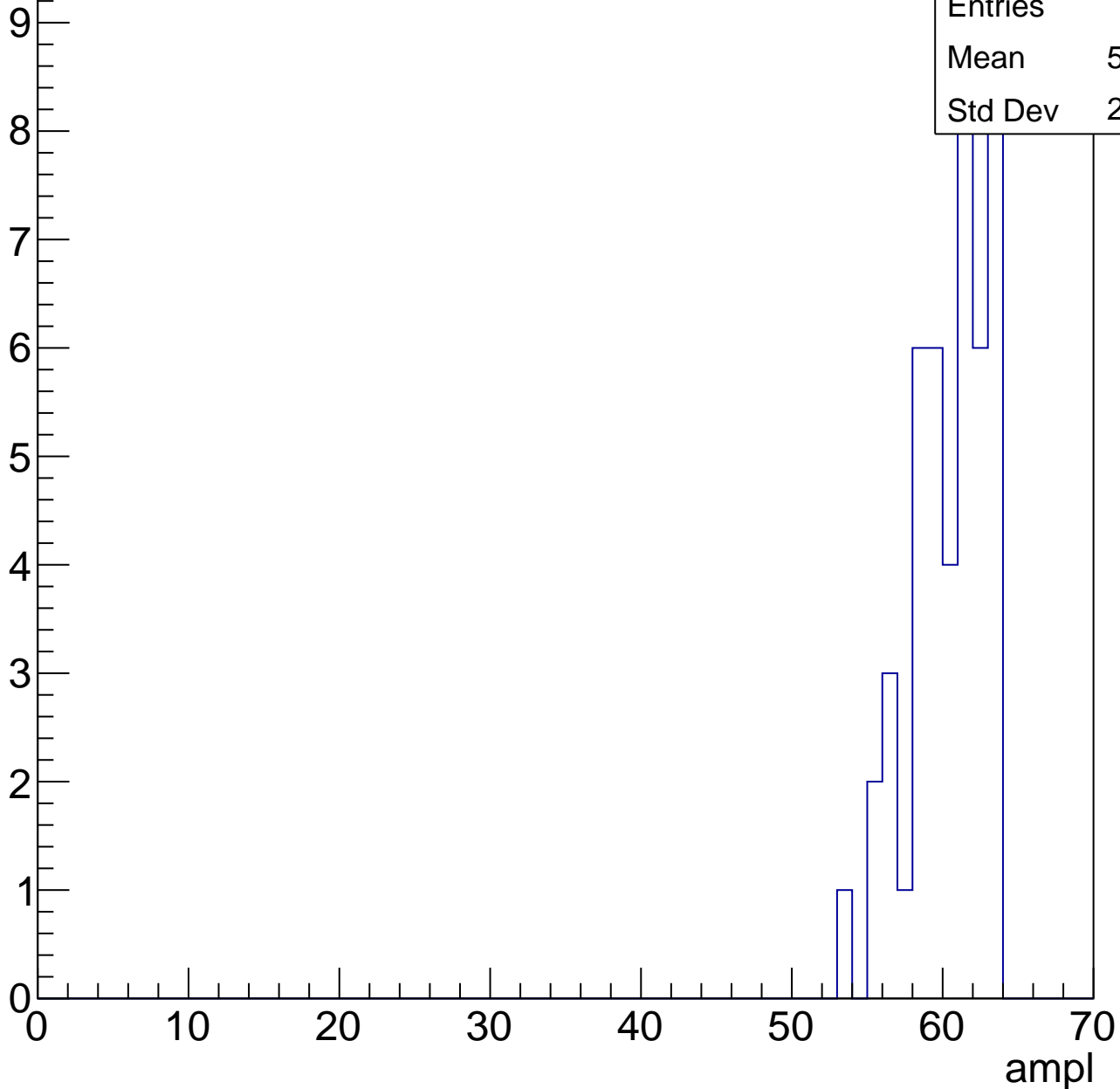


B1L102S, U10-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.89
Std Dev	2.513



B1L102S, U10-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch69, adc0

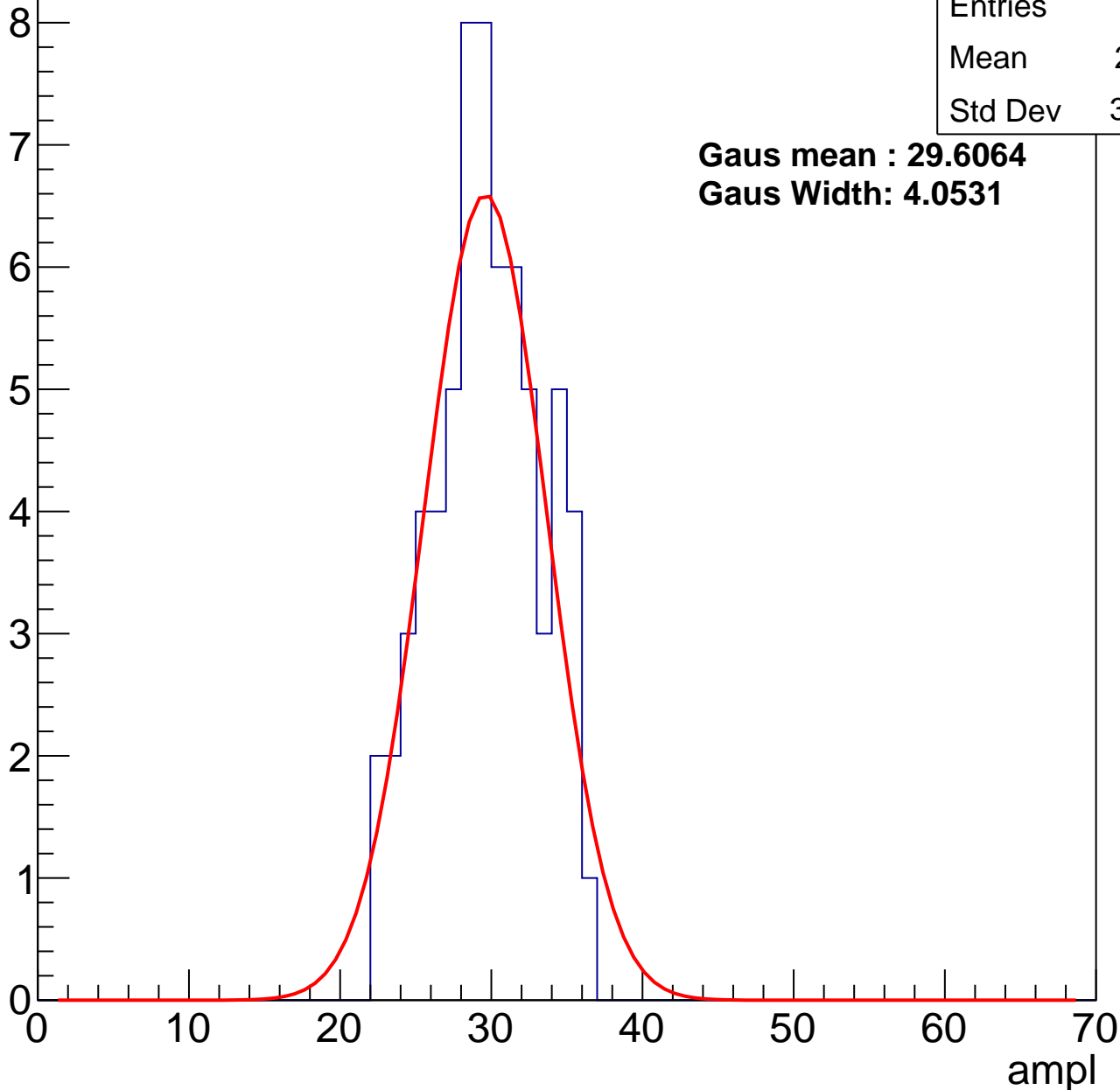
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.21
Std Dev	3.505

Gaus mean : 29.6064

Gaus Width: 4.0531



B1L102S, U10-ch69, adc1

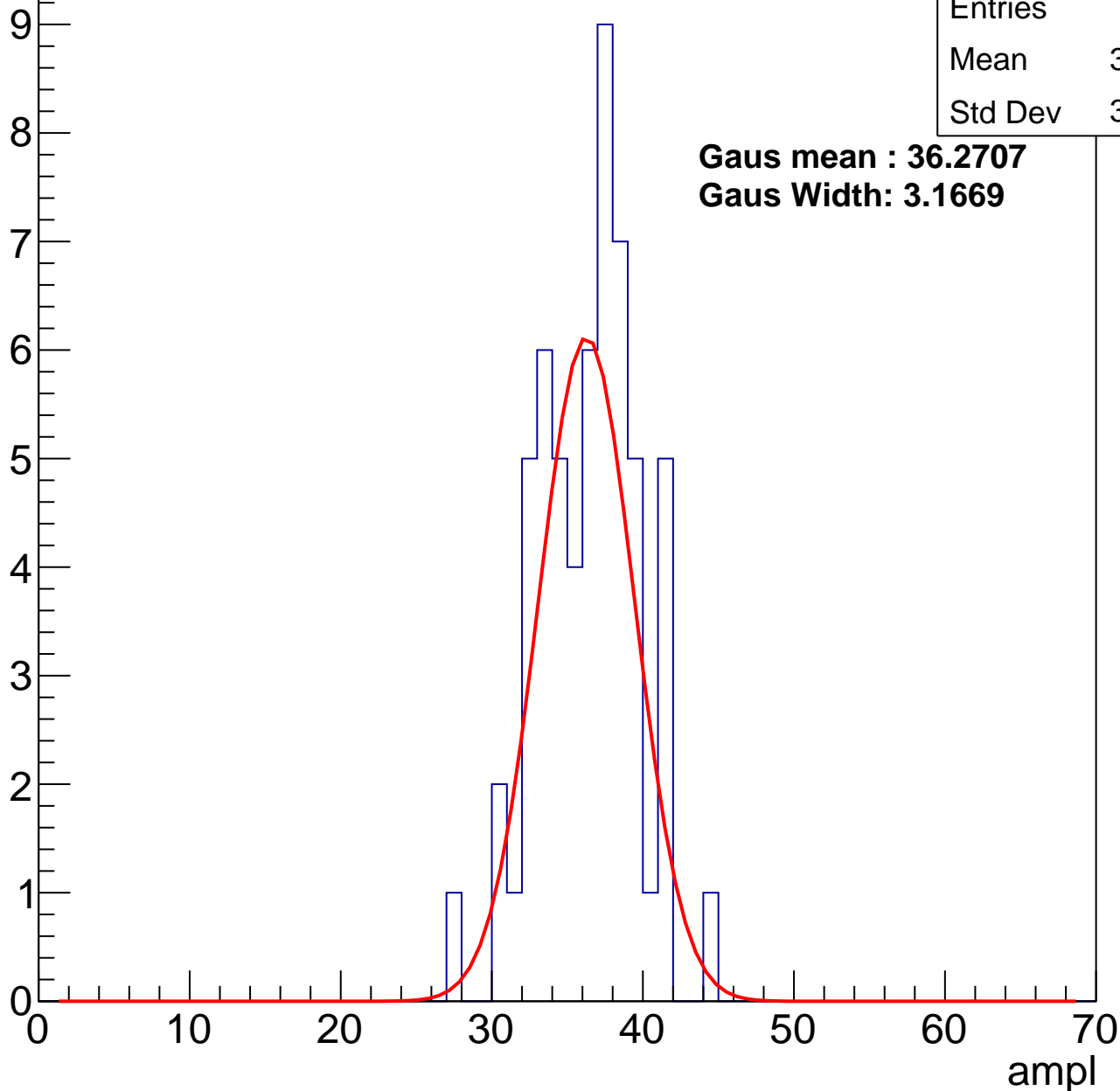
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.95
Std Dev	3.277

Gaus mean : 36.2707

Gaus Width: 3.1669



B1L102S, U10-ch69, adc2

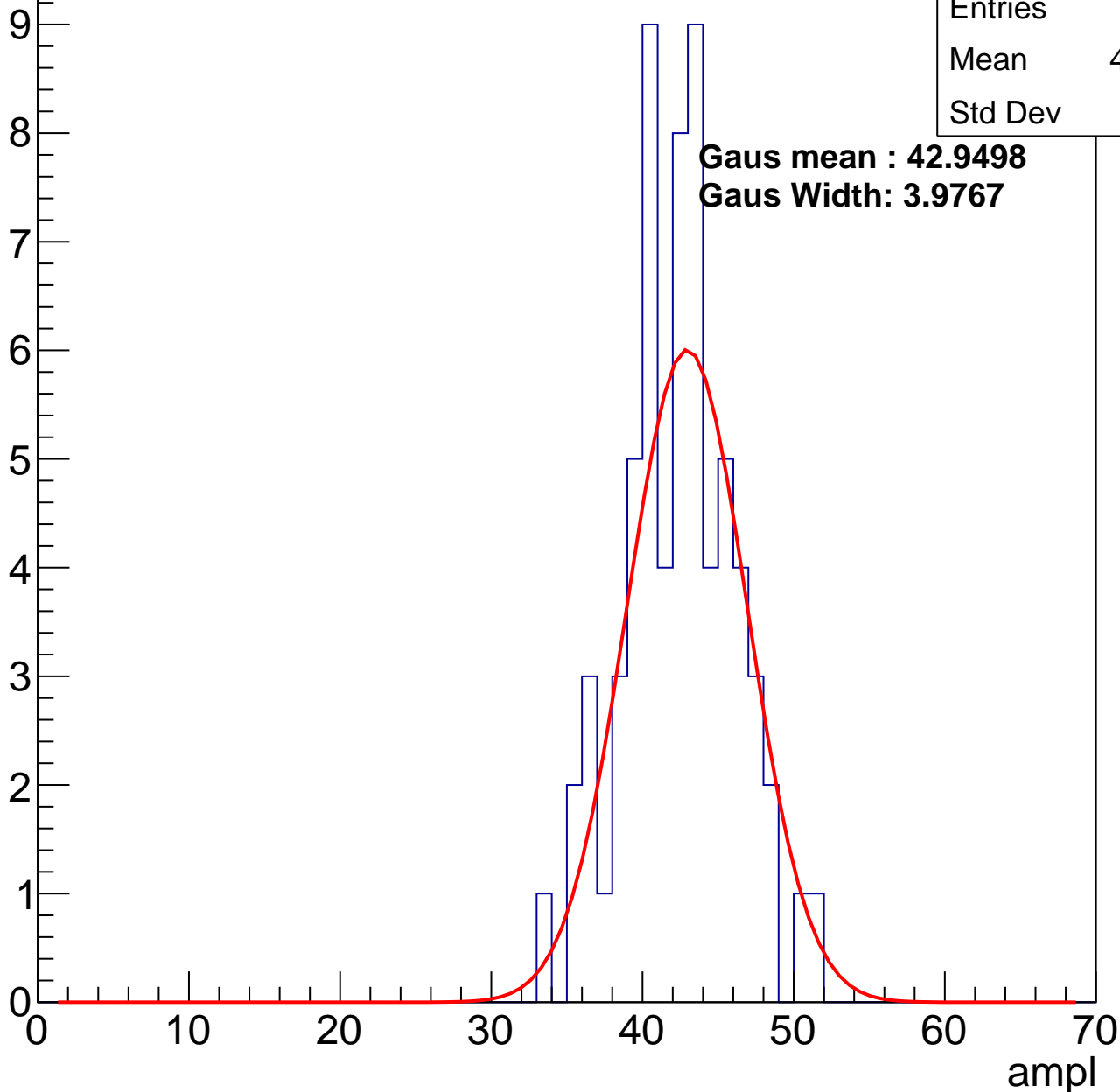
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.95
Std Dev	3.66

Gaus mean : 42.9498

Gaus Width: 3.9767

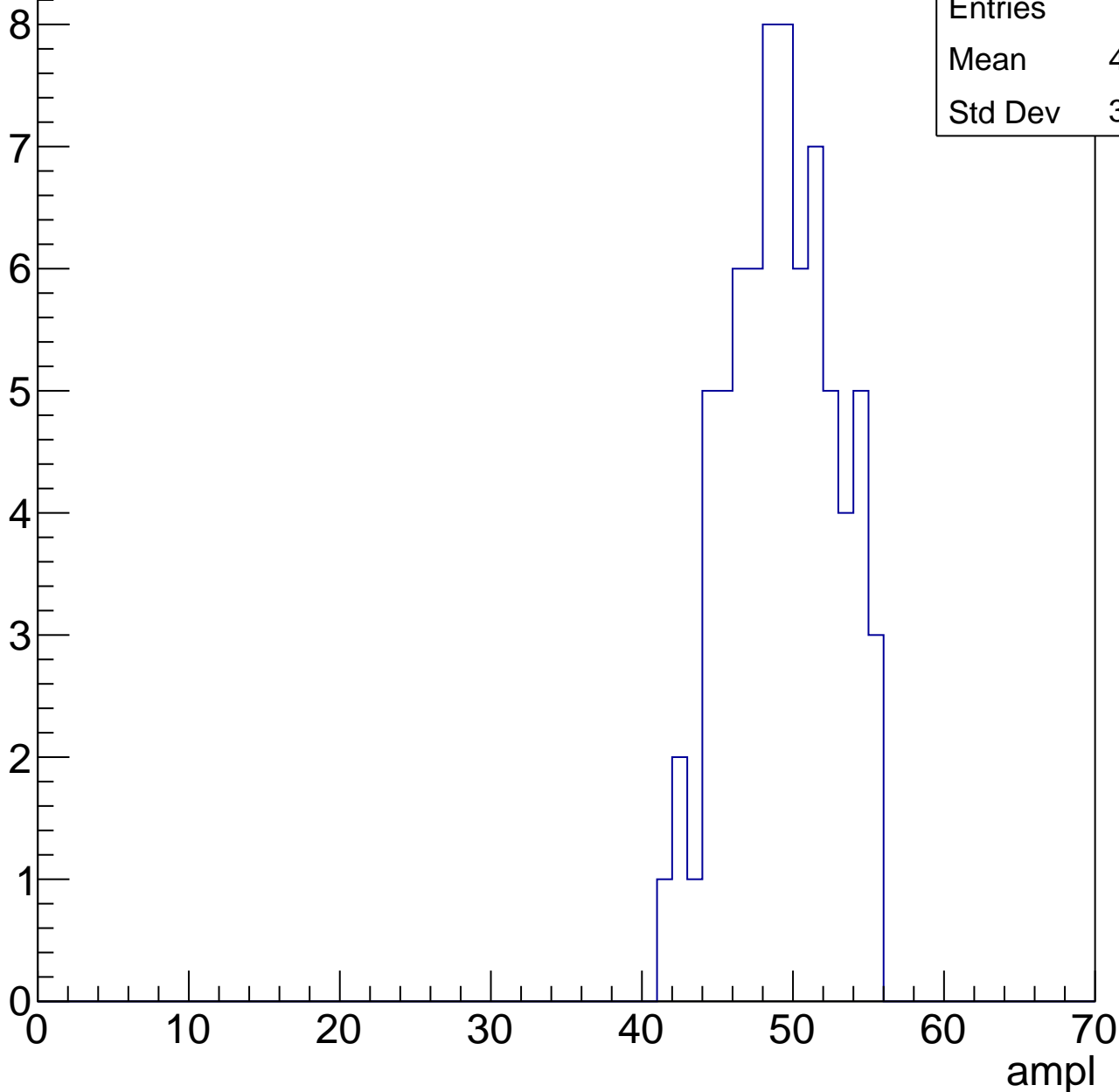


B1L102S, U10-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.76
Std Dev	3.458

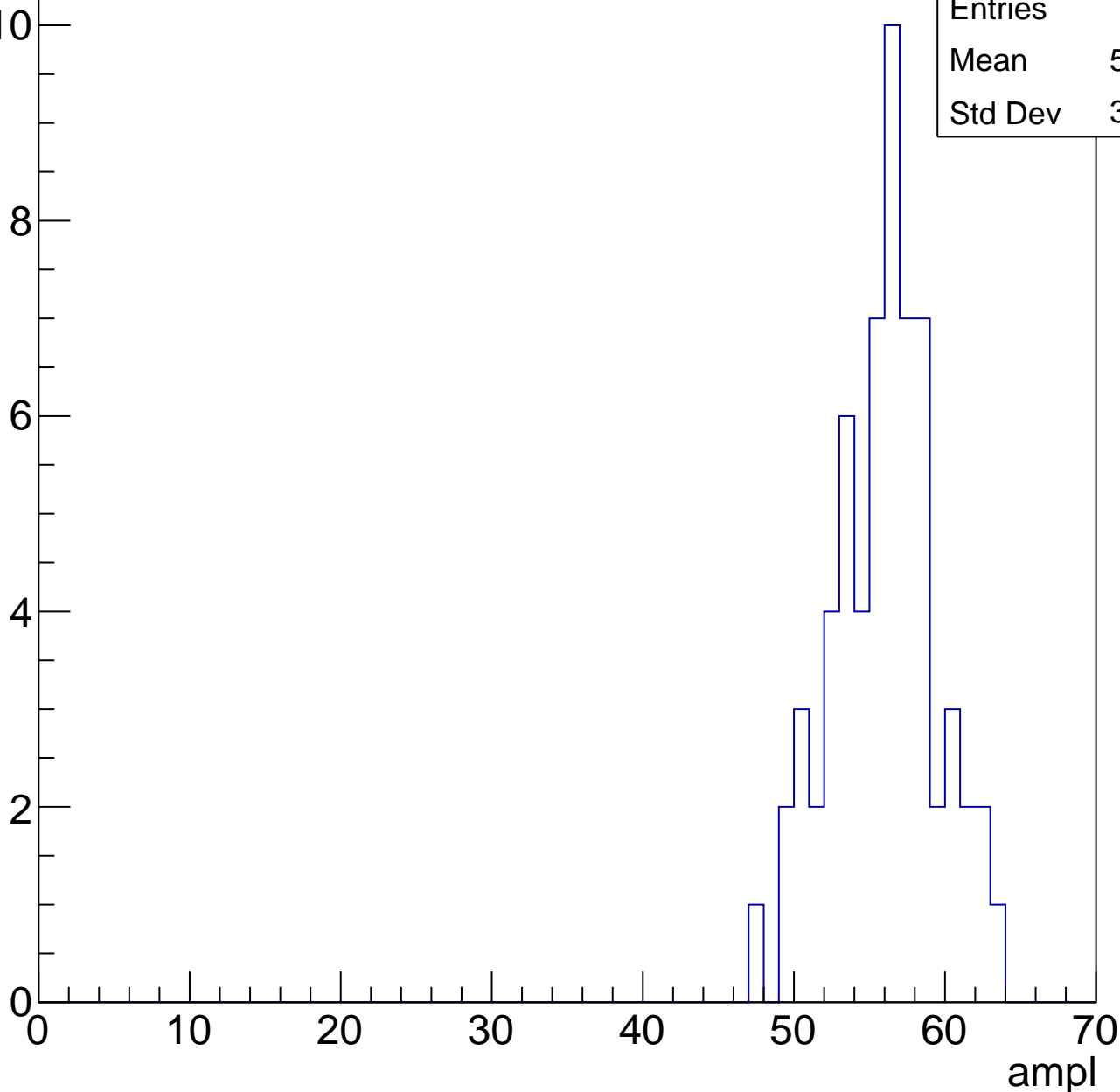


B1L102S, U10-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.49
Std Dev	3.413

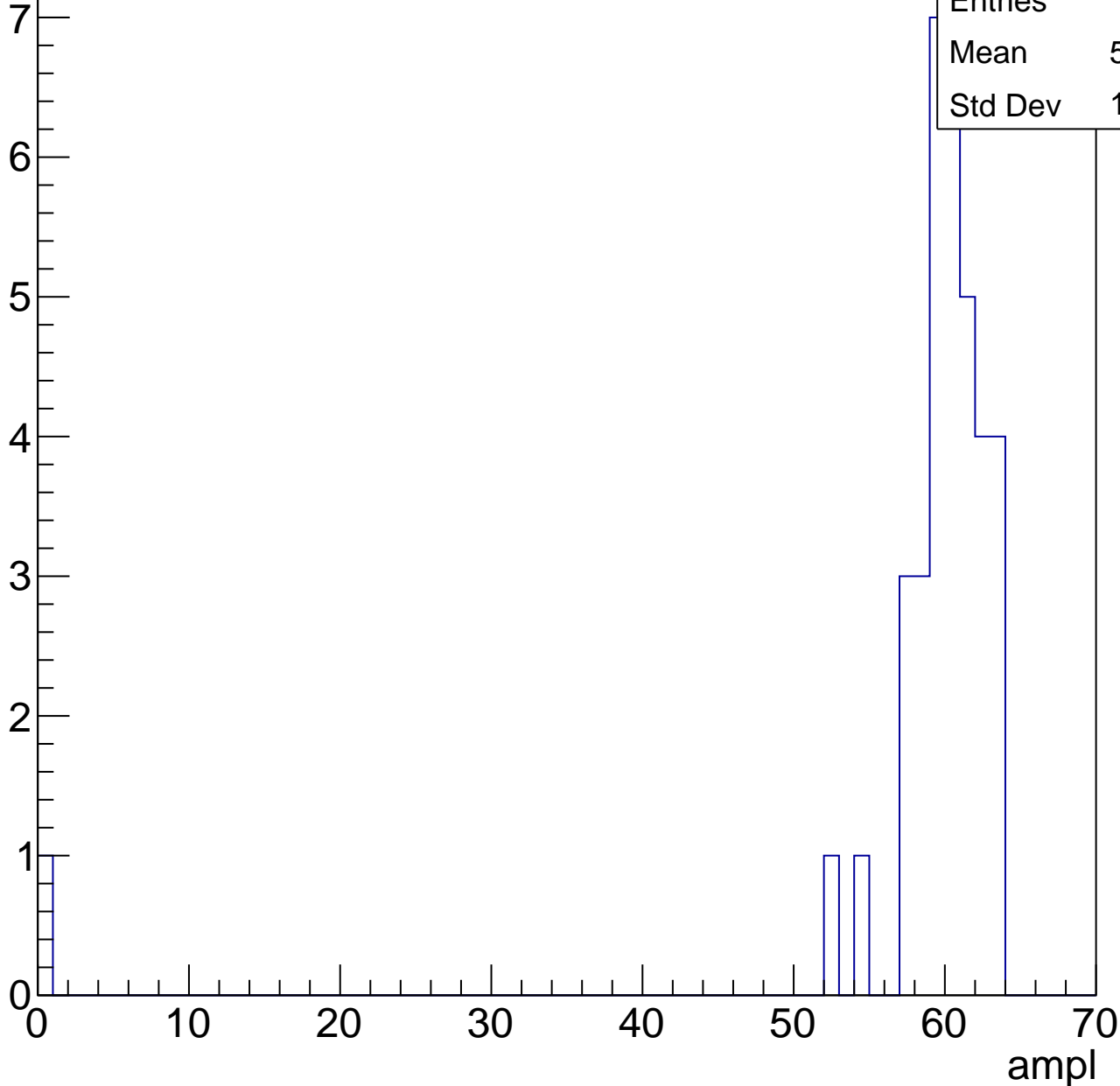


B1L102S, U10-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.03
Std Dev	10.09

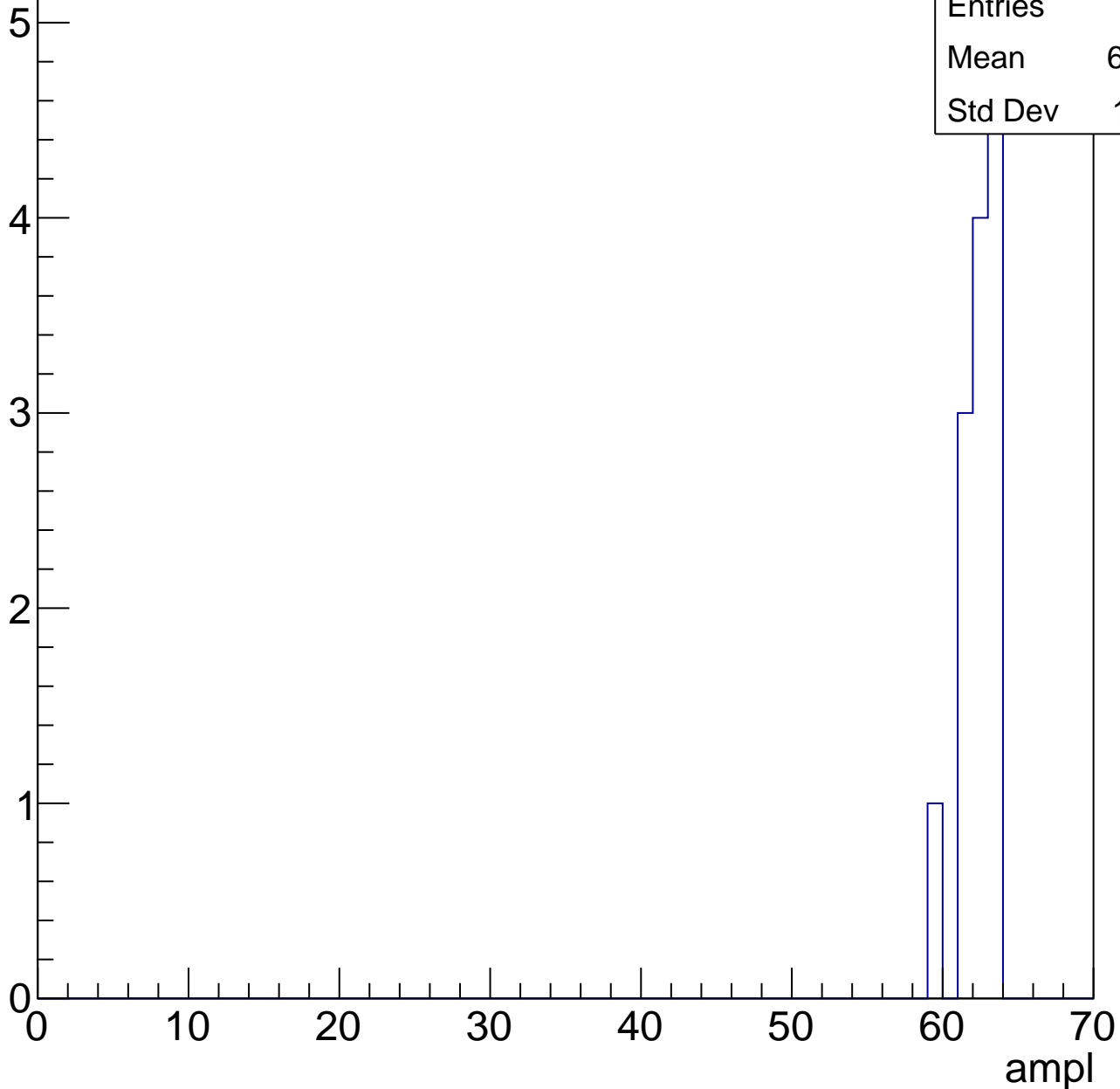


B1L102S, U10-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.92
Std Dev	1.141



B1L102S, U10-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch70, adc0

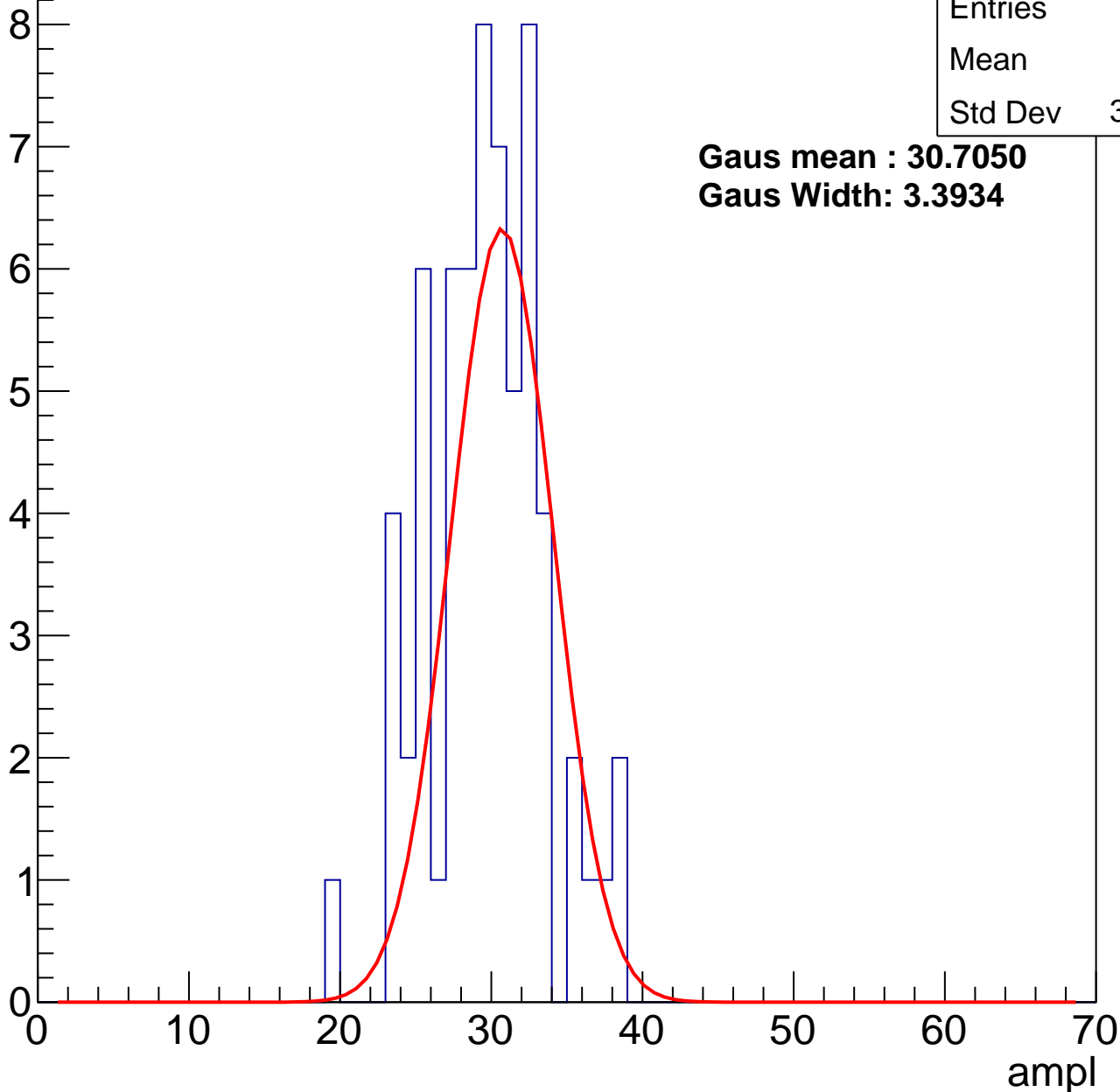
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.2
Std Dev	3.825

Gaus mean : 30.7050

Gaus Width: 3.3934



B1L102S, U10-ch70, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.35
Std Dev	3.799

Gaus mean : 36.2750

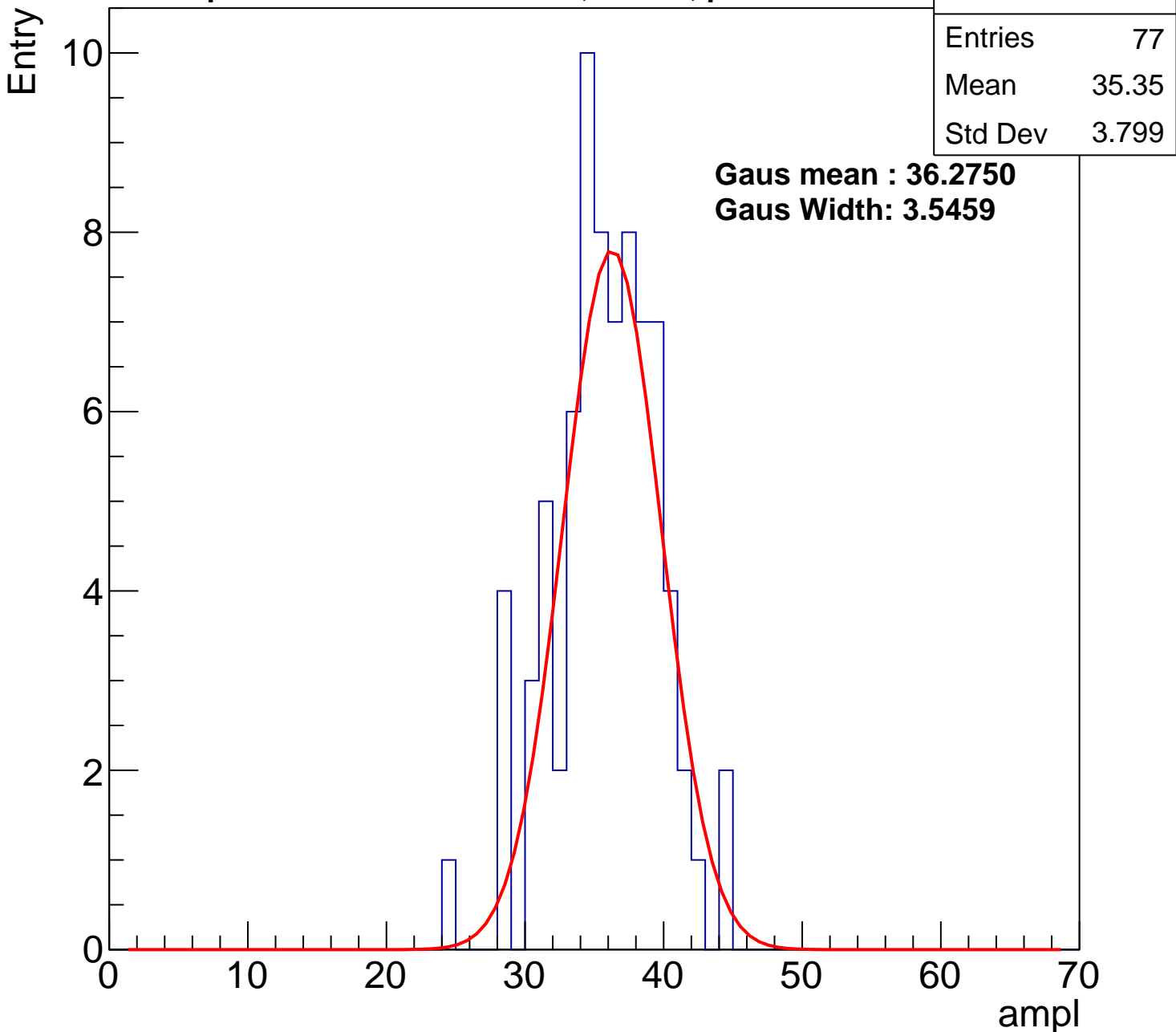
Gaus Width: 3.5459

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch70, adc2

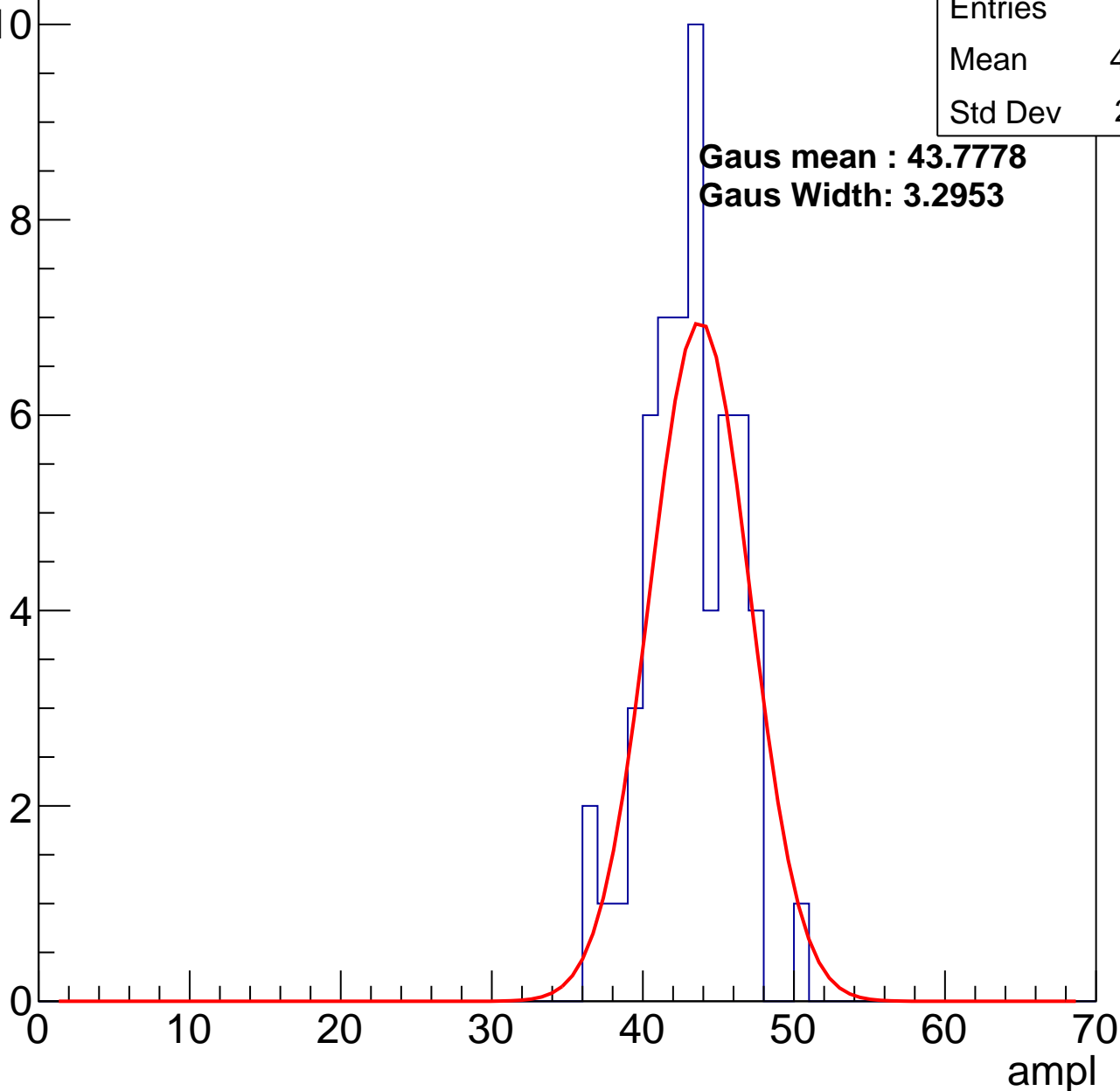
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.67
Std Dev	2.891

Gaus mean : 43.7778

Gaus Width: 3.2953

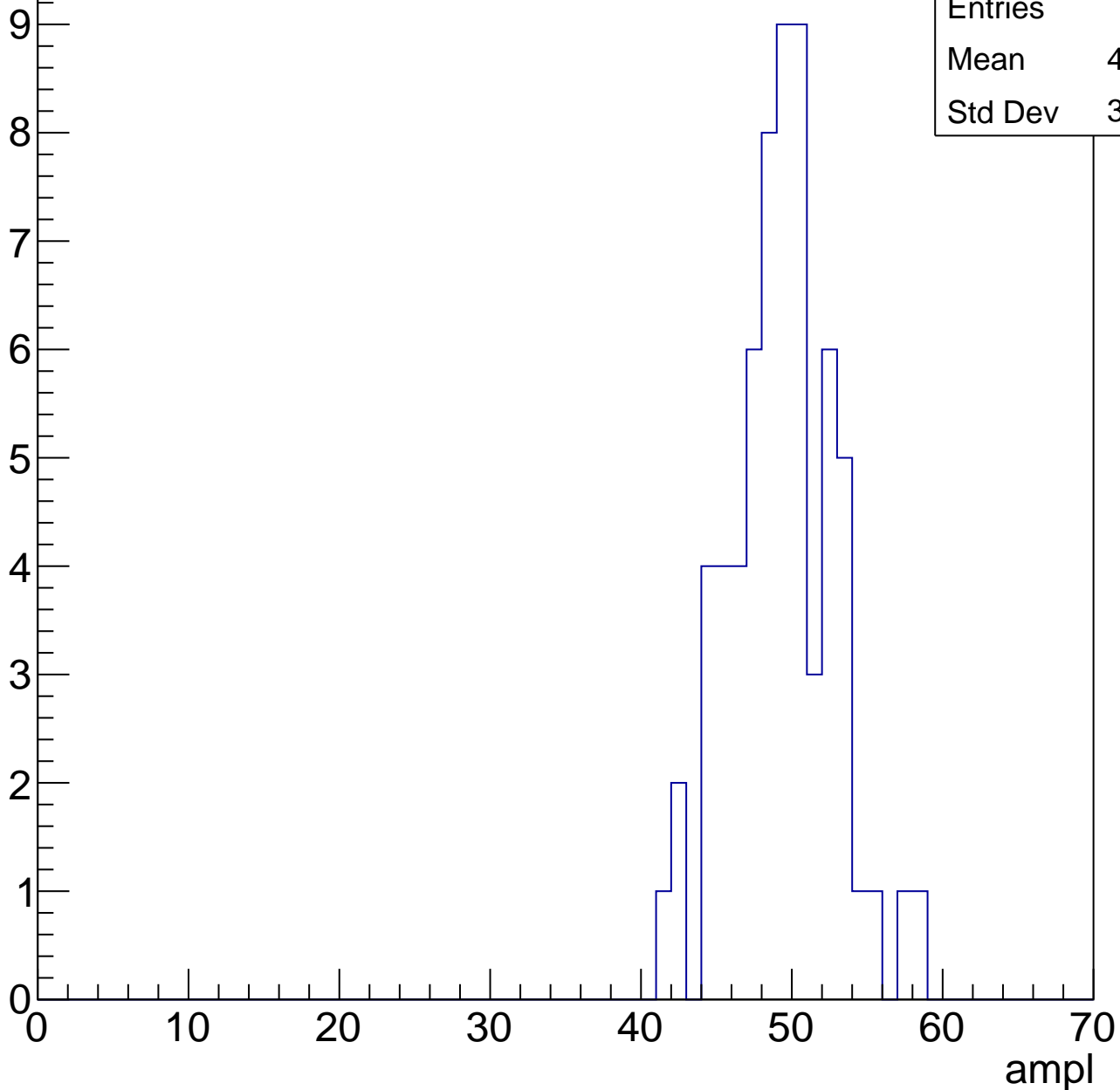


B1L102S, U10-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.86
Std Dev	3.405

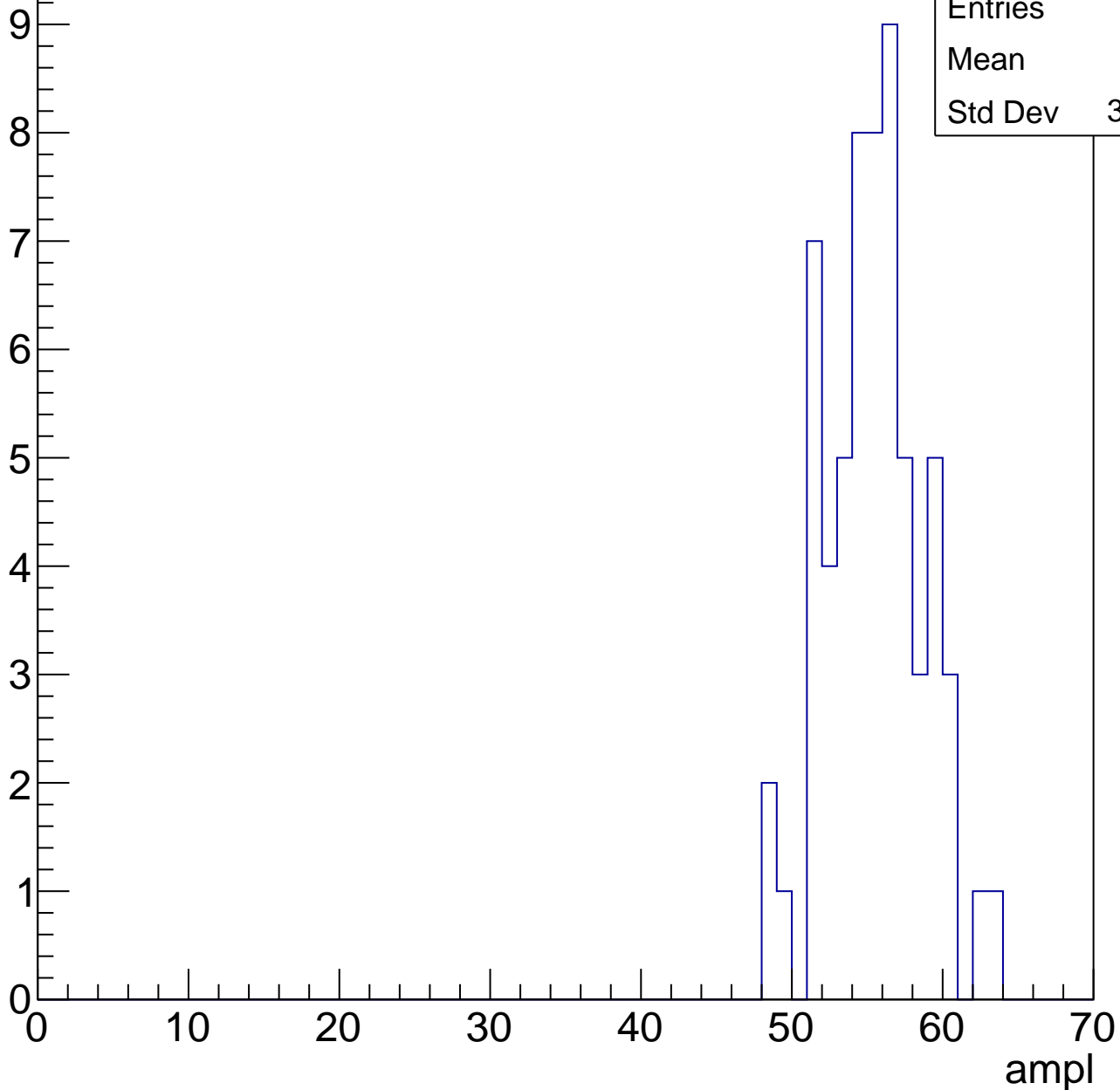


B1L102S, U10-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55
Std Dev	3.198

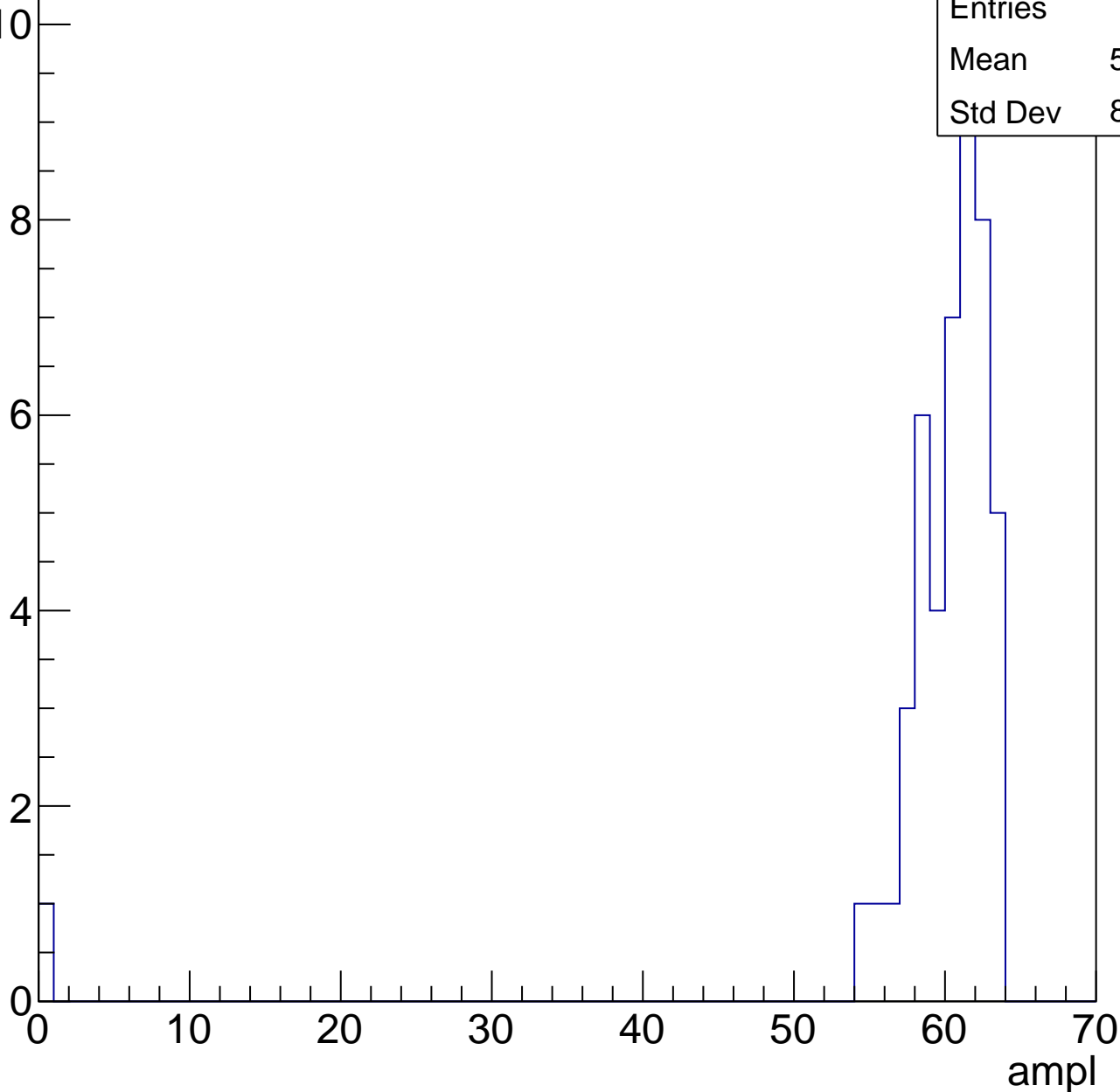


B1L102S, U10-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

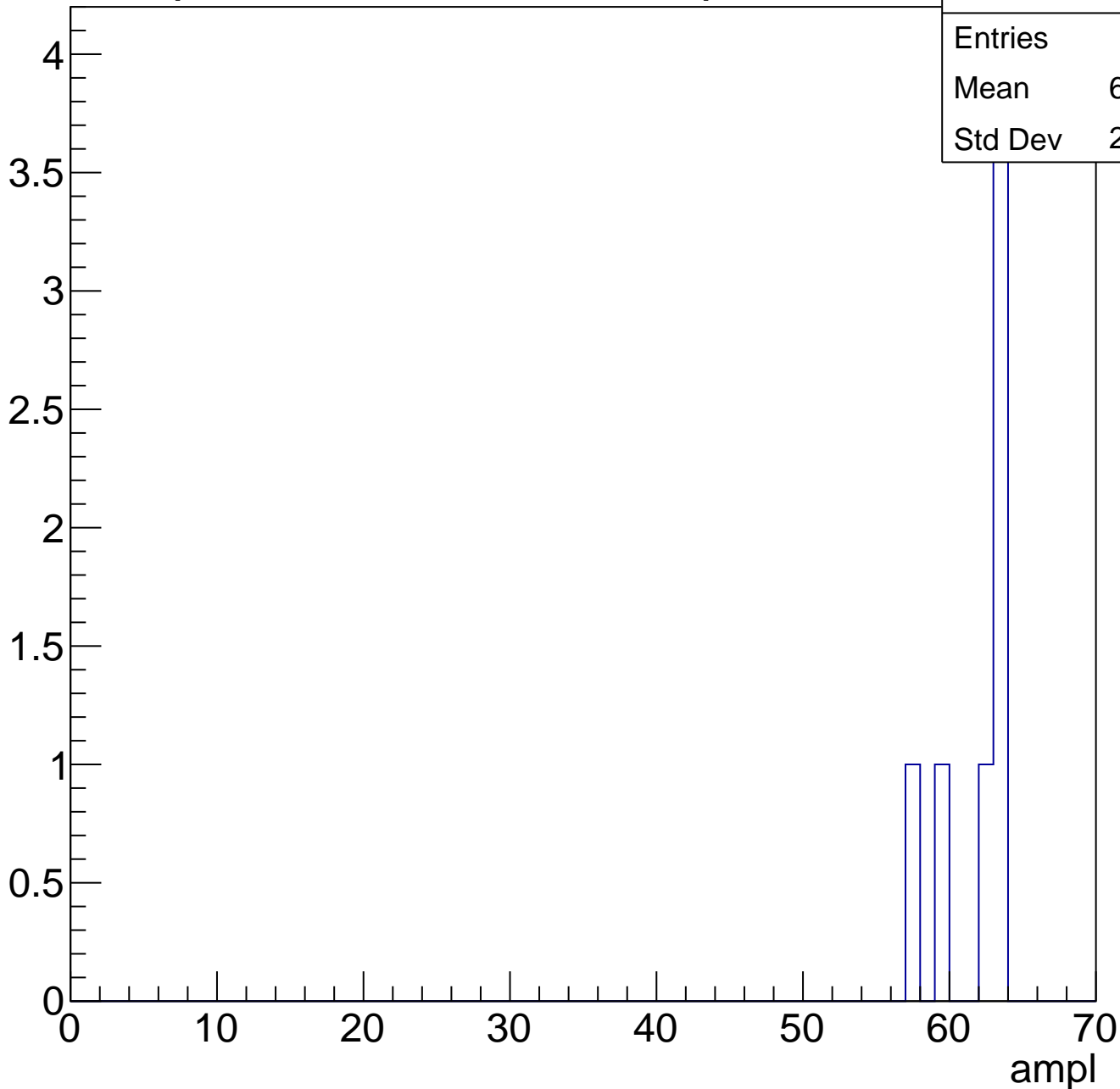
Entries	47
Mean	58.74
Std Dev	8.926



B1L102S, U10-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch71, adc0

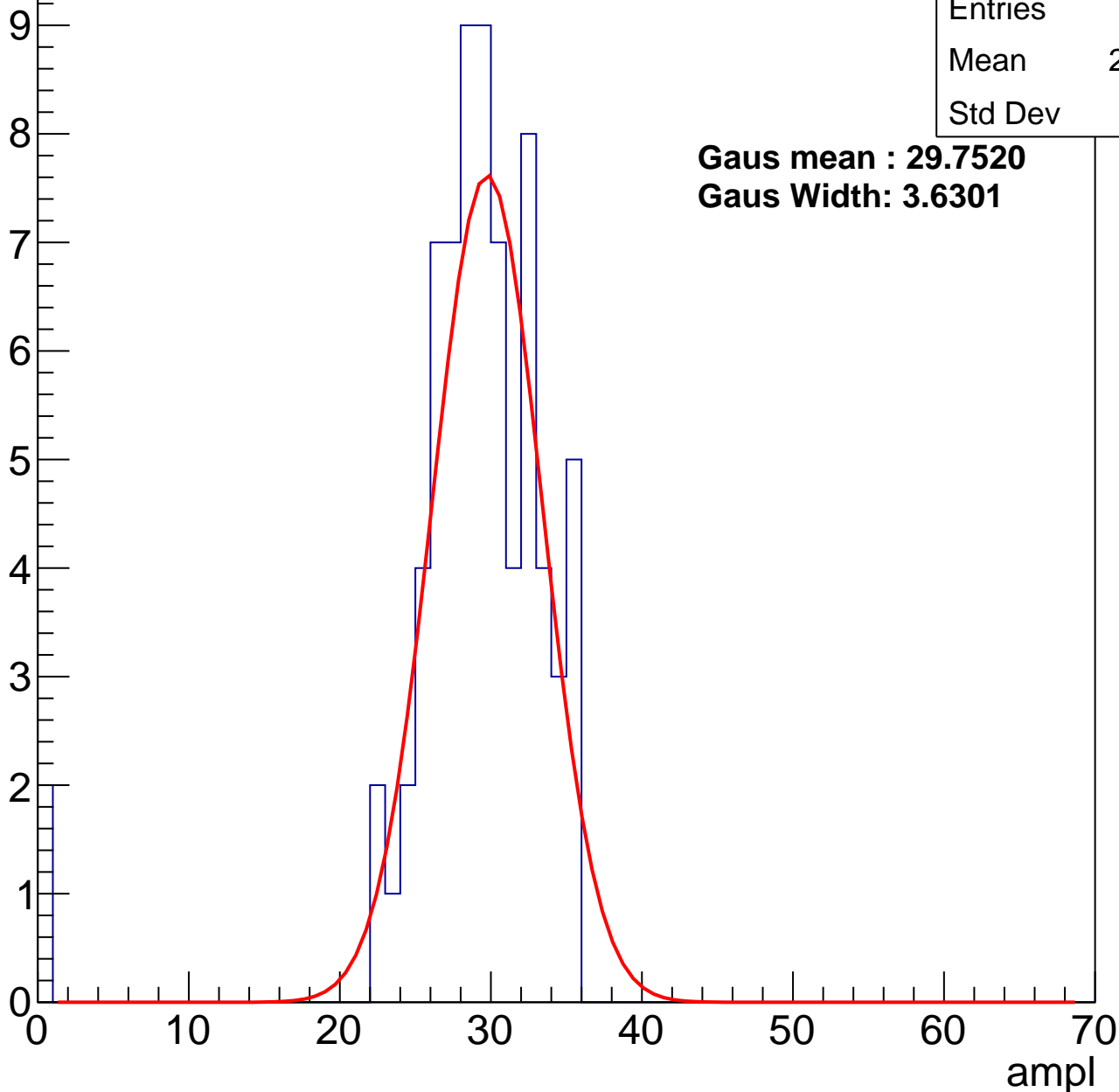
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.35
Std Dev	5.71

Gaus mean : 29.7520

Gaus Width: 3.6301



B1L102S, U10-ch71, adc1

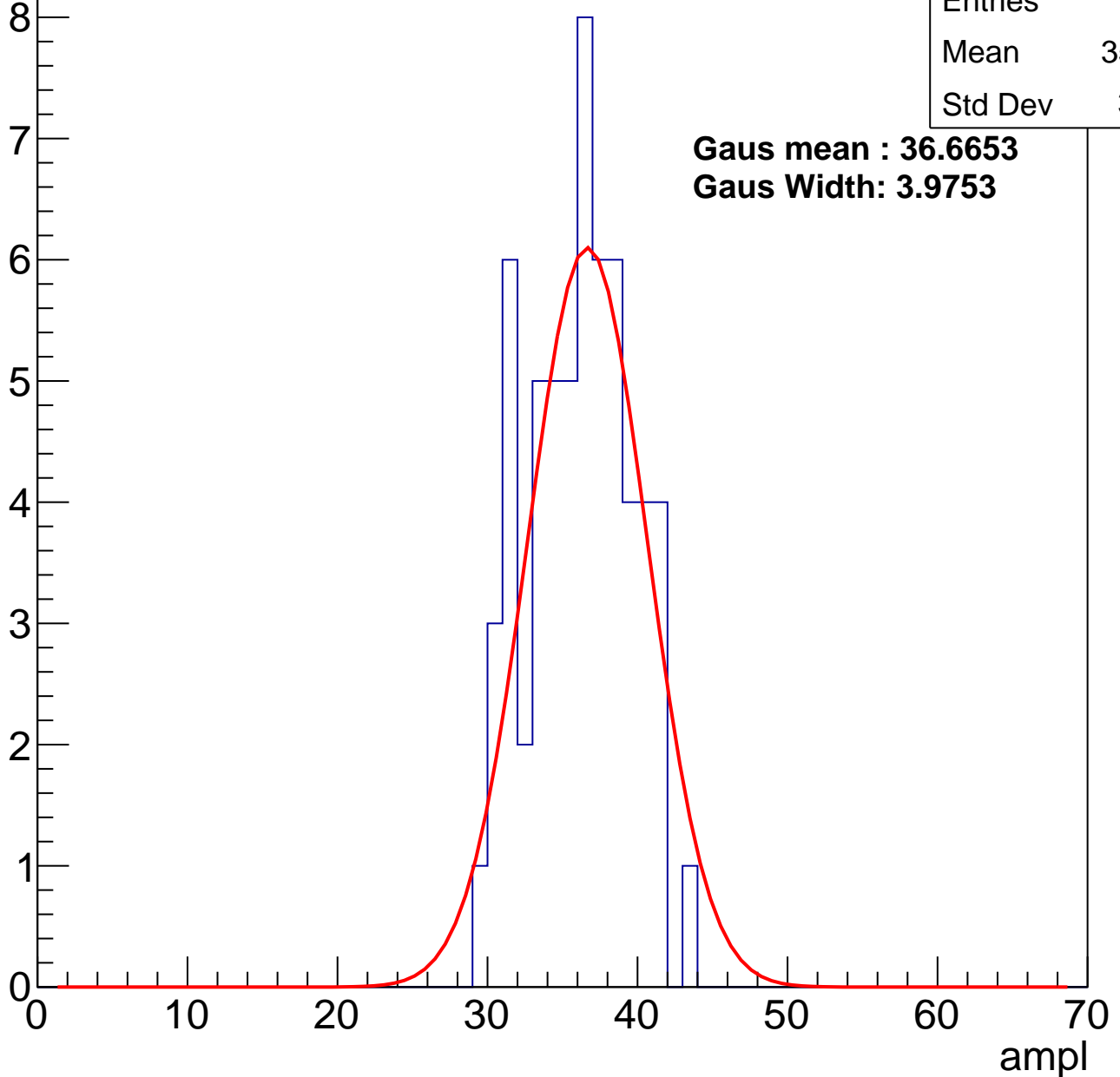
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.67
Std Dev	3.37

Gaus mean : 36.6653

Gaus Width: 3.9753



B1L102S, U10-ch71, adc2

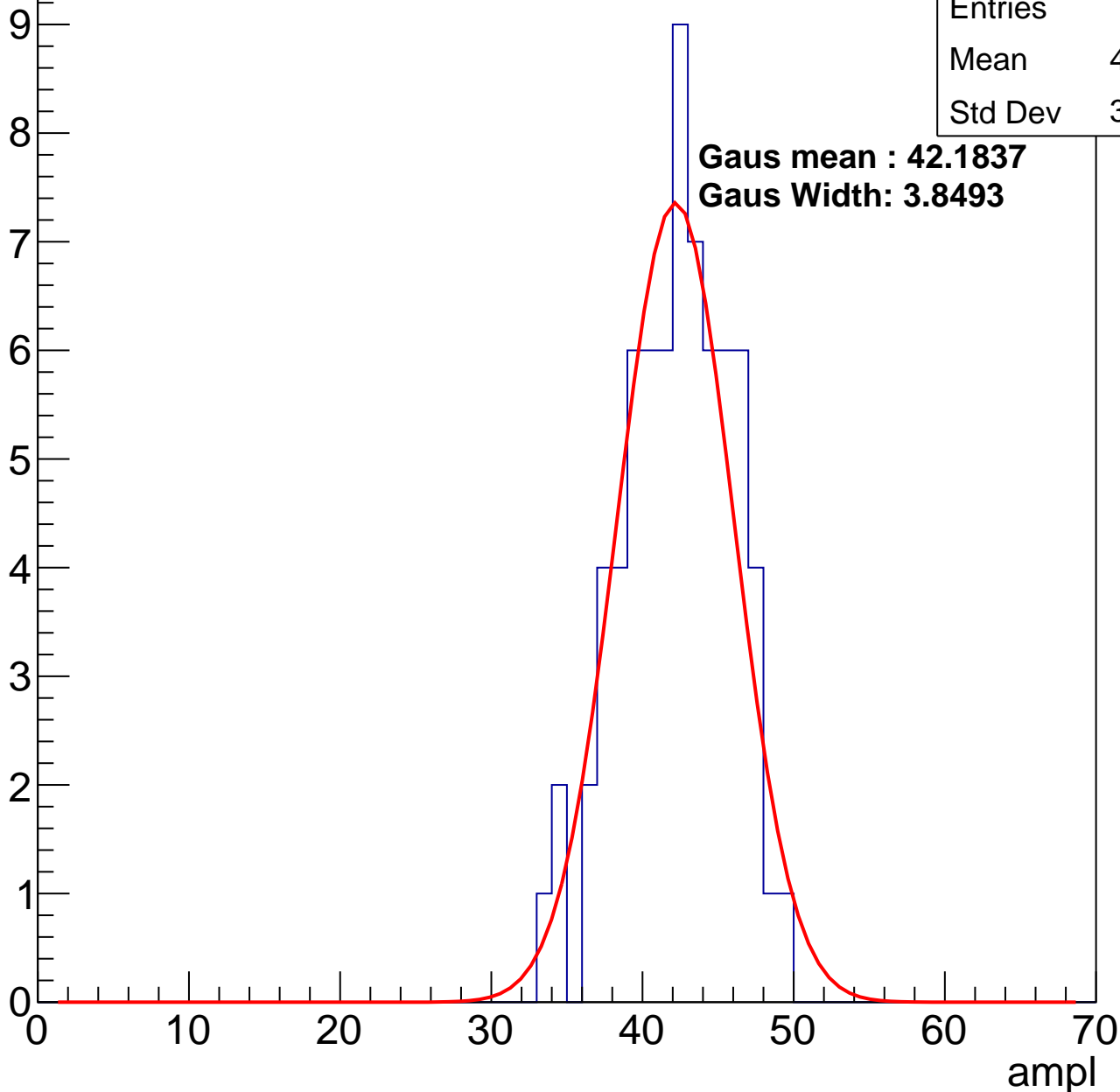
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.79
Std Dev	3.544

Gaus mean : 42.1837

Gaus Width: 3.8493

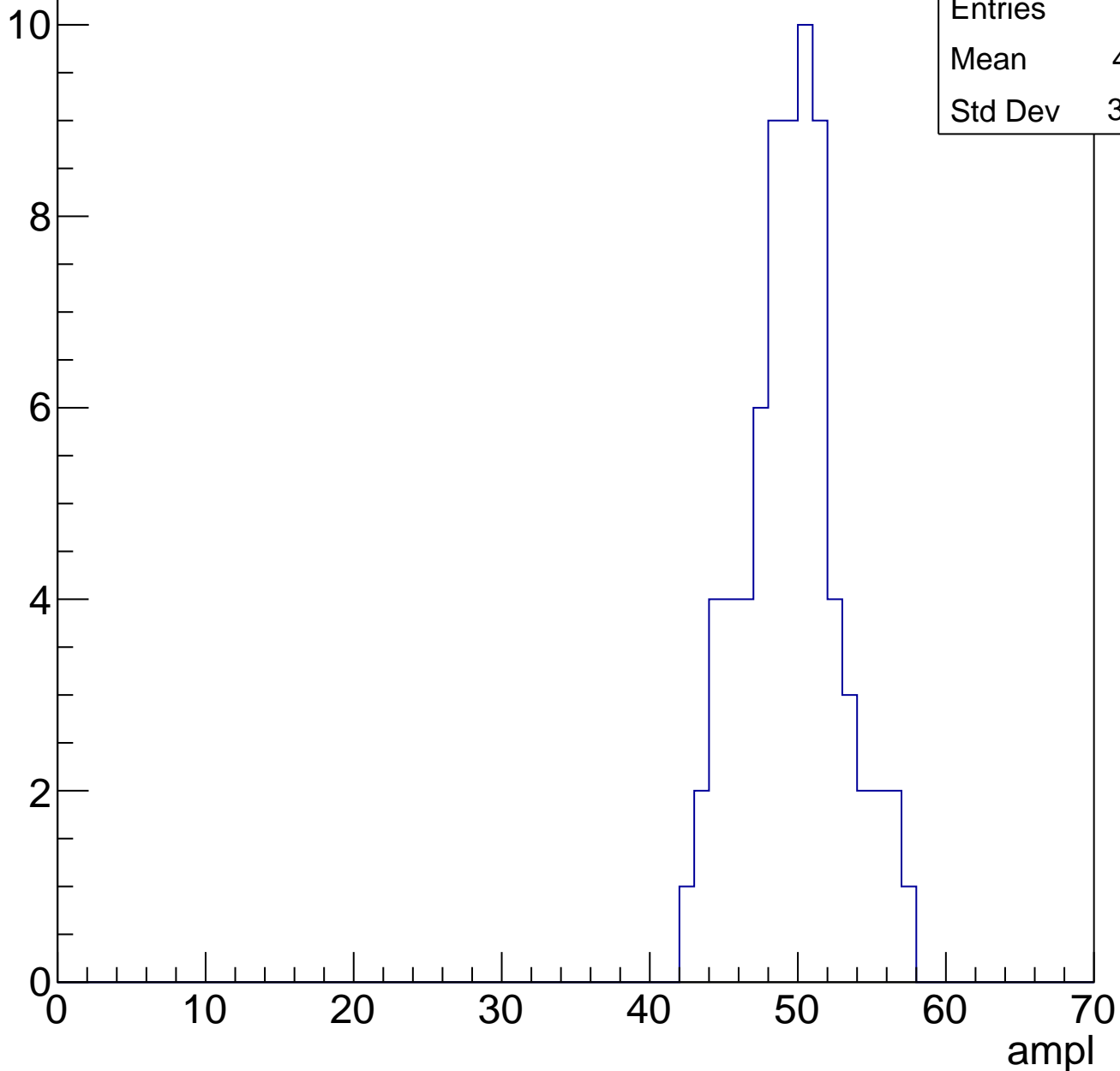


B1L102S, U10-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	49.11
Std Dev	3.264

Entry

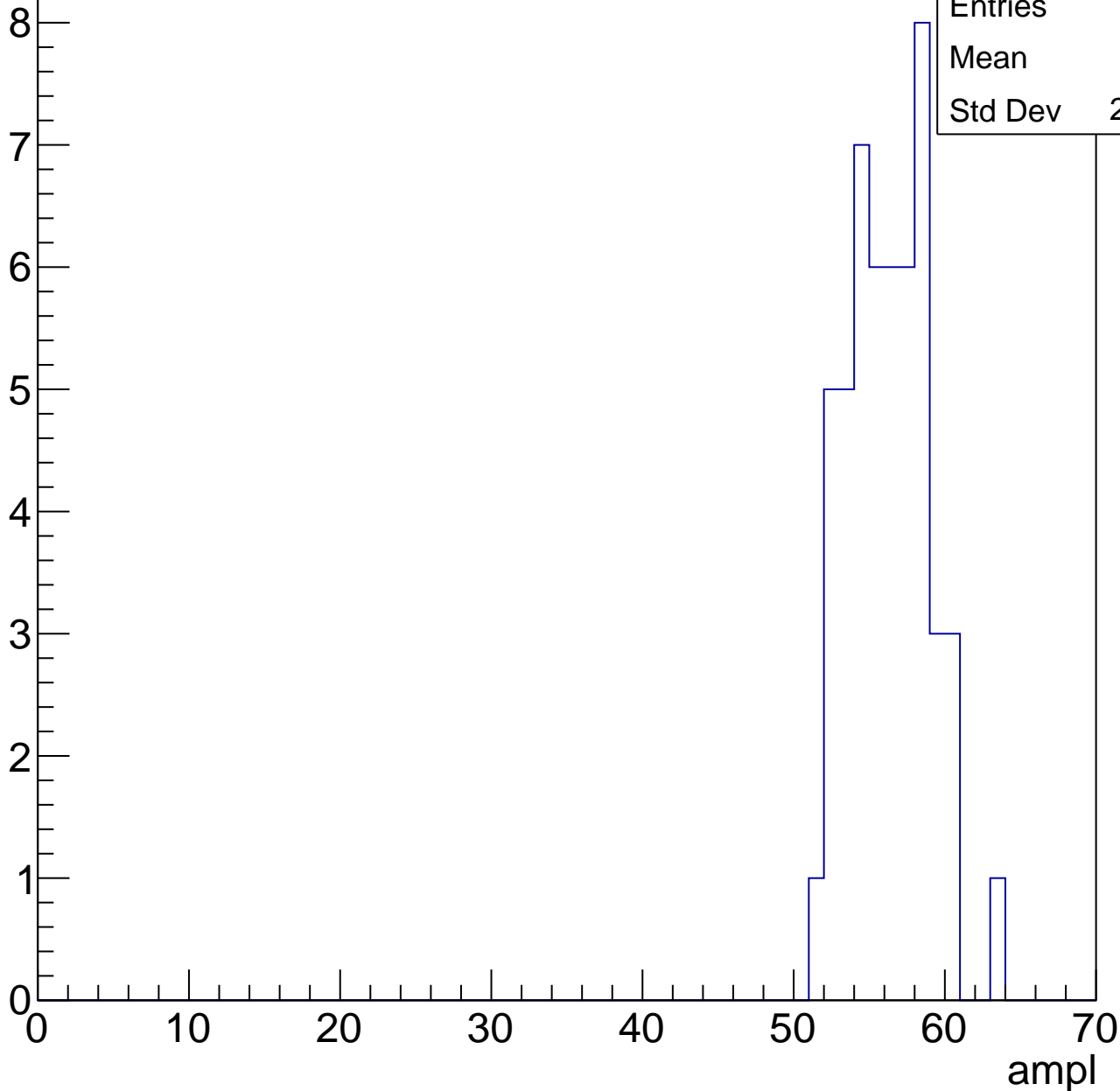


B1L102S, U10-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.8
Std Dev	2.597

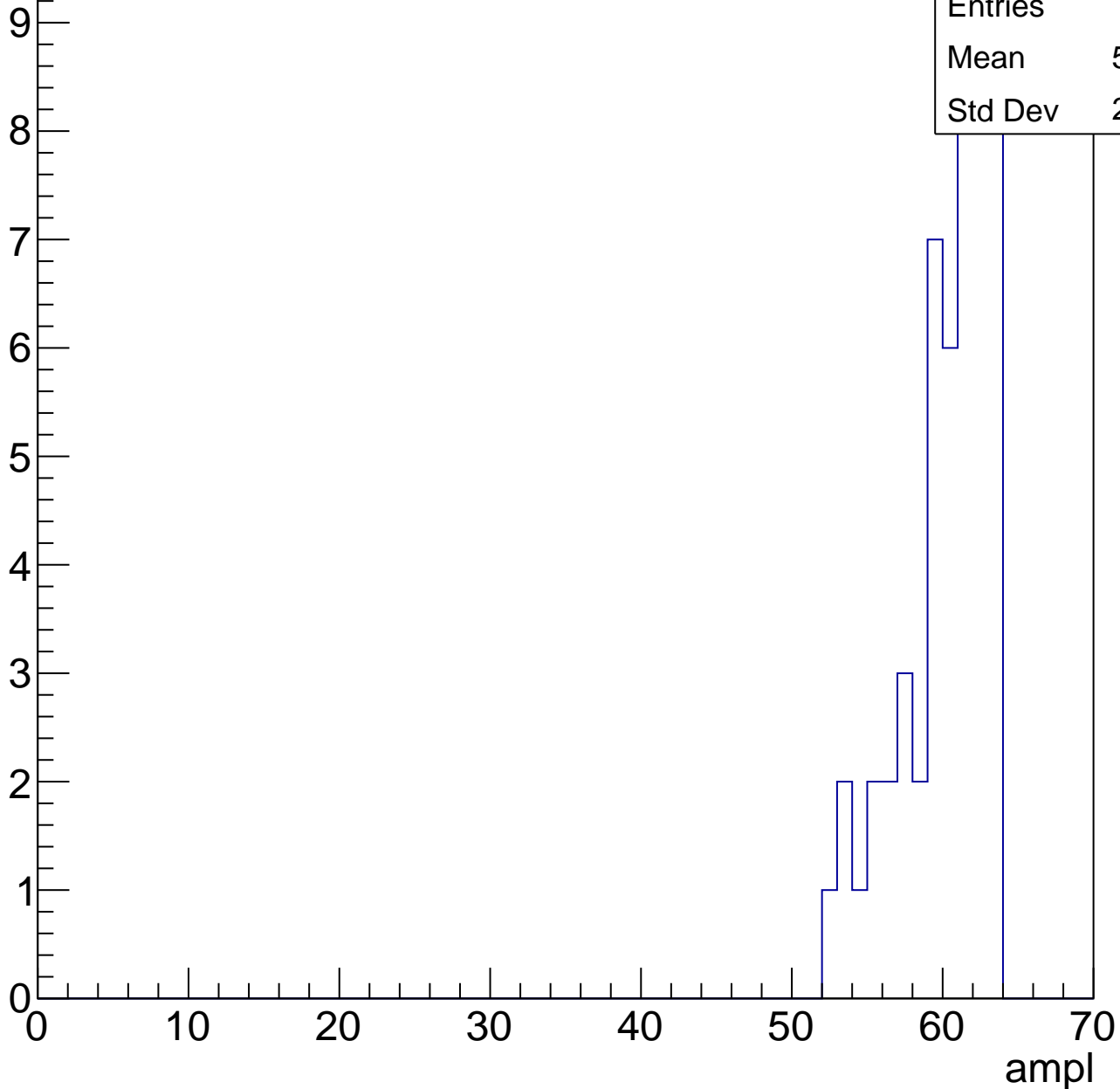


B1L102S, U10-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

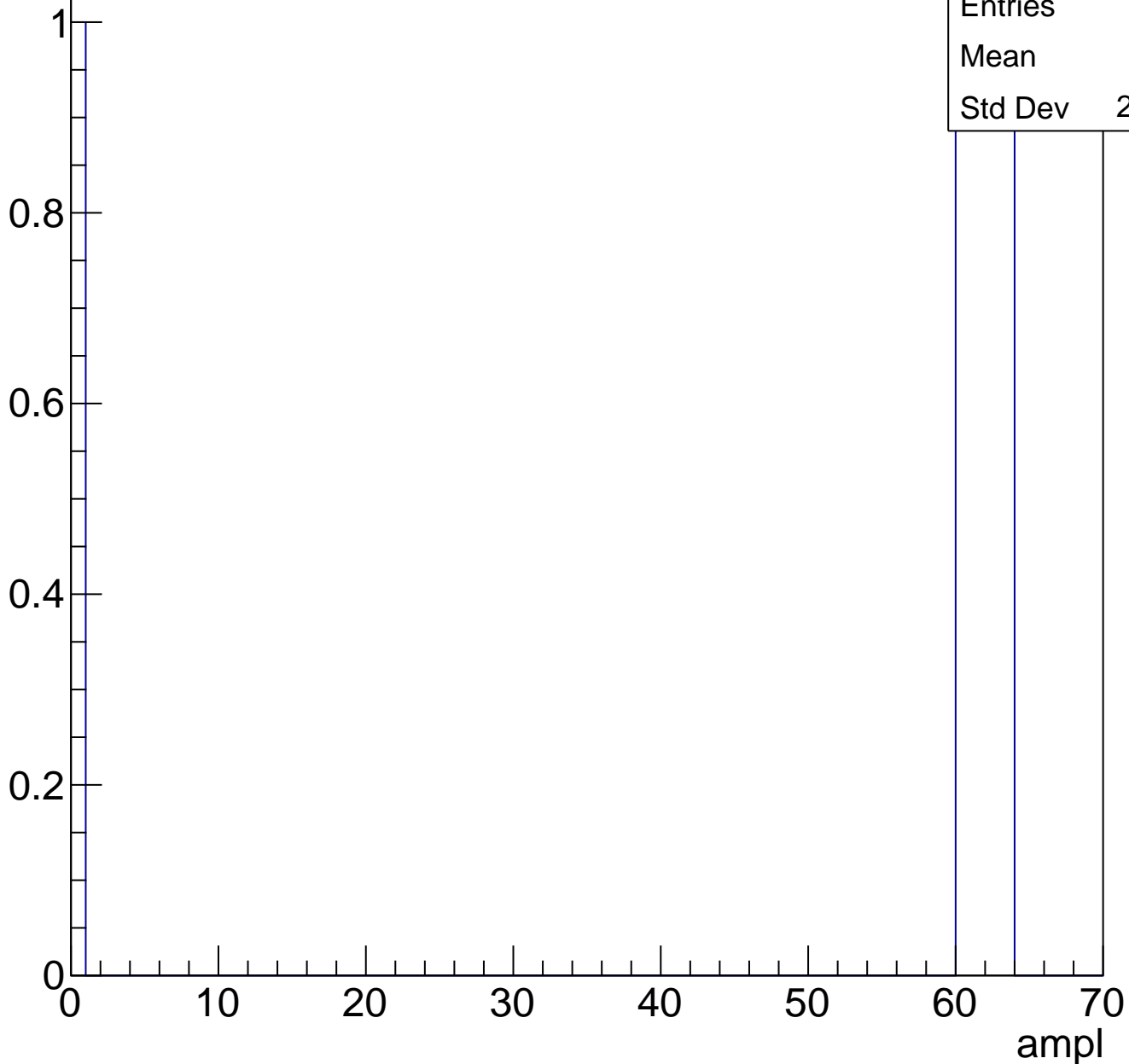
Entries	52
Mean	59.71
Std Dev	2.891



B1L102S, U10-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch72, adc0

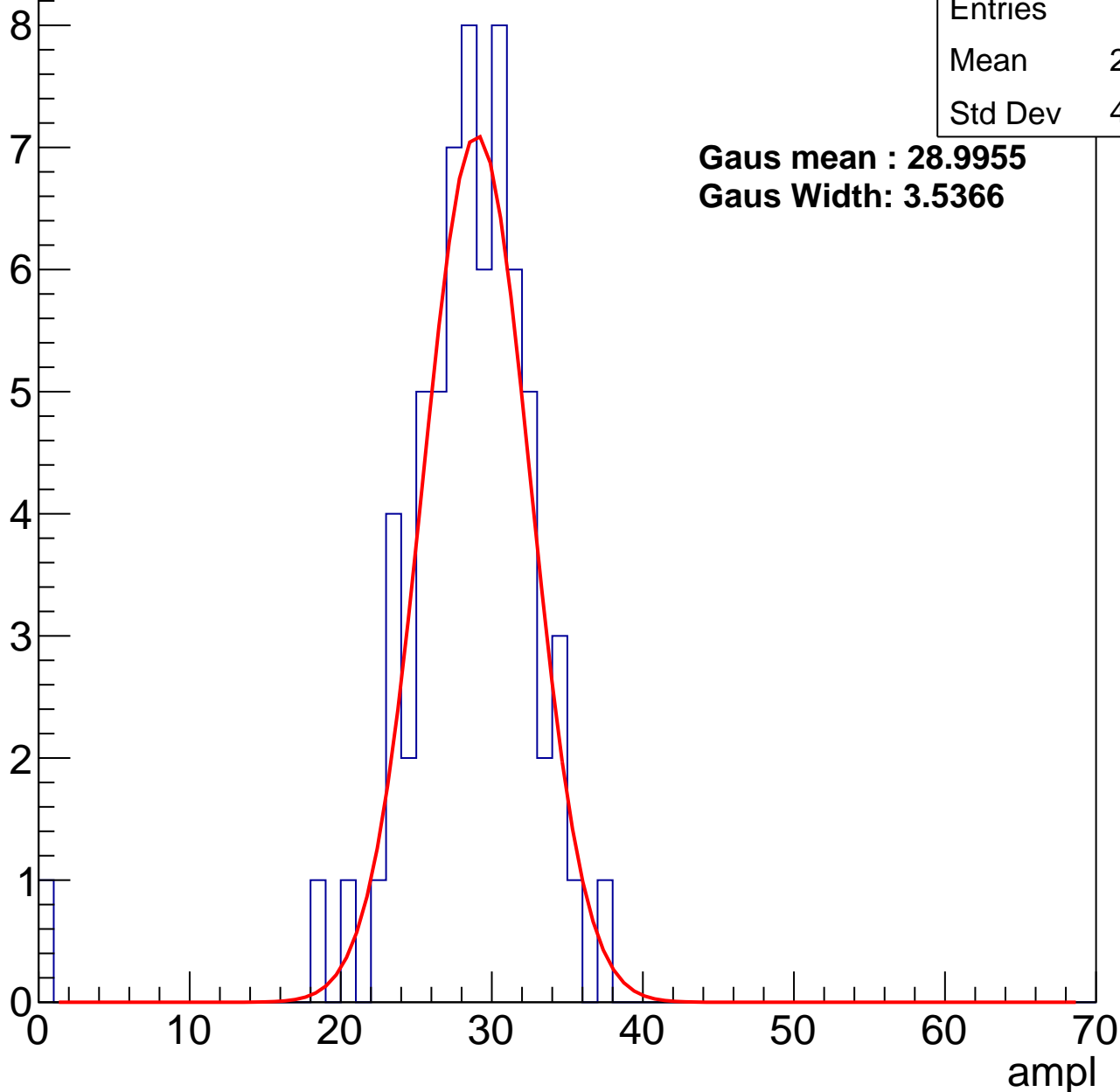
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.88
Std Dev	4.958

Gaus mean : 28.9955

Gaus Width: 3.5366



B1L102S, U10-ch72, adc1

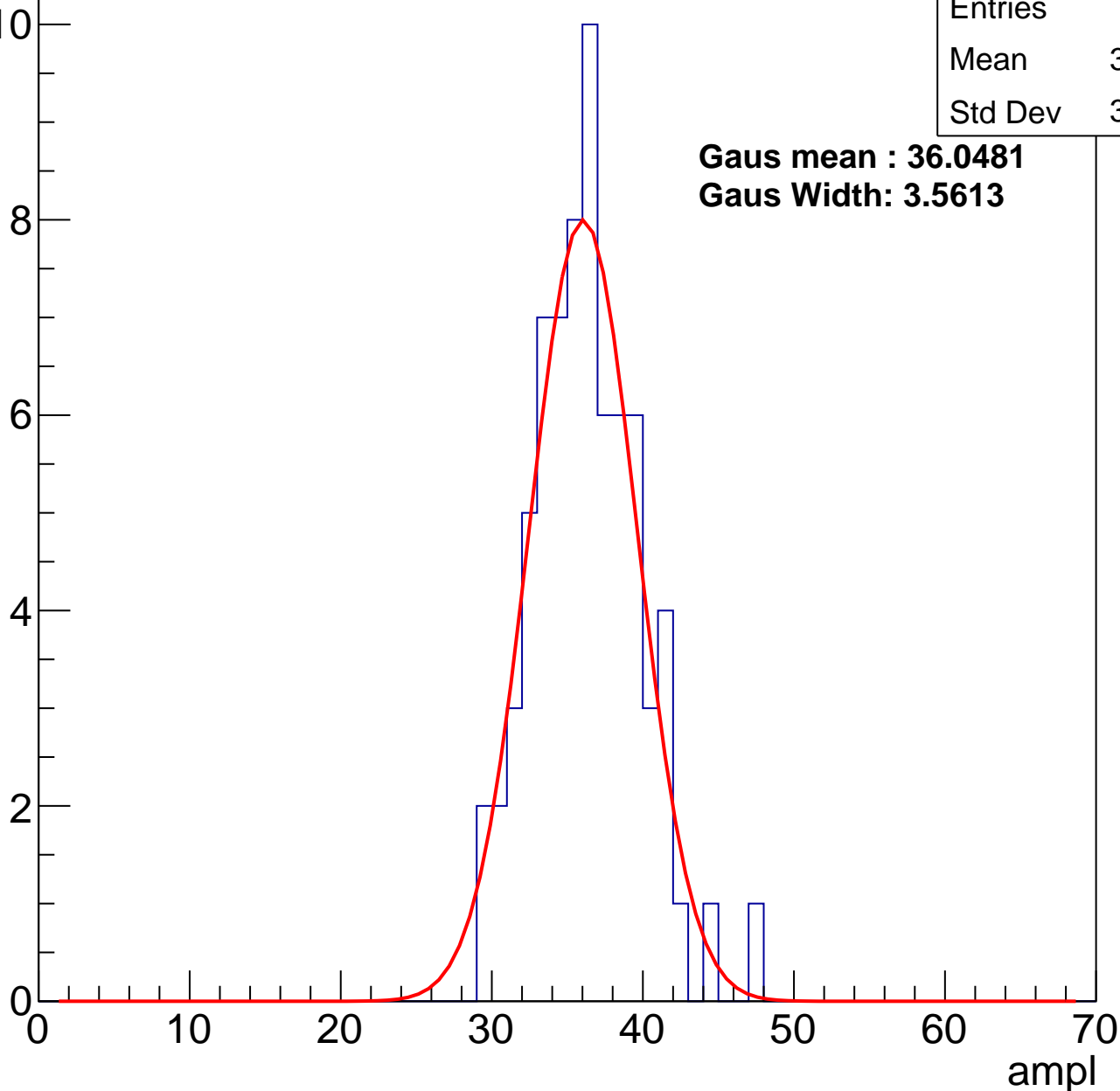
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.85
Std Dev	3.487

Gaus mean : 36.0481

Gaus Width: 3.5613

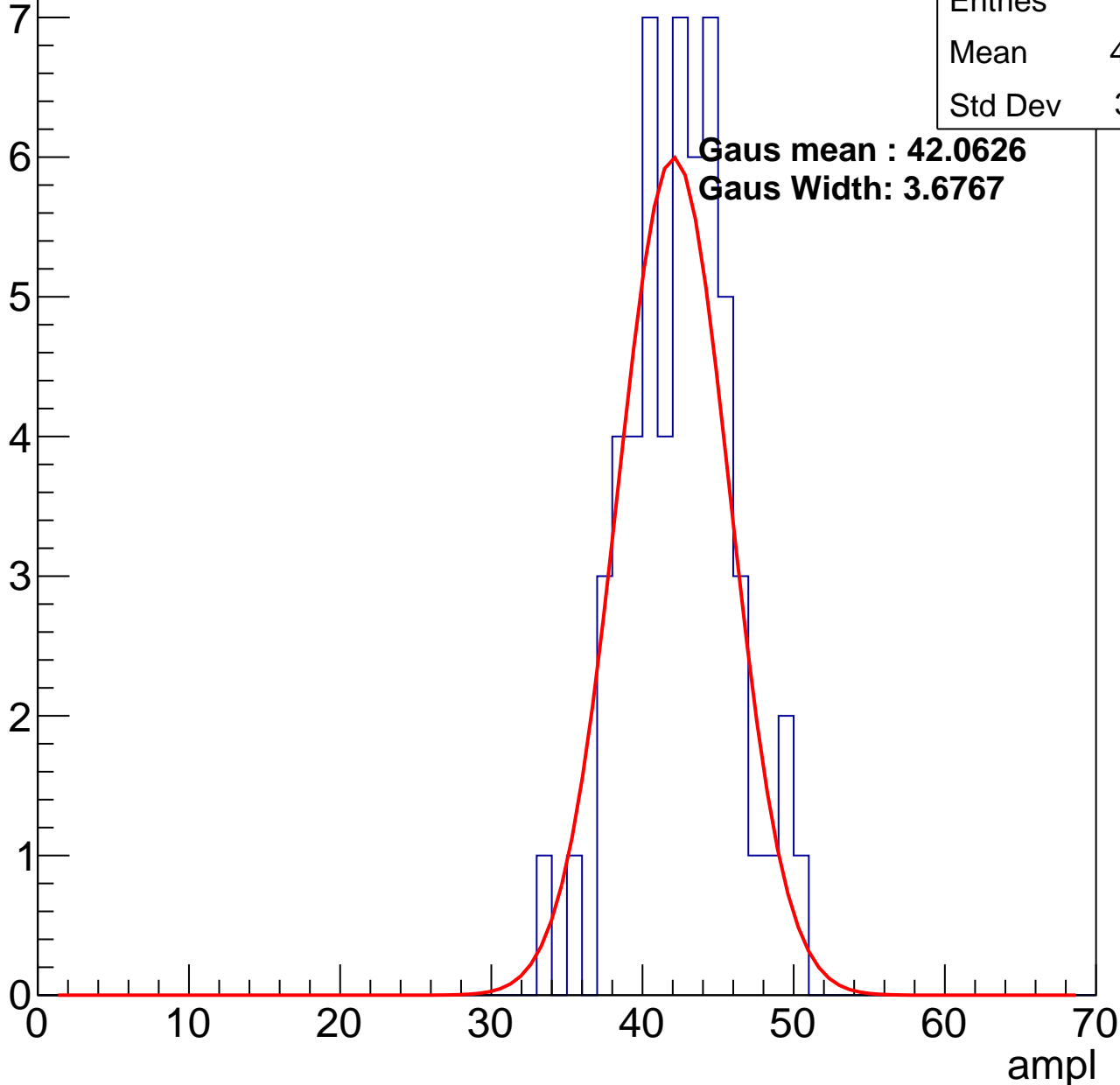


B1L102S, U10-ch72, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

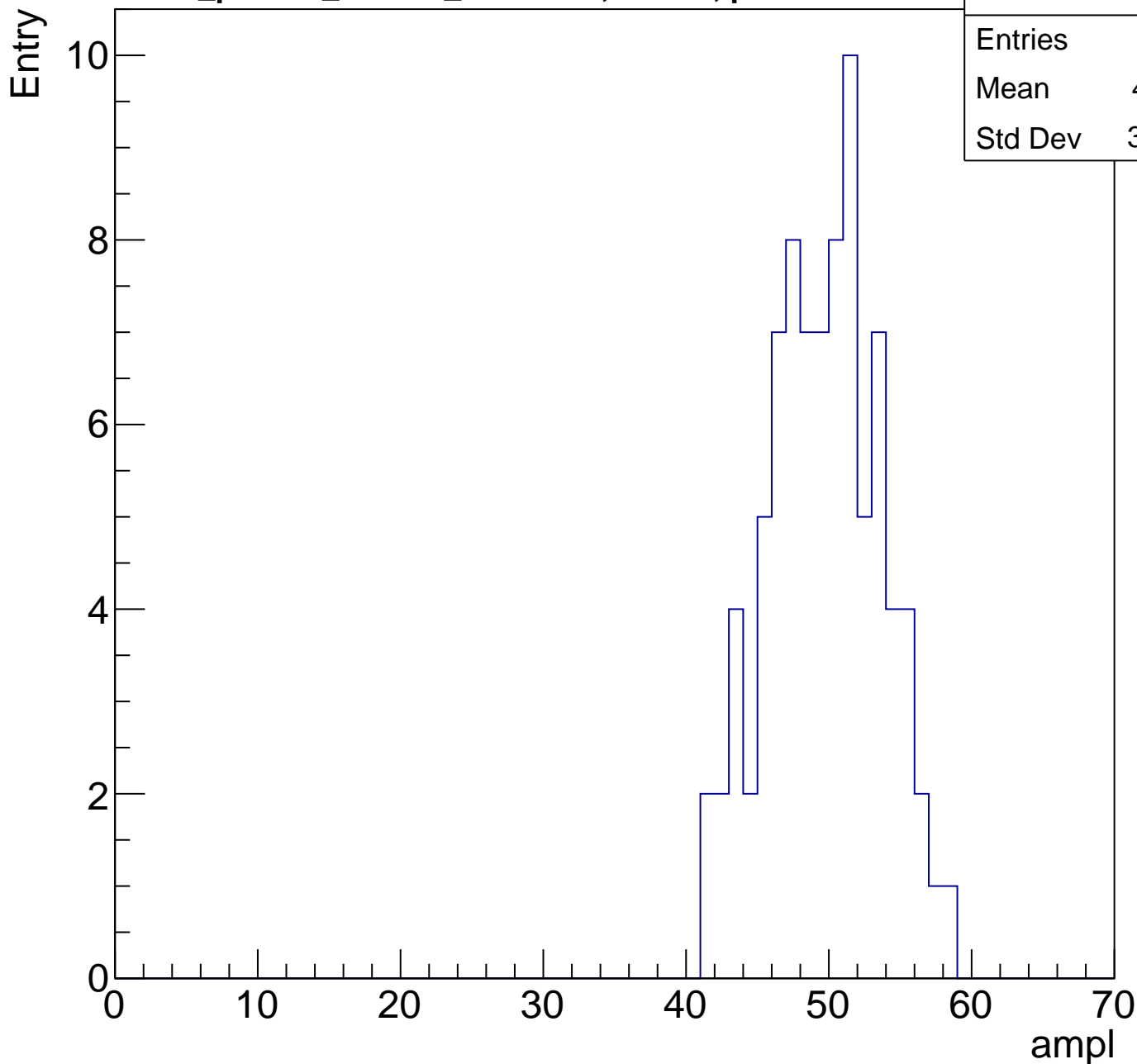
Entries	57
Mean	42.05
Std Dev	3.471



B1L102S, U10-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	49.21
Std Dev	3.873

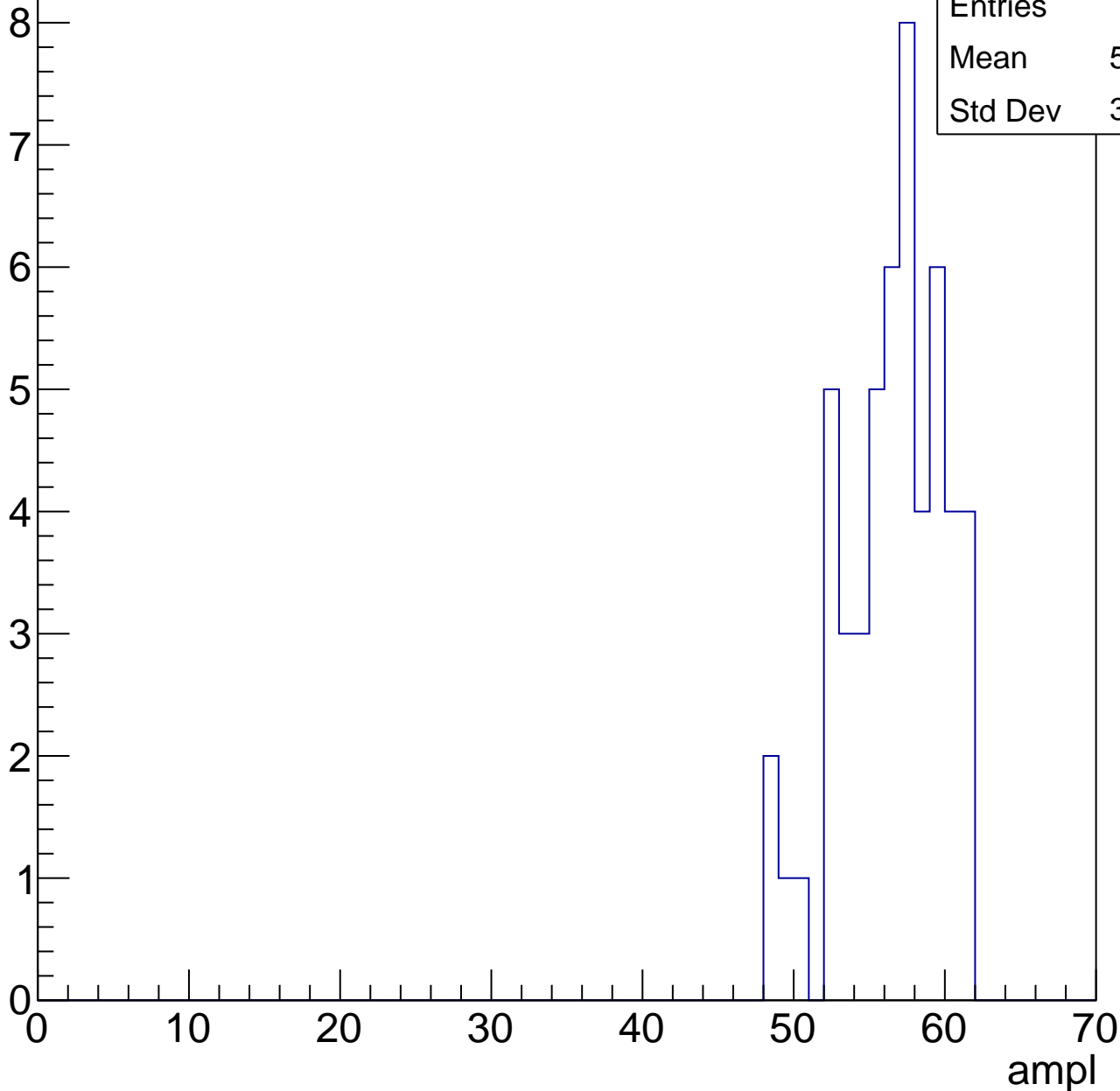


B1L102S, U10-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.02
Std Dev	3.337

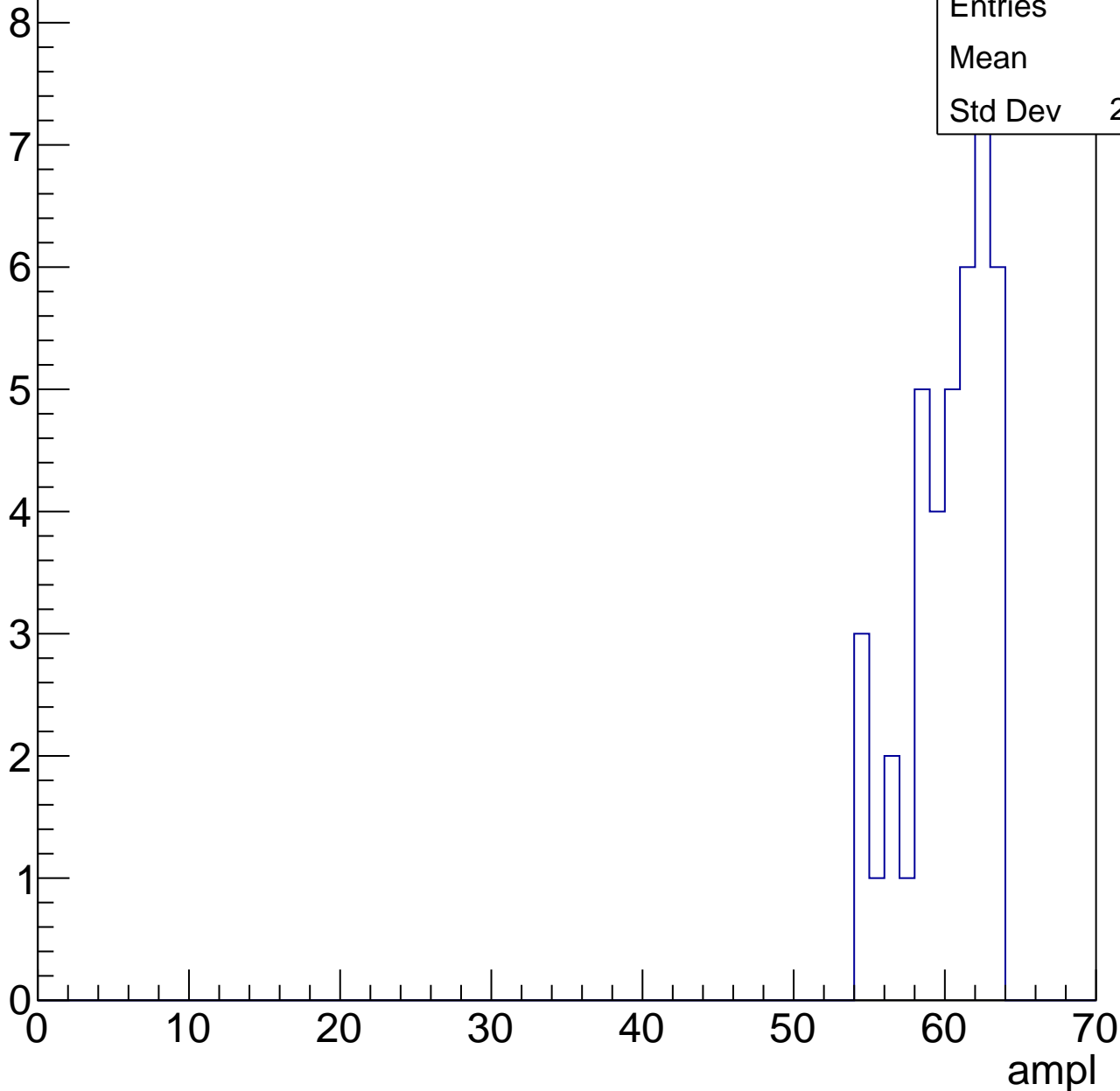


B1L102S, U10-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

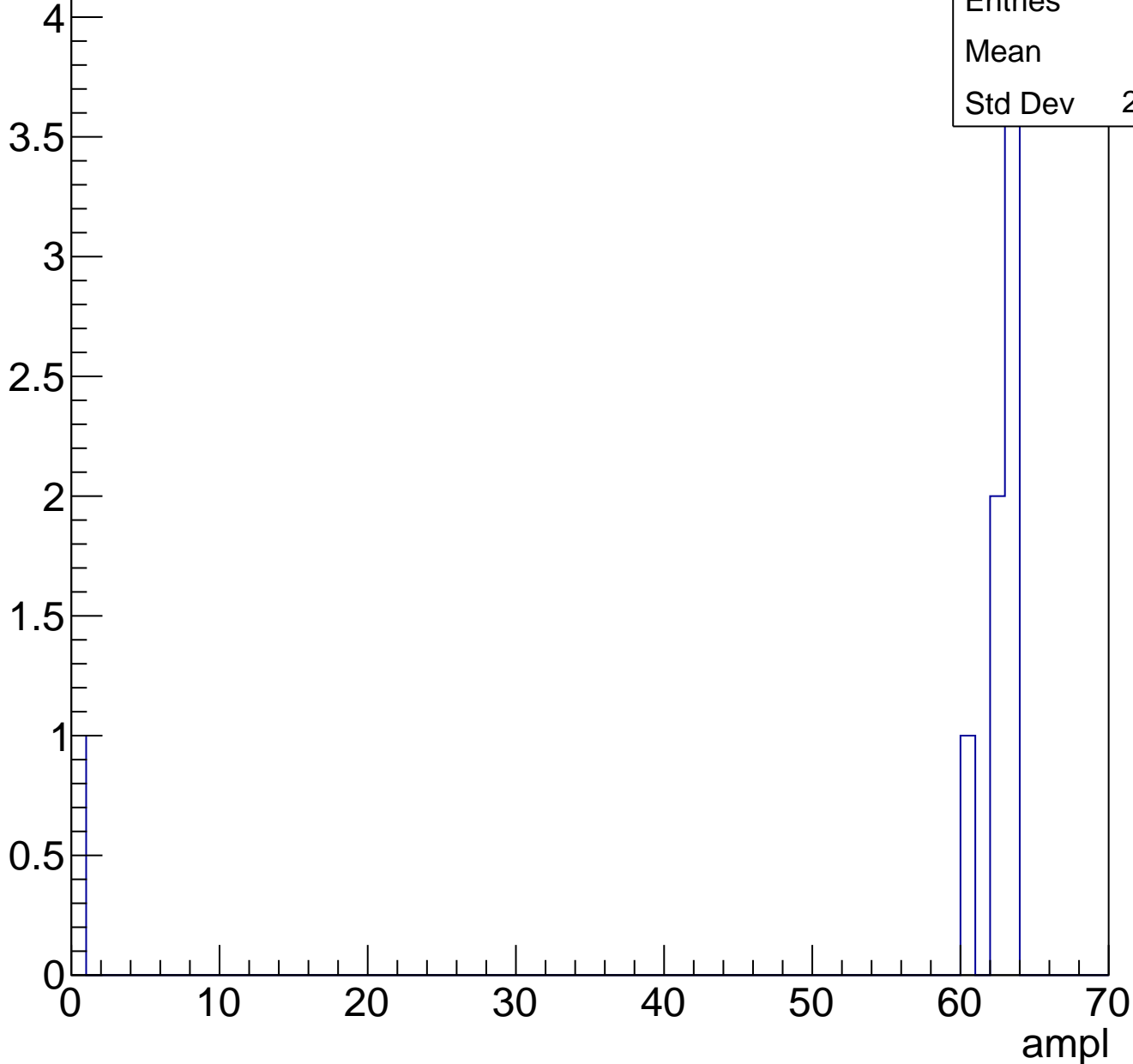
Entries	41
Mean	59.8
Std Dev	2.652



B1L102S, U10-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch73, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	29.23
Std Dev	4.804

Gaus mean : 29.9828

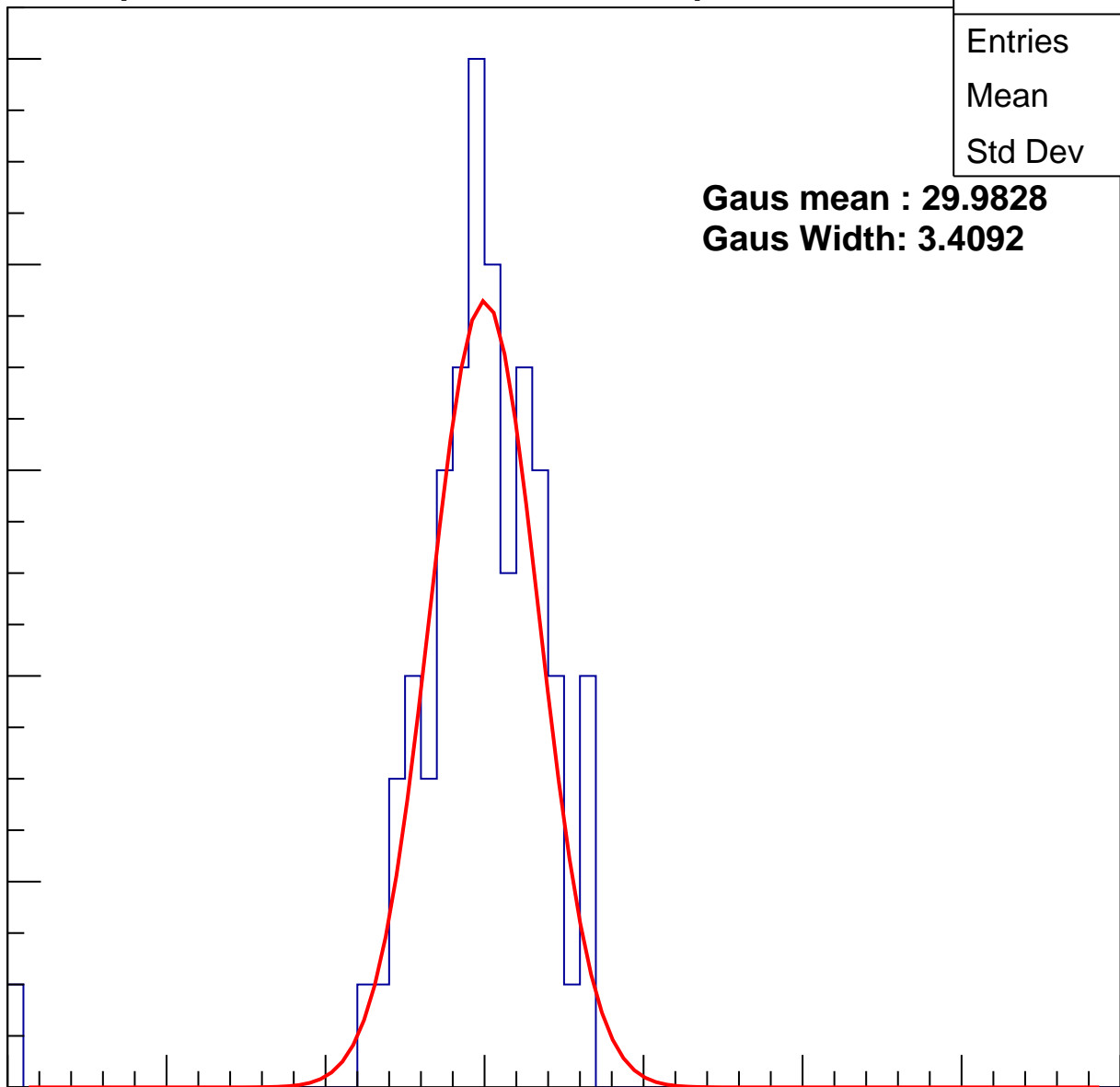
Gaus Width: 3.4092

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch73, adc1

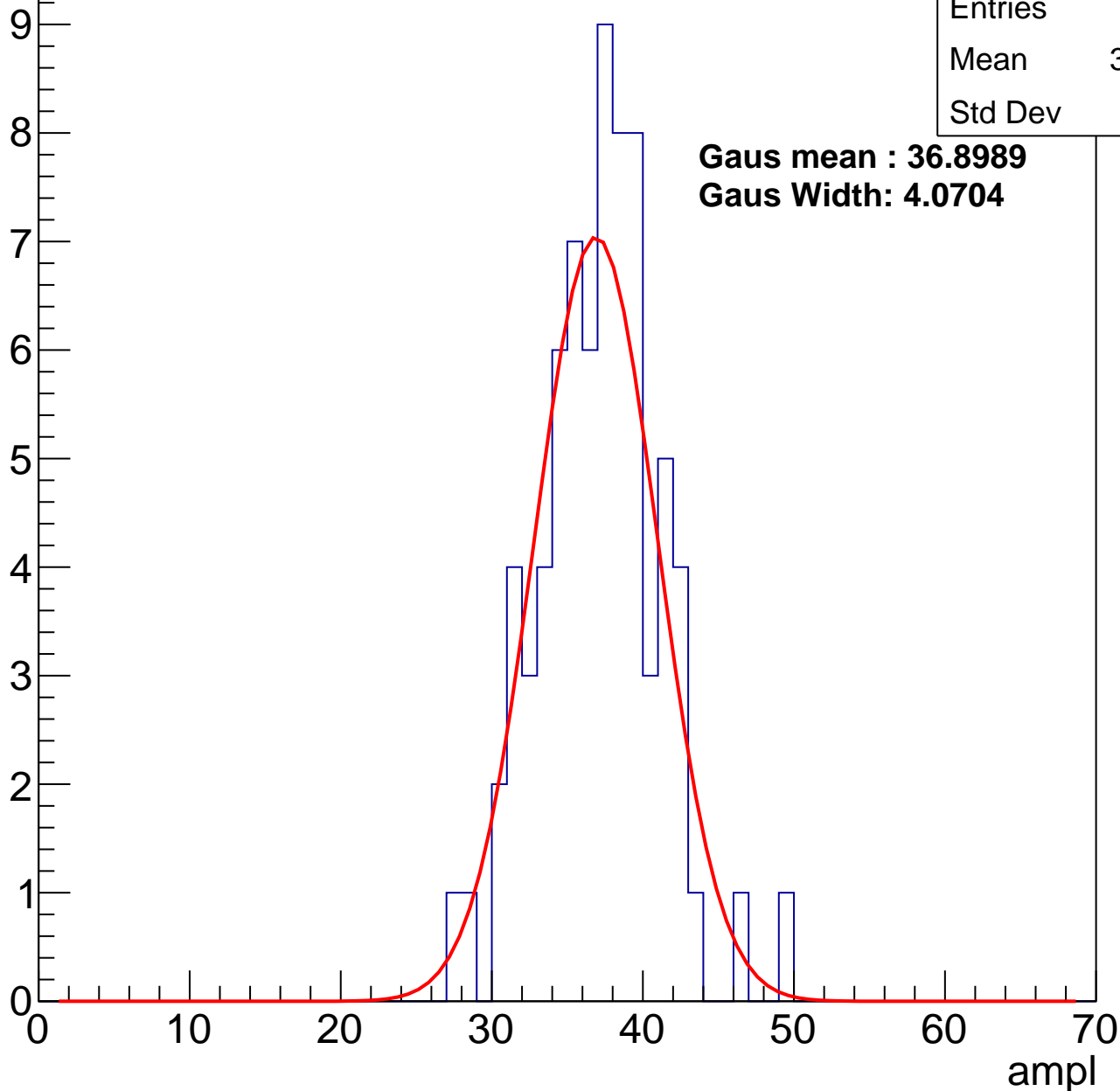
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.65
Std Dev	3.94

Gaus mean : 36.8989

Gaus Width: 4.0704



B1L102S, U10-ch73, adc2

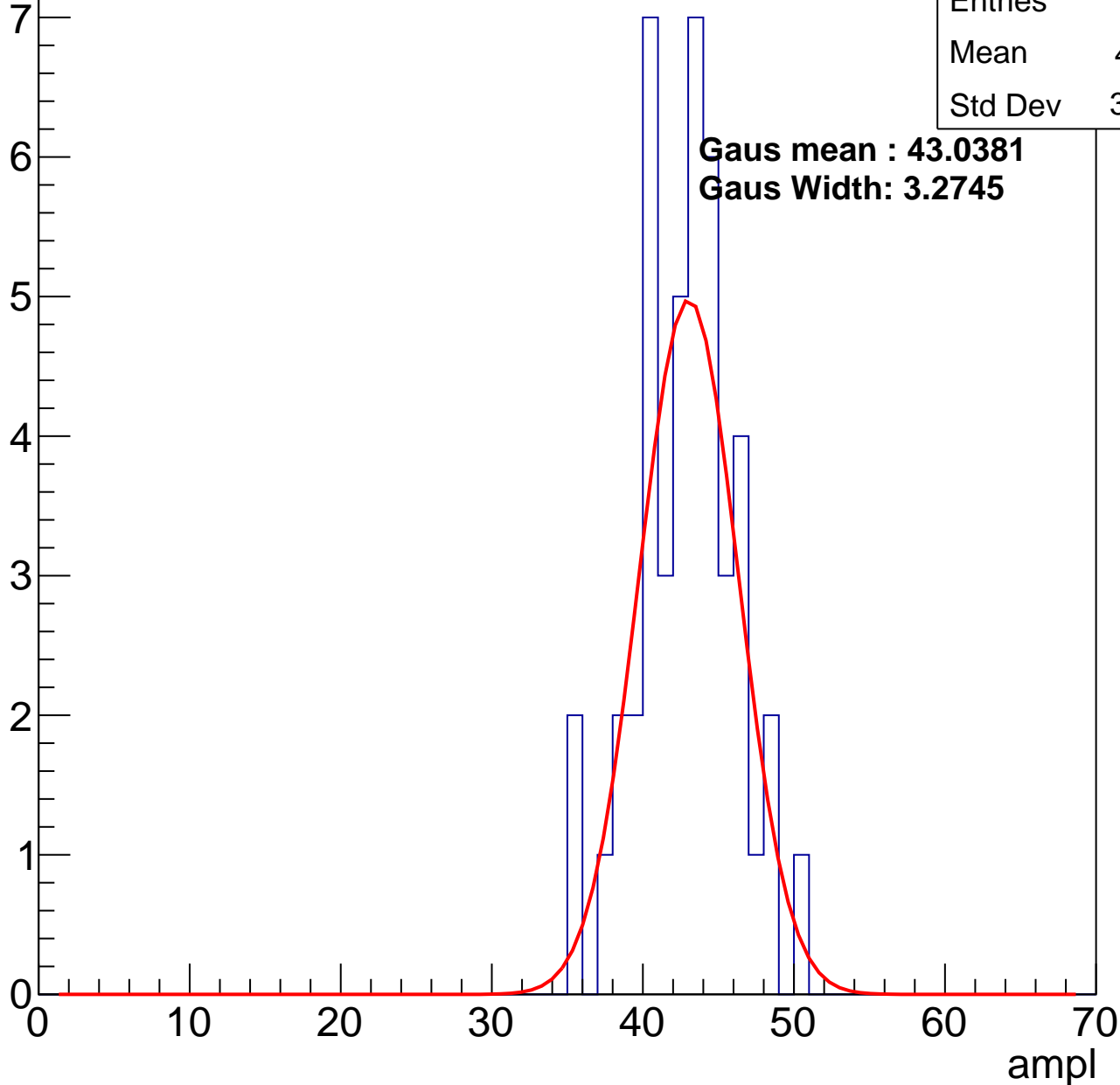
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	42.41
Std Dev	3.227

Gaus mean : 43.0381

Gaus Width: 3.2745

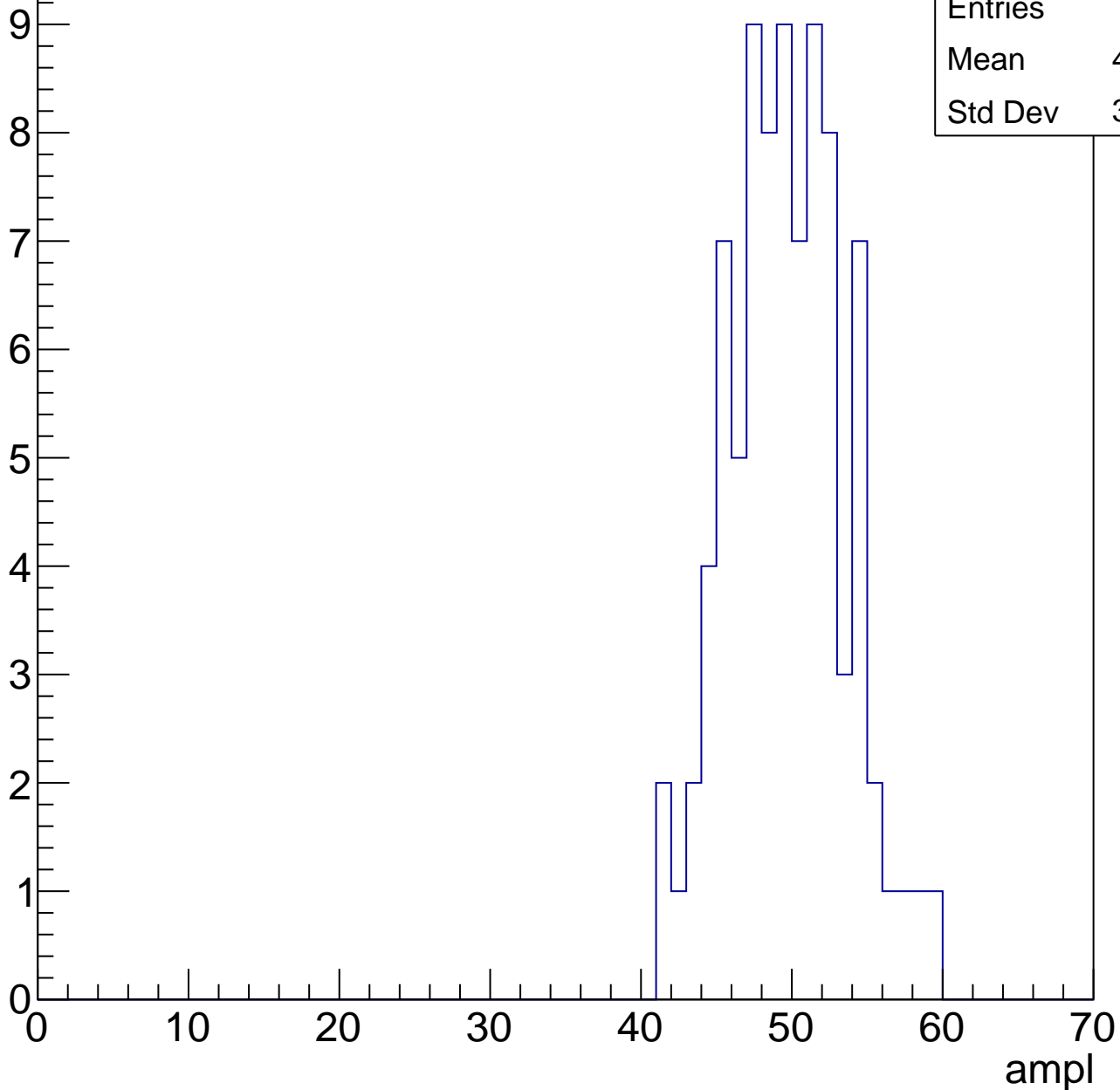


B1L102S, U10-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	49.21
Std Dev	3.791



B1L102S, U10-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

6

5

4

3

2

1

0

Entries	44
Mean	55.86
Std Dev	2.857

ampl

0

10

20

30

40

50

60

70

B1L102S, U10-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	53
Mean	59.96
Std Dev	2.562

Entry

10

8

6

4

2

0

0

10

20

30

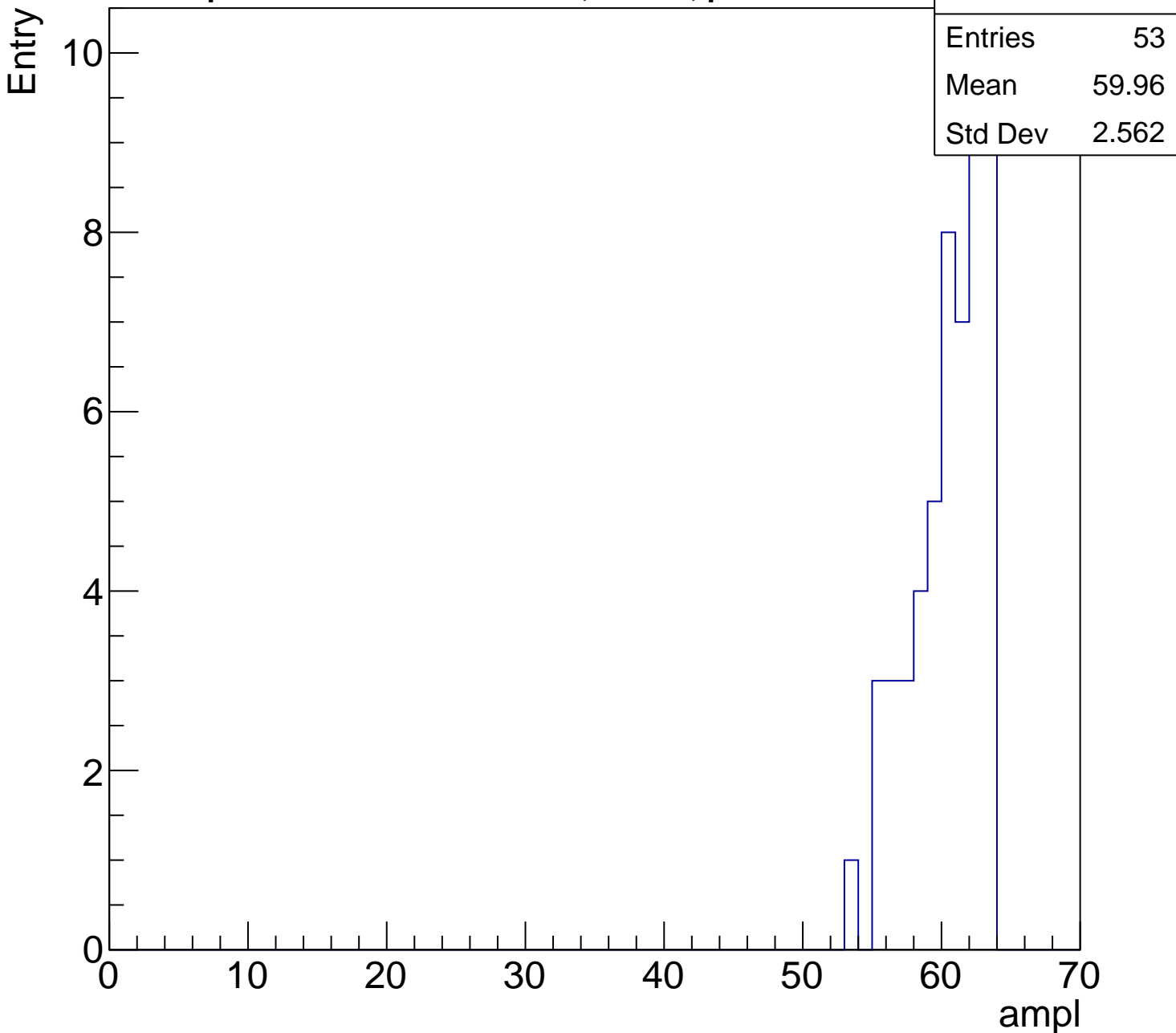
40

50

ampl

60

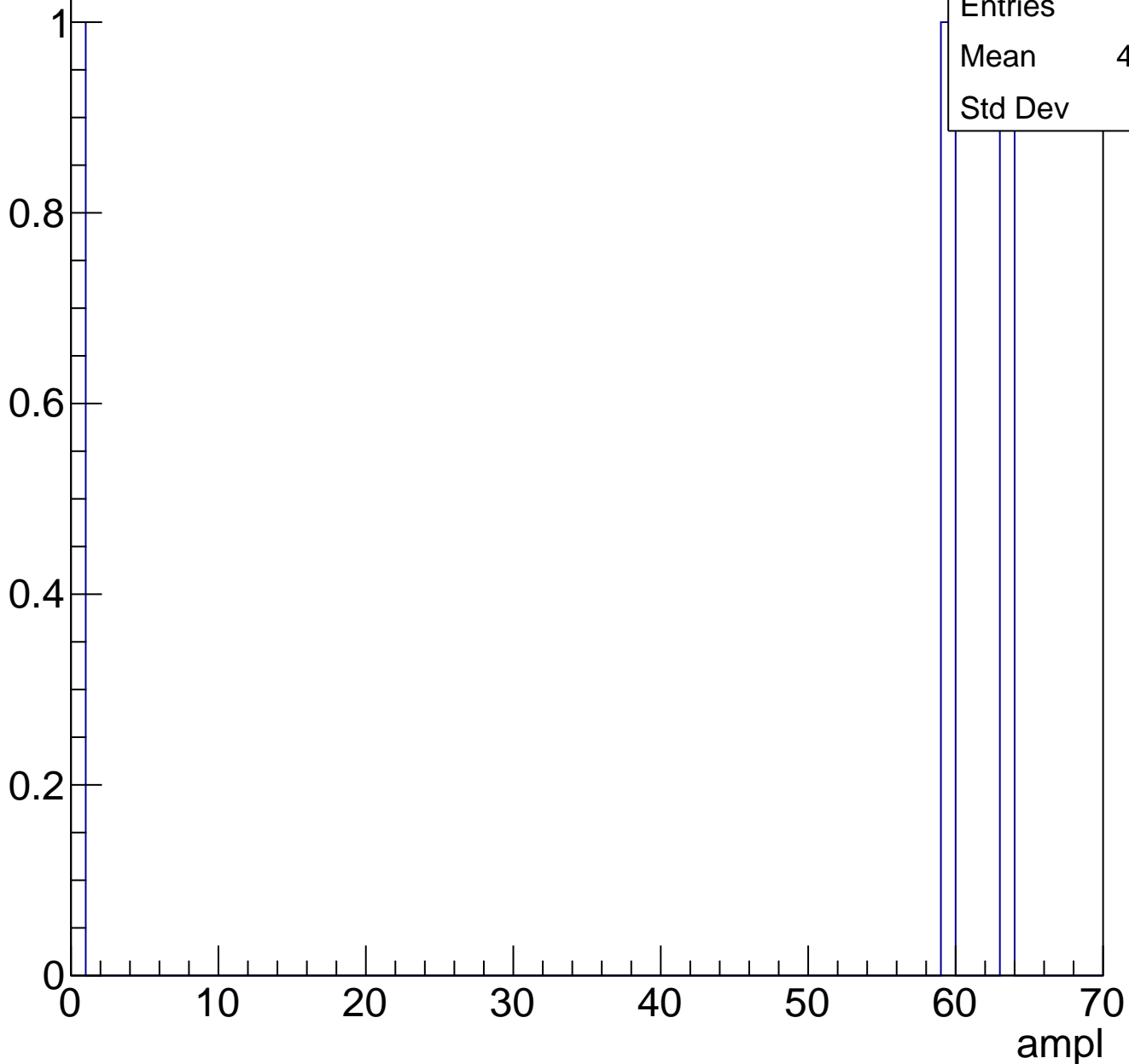
70



B1L102S, U10-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

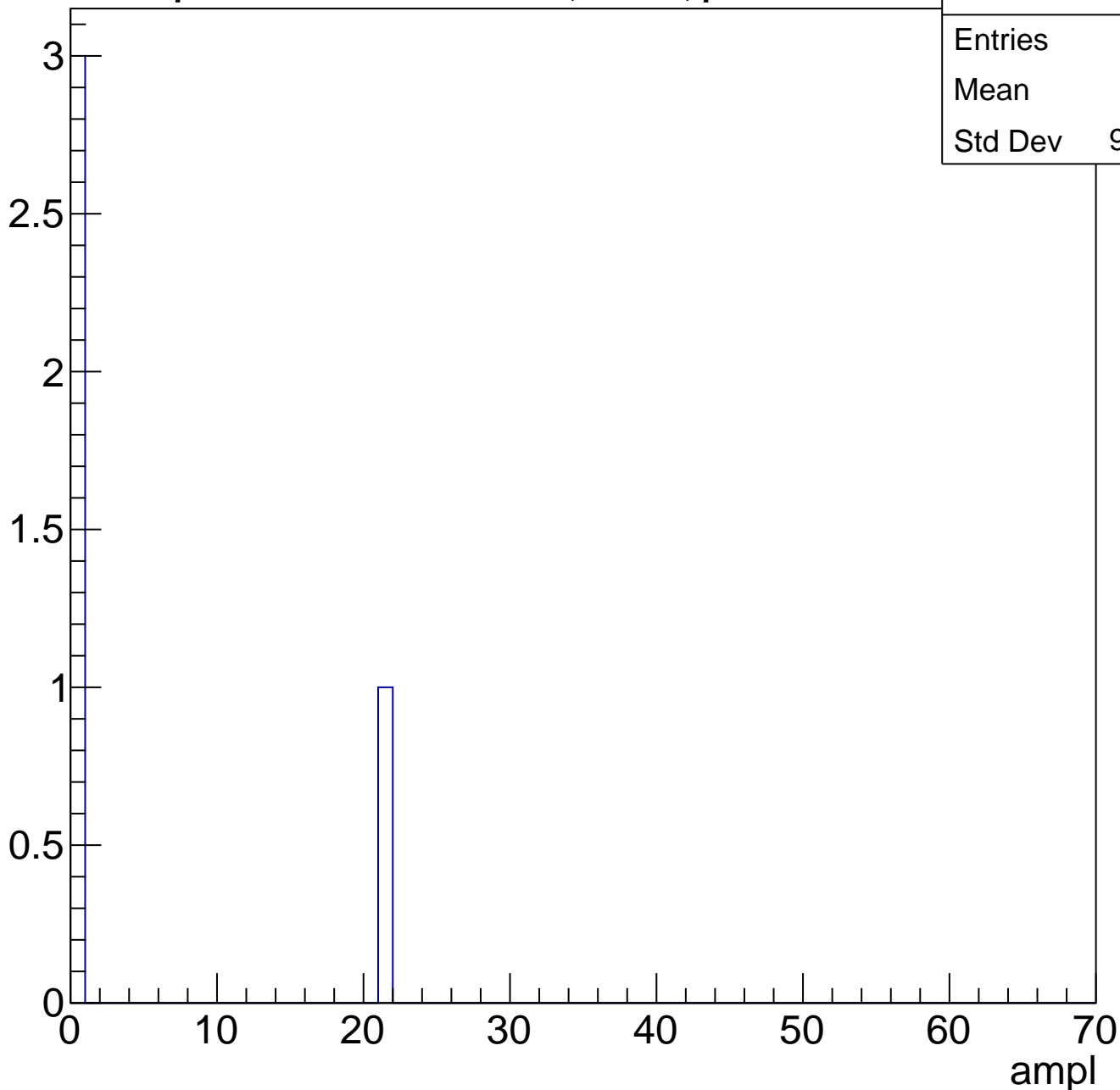
Entry



B1L102S, U10-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L102S, U10-ch74, adc0

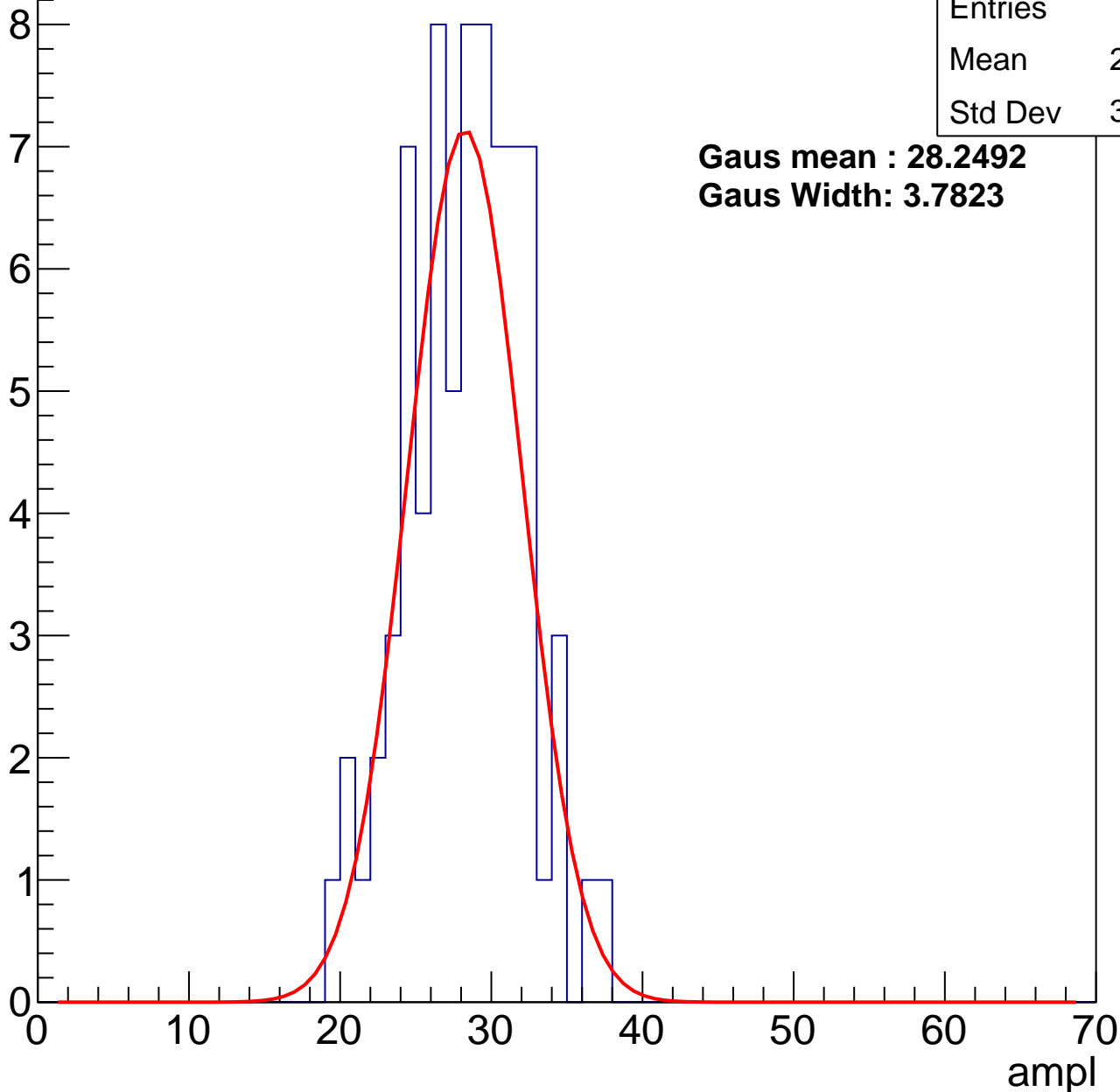
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.88
Std Dev	3.766

Gaus mean : 28.2492

Gaus Width: 3.7823



B1L102S, U10-ch74, adc1

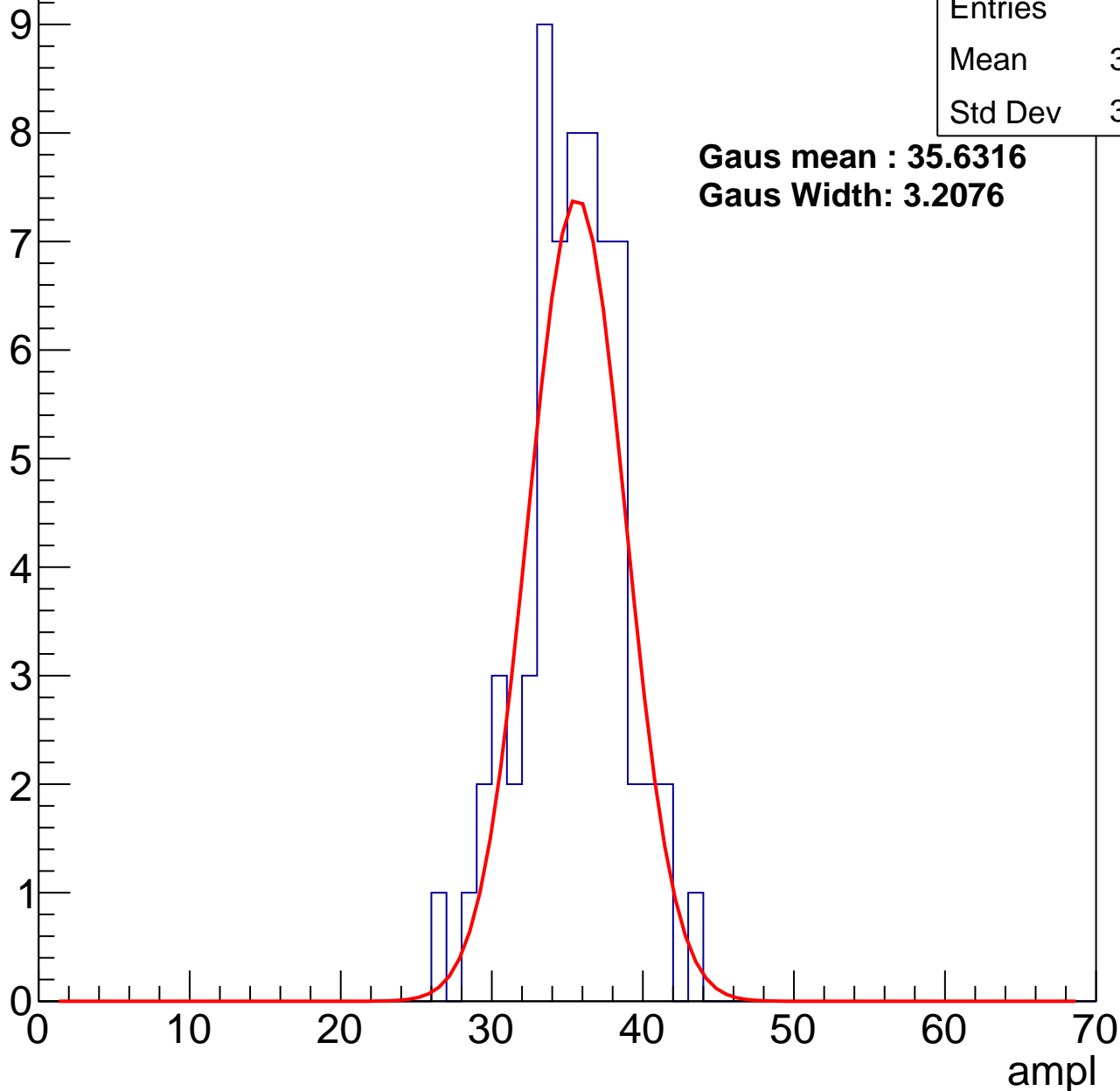
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.94
Std Dev	3.272

Gaus mean : 35.6316

Gaus Width: 3.2076



B1L102S, U10-ch74, adc2

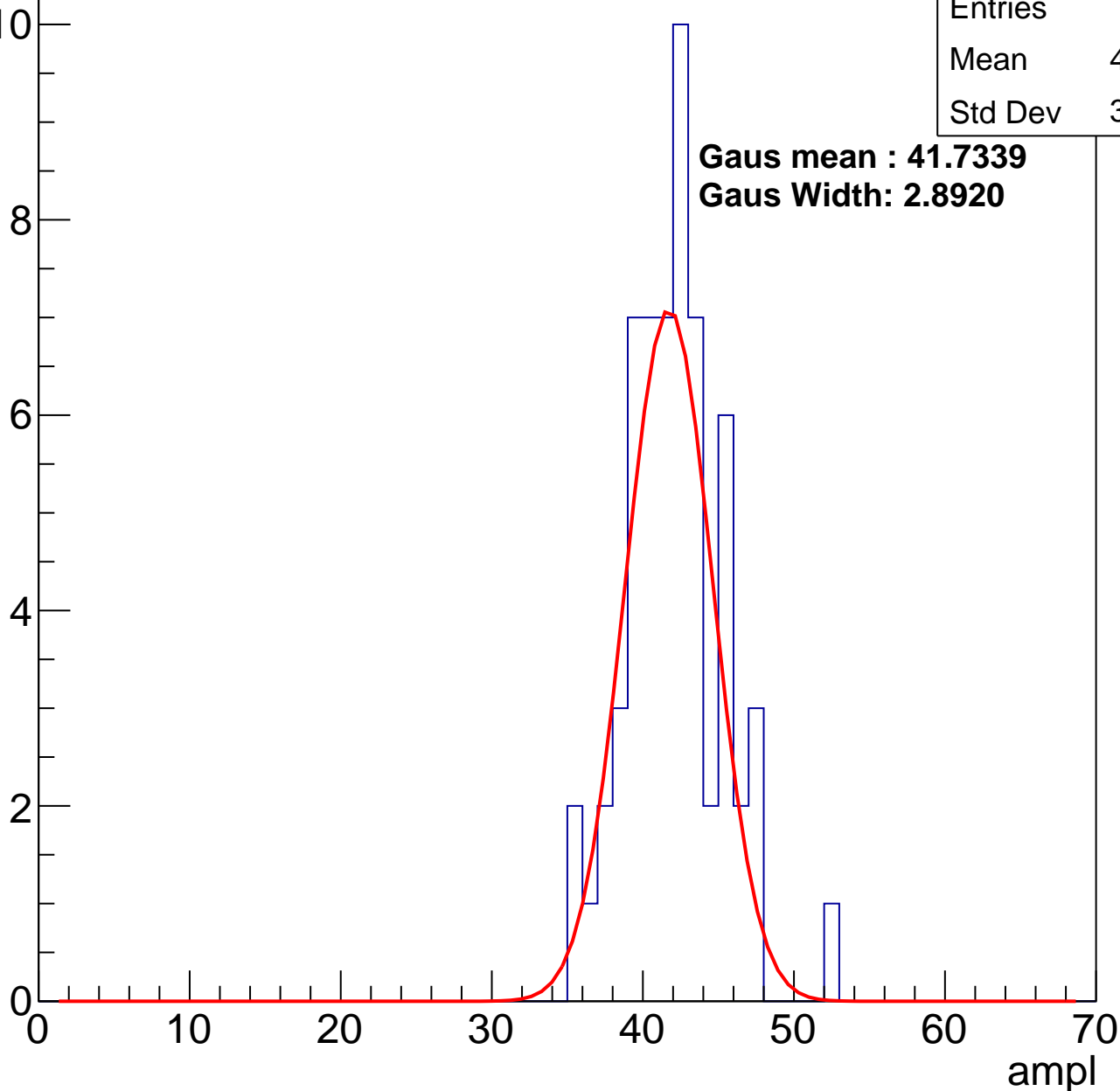
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.63
Std Dev	3.157

Gaus mean : 41.7339

Gaus Width: 2.8920



B1L102S, U10-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

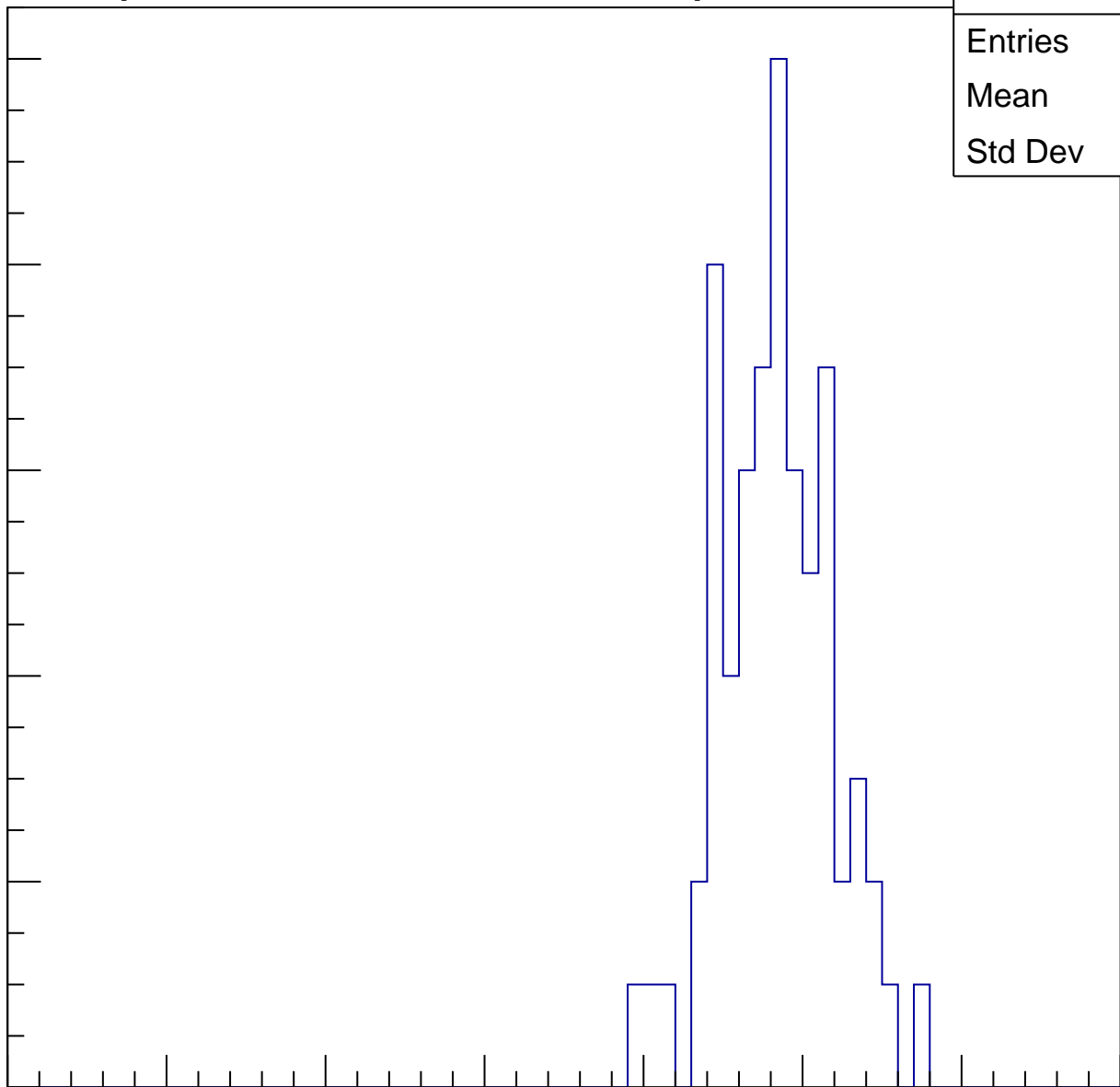
Entries	67
Mean	47.87
Std Dev	3.532

Entry

10
8
6
4
2
0

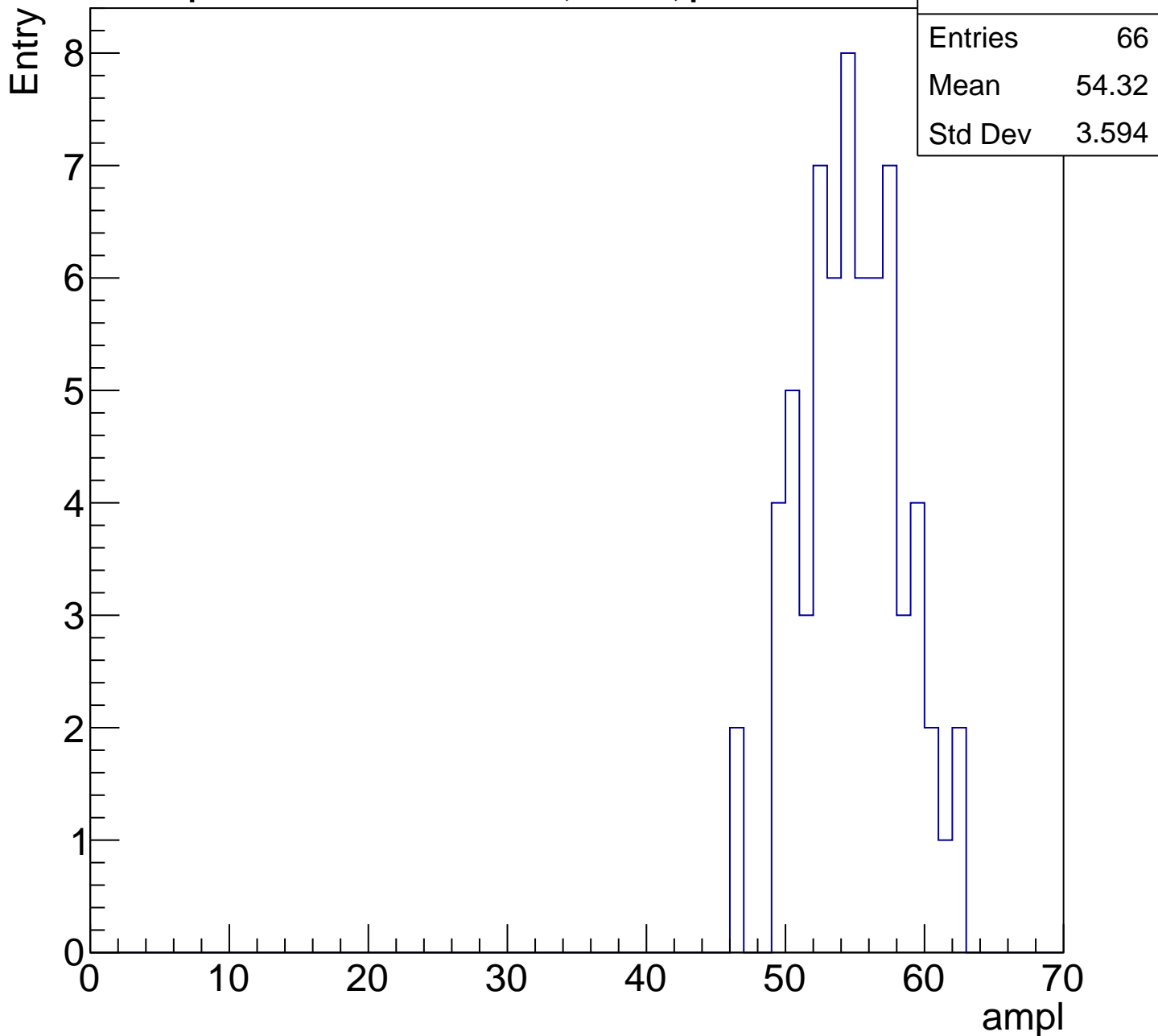
0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

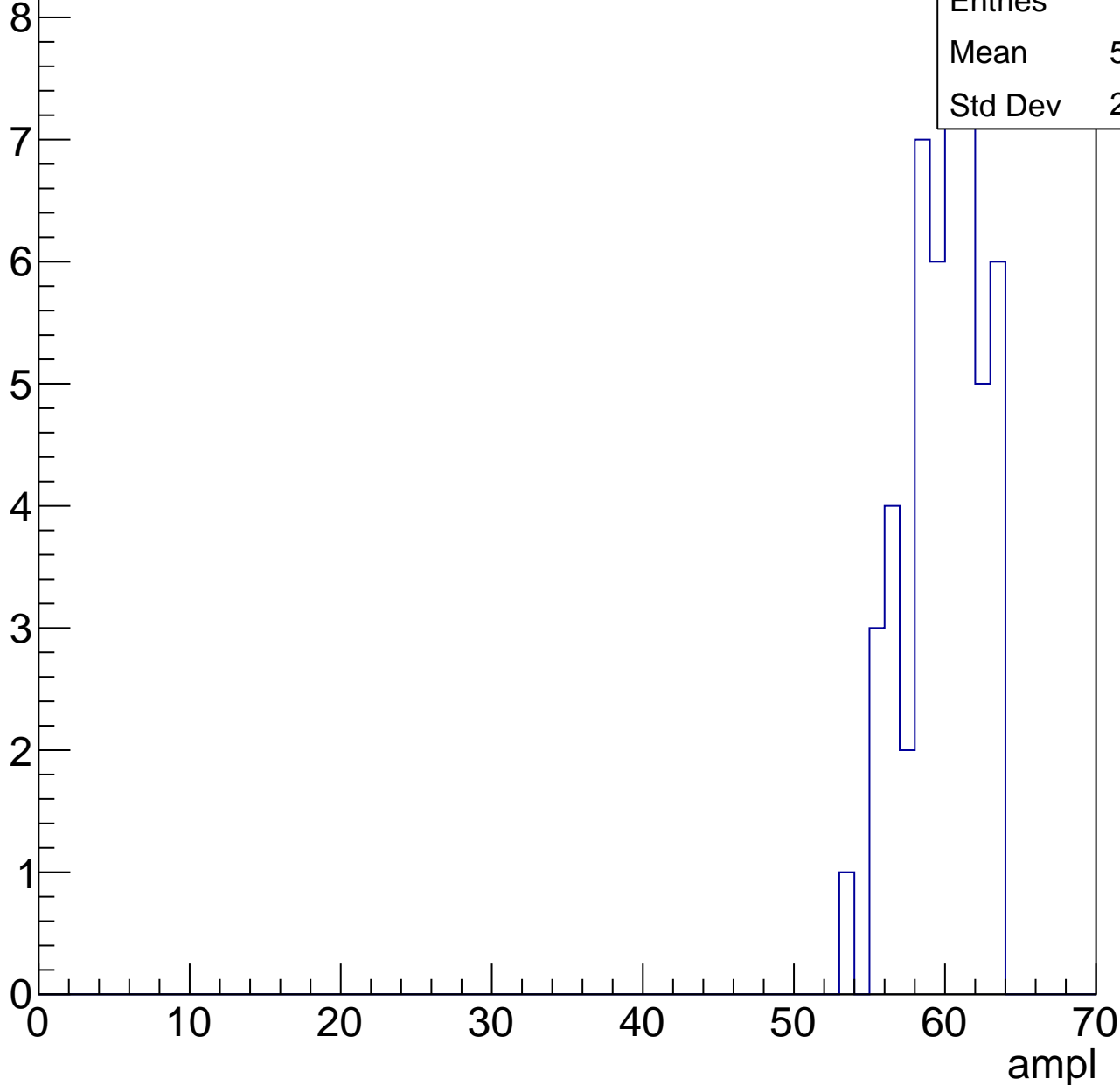


B1L102S, U10-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

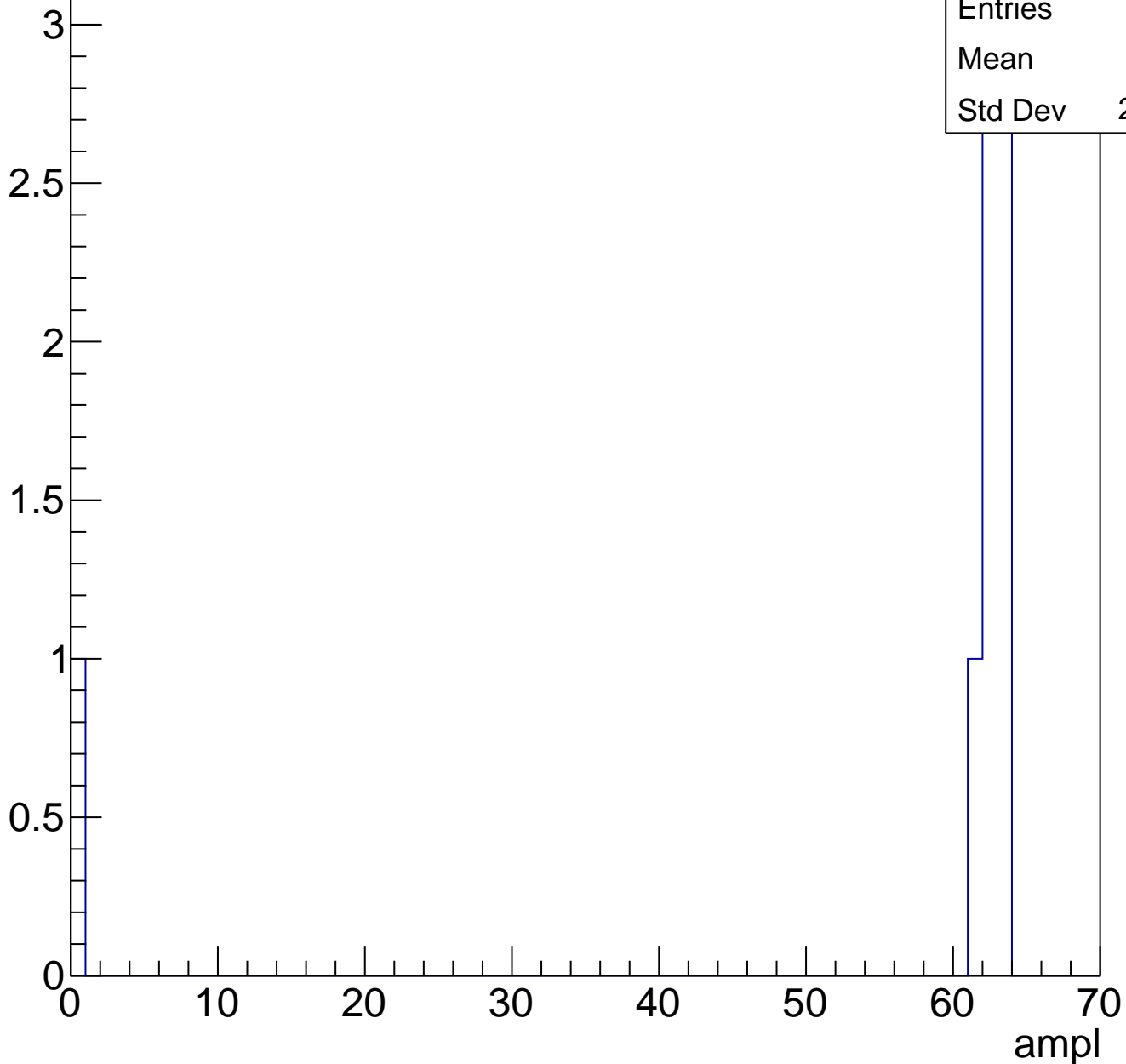
Entries	50
Mean	59.44
Std Dev	2.475



B1L102S, U10-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch75, adc0

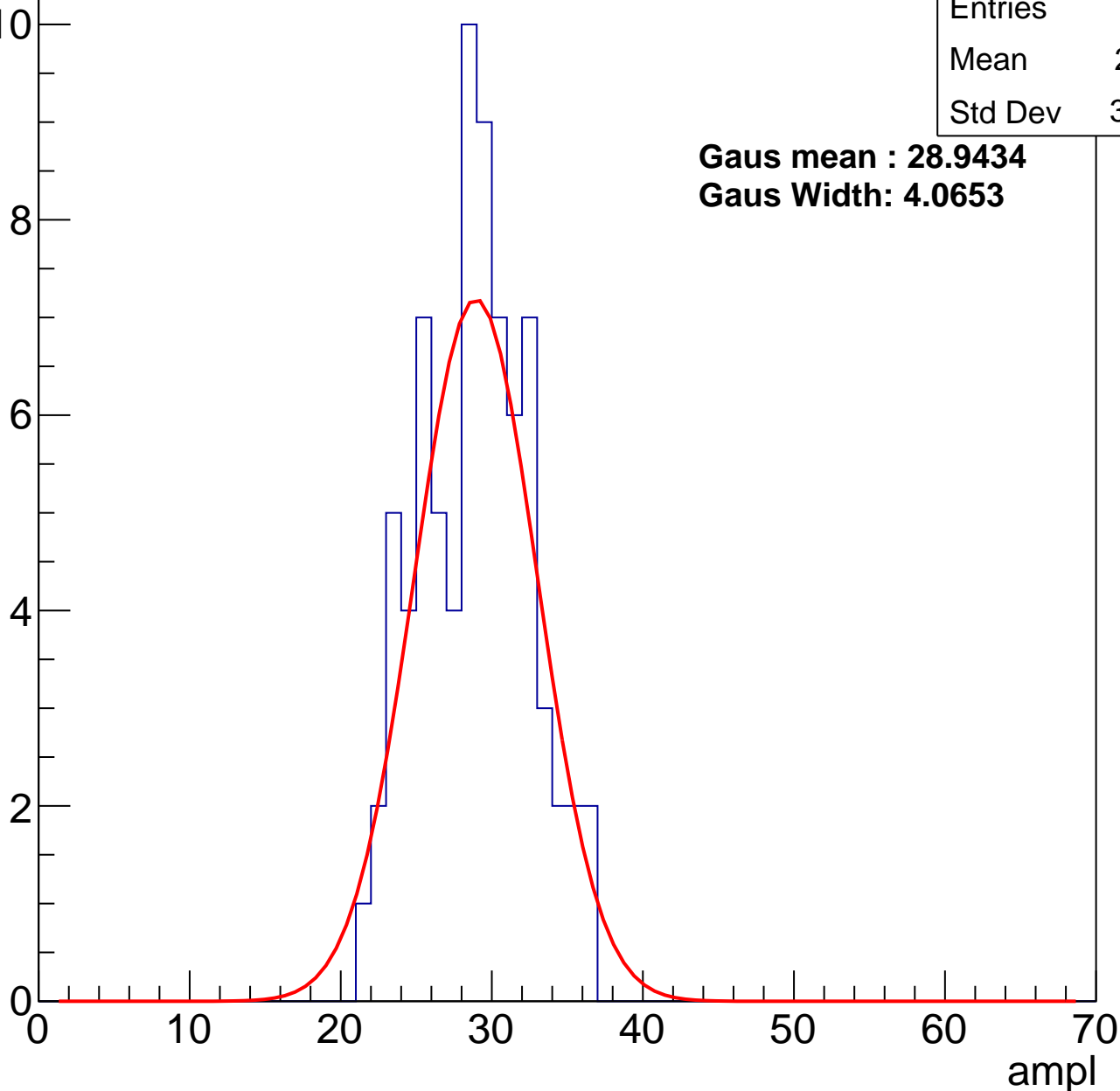
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.41
Std Dev	3.555

Gaus mean : 28.9434

Gaus Width: 4.0653



B1L102S, U10-ch75, adc1

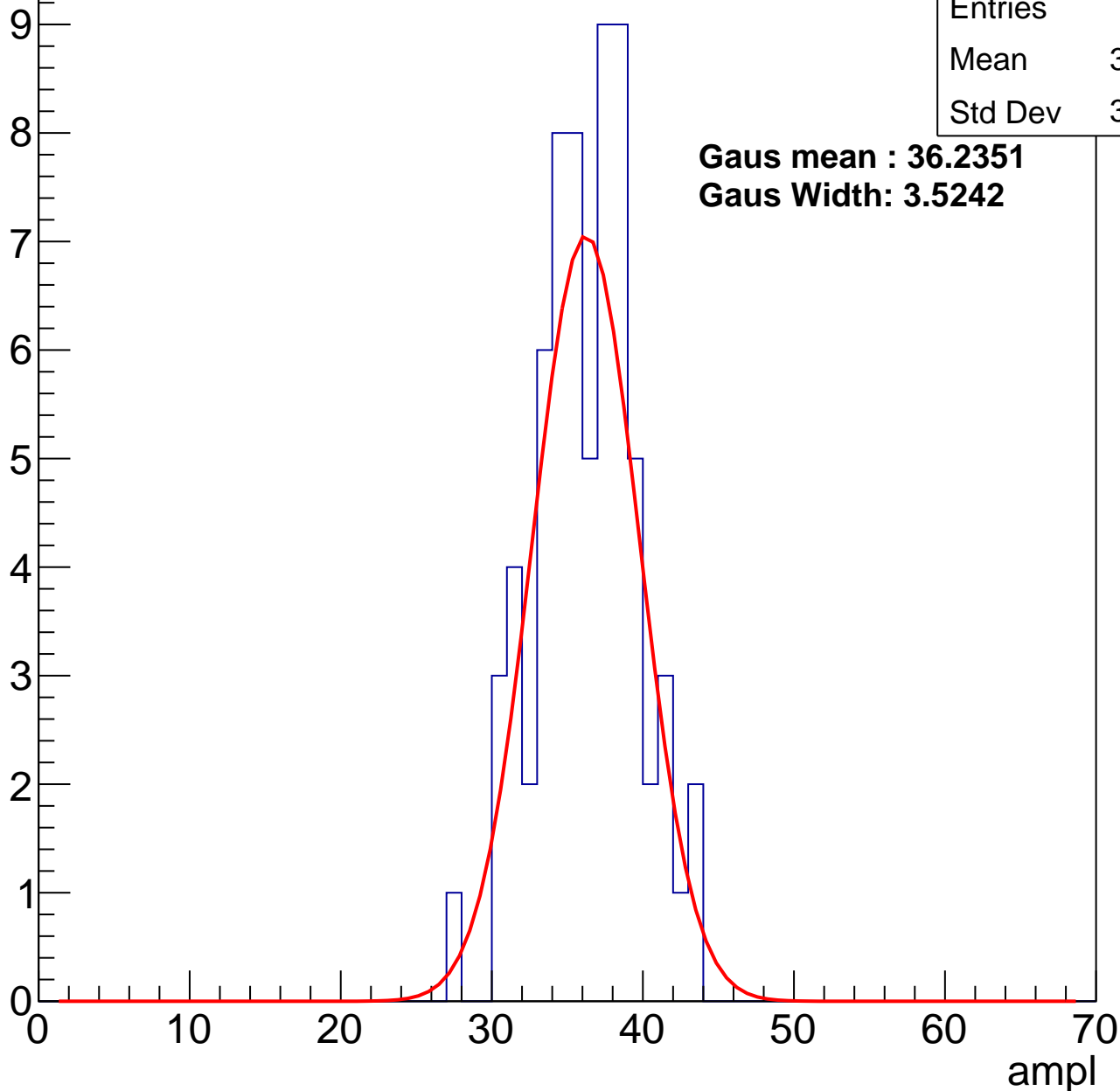
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.82
Std Dev	3.307

Gaus mean : 36.2351

Gaus Width: 3.5242



B1L102S, U10-ch75, adc2

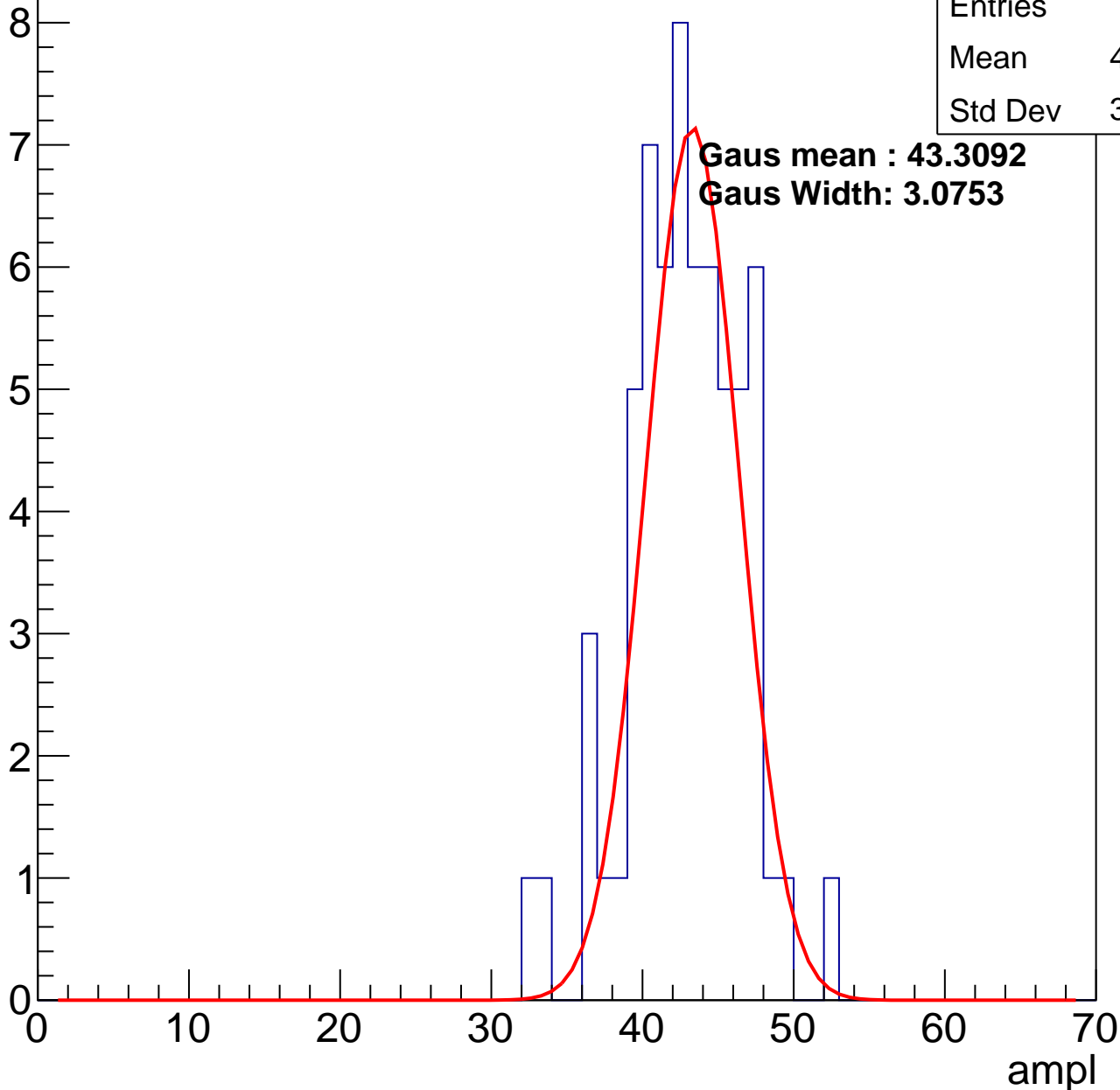
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.39
Std Dev	3.728

Gaus mean : 43.3092

Gaus Width: 3.0753

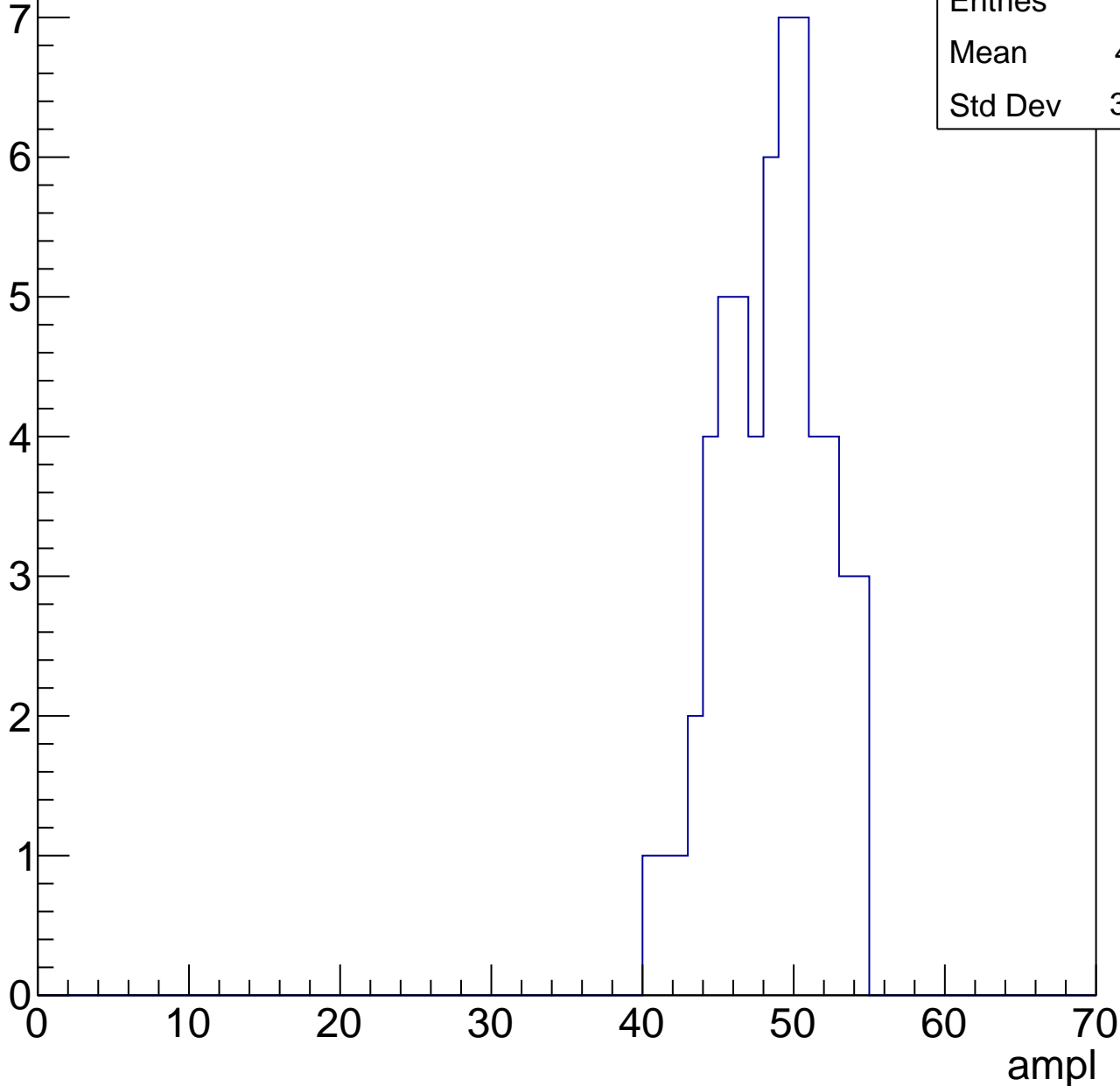


B1L102S, U10-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

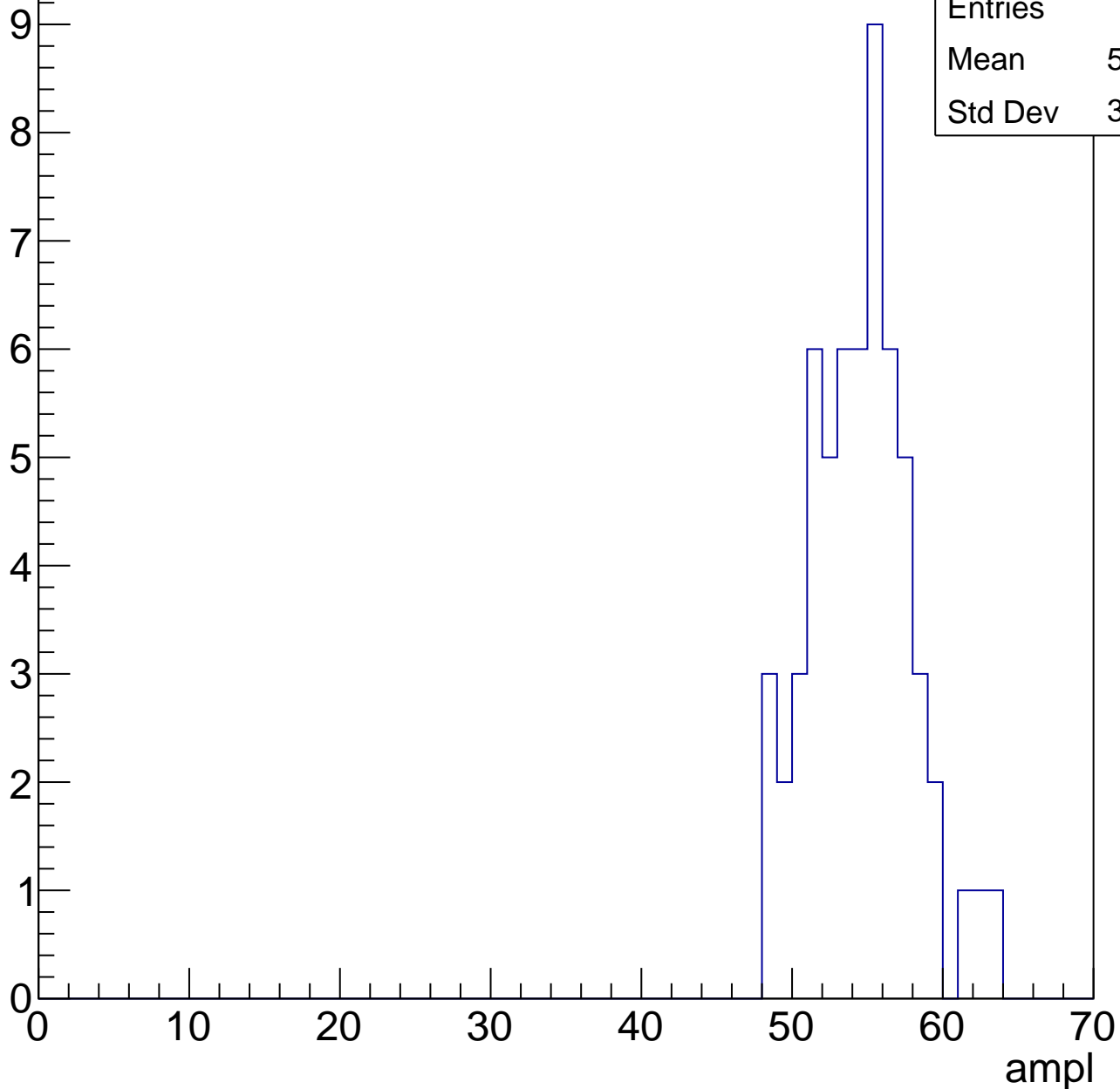
Entries	57
Mean	48.11
Std Dev	3.375



B1L102S, U10-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

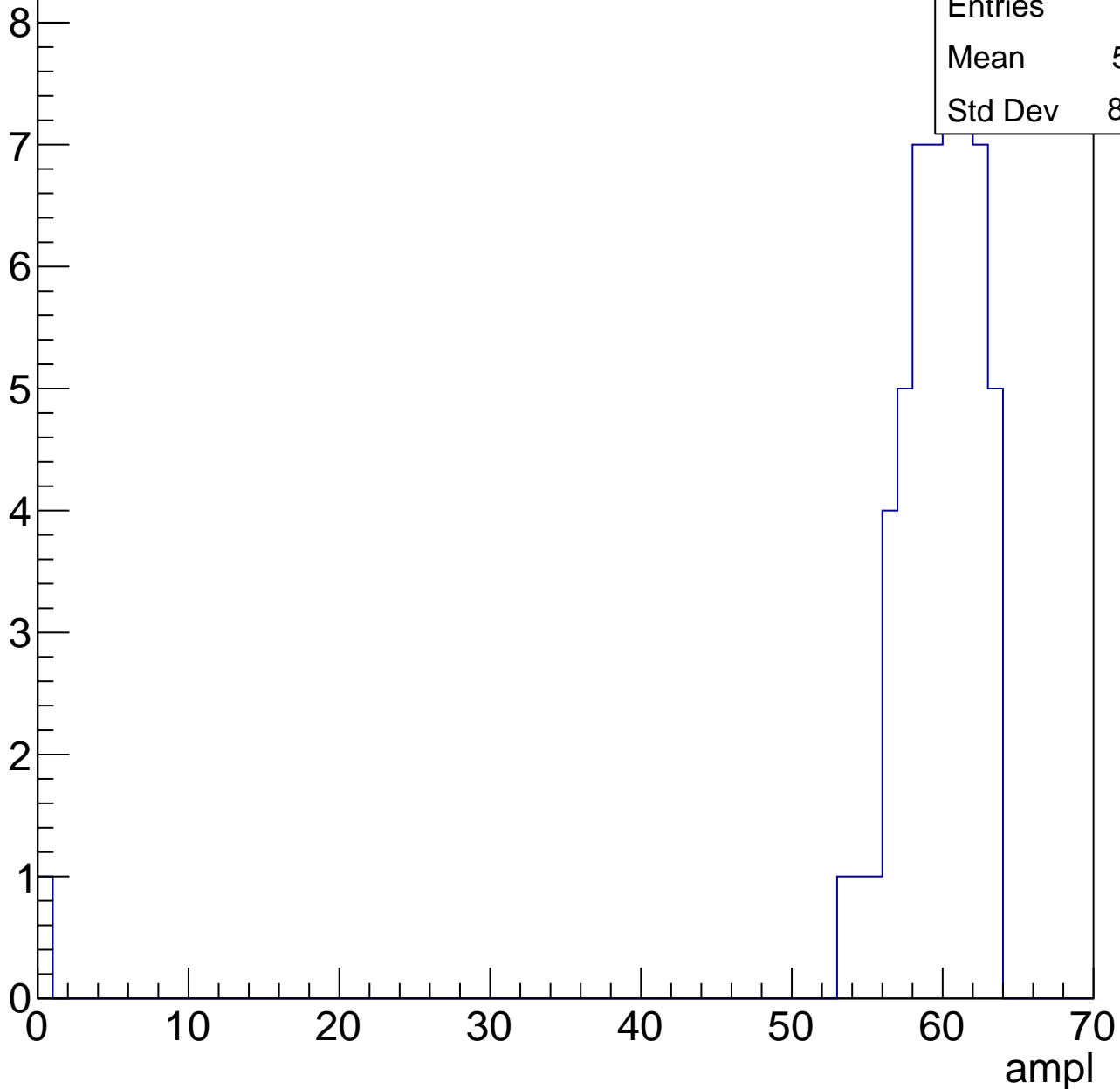


B1L102S, U10-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

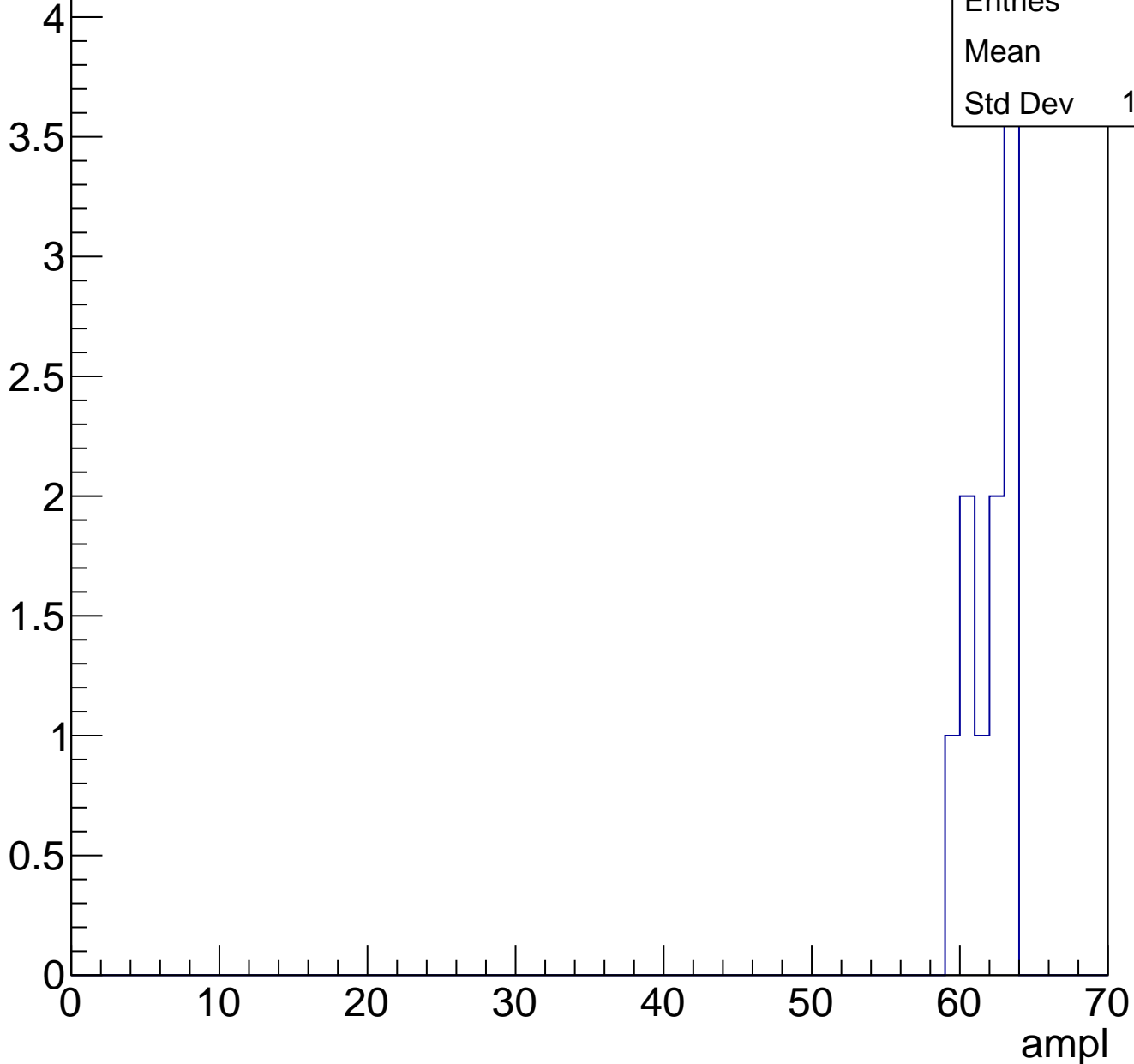
Entries	55
Mean	58.31
Std Dev	8.288



B1L102S, U10-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.6
Std Dev	1.428

B1L102S, U10-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch76, adc0

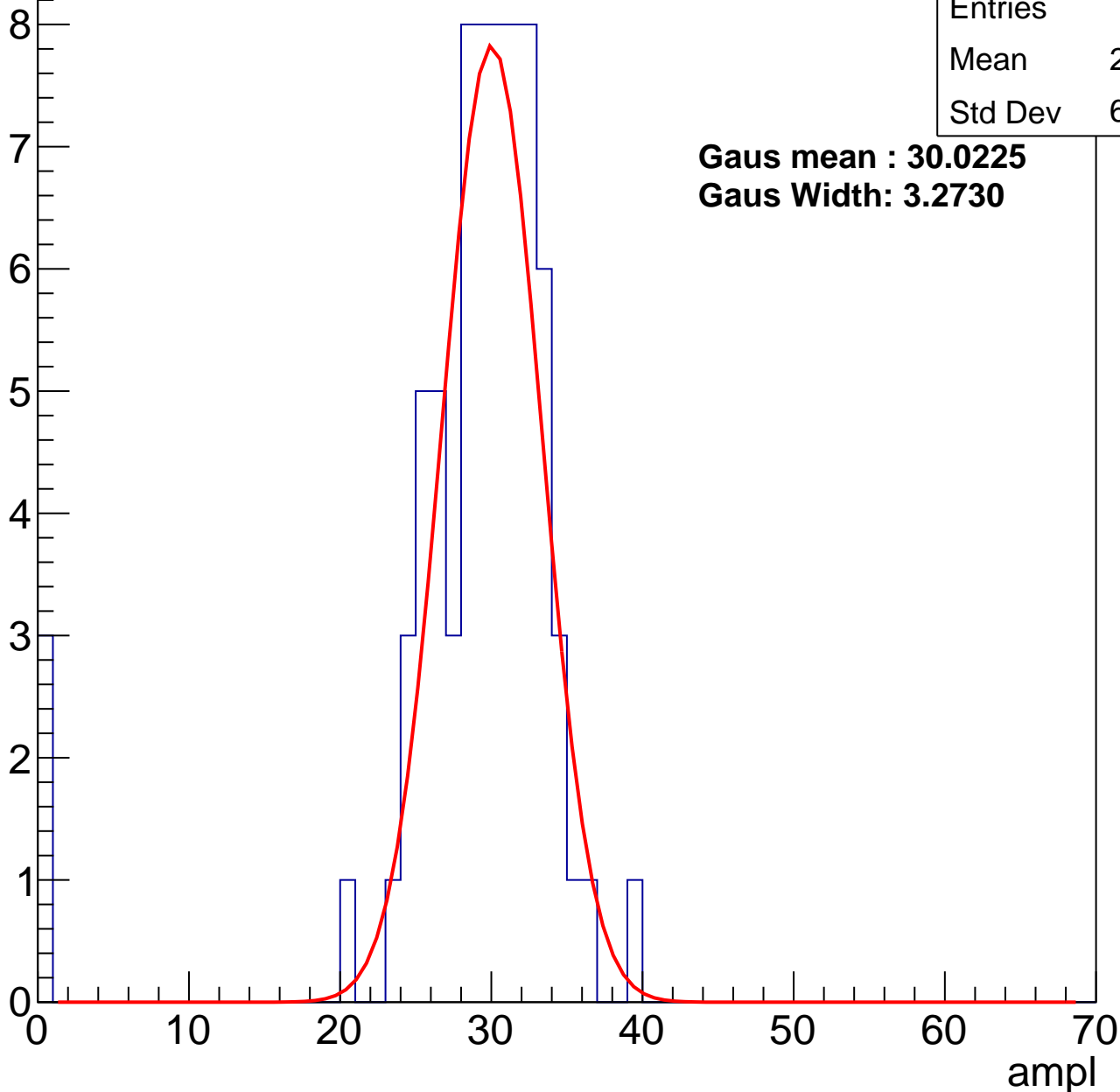
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.23
Std Dev	6.708

Gaus mean : 30.0225

Gaus Width: 3.2730



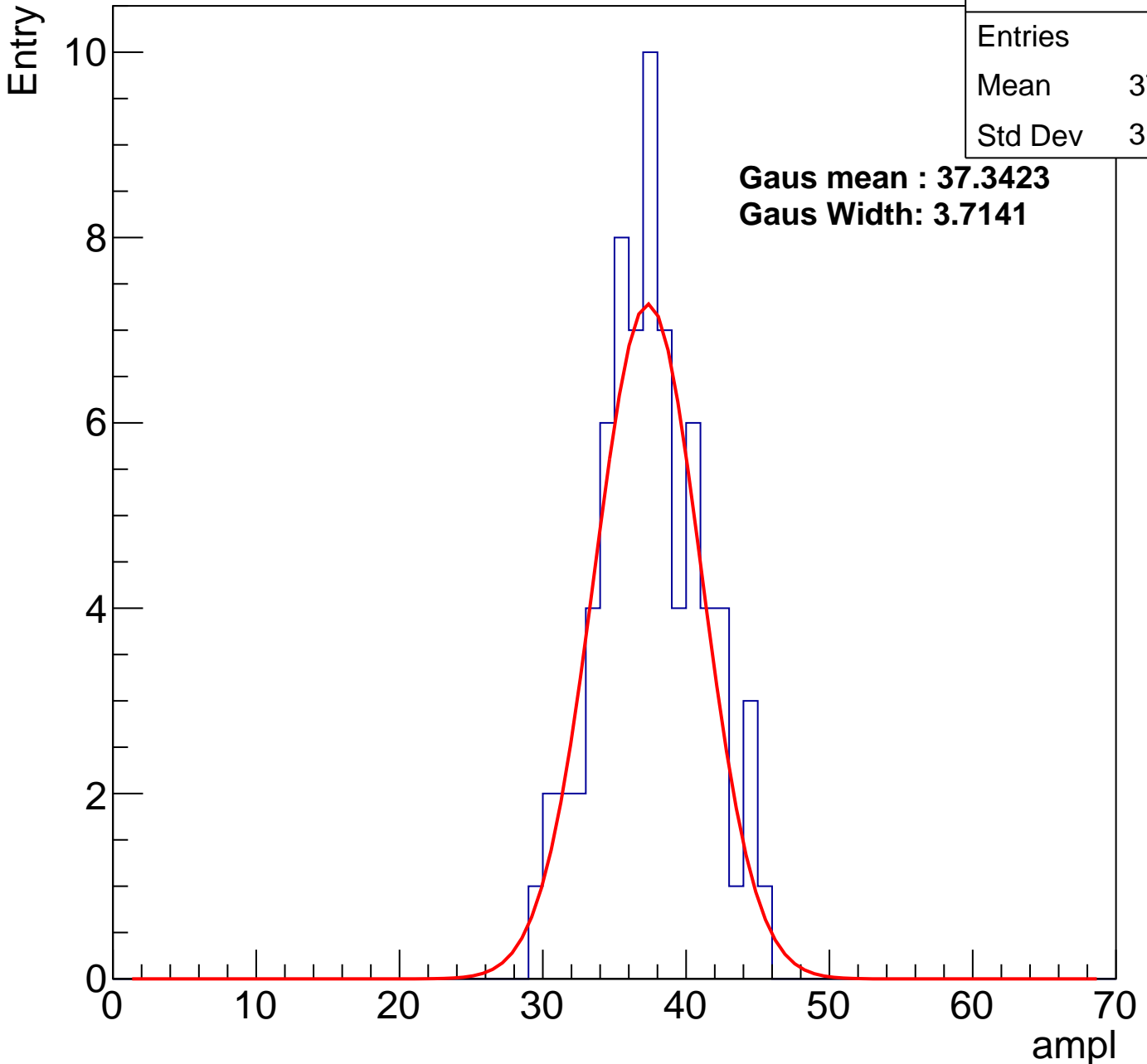
B1L102S, U10-ch76, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	37.04
Std Dev	3.592

Gaus mean : 37.3423

Gaus Width: 3.7141



B1L102S, U10-ch76, adc2

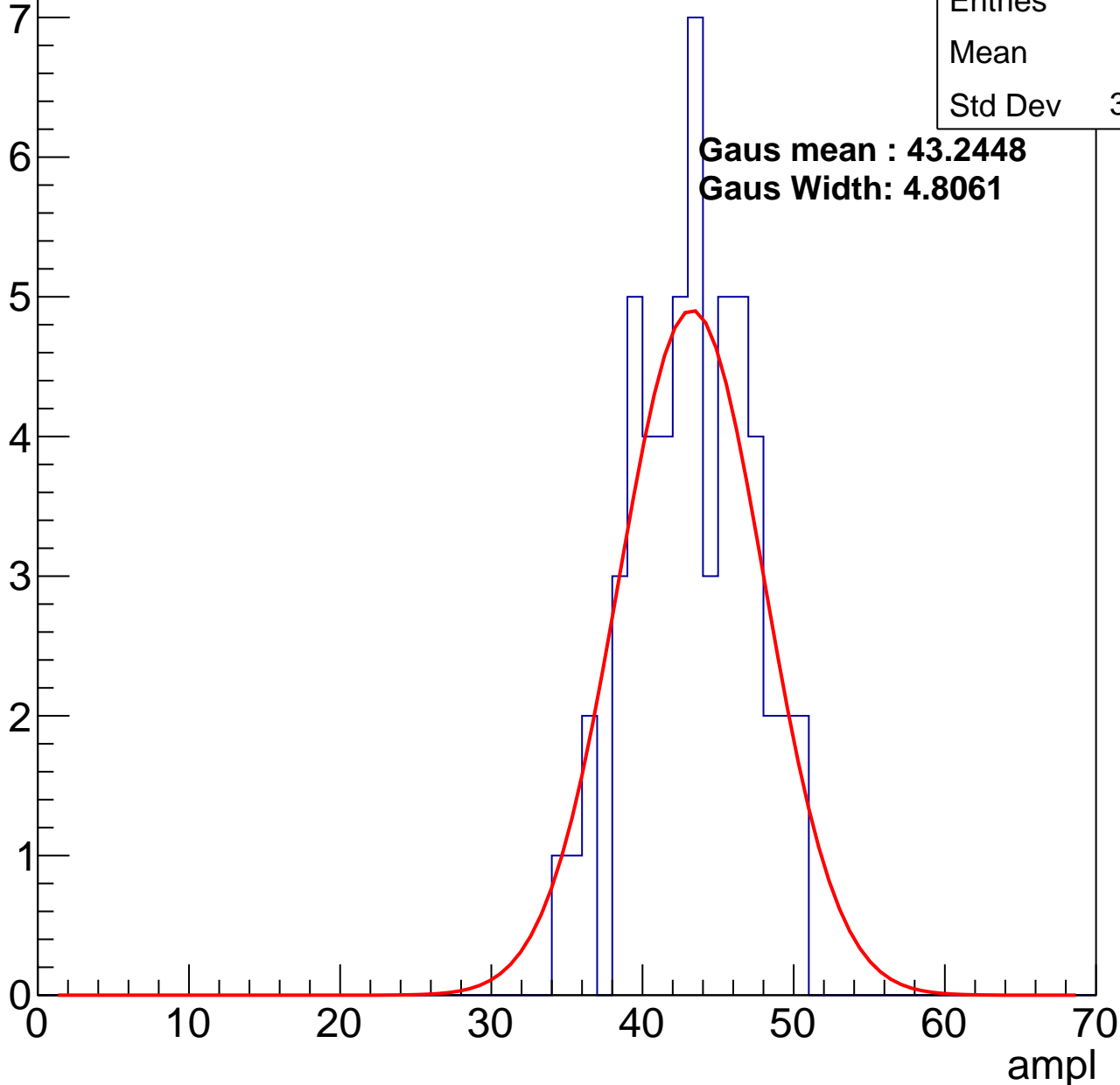
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.8
Std Dev	3.825

Gaus mean : 43.2448

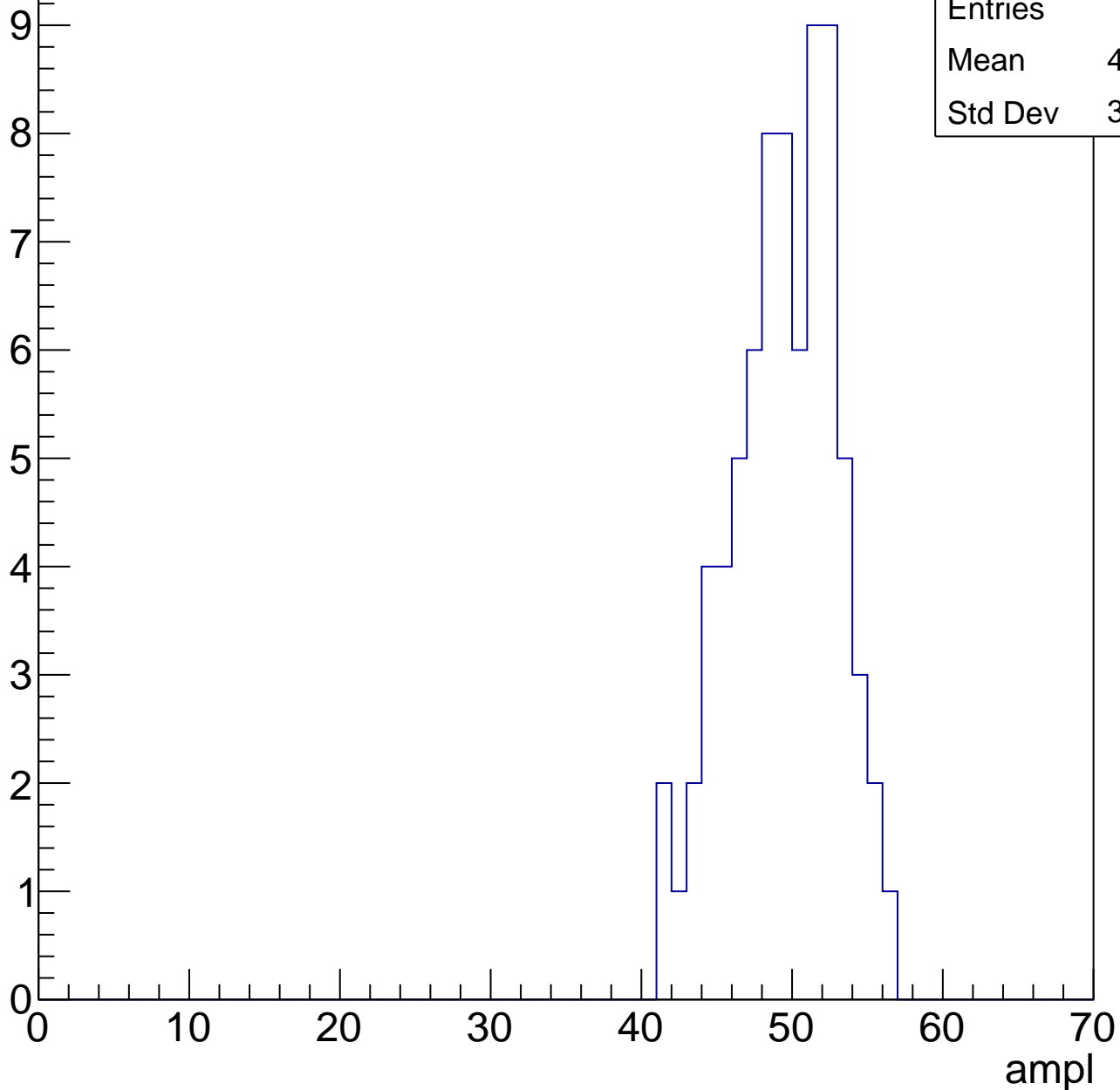
Gaus Width: 4.8061



B1L102S, U10-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

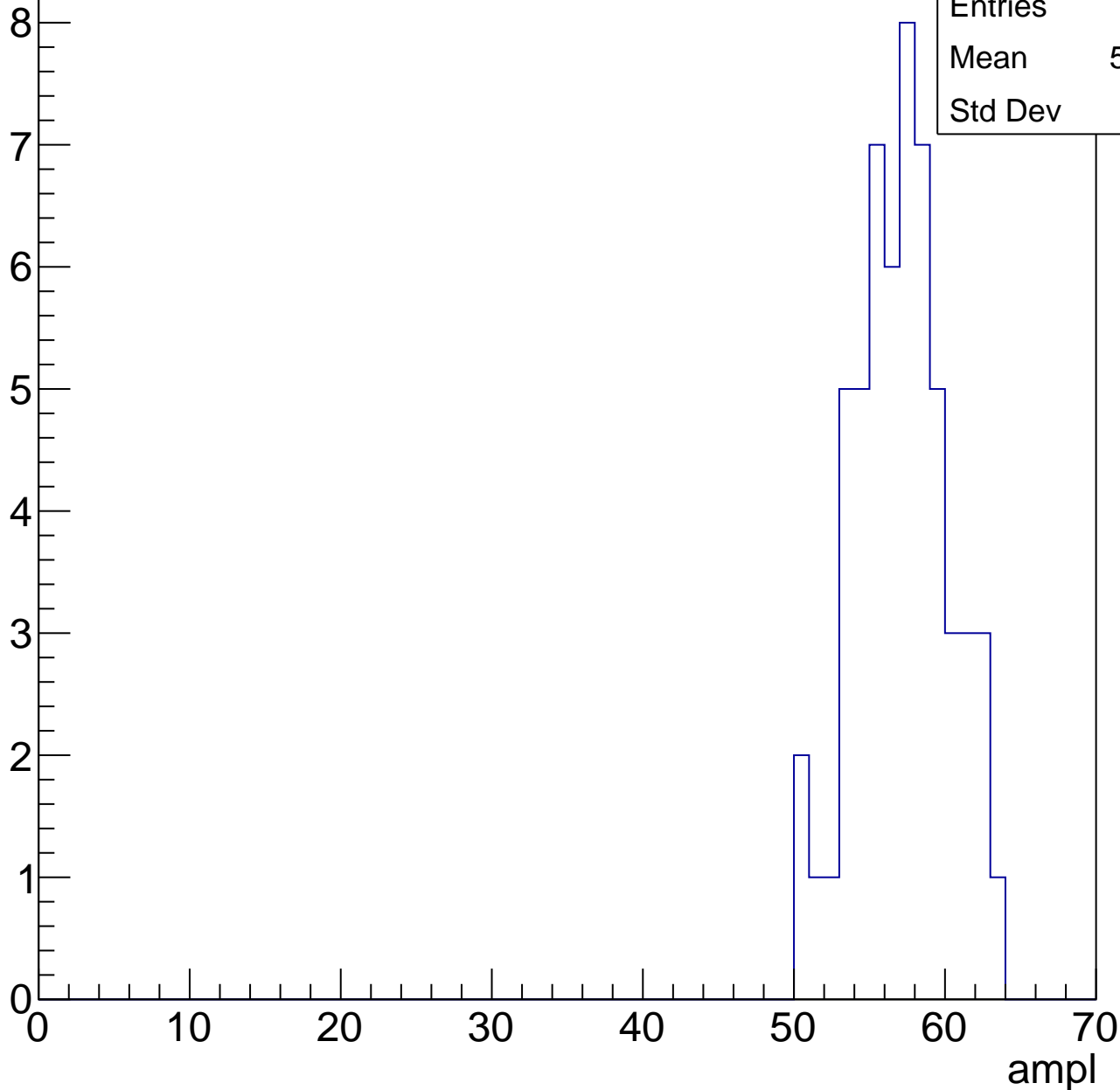


Entries	75
Mean	48.99
Std Dev	3.443

B1L102S, U10-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

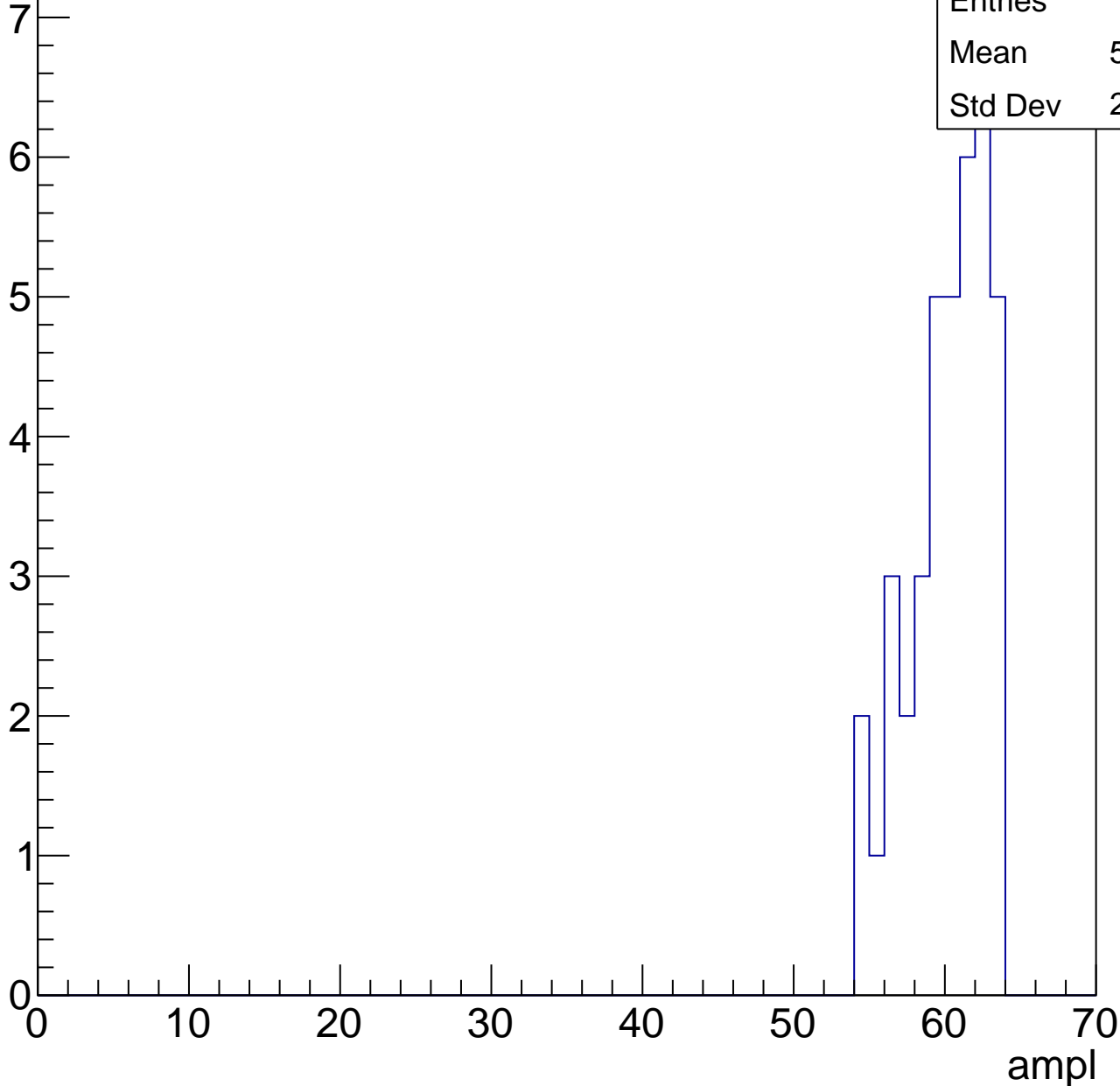


B1L102S, U10-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

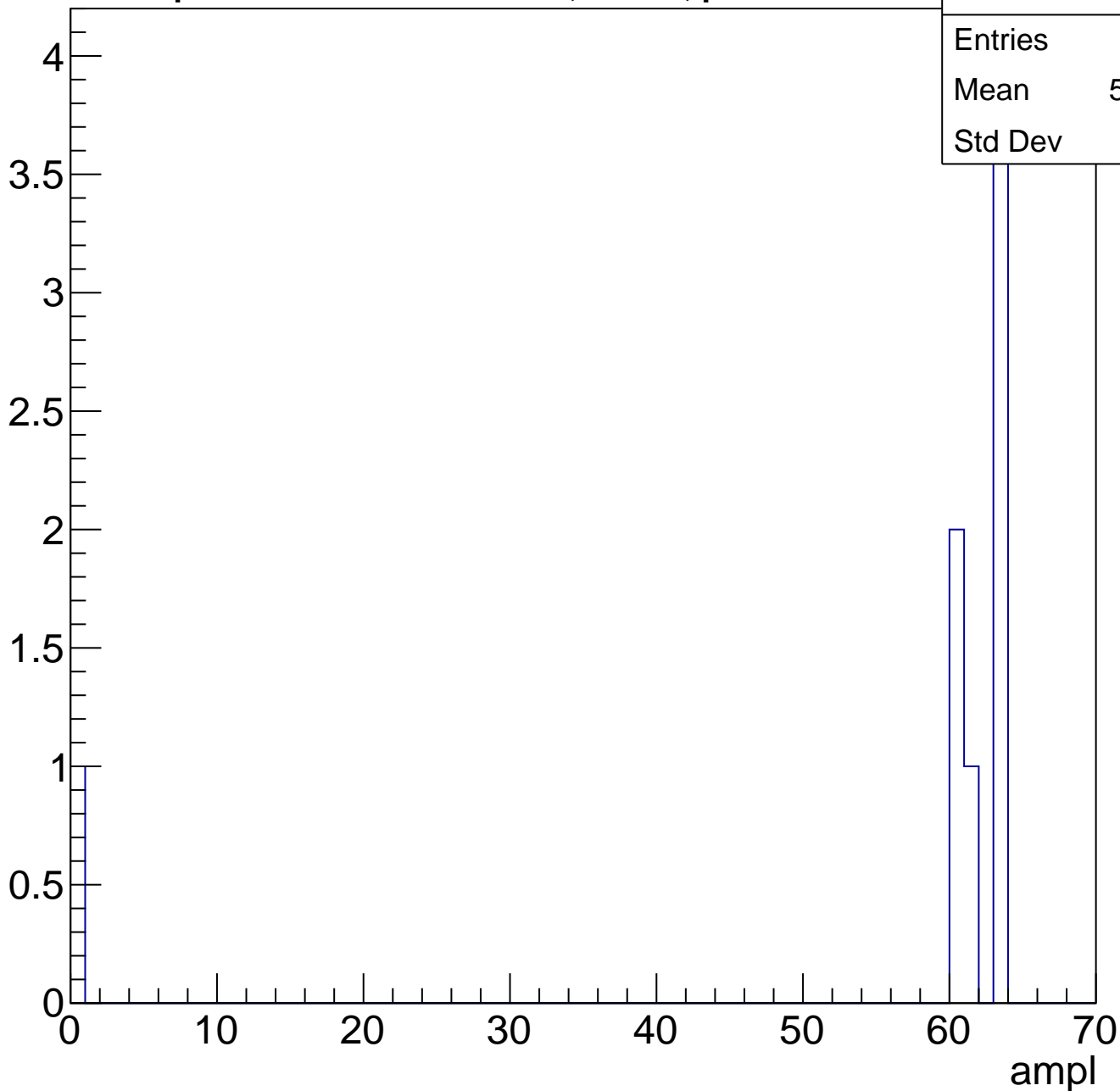
Entries	39
Mean	59.72
Std Dev	2.562



B1L102S, U10-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch77, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	29.85
Std Dev	3.491

Gaus mean : 30.6030

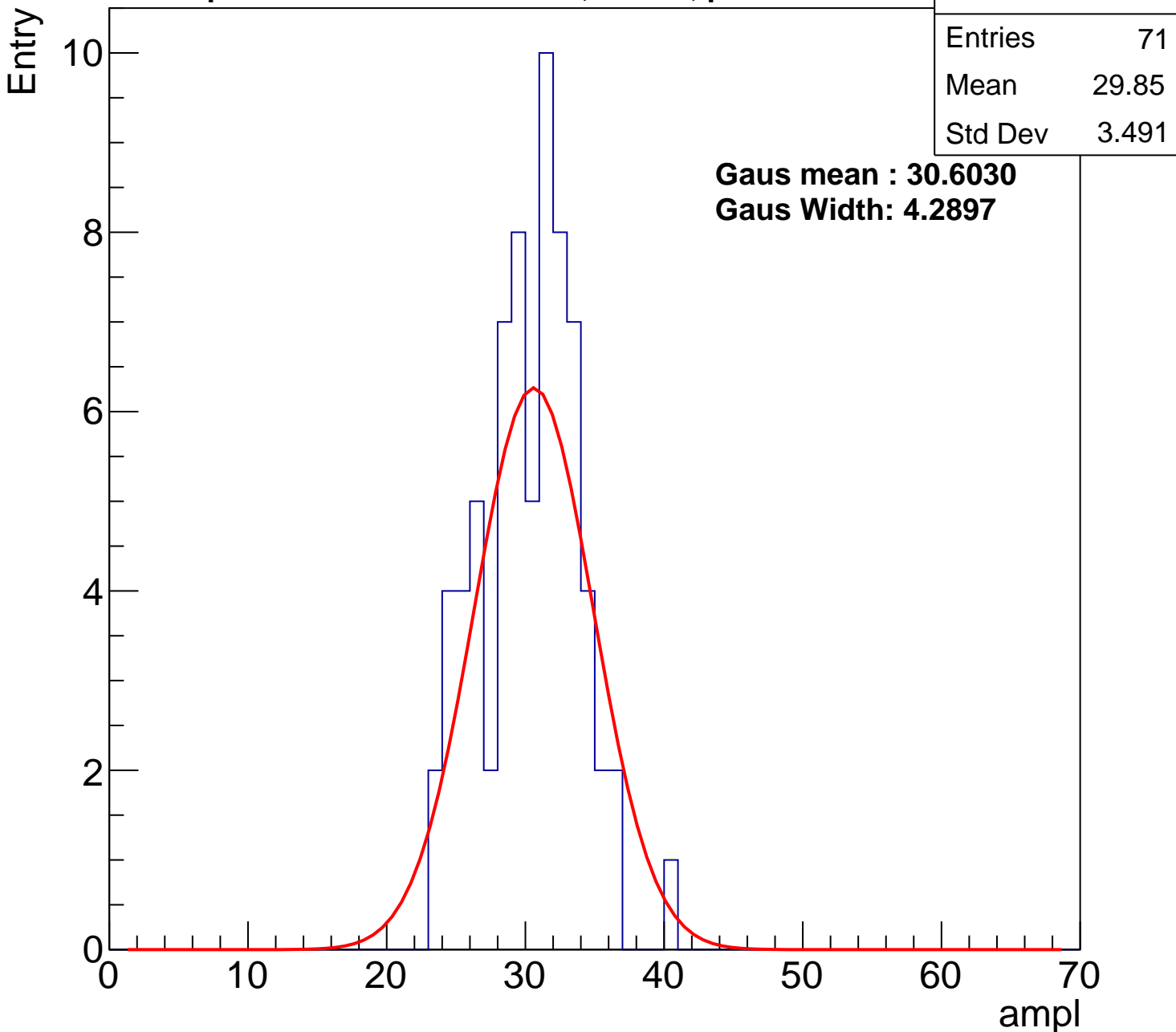
Gaus Width: 4.2897

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch77, adc1

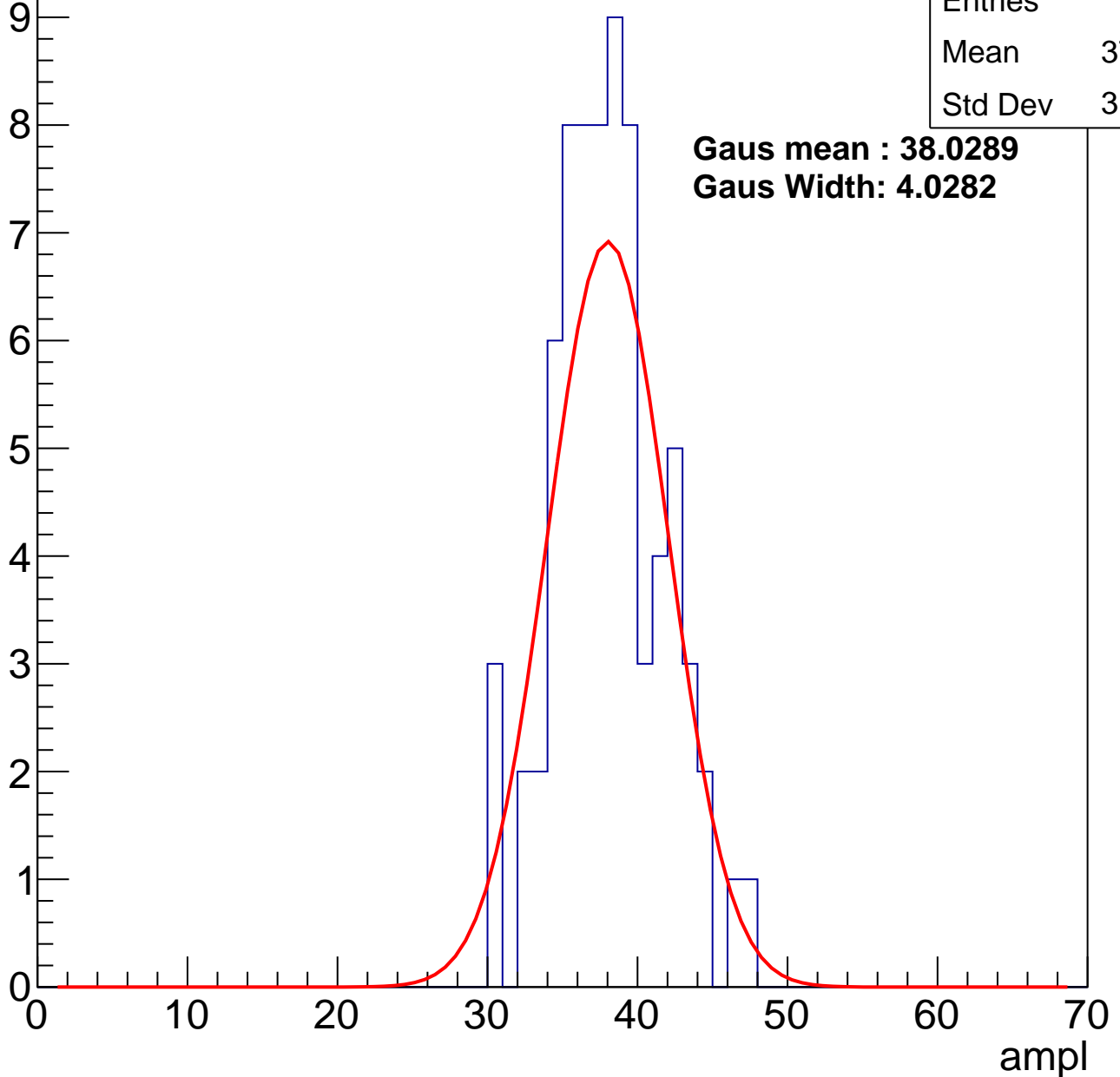
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.62
Std Dev	3.587

Gaus mean : 38.0289

Gaus Width: 4.0282



B1L102S, U10-ch77, adc2

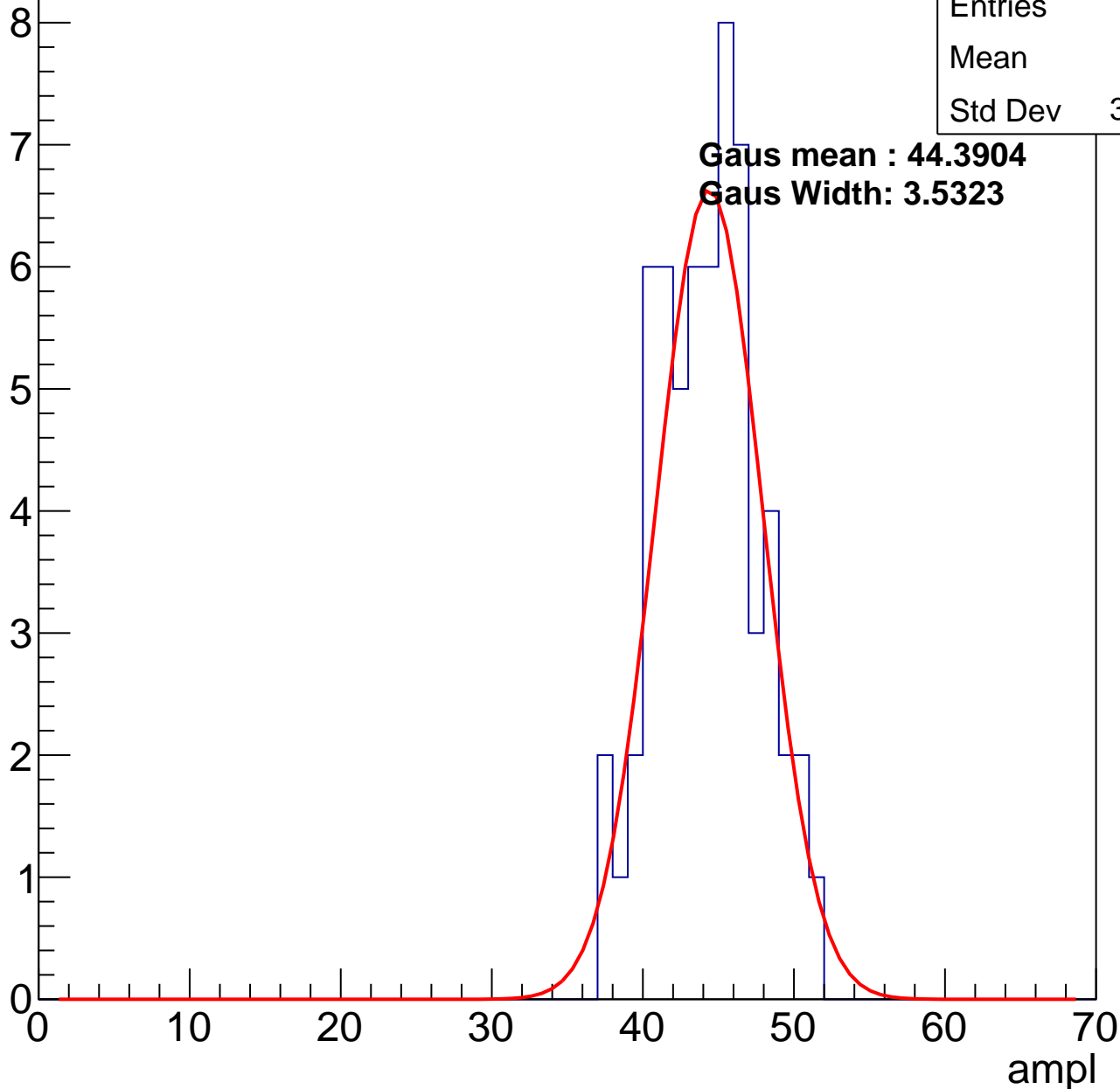
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.8
Std Dev	3.278

Gaus mean : 44.3904

Gaus Width: 3.5323

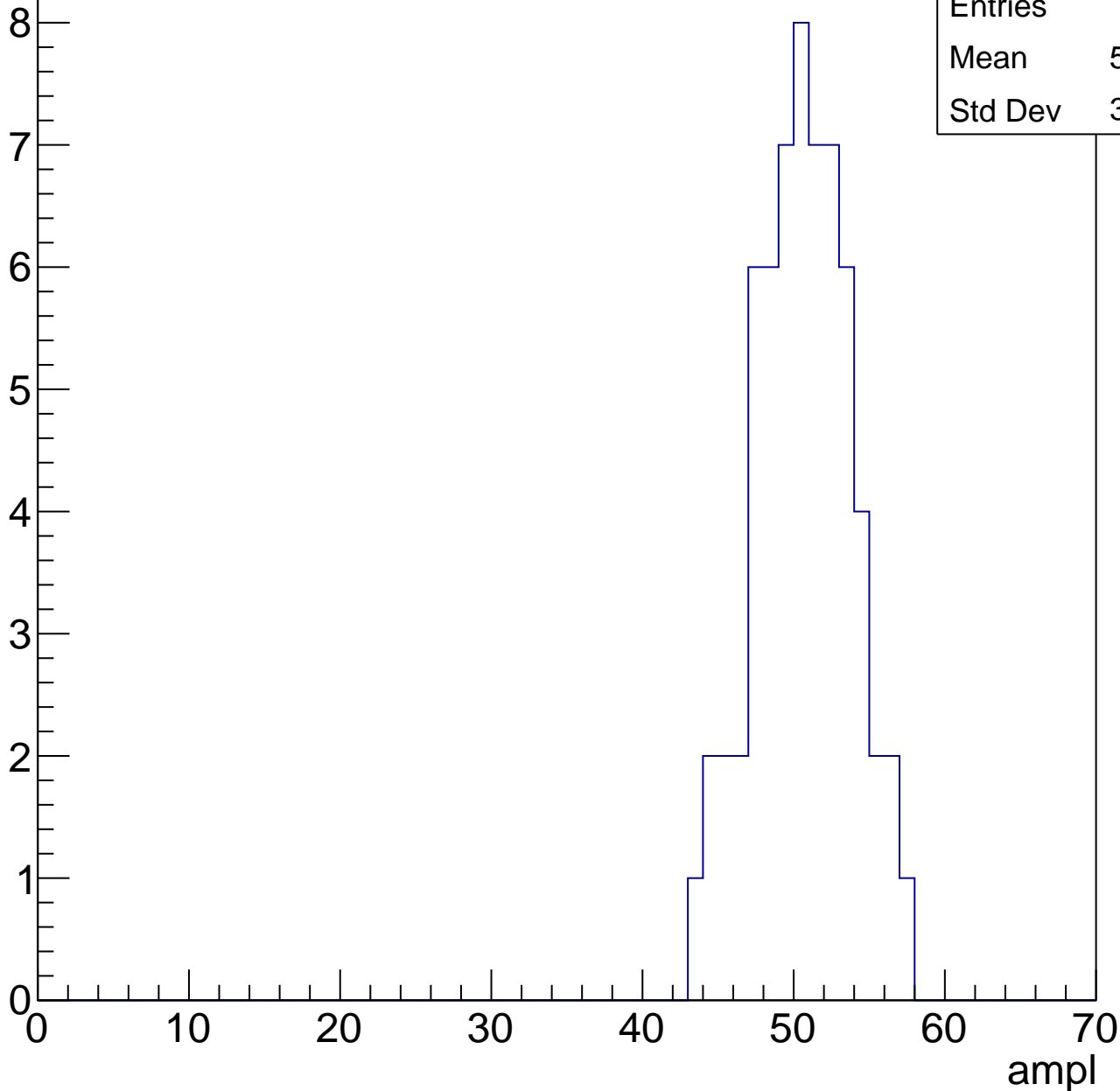


B1L102S, U10-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.16
Std Dev	3.113

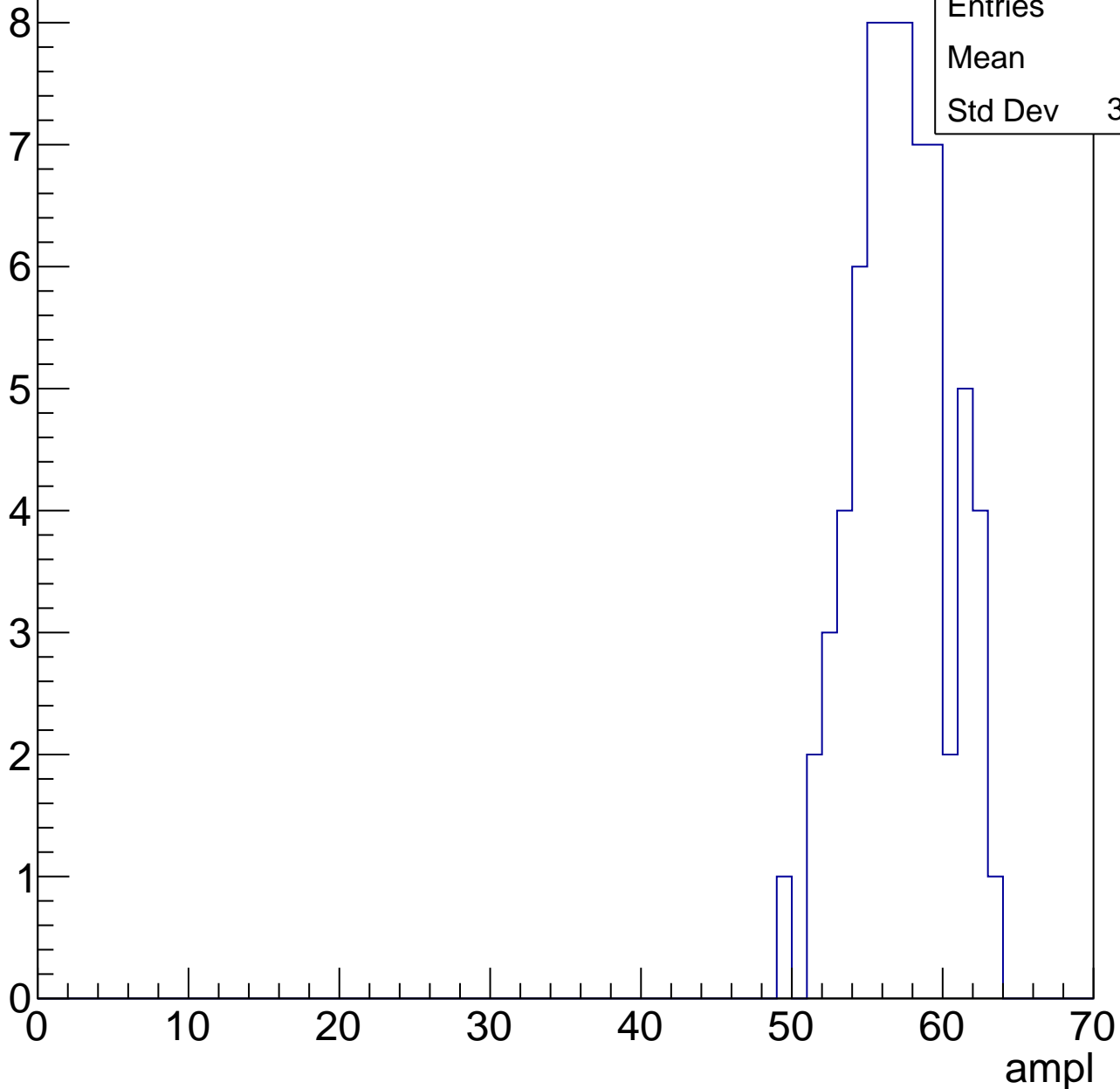


B1L102S, U10-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

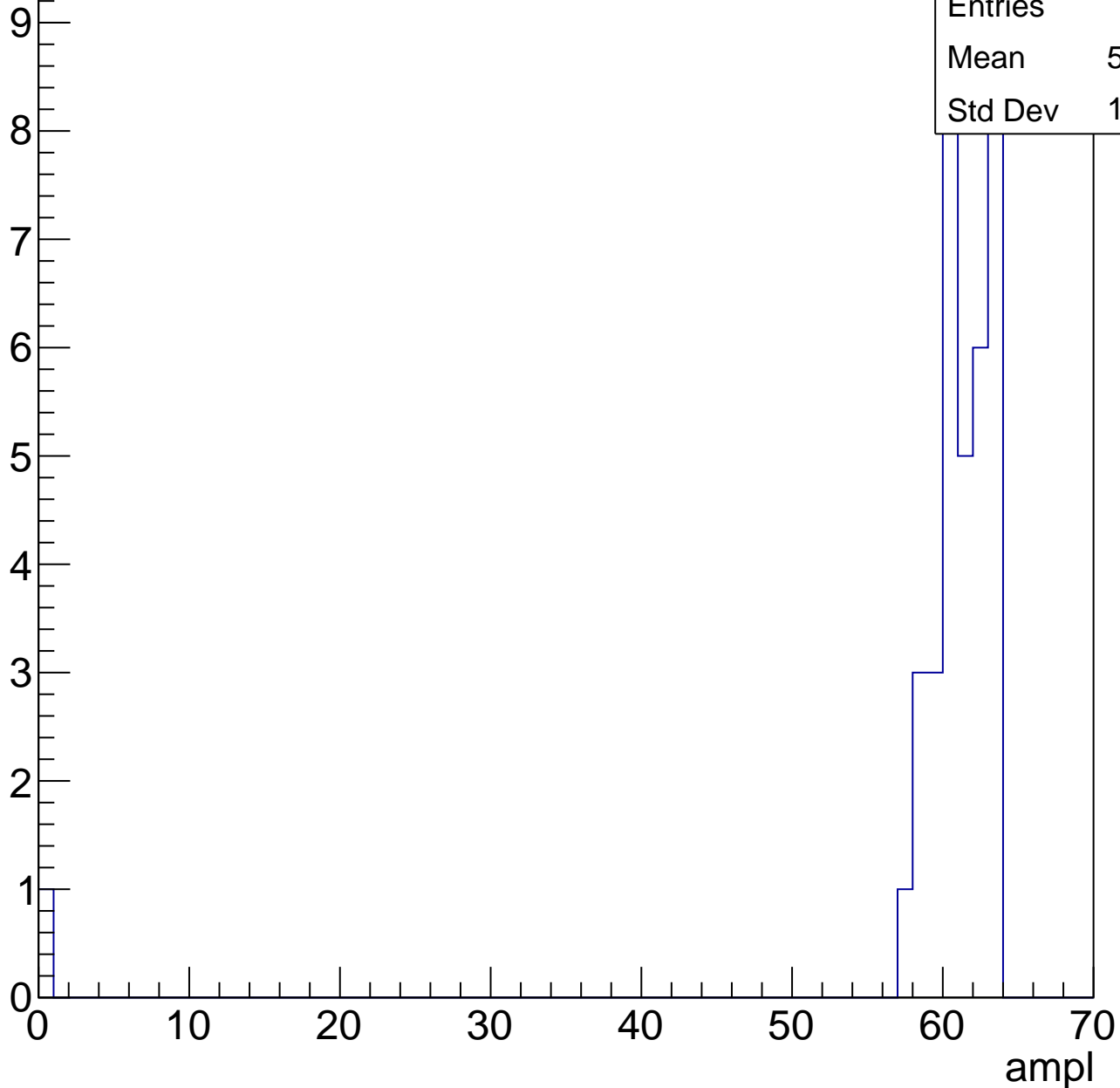
Entries	66
Mean	56.7
Std Dev	3.094



B1L102S, U10-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch78, adc0

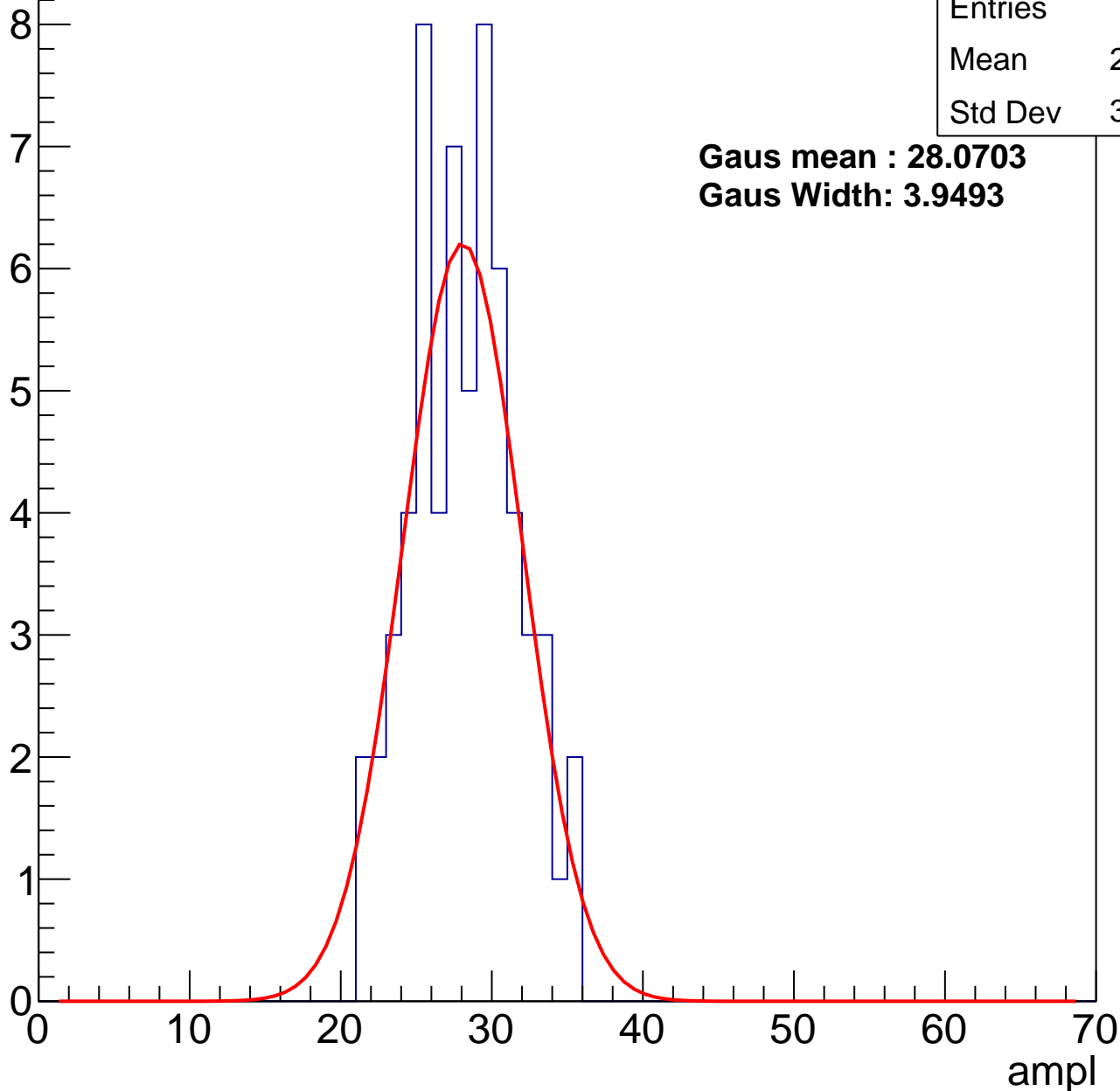
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.73
Std Dev	3.418

Gaus mean : 28.0703

Gaus Width: 3.9493



B1L102S, U10-ch78, adc1

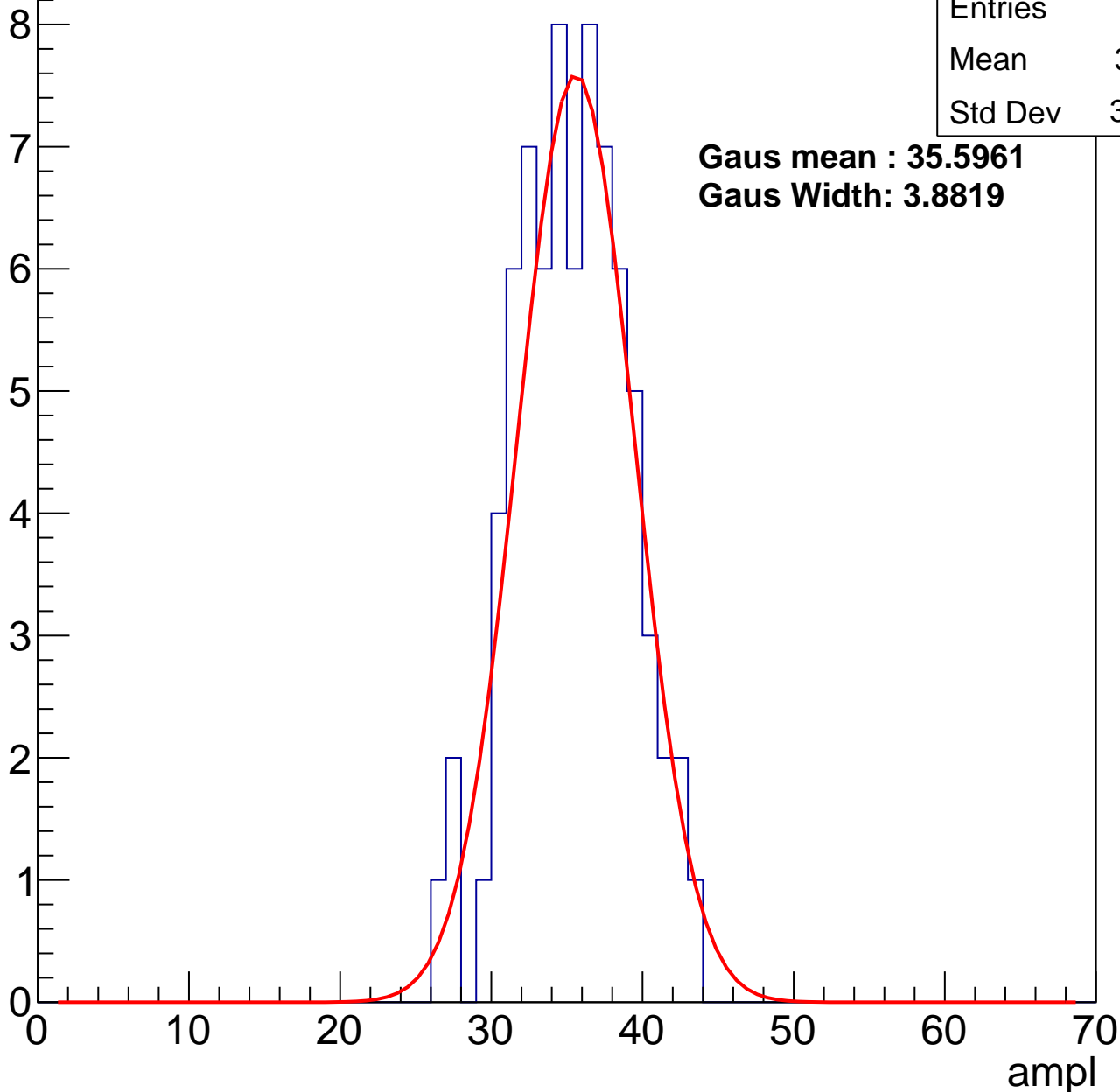
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.91
Std Dev	3.678

Gaus mean : 35.5961

Gaus Width: 3.8819



B1L102S, U10-ch78, adc2

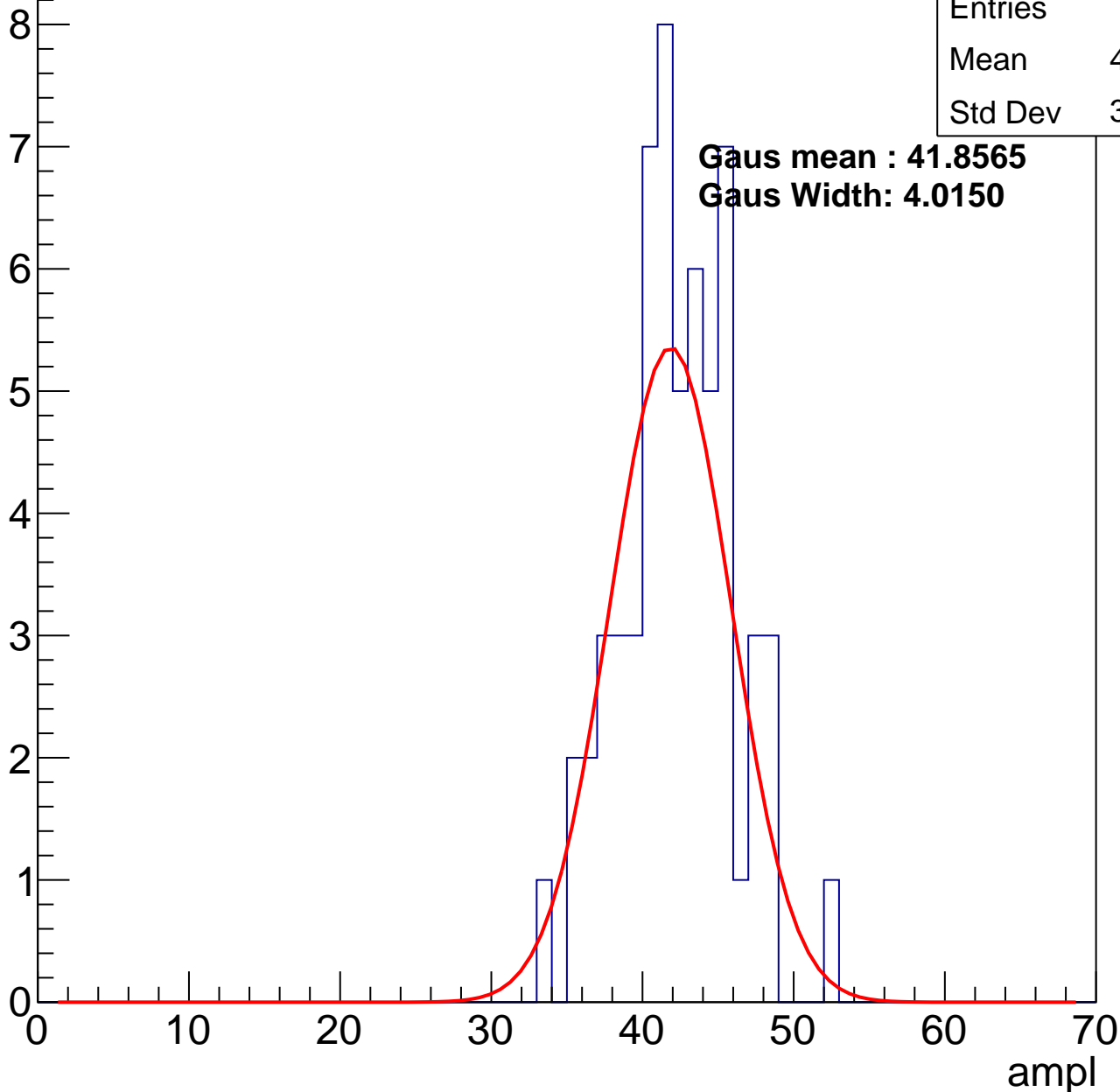
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.85
Std Dev	3.705

Gaus mean : 41.8565

Gaus Width: 4.0150

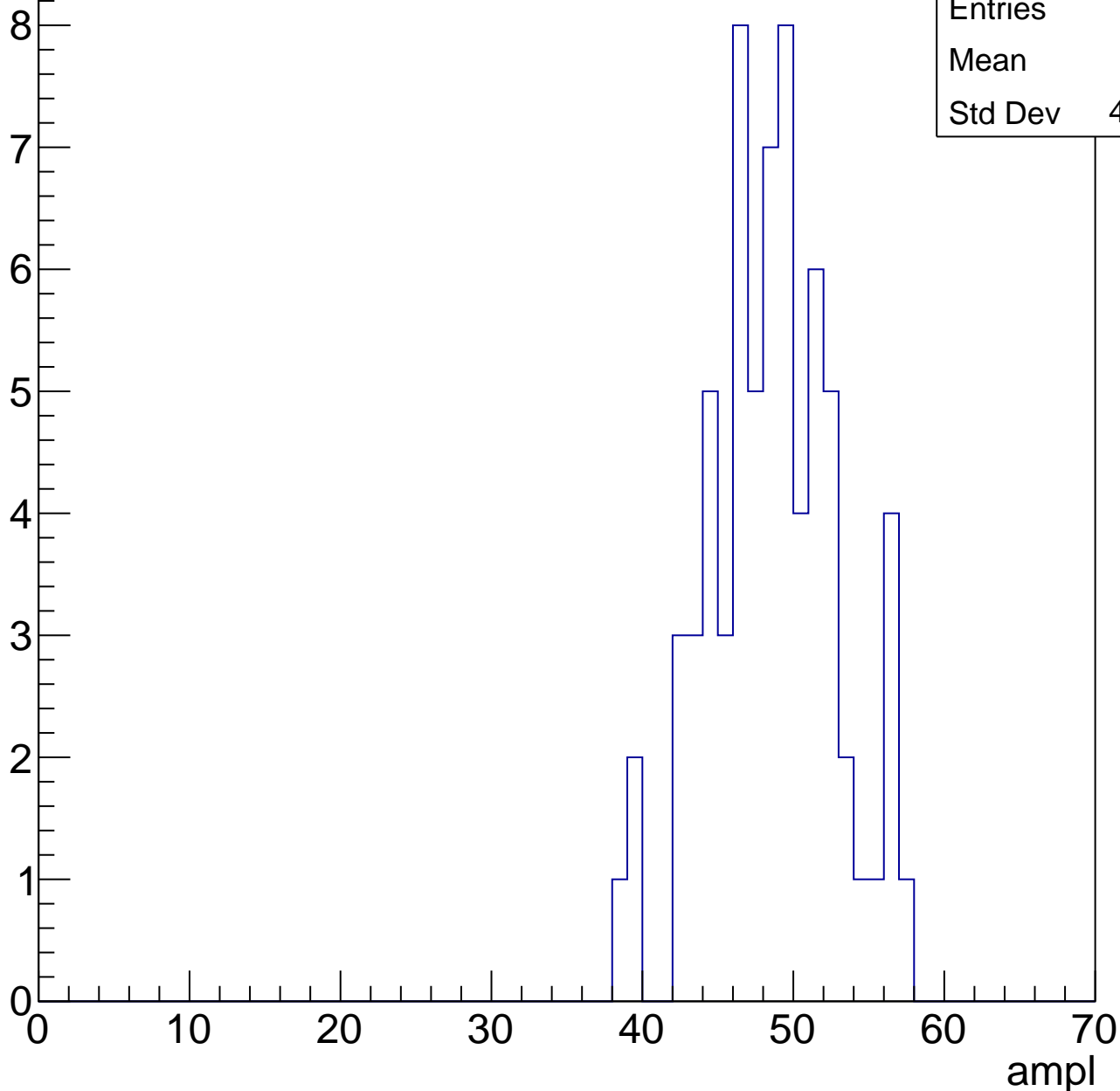


B1L102S, U10-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.1
Std Dev	4.202

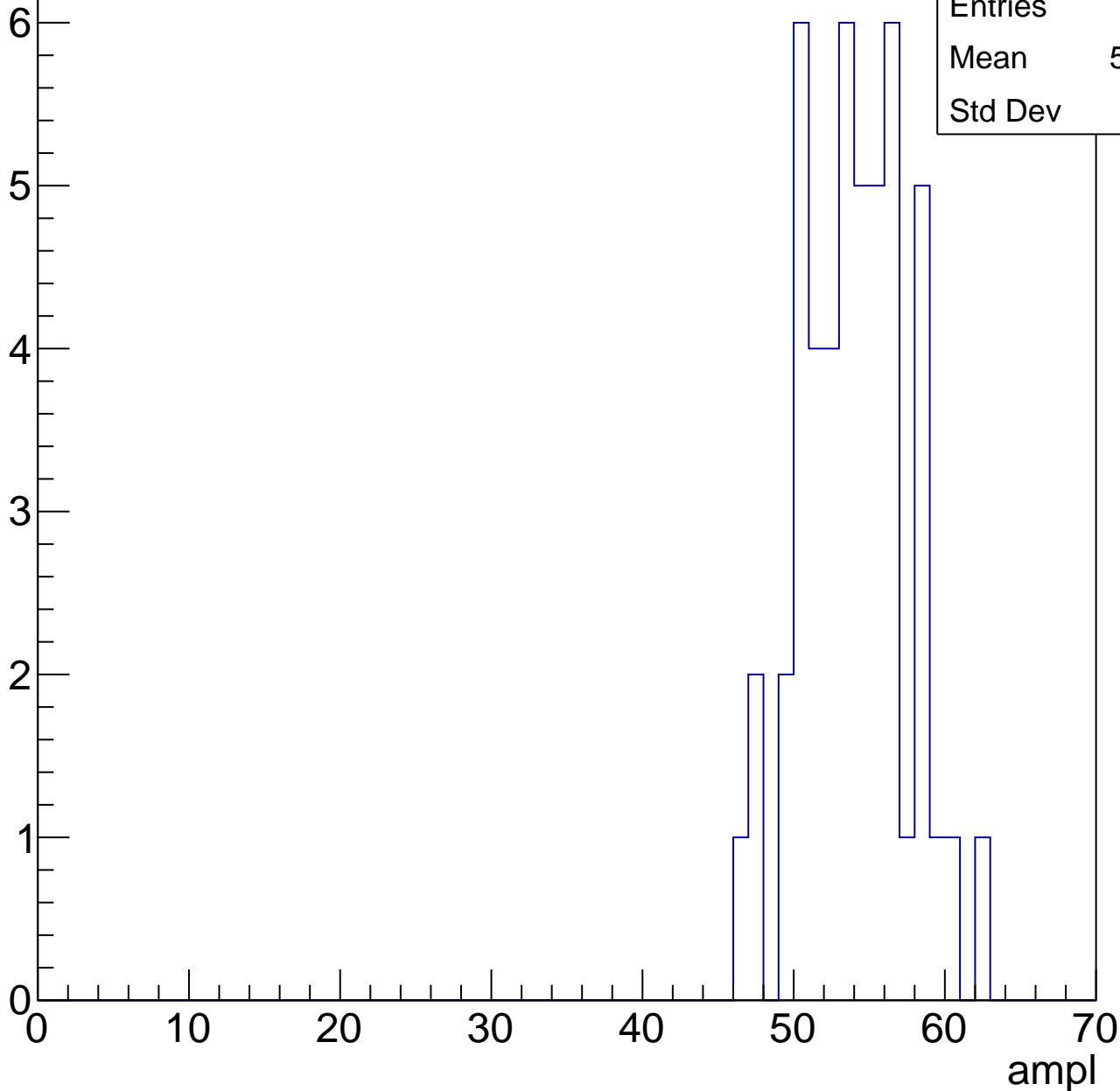


B1L102S, U10-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	53.54
Std Dev	3.46

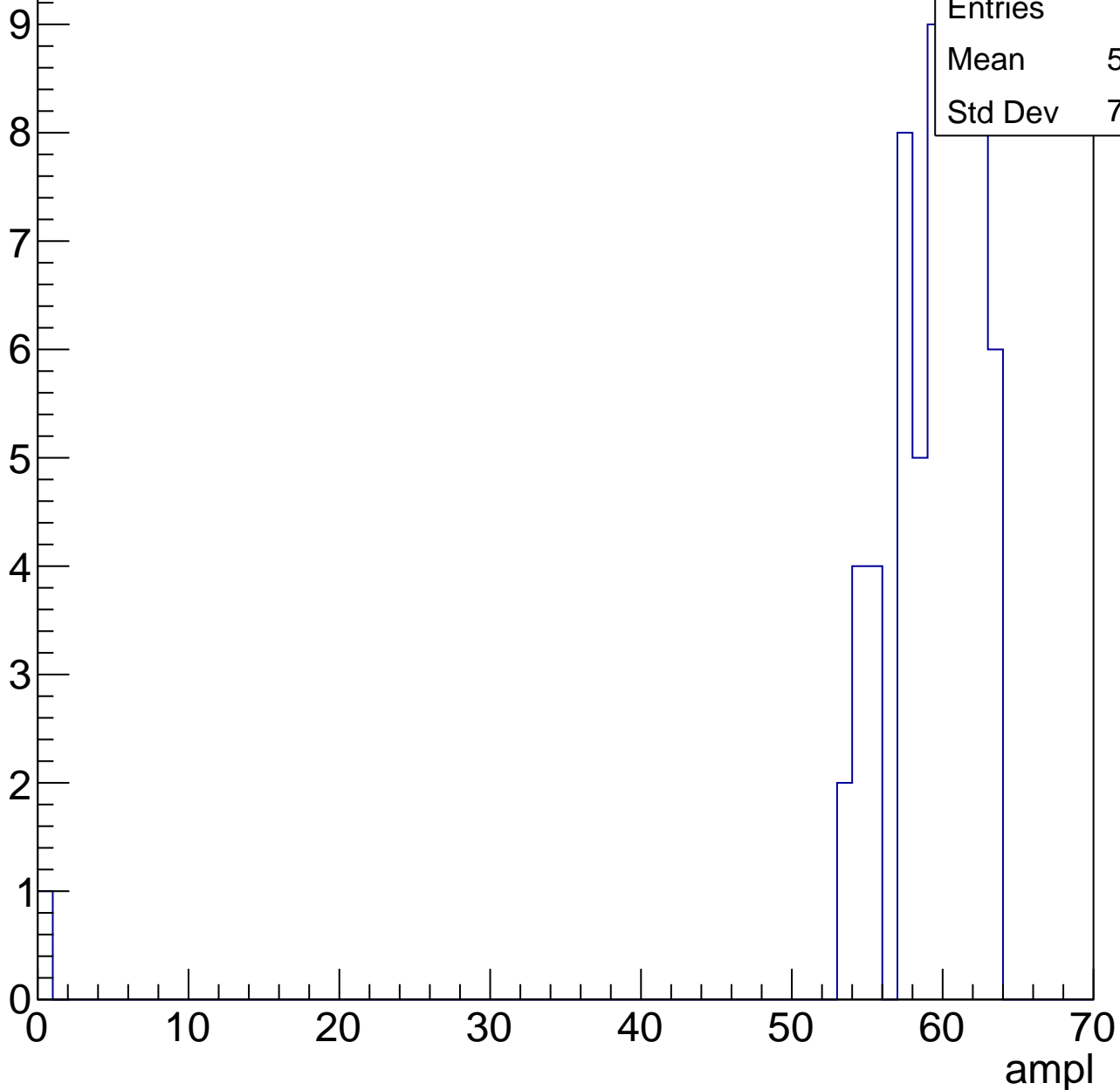


B1L102S, U10-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	58.17
Std Dev	7.833



B1L102S, U10-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

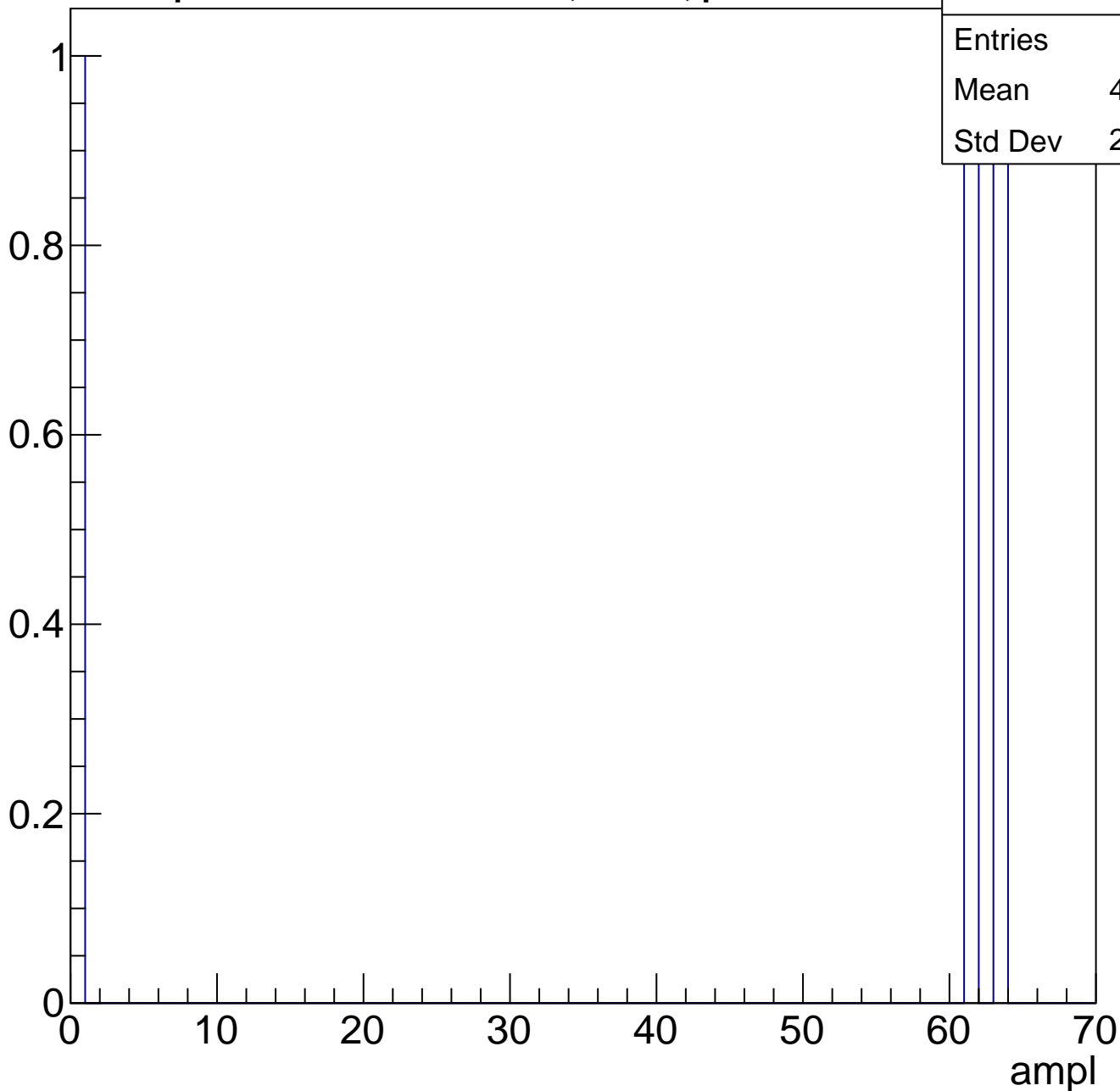
Entry



B1L102S, U10-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch79, adc0

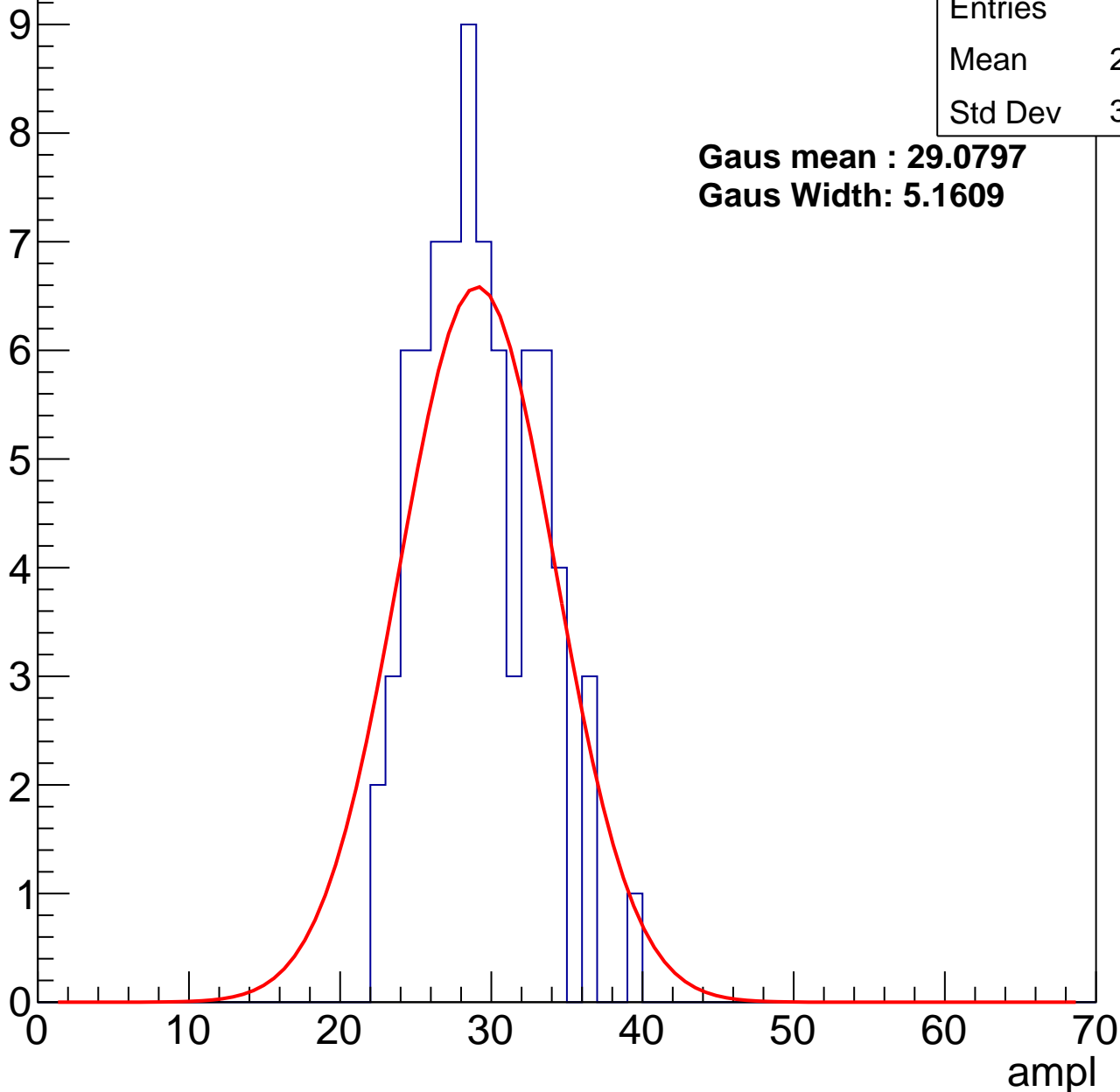
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.67
Std Dev	3.732

Gaus mean : 29.0797

Gaus Width: 5.1609



B1L102S, U10-ch79, adc1

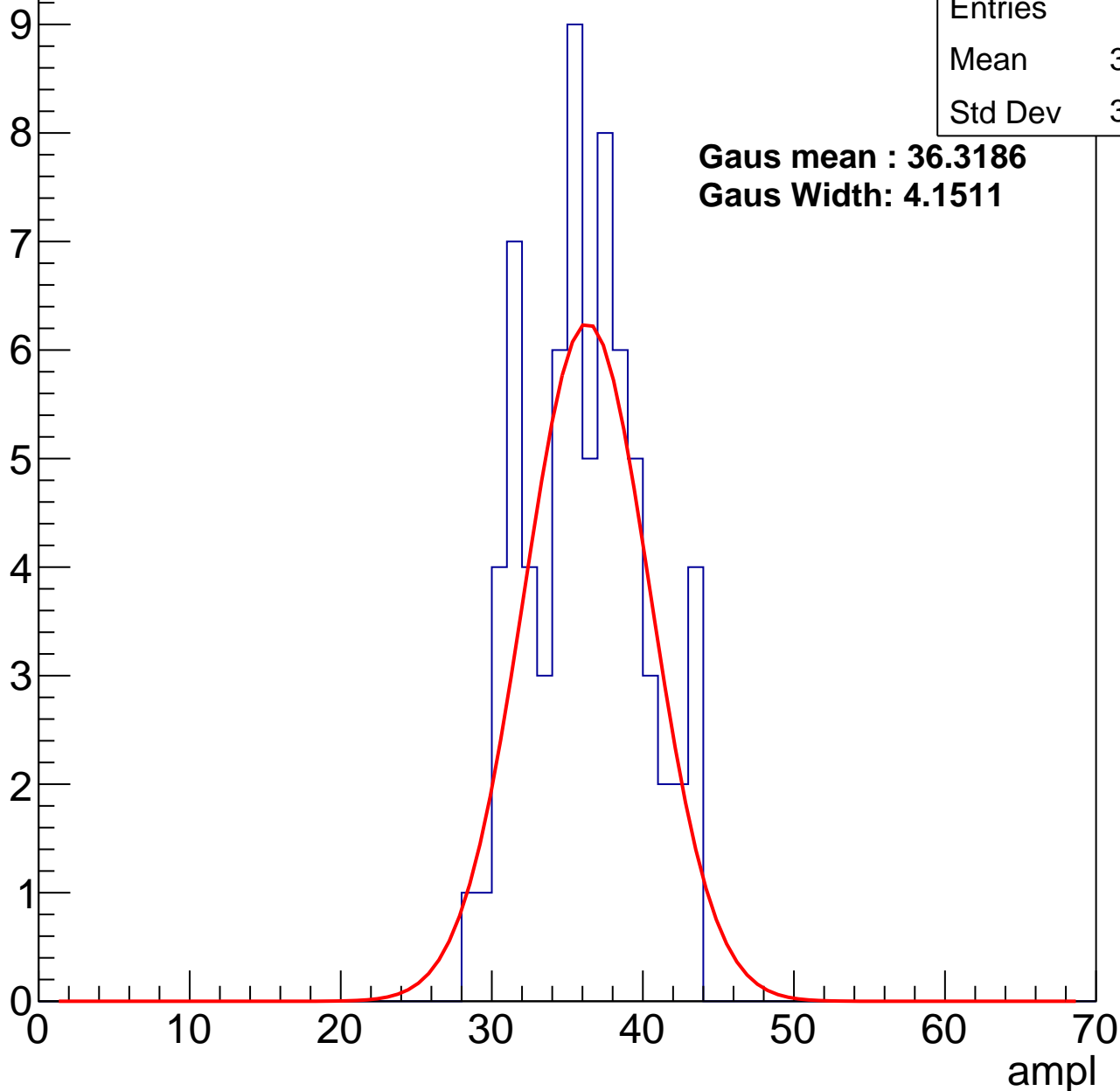
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.67
Std Dev	3.763

Gaus mean : 36.3186

Gaus Width: 4.1511



B1L102S, U10-ch79, adc2

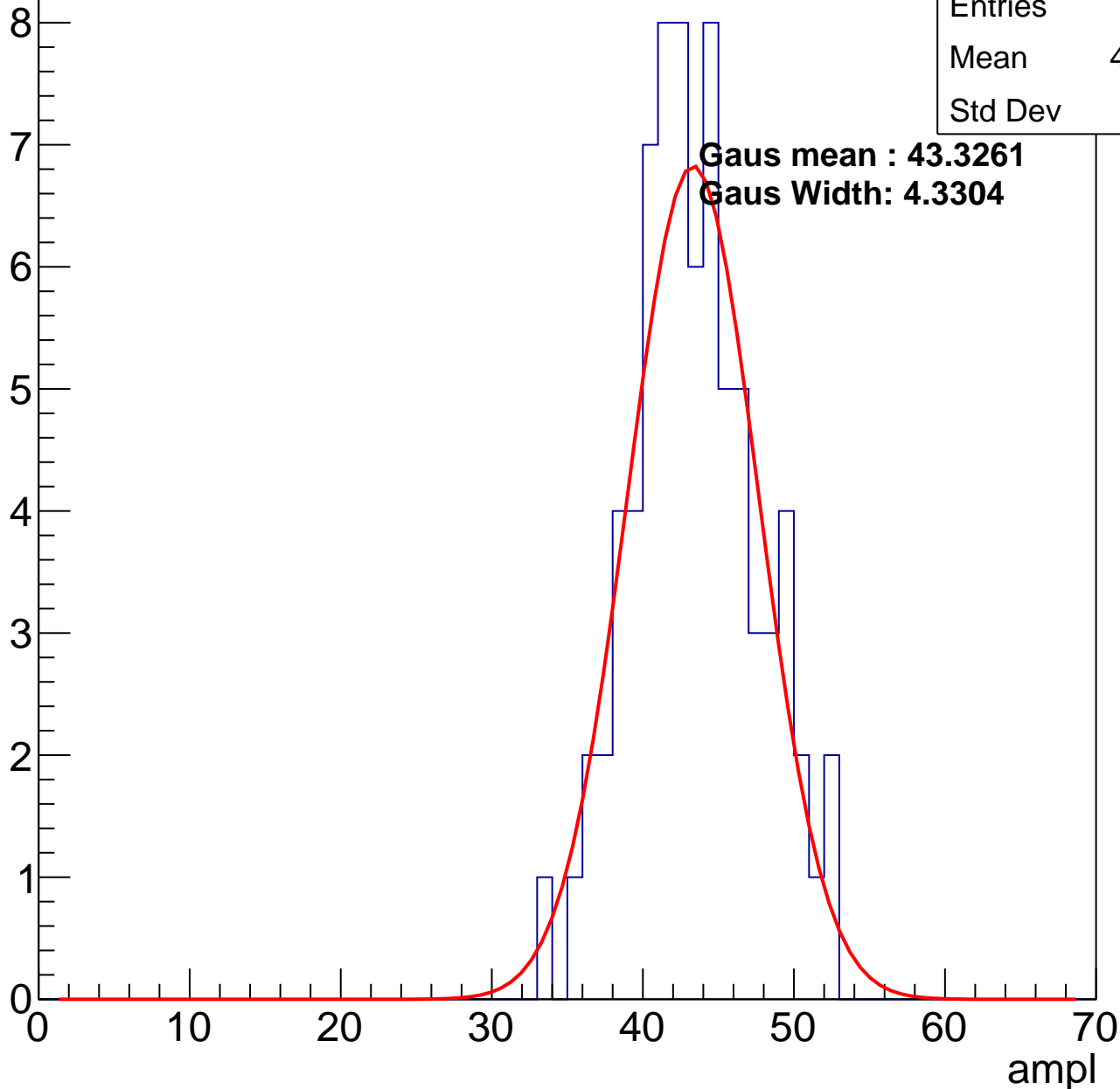
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.99
Std Dev	4.08

Gaus mean : 43.3261

Gaus Width: 4.3304

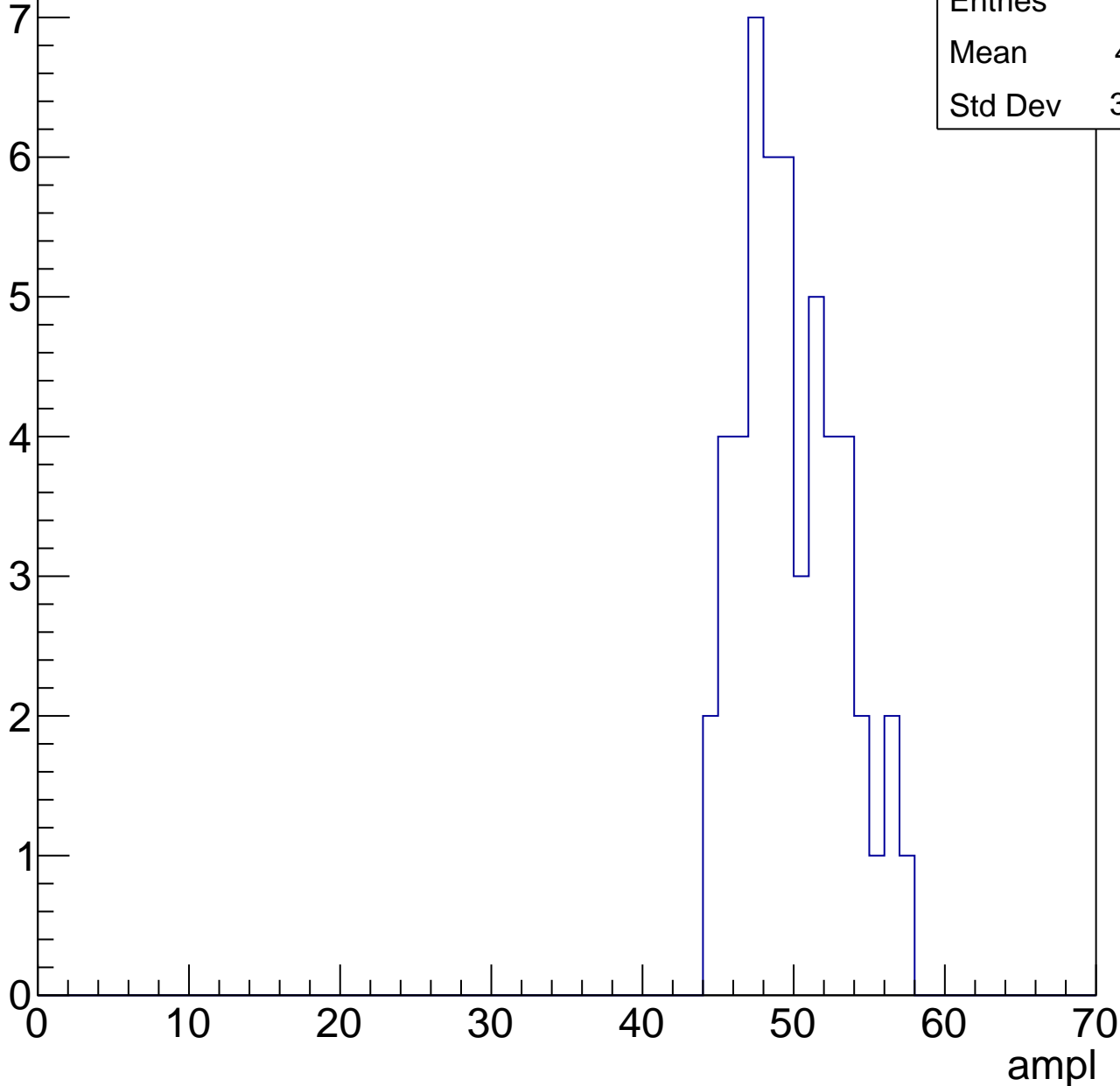


B1L102S, U10-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.41
Std Dev	3.273

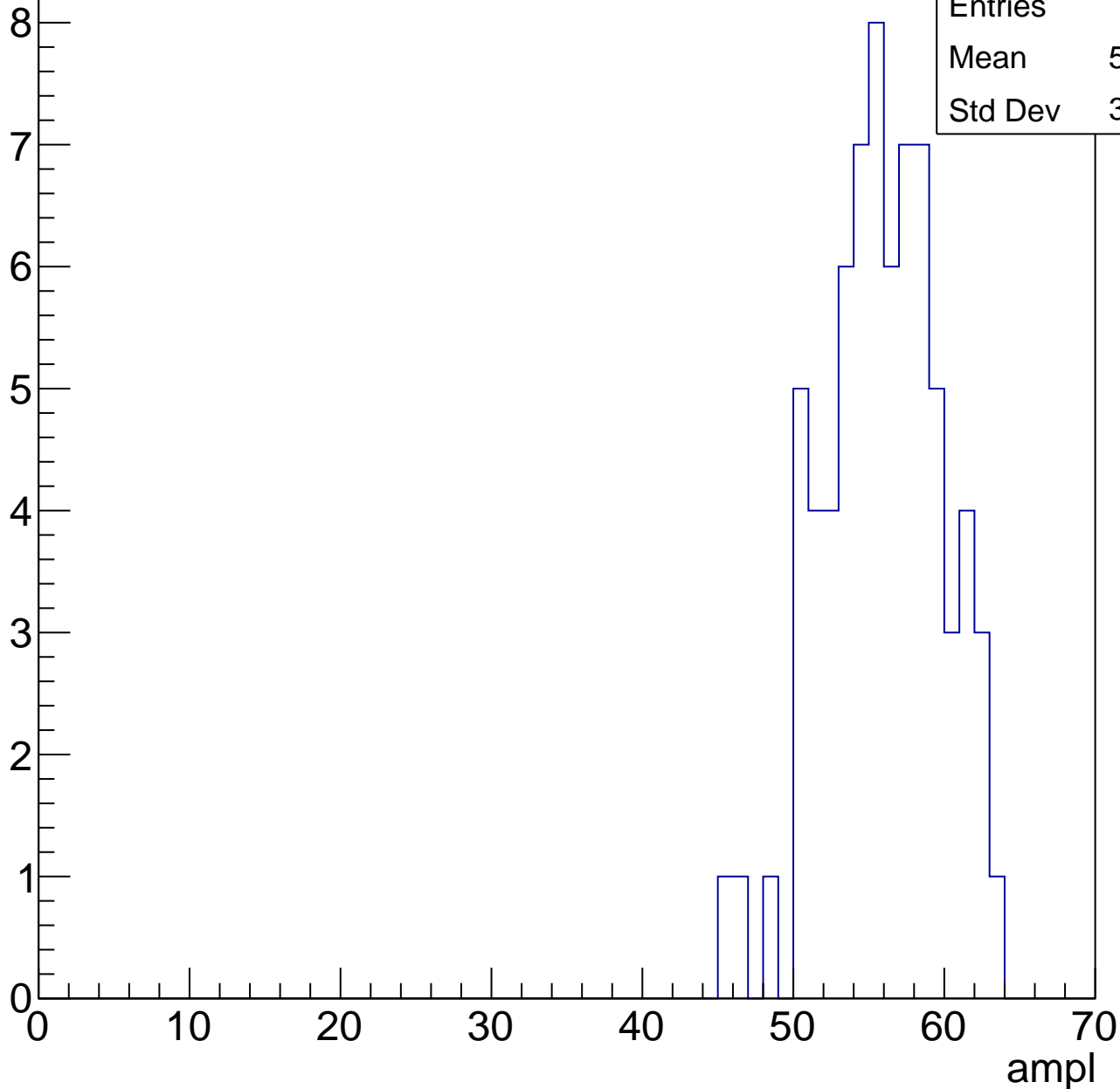


B1L102S, U10-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	55.42
Std Dev	3.846



B1L102S, U10-ch79, adc5

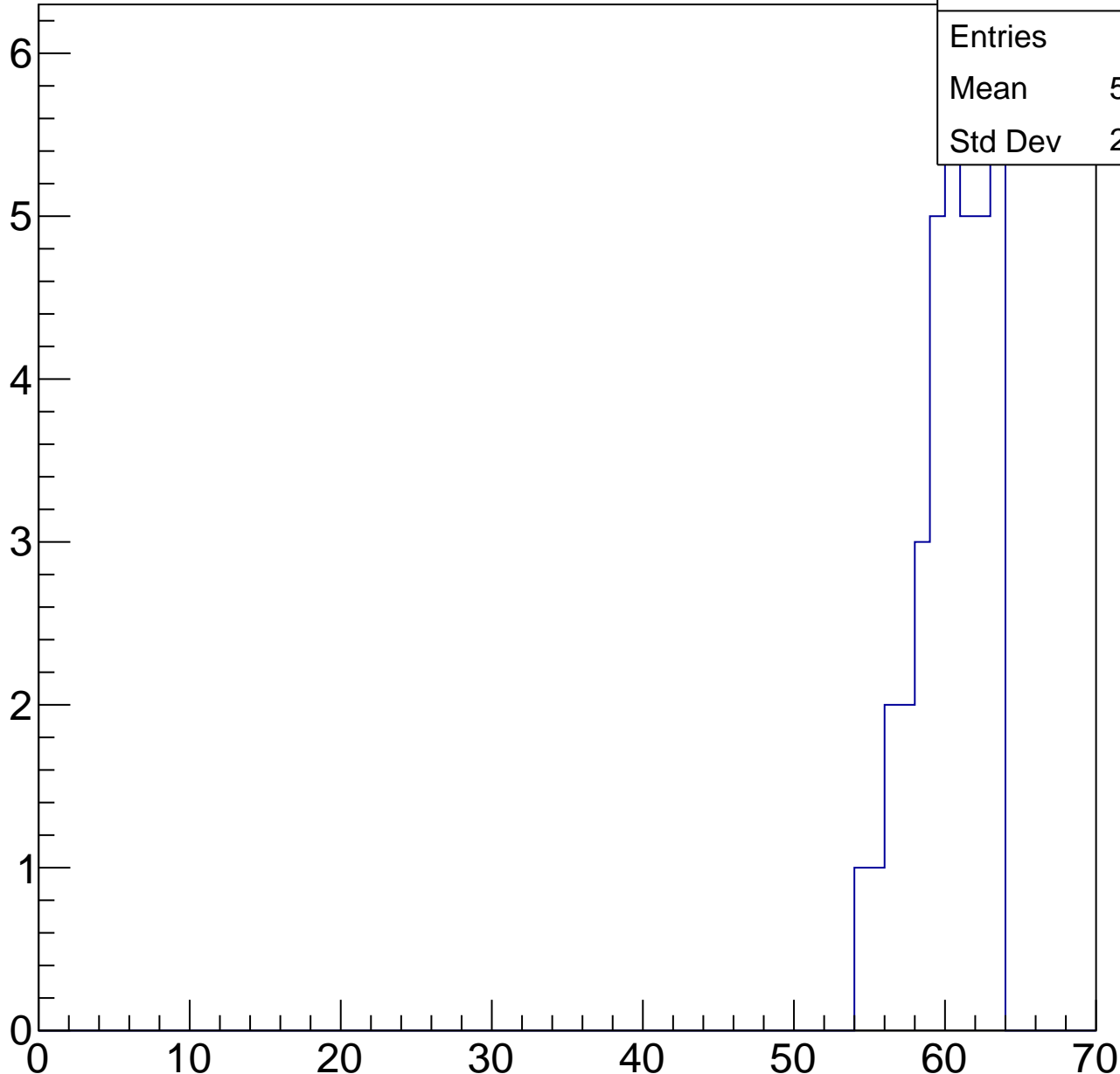
calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	36
Mean	59.92
Std Dev	2.396

ampl



B1L102S, U10-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

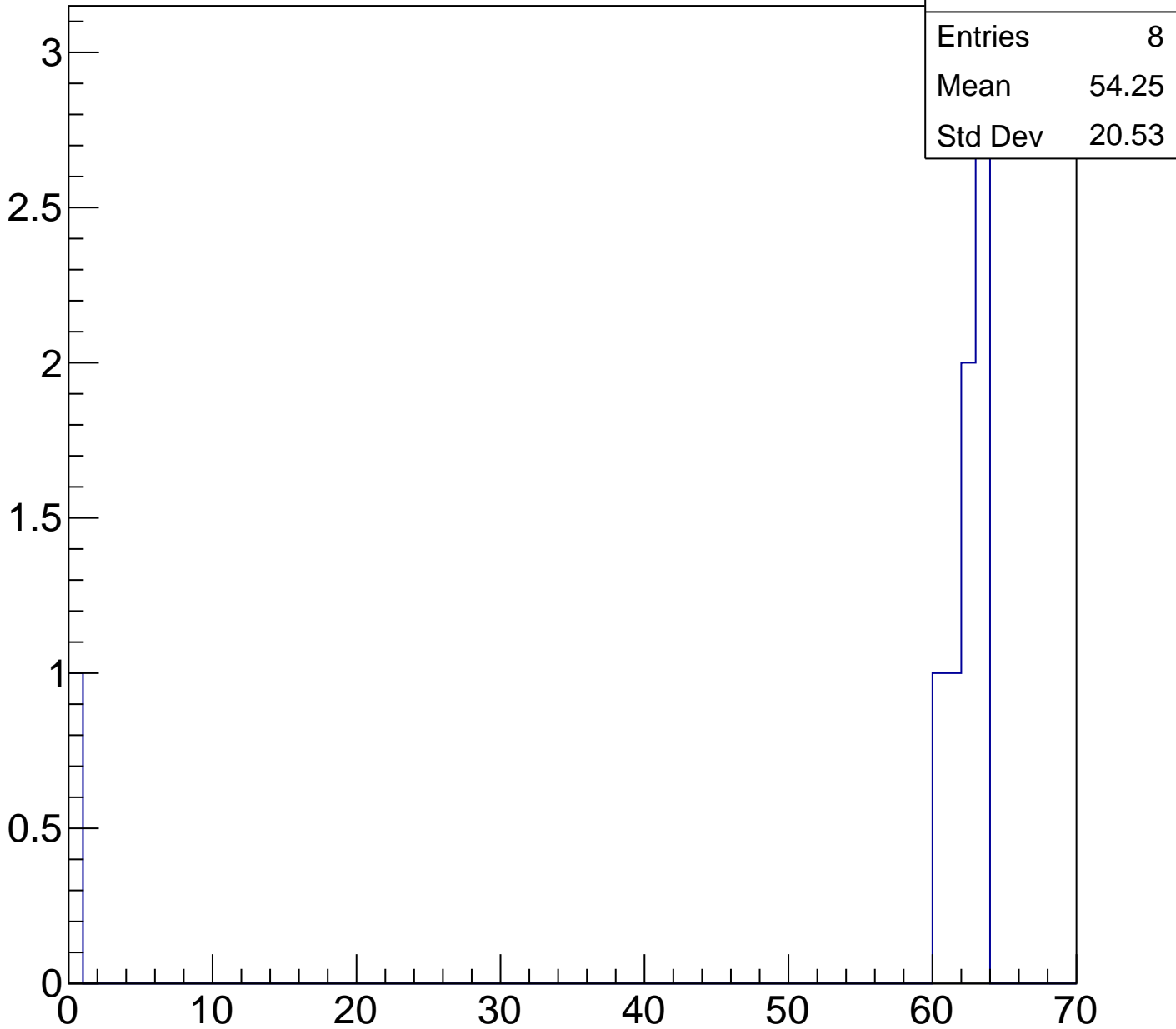
1

0.5

0

ampl

Entries	8
Mean	54.25
Std Dev	20.53



B1L102S, U10-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch80, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	29.88
Std Dev	5.041

Gaus mean : 30.4691

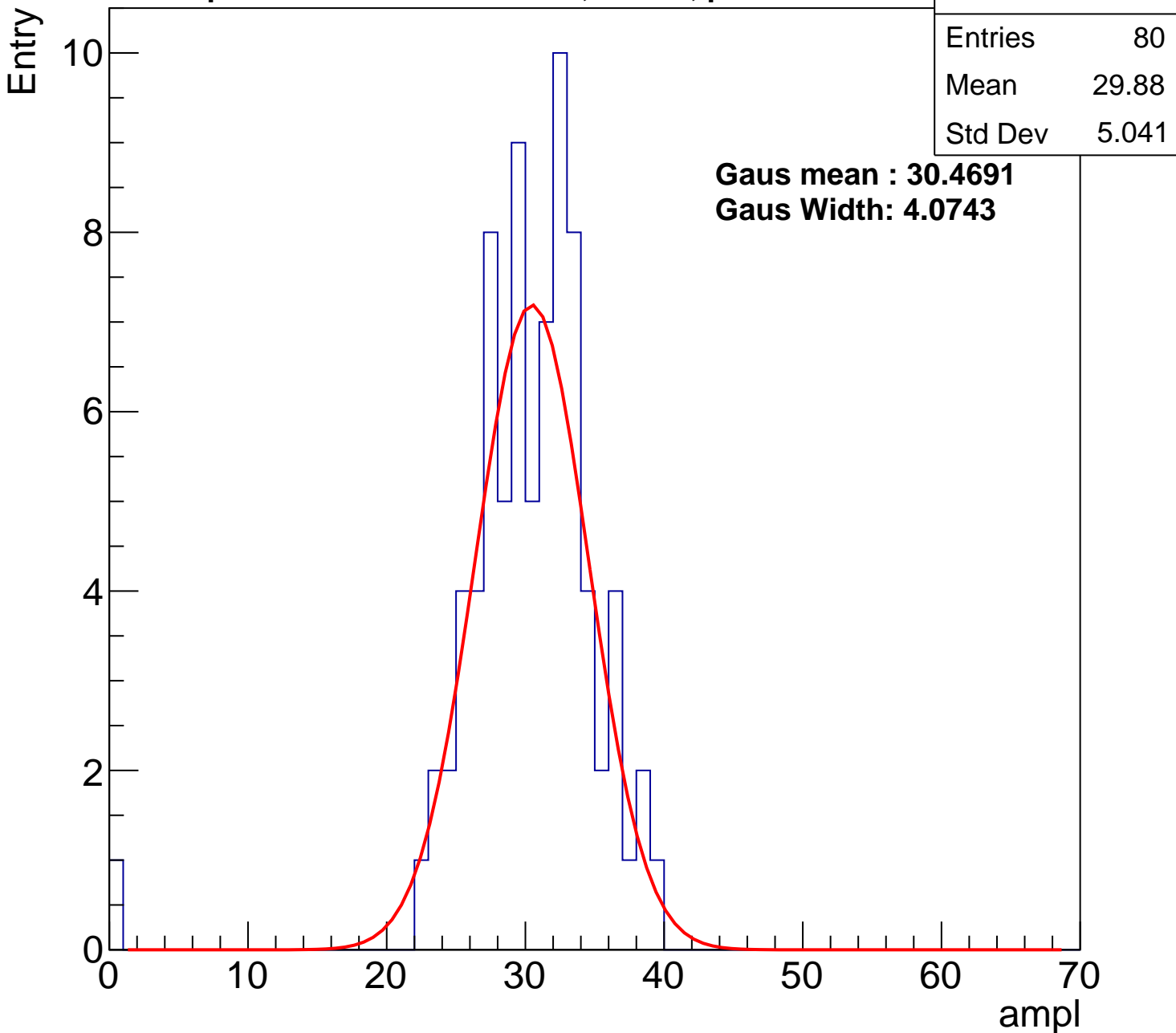
Gaus Width: 4.0743

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch80, adc1

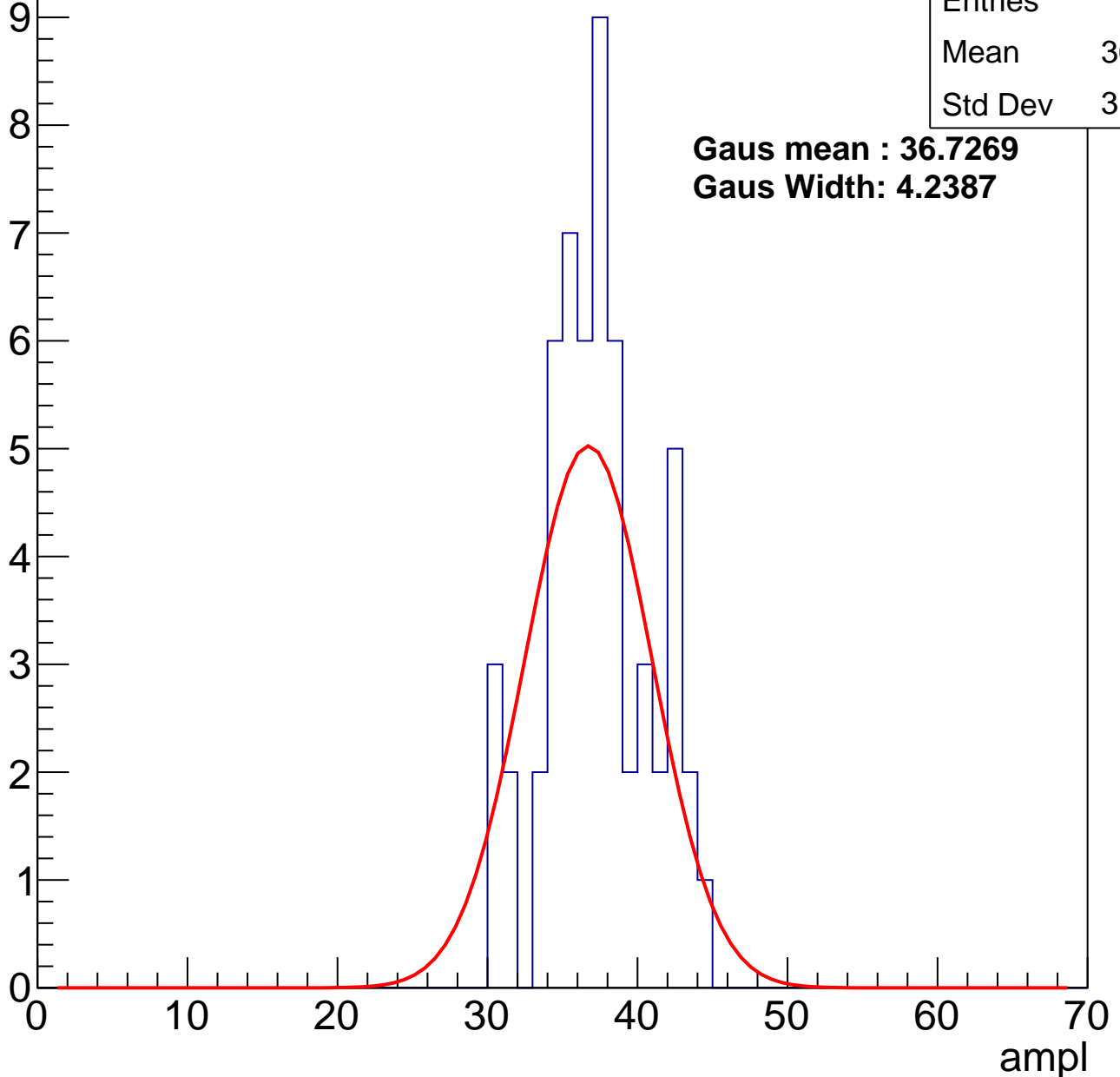
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	36.86
Std Dev	3.425

Gaus mean : 36.7269

Gaus Width: 4.2387



B1L102S, U10-ch80, adc2

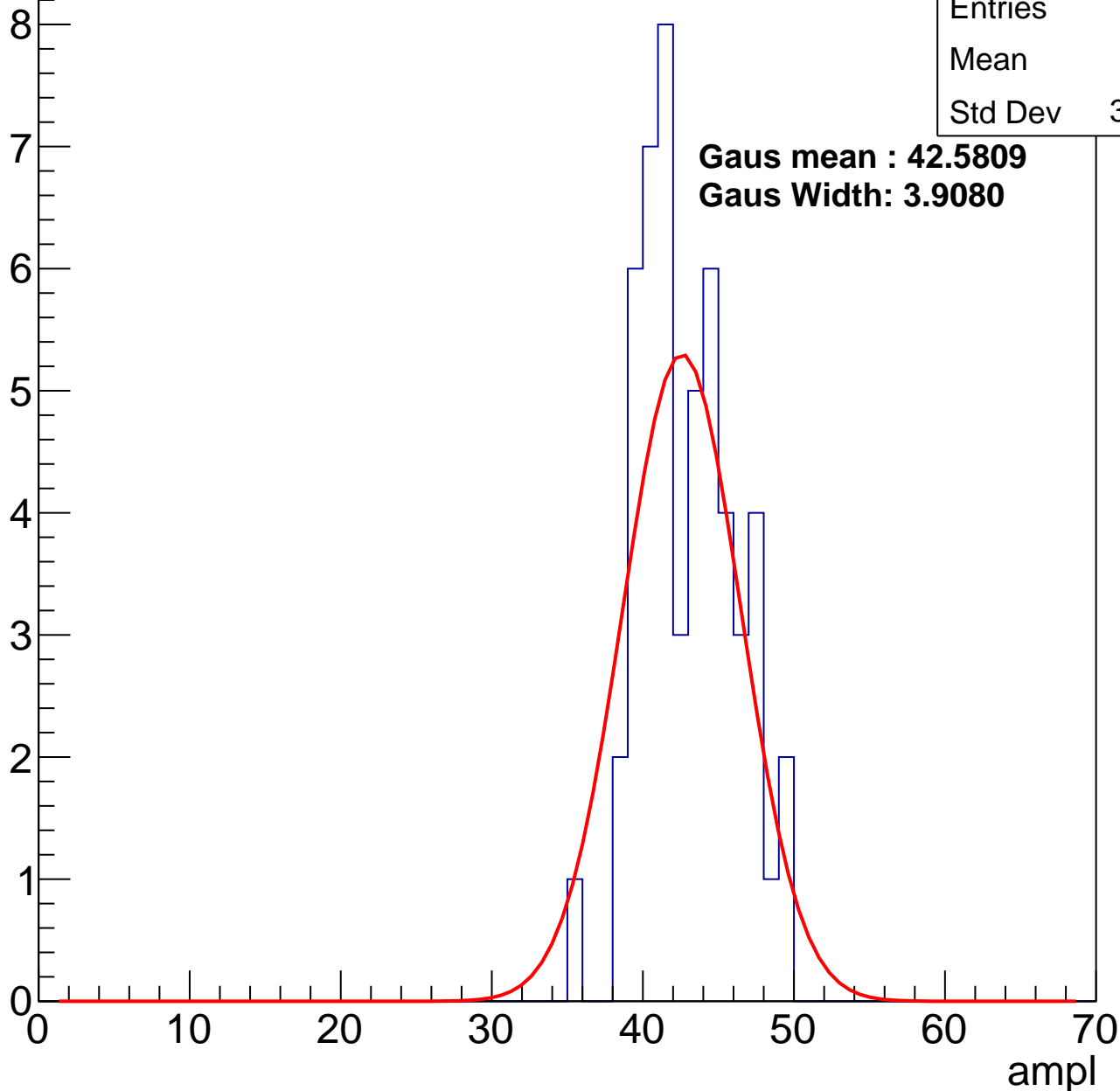
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.5
Std Dev	3.116

Gaus mean : 42.5809

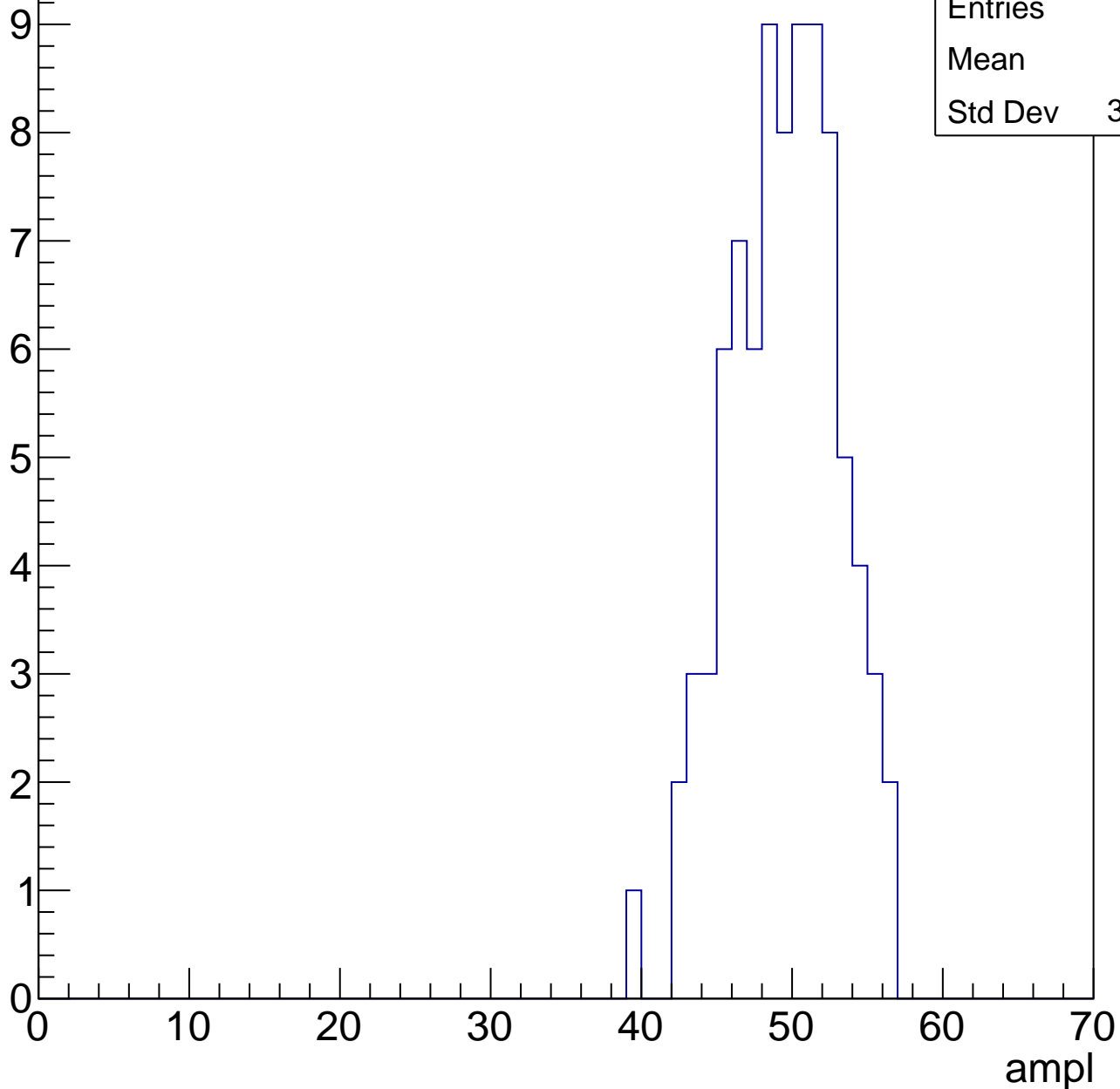
Gaus Width: 3.9080



B1L102S, U10-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



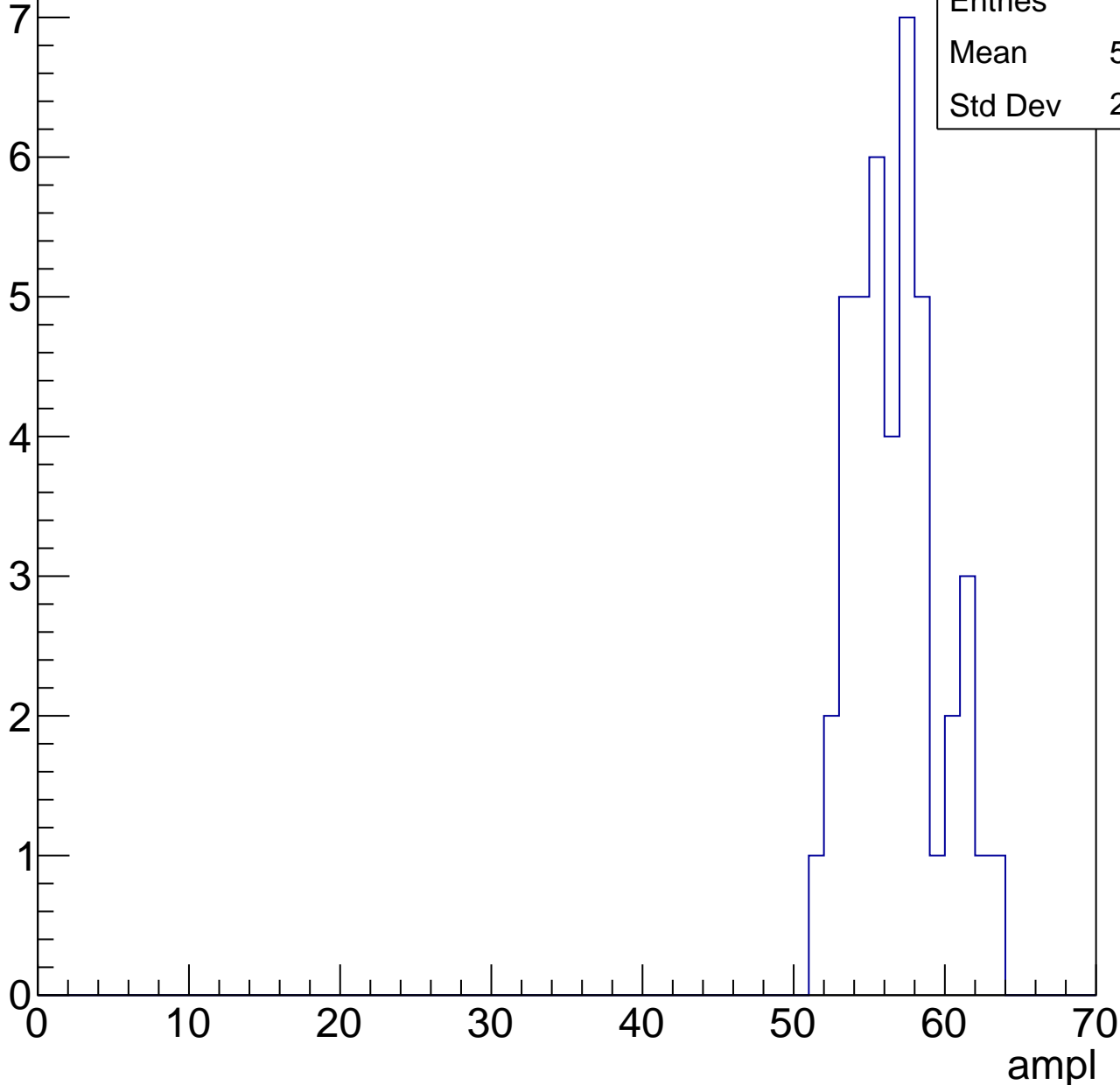
Entries	85
Mean	49
Std Dev	3.558

B1L102S, U10-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	56.28
Std Dev	2.864

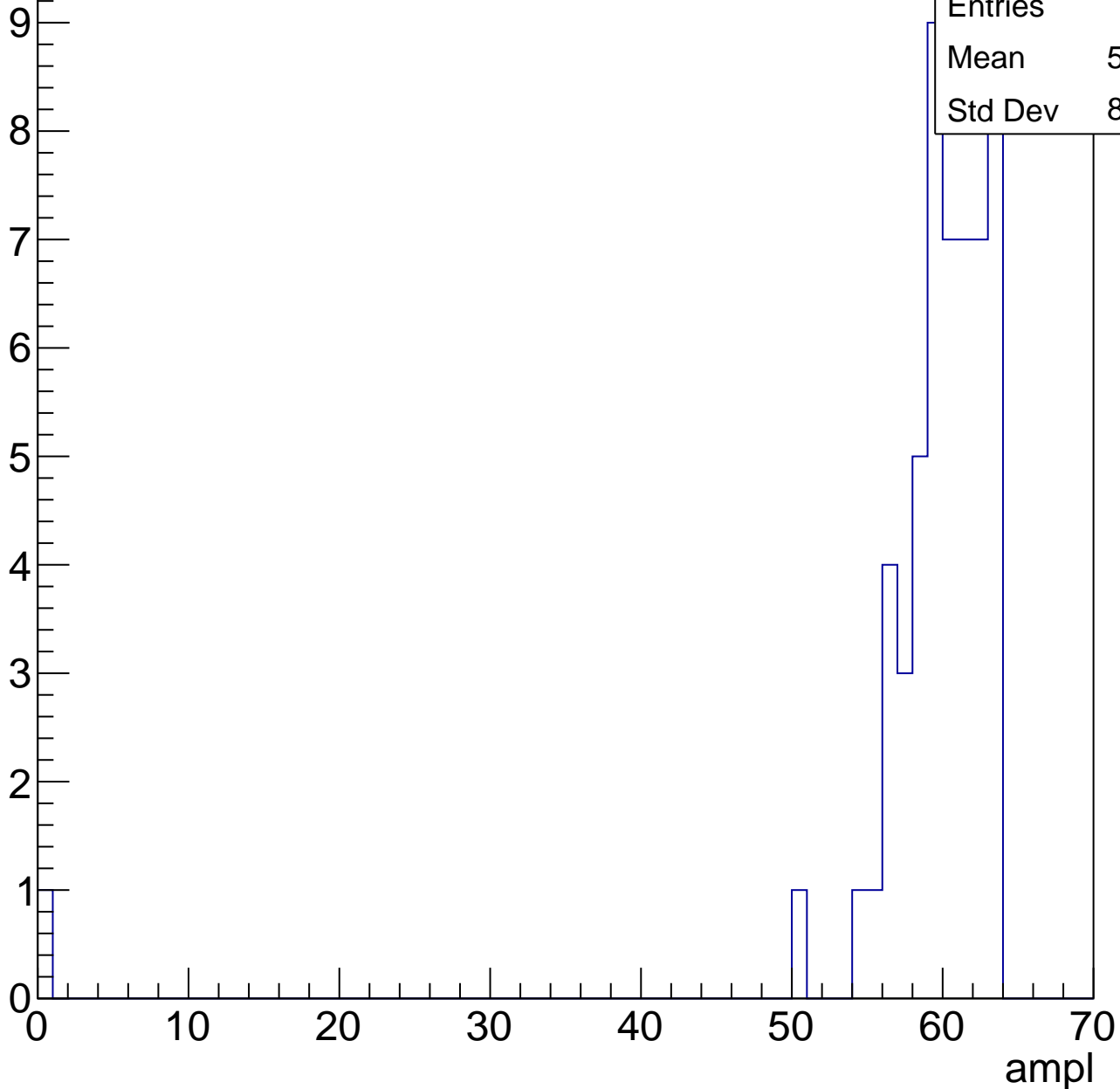


B1L102S, U10-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.52
Std Dev	8.465



B1L102S, U10-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch81, adc0

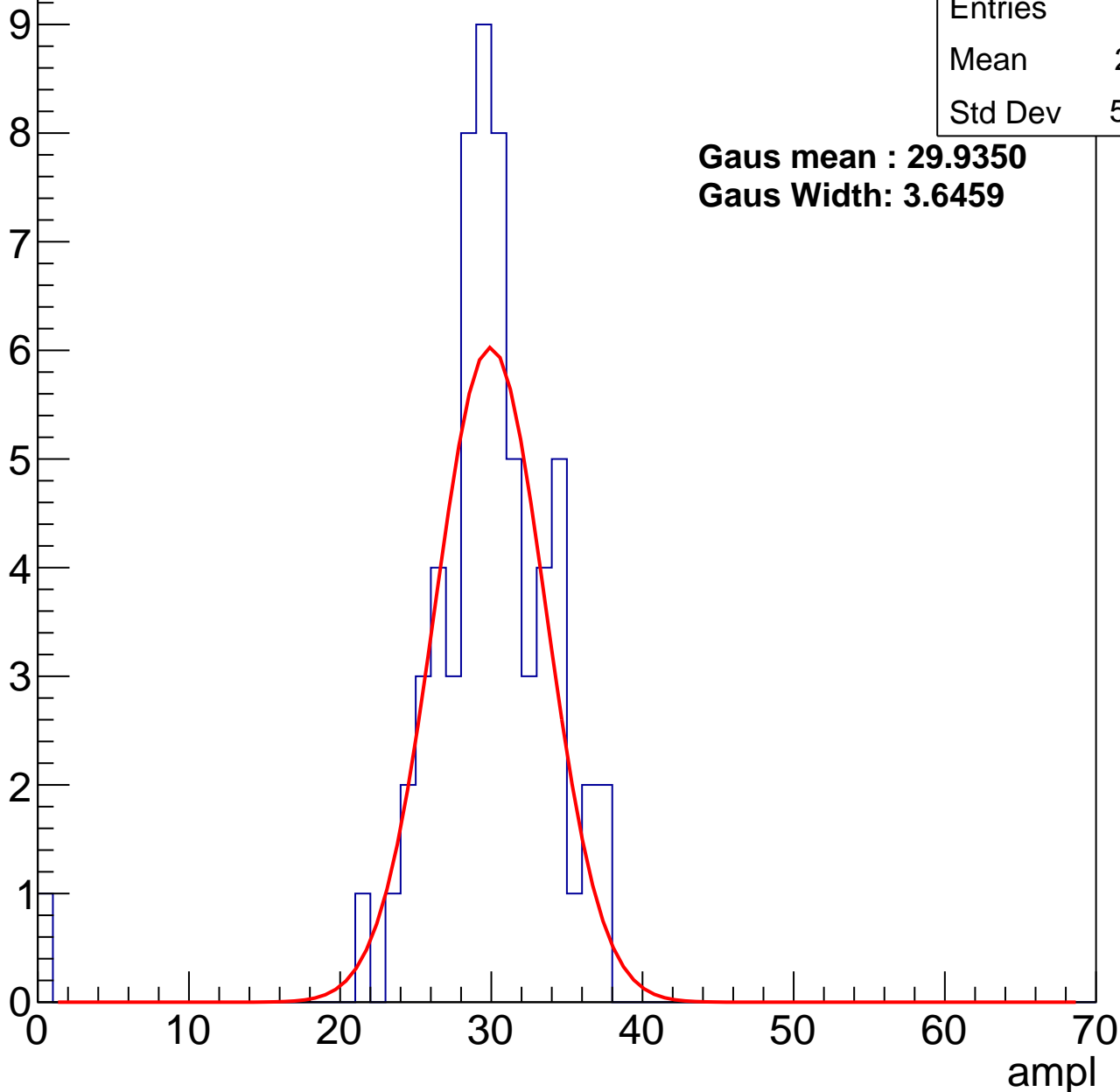
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.21
Std Dev	5.077

Gaus mean : 29.9350

Gaus Width: 3.6459



B1L102S, U10-ch81, adc1

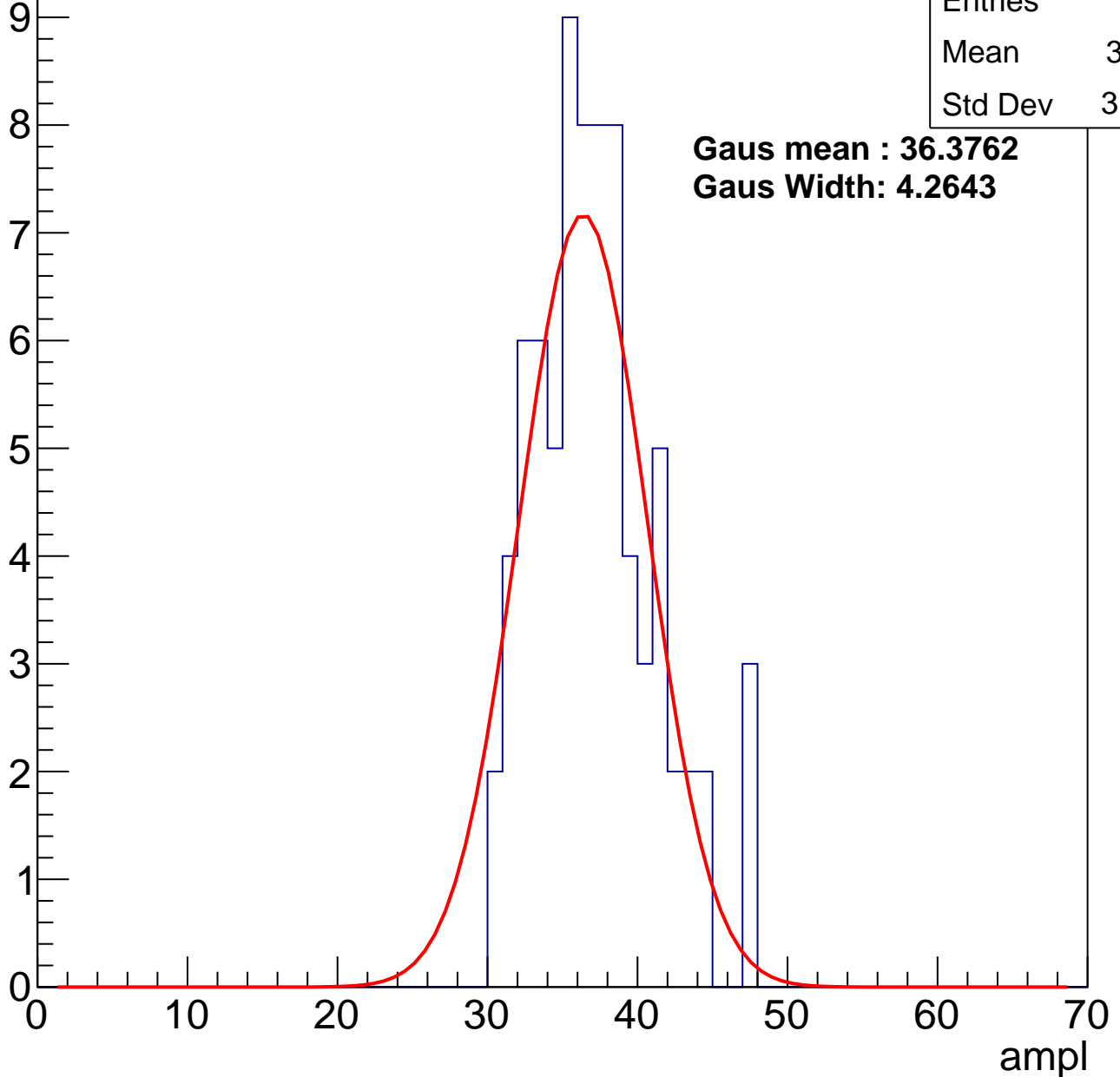
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.71
Std Dev	3.983

Gaus mean : 36.3762

Gaus Width: 4.2643

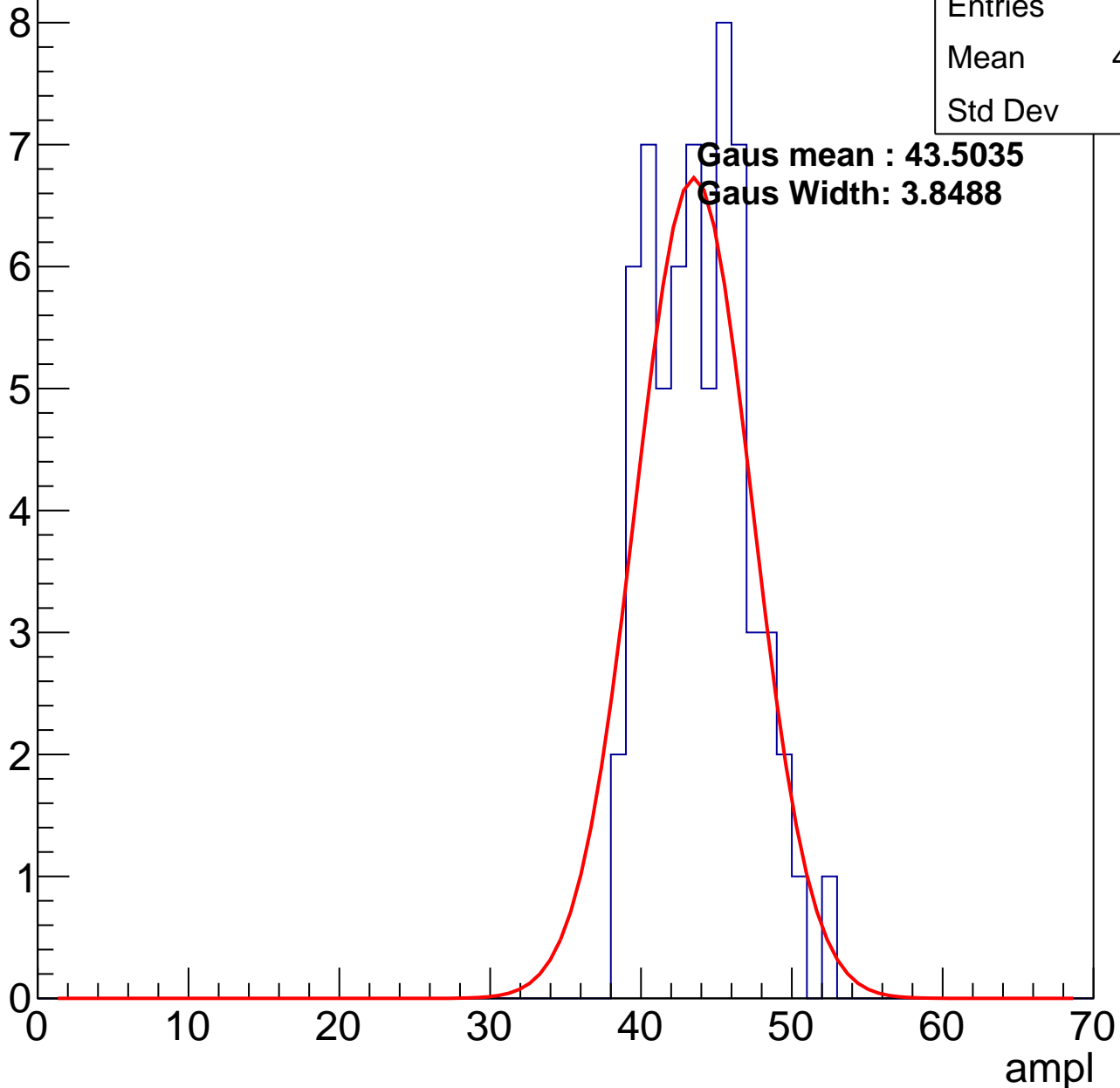


B1L102S, U10-ch81, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.41
Std Dev	3.21

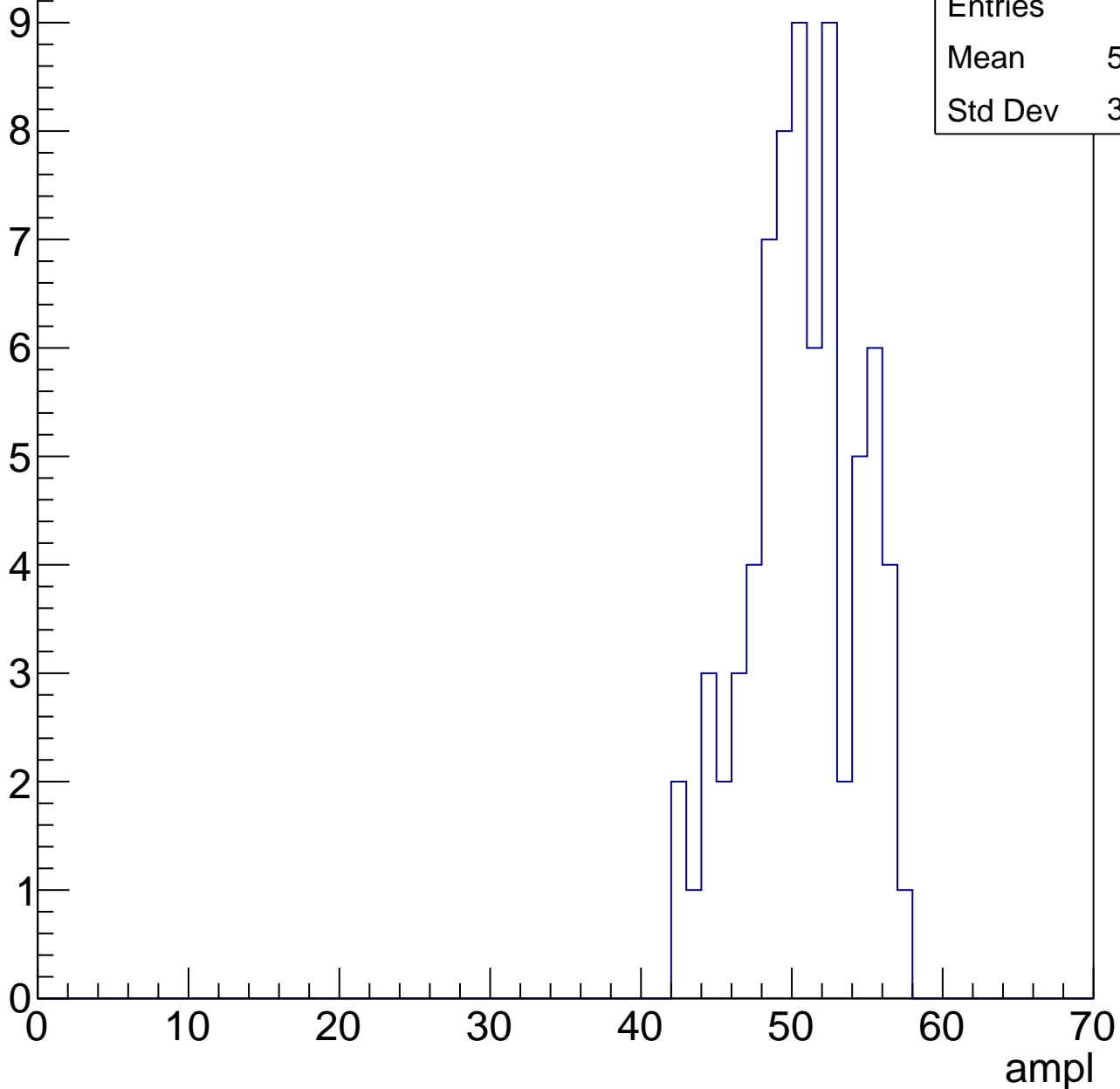


B1L102S, U10-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	50.19
Std Dev	3.604

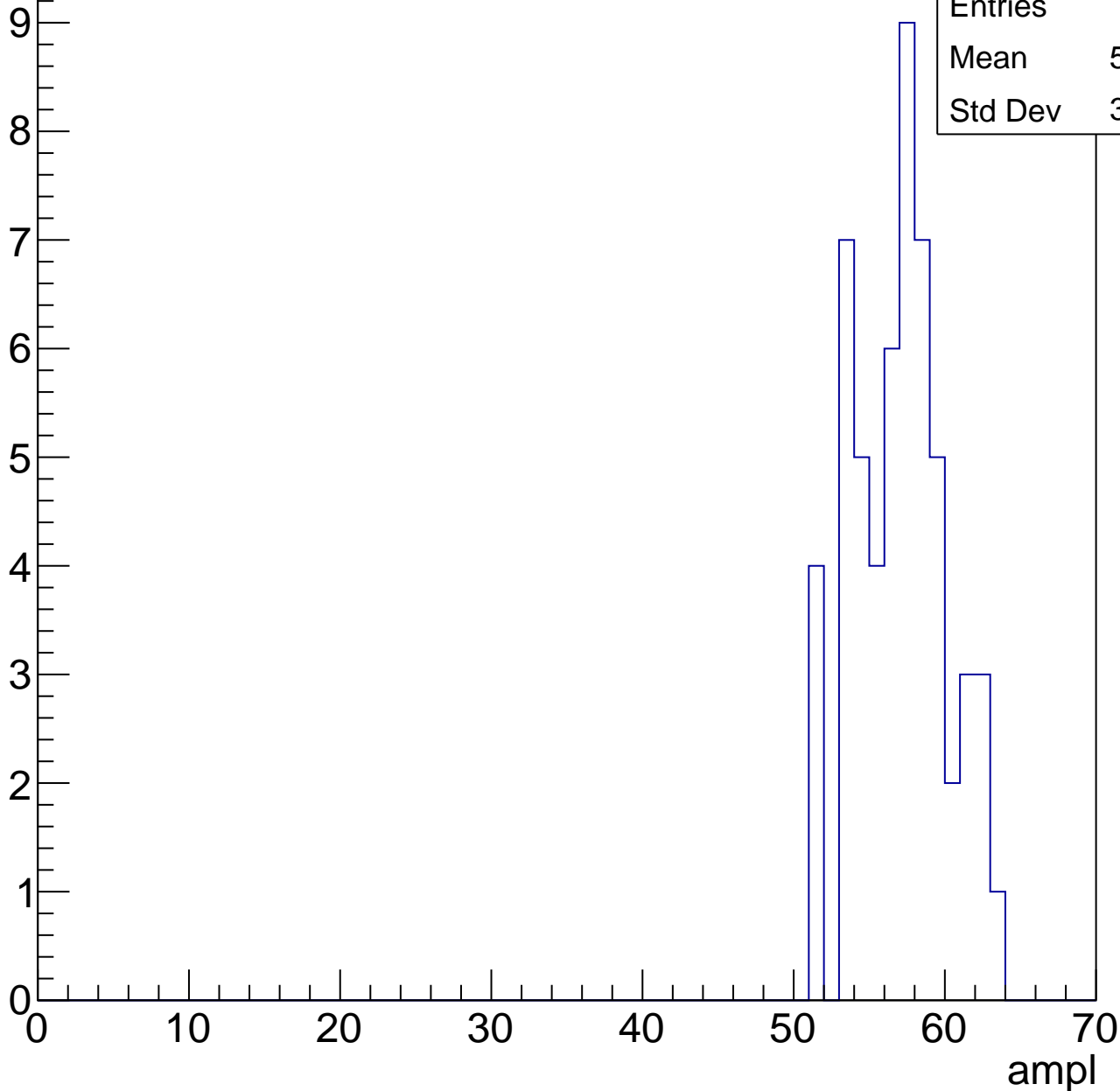


B1L102S, U10-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

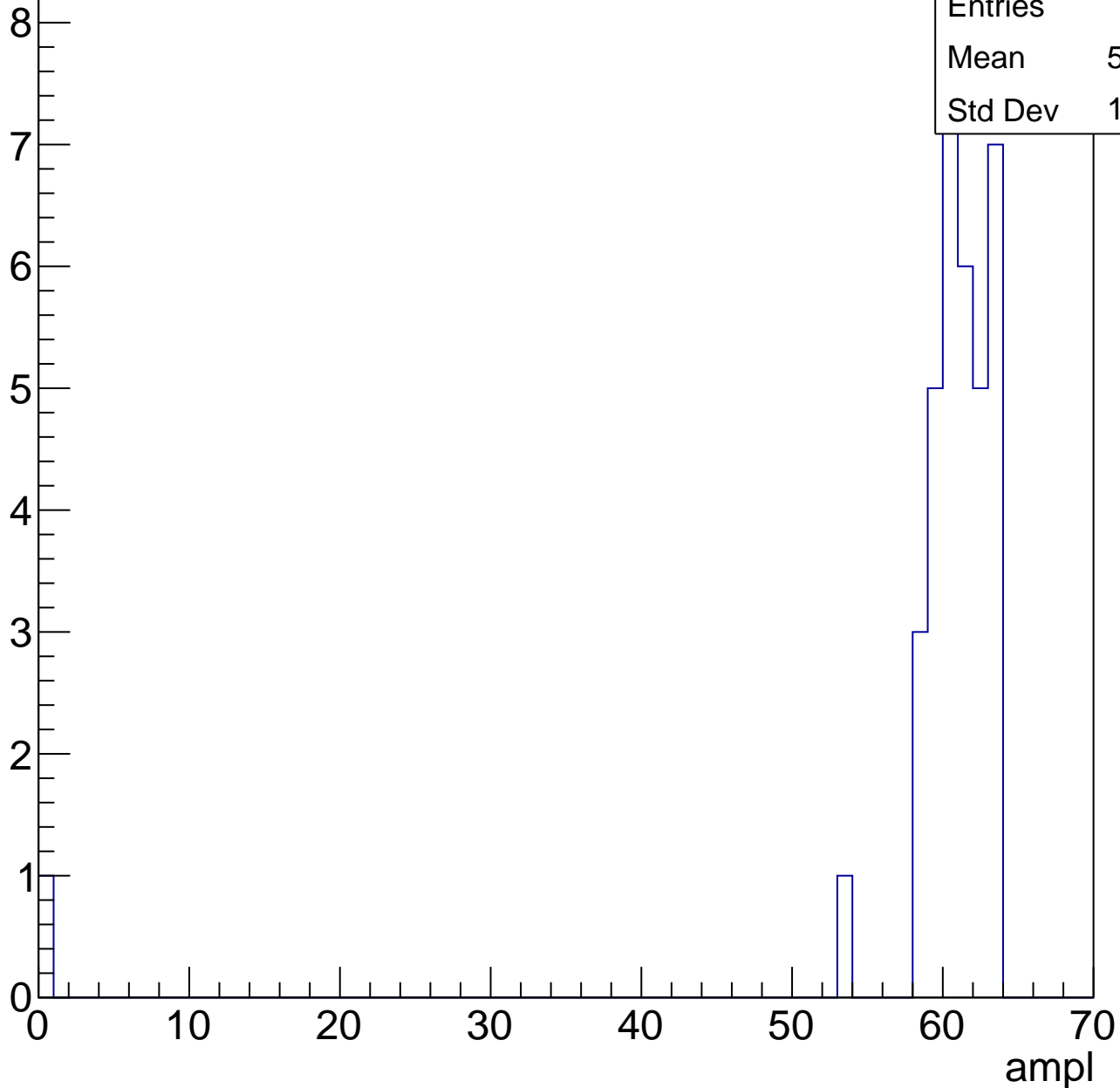
Entries	56
Mean	56.55
Std Dev	3.035



B1L102S, U10-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch82, adc0

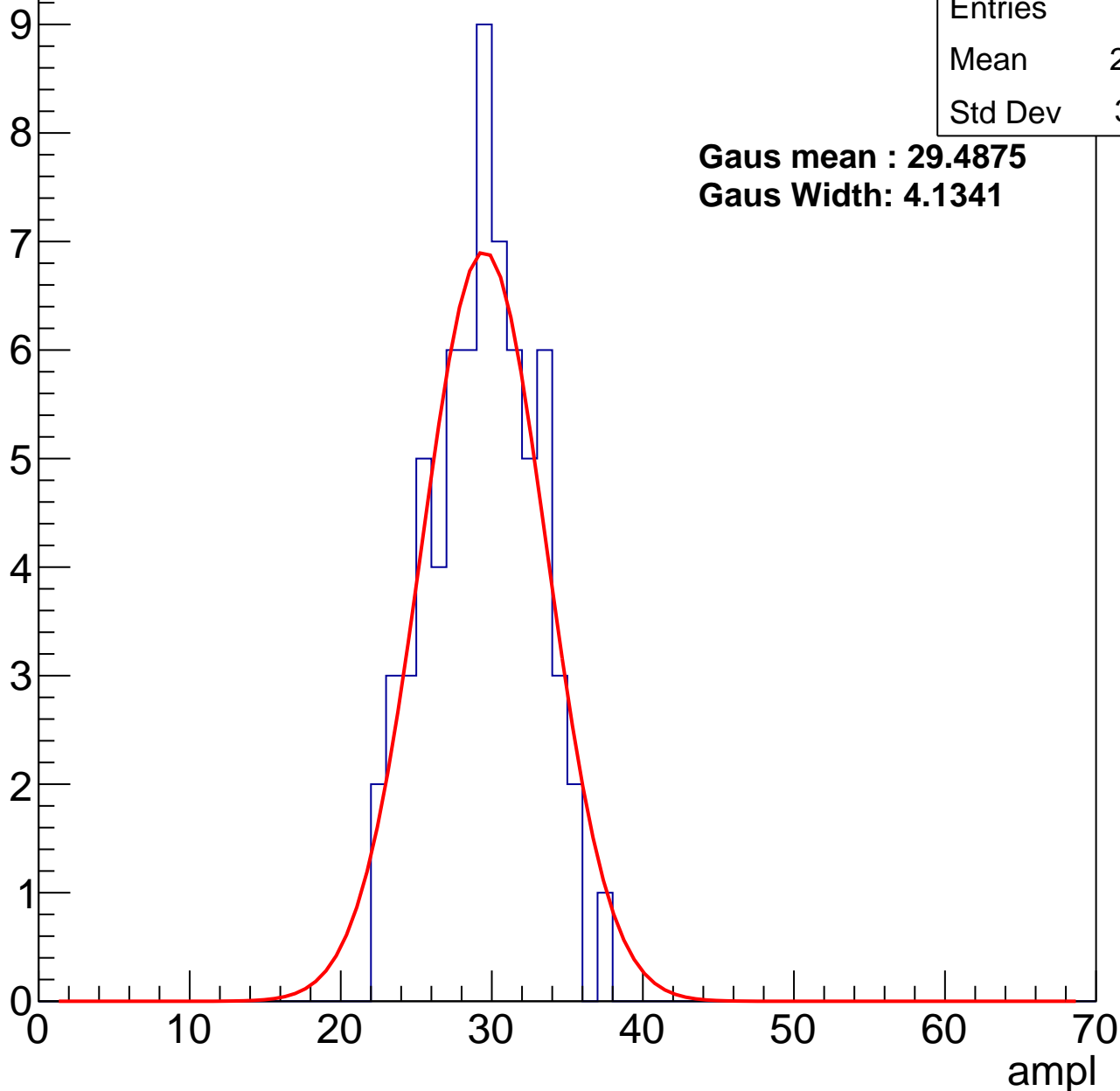
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.94
Std Dev	3.451

Gaus mean : 29.4875

Gaus Width: 4.1341



B1L102S, U10-ch82, adc1

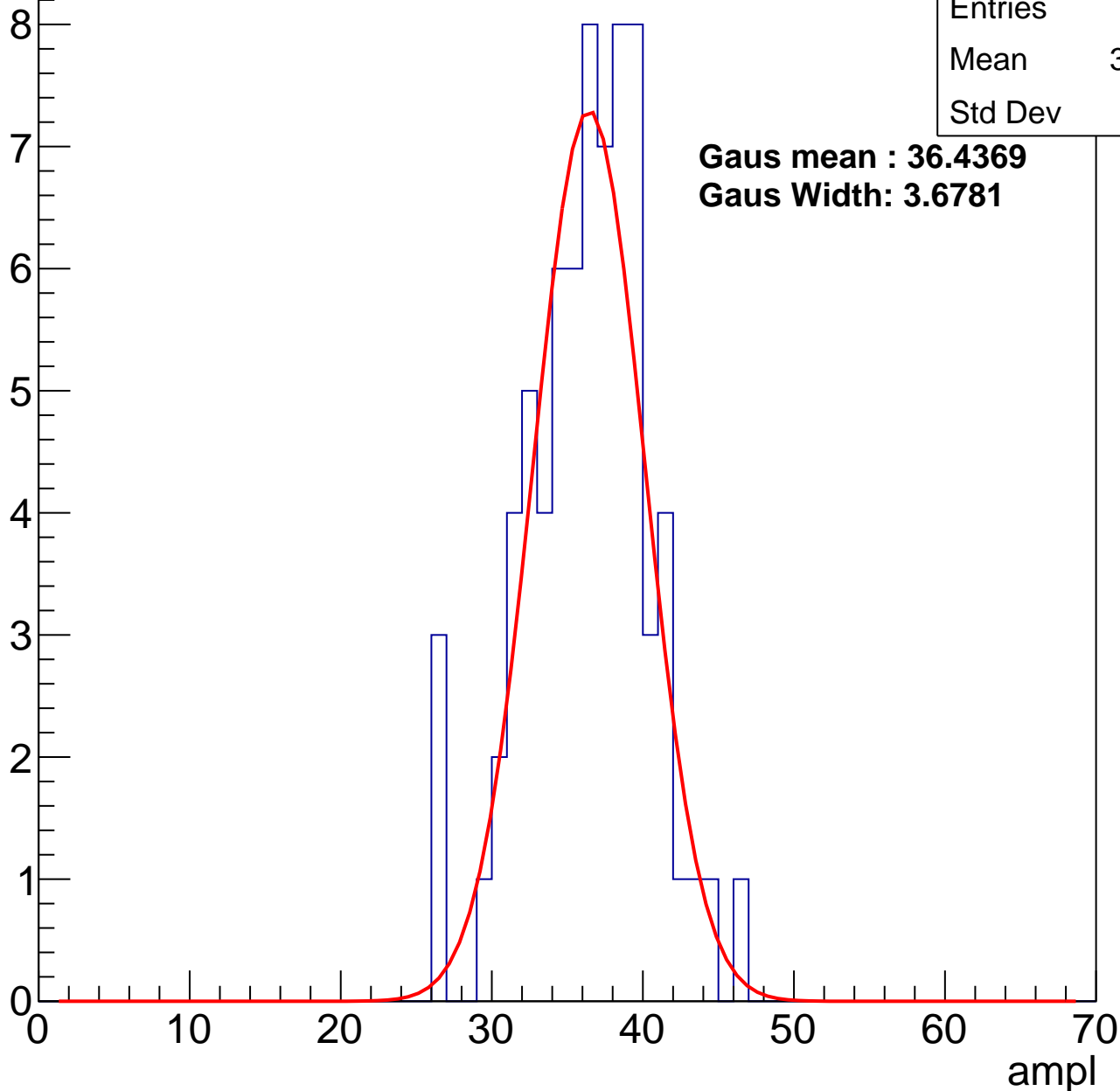
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.88
Std Dev	4.01

Gaus mean : 36.4369

Gaus Width: 3.6781



B1L102S, U10-ch82, adc2

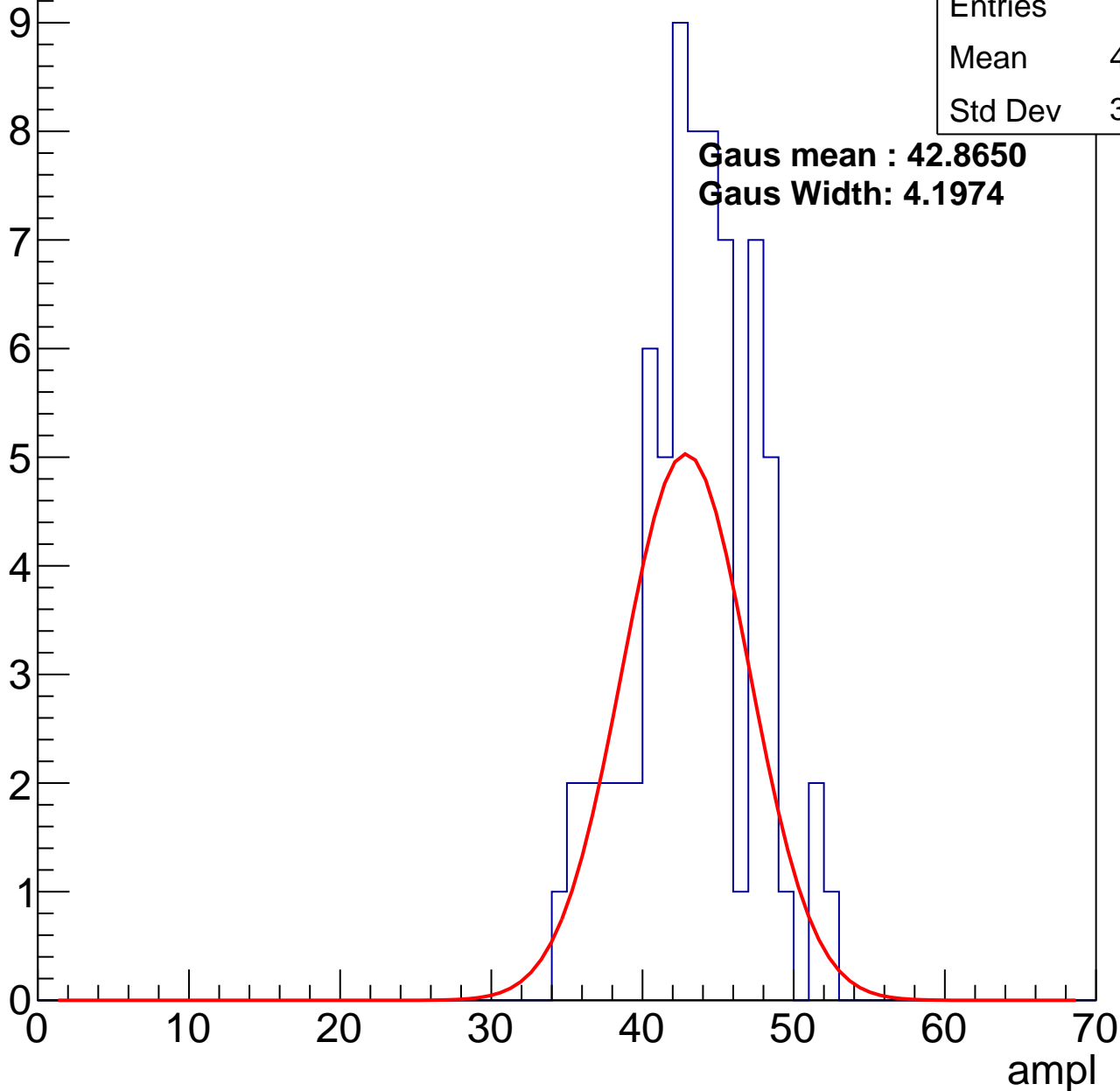
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.04
Std Dev	3.884

Gaus mean : 42.8650

Gaus Width: 4.1974

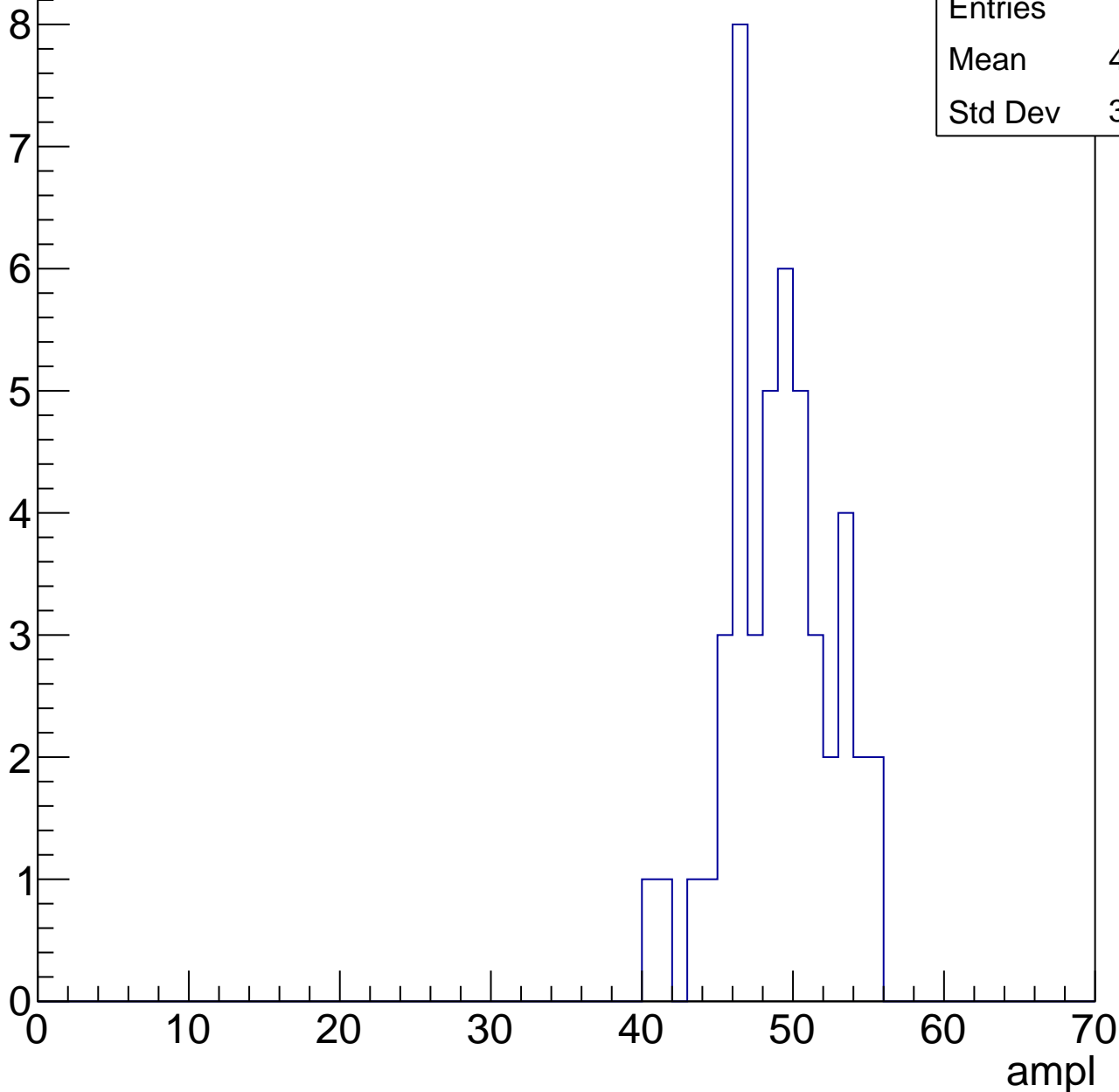


B1L102S, U10-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	48.57
Std Dev	3.438



B1L102S, U10-ch82, adc4

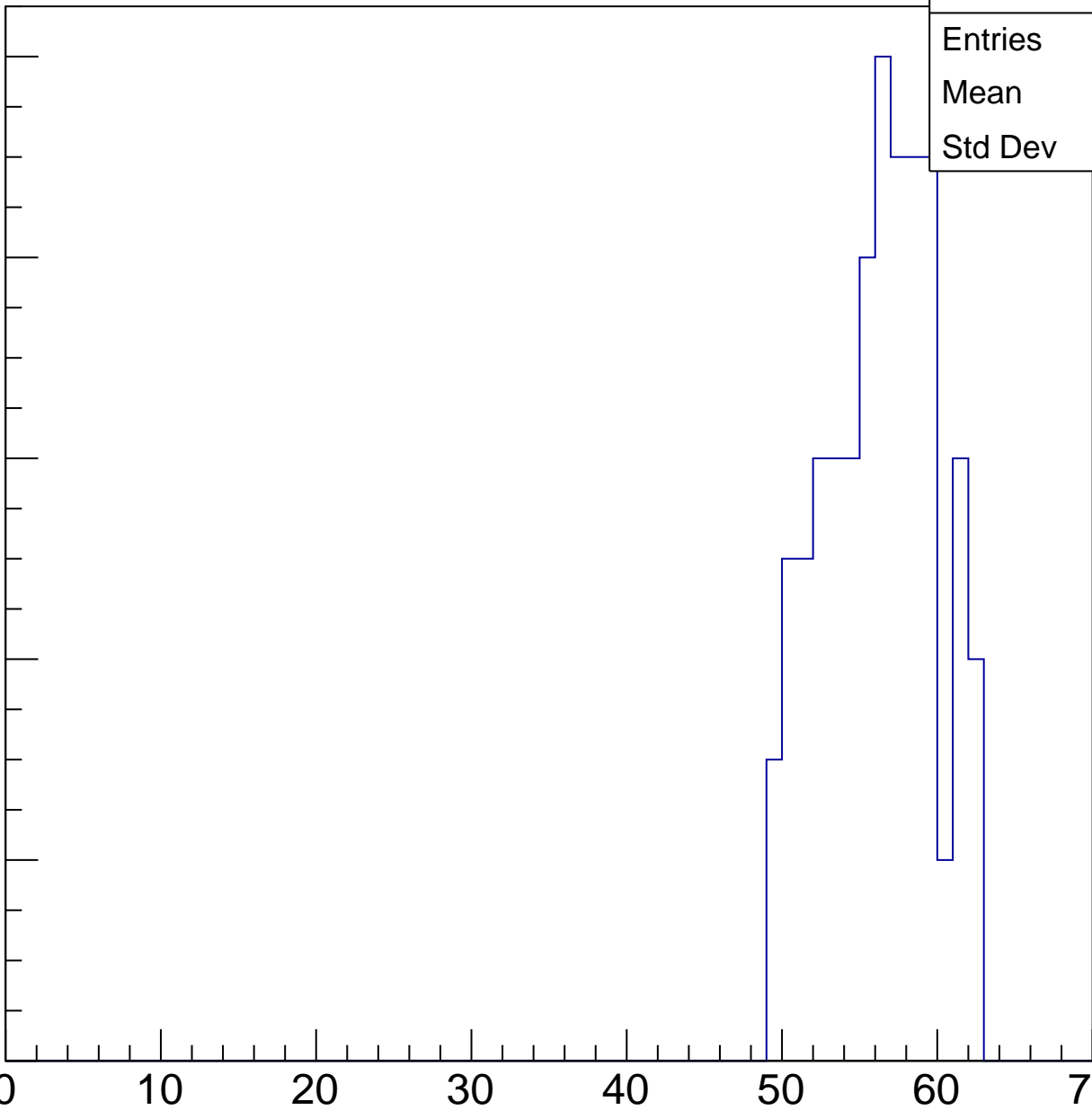
calib_packv5_042523_0143.root, FC#11, port A2

Entry

10
8
6
4
2
0

Entries	88
Mean	55.75
Std Dev	3.504

ampl



B1L102S, U10-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

28

Mean

58

Std Dev

11.51

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

7

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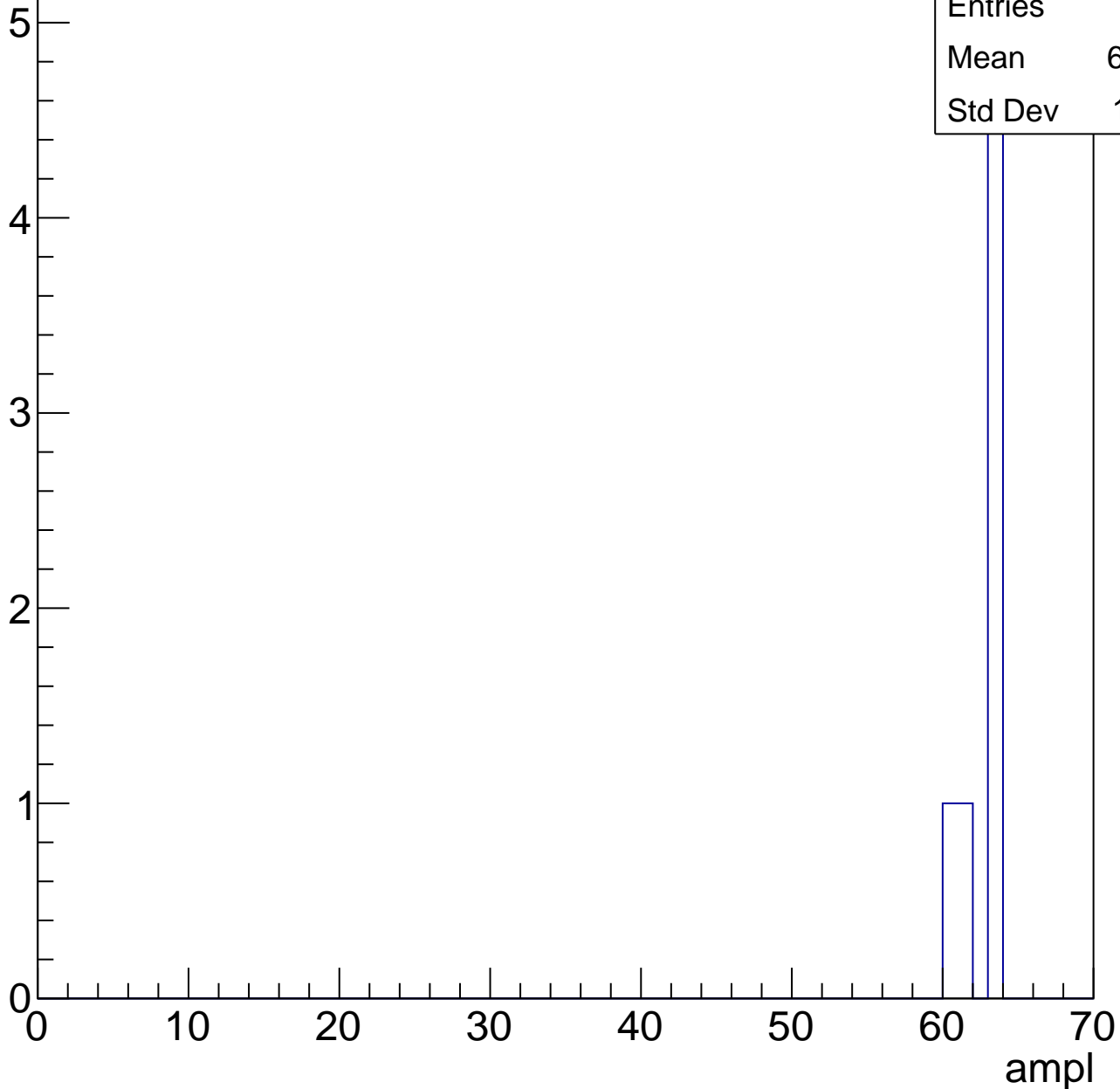
307

B1L102S, U10-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.29
Std Dev	1.161



B1L102S, U10-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch83, adc0

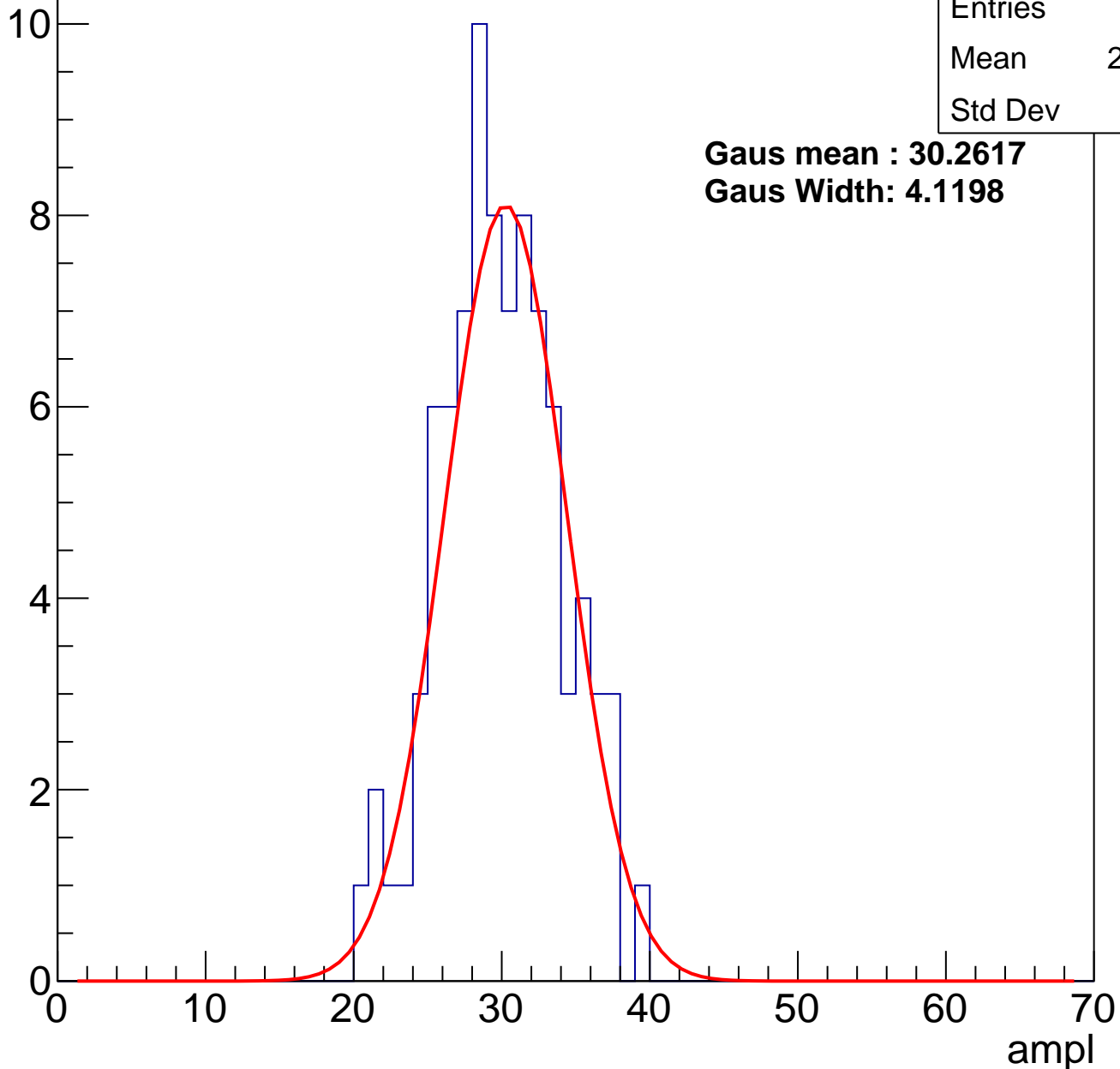
calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	29.49
Std Dev	4

Gaus mean : 30.2617

Gaus Width: 4.1198

Entry



B1L102S, U10-ch83, adc1

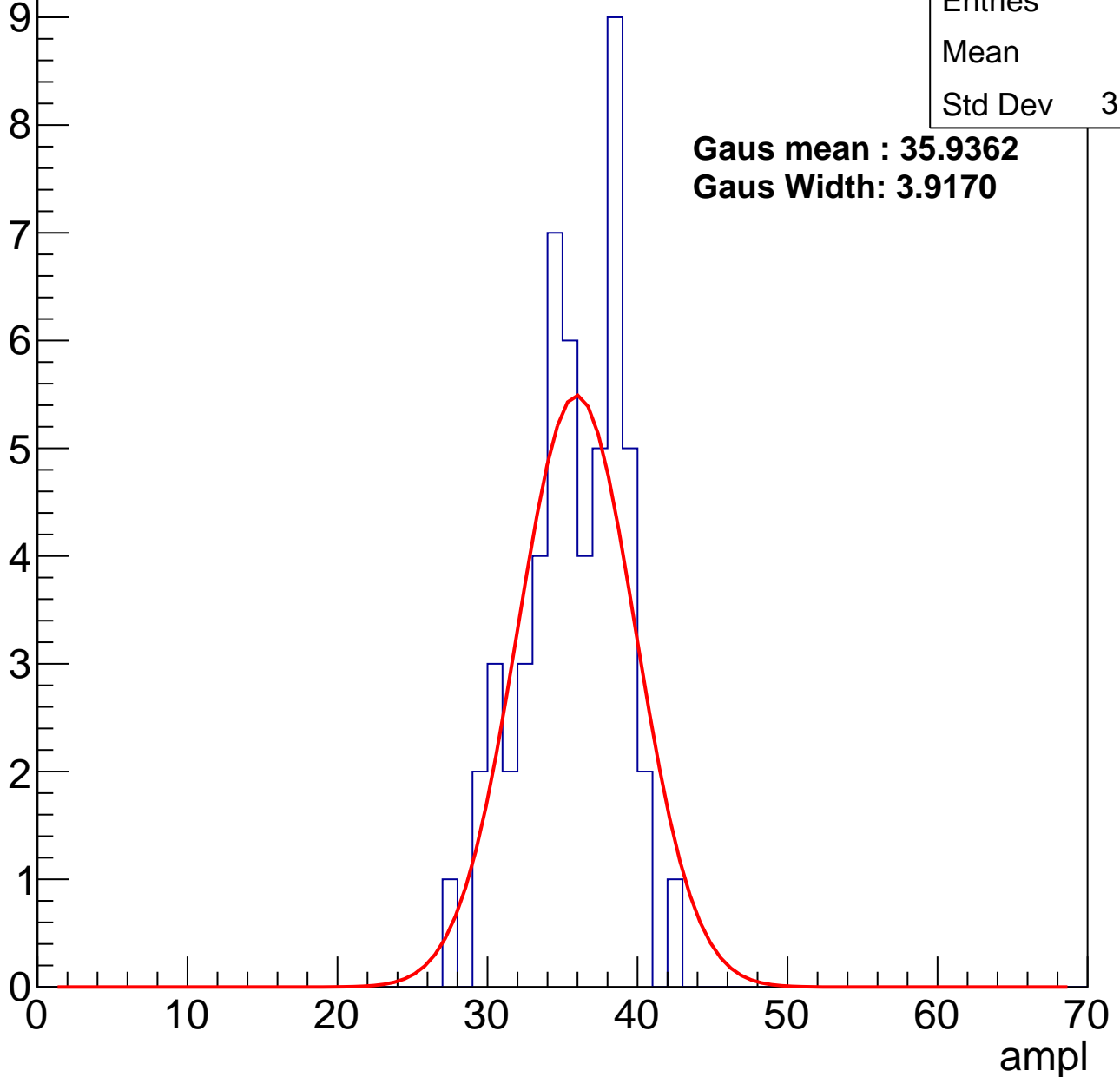
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.2
Std Dev	3.262

Gaus mean : 35.9362

Gaus Width: 3.9170



B1L102S, U10-ch83, adc2

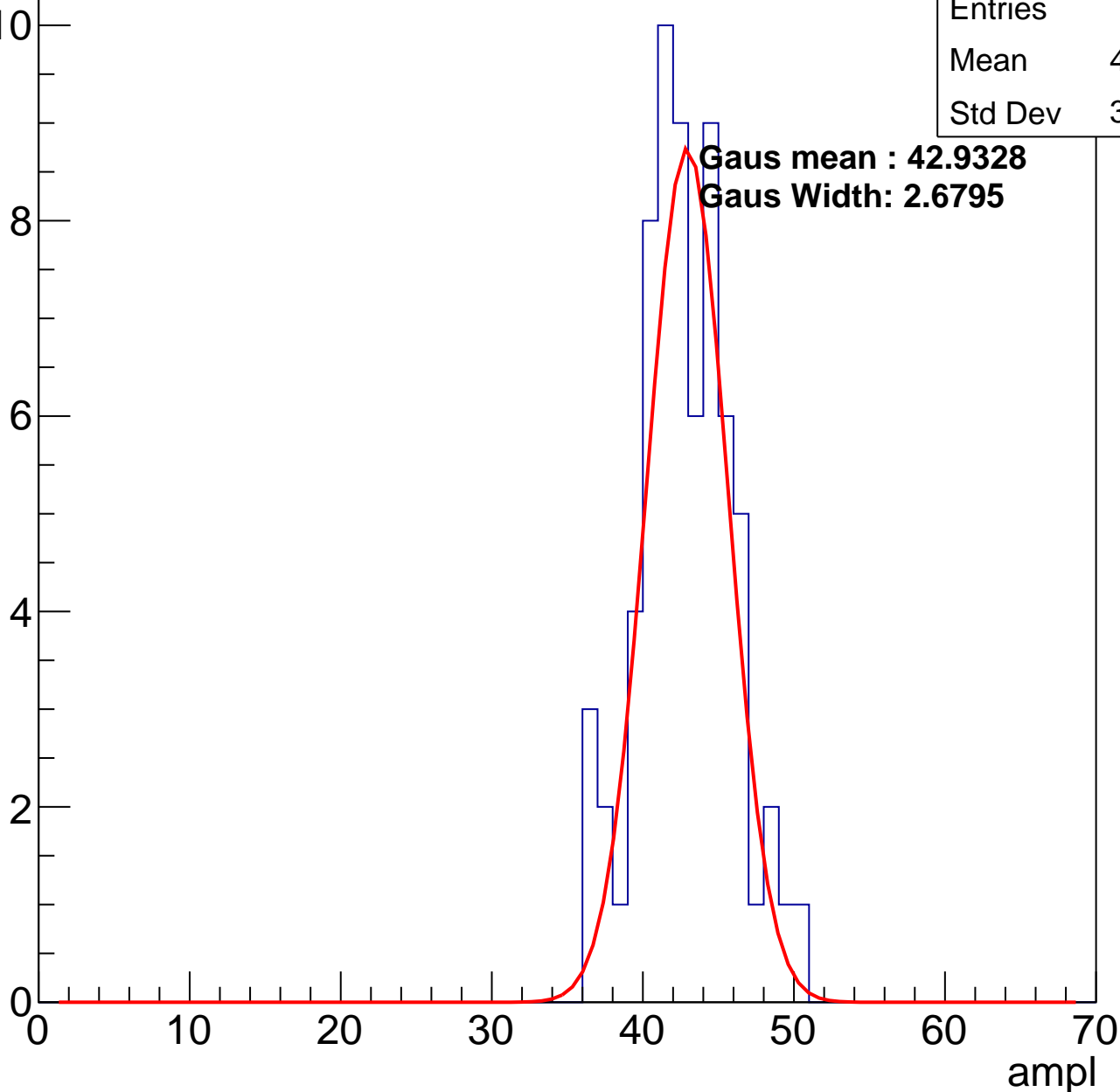
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.35
Std Dev	3.043

Gaus mean : 42.9328

Gaus Width: 2.6795

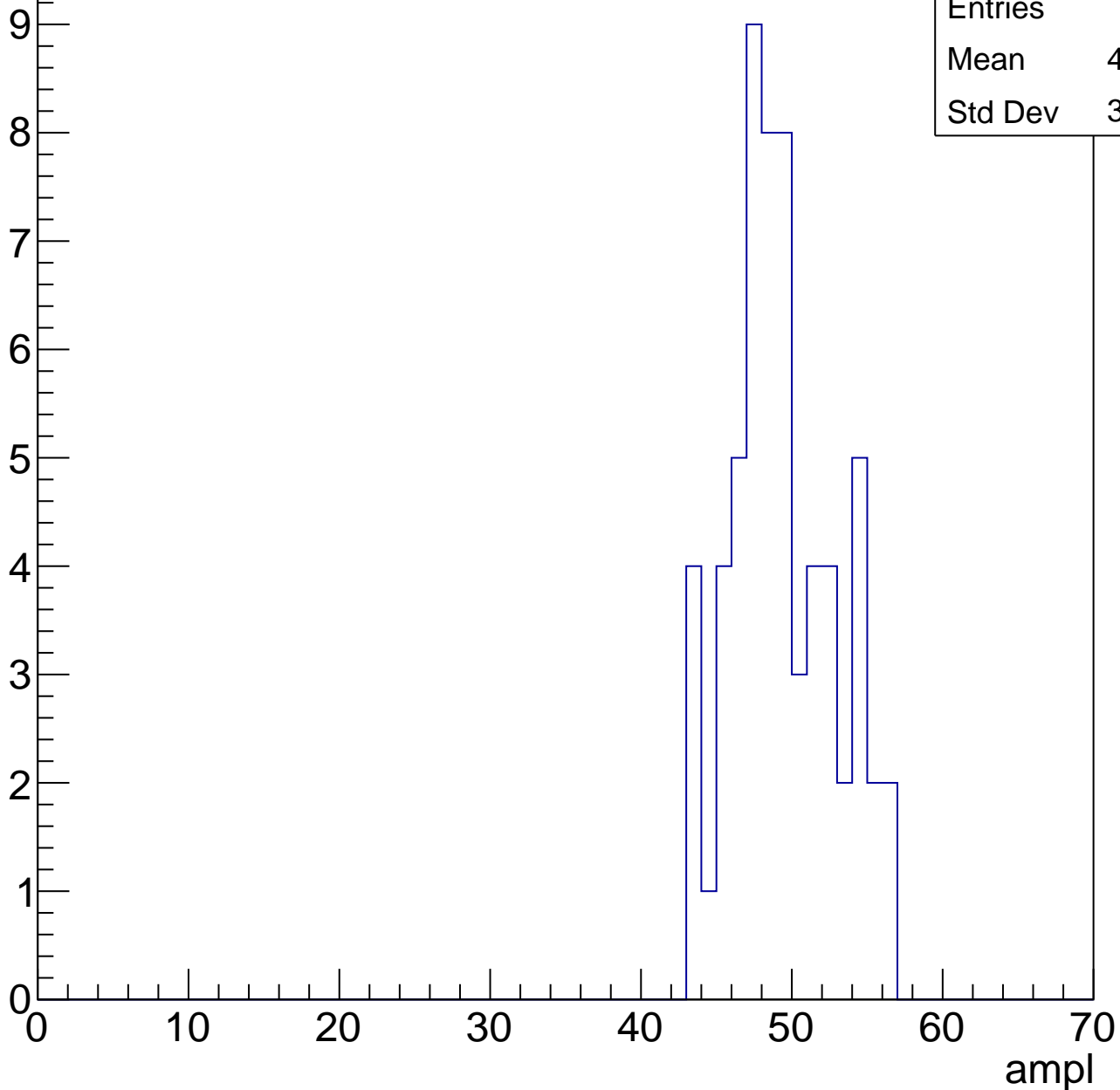


B1L102S, U10-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.93
Std Dev	3.397

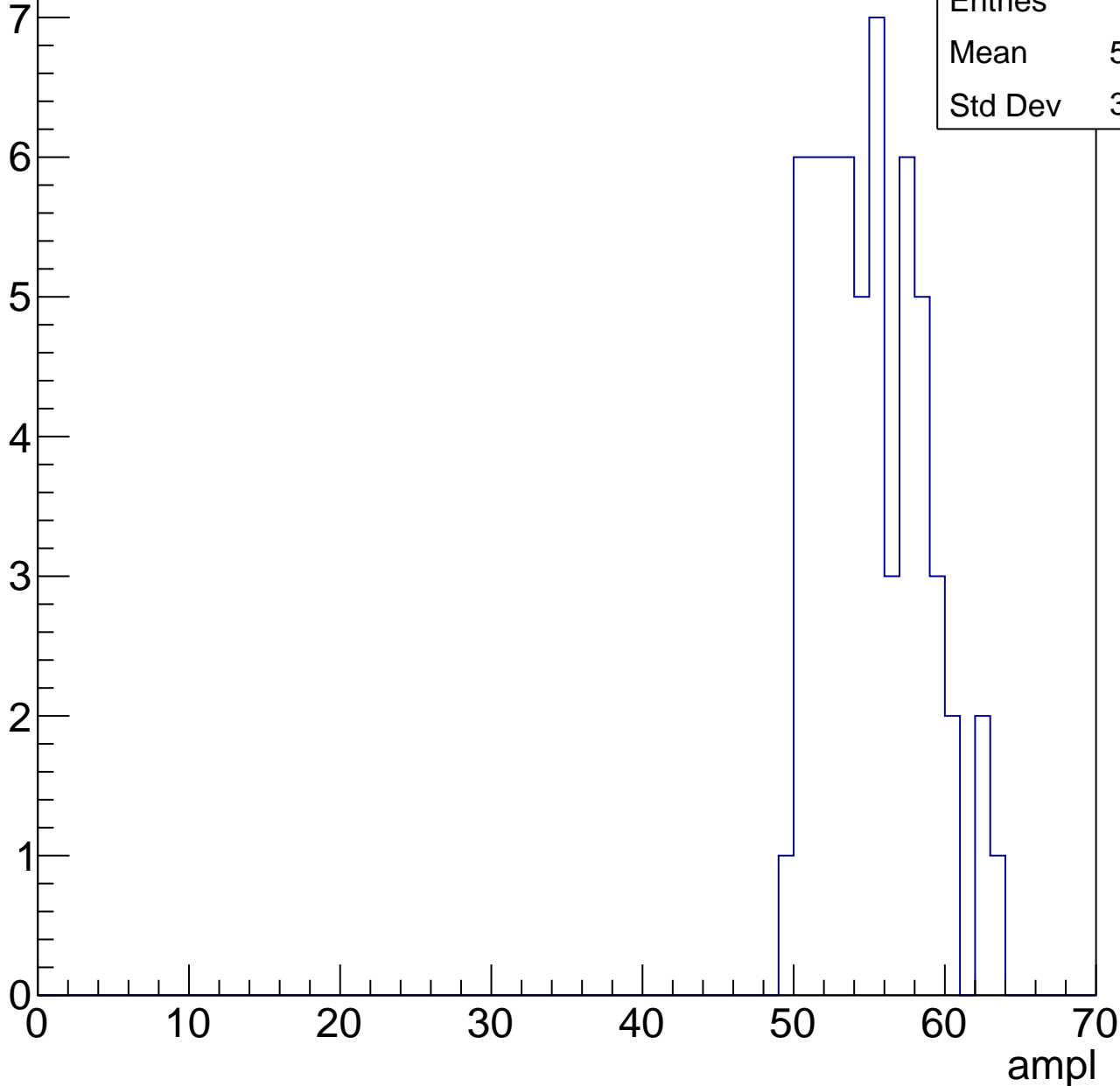


B1L102S, U10-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.64
Std Dev	3.419

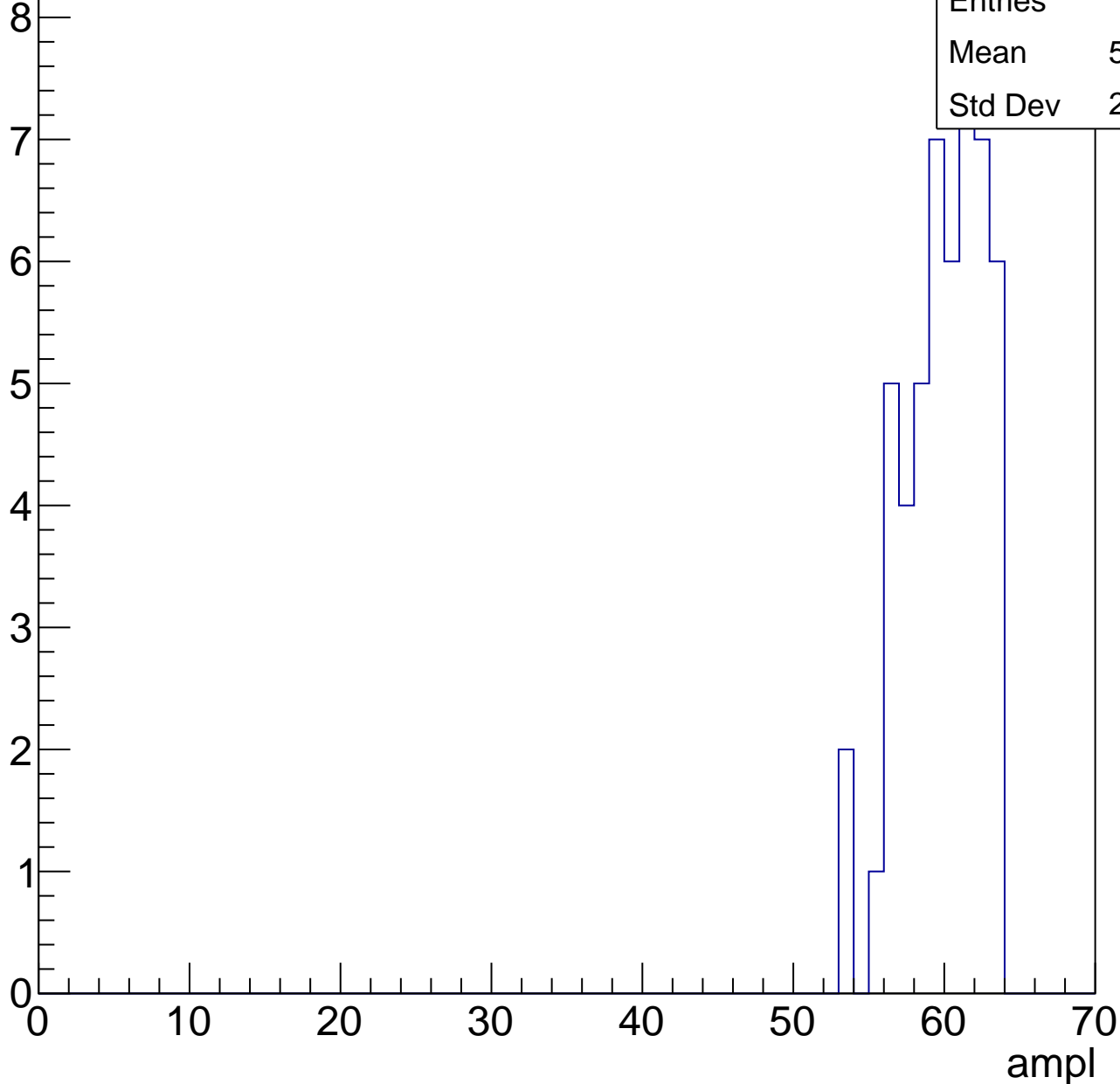


B1L102S, U10-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

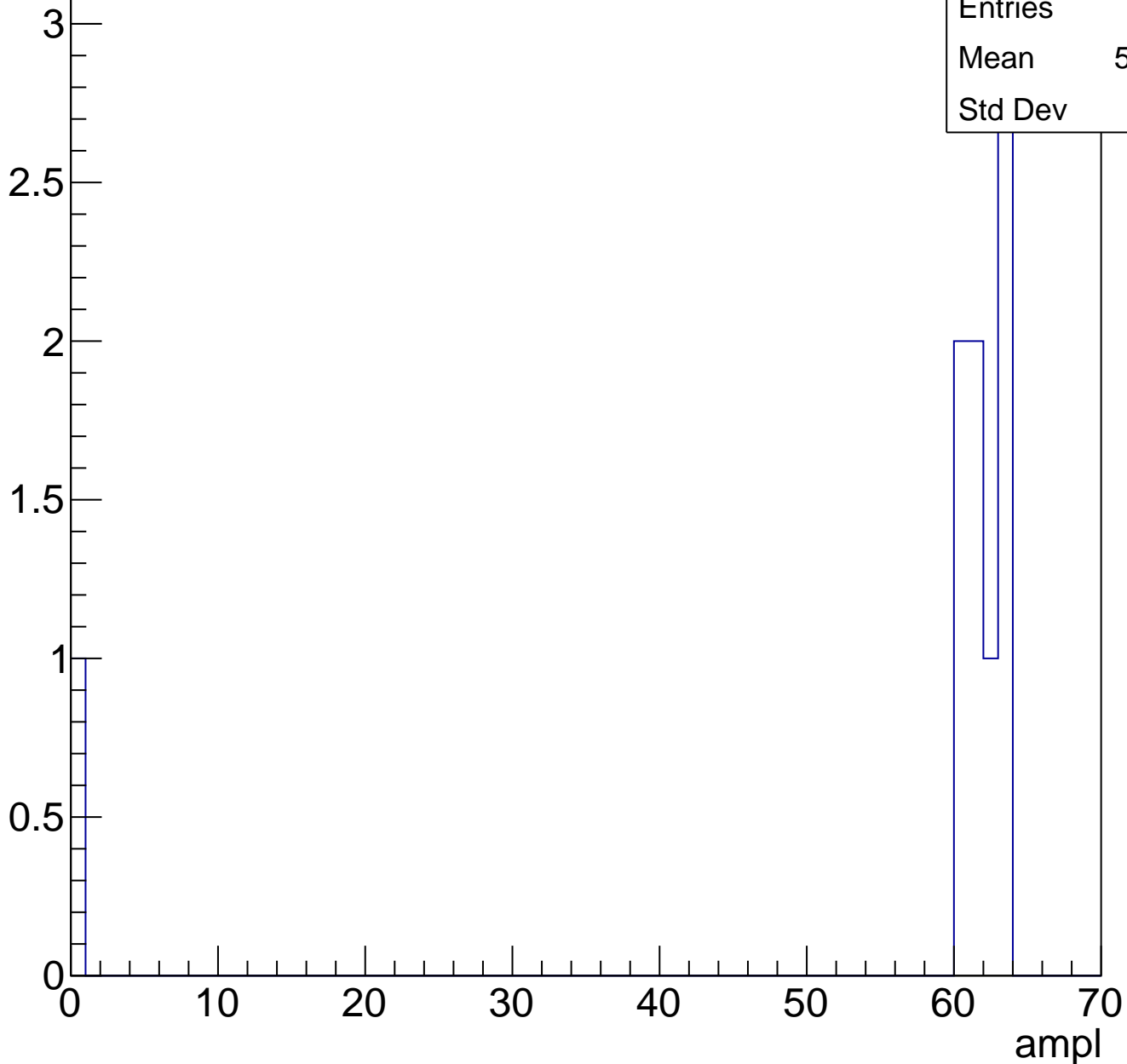
Entries	51
Mean	59.45
Std Dev	2.584



B1L102S, U10-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch84, adc0

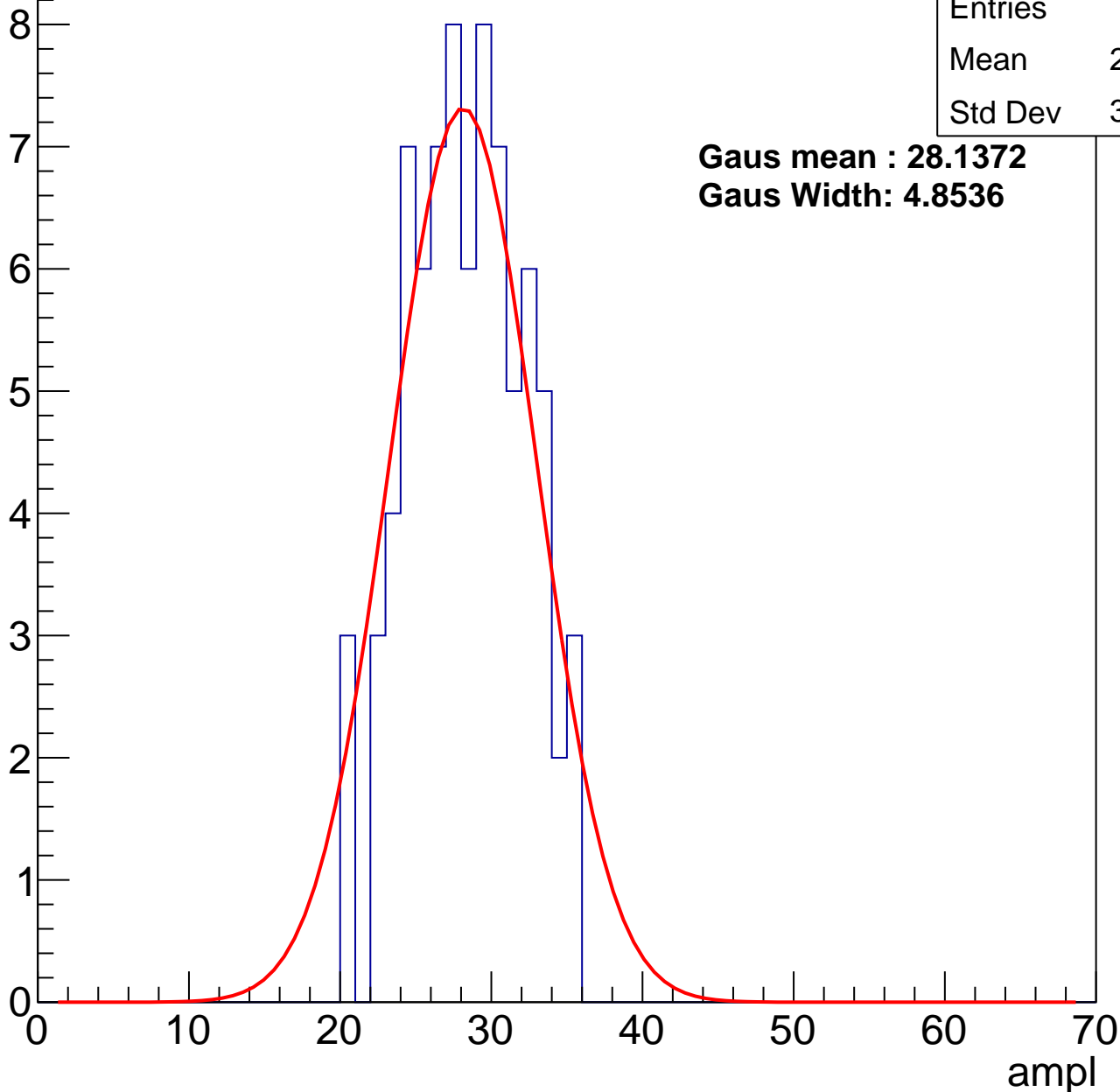
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.86
Std Dev	3.744

Gaus mean : 28.1372

Gaus Width: 4.8536



B1L102S, U10-ch84, adc1

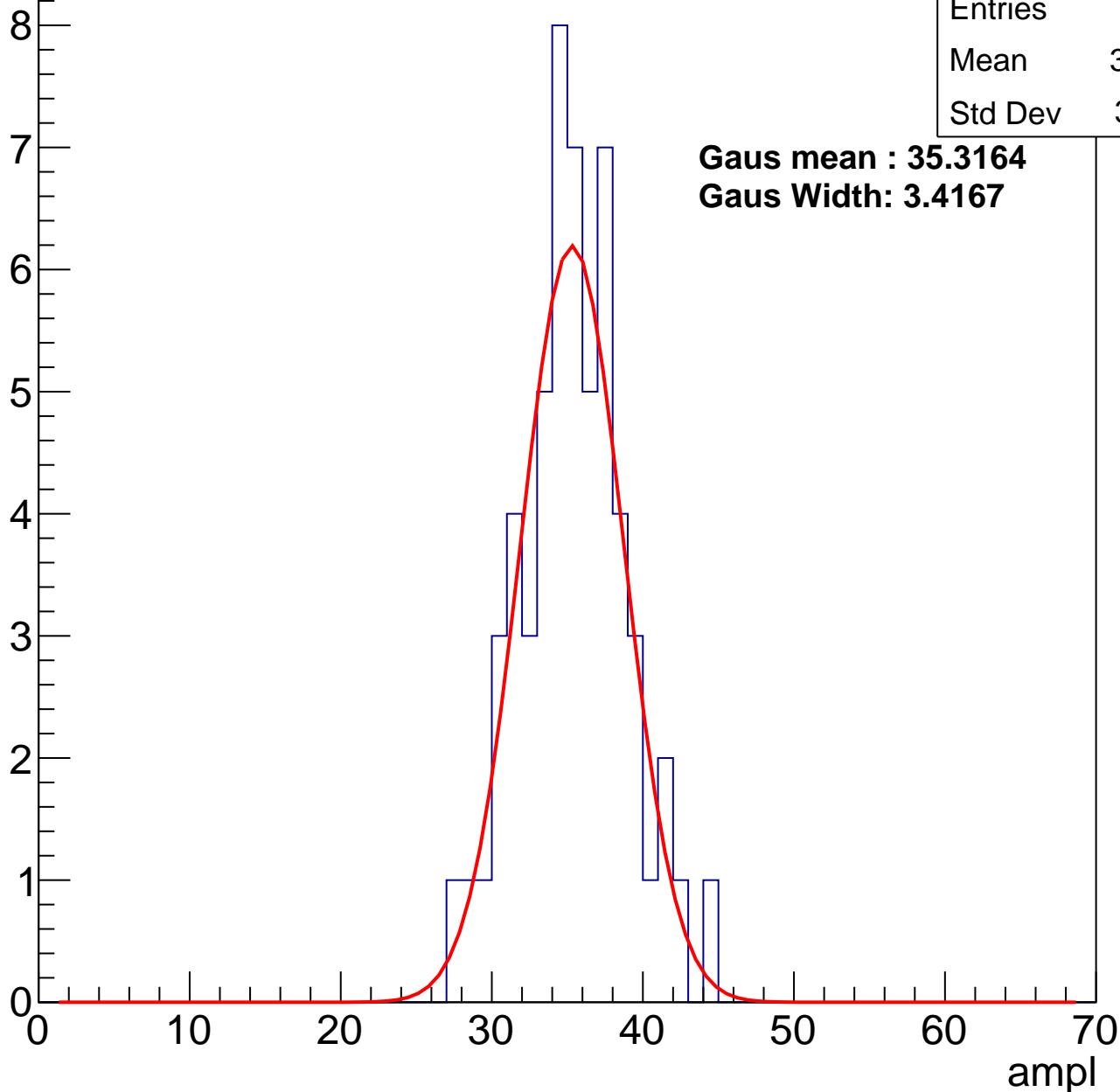
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.95
Std Dev	3.471

Gaus mean : 35.3164

Gaus Width: 3.4167



B1L102S, U10-ch84, adc2

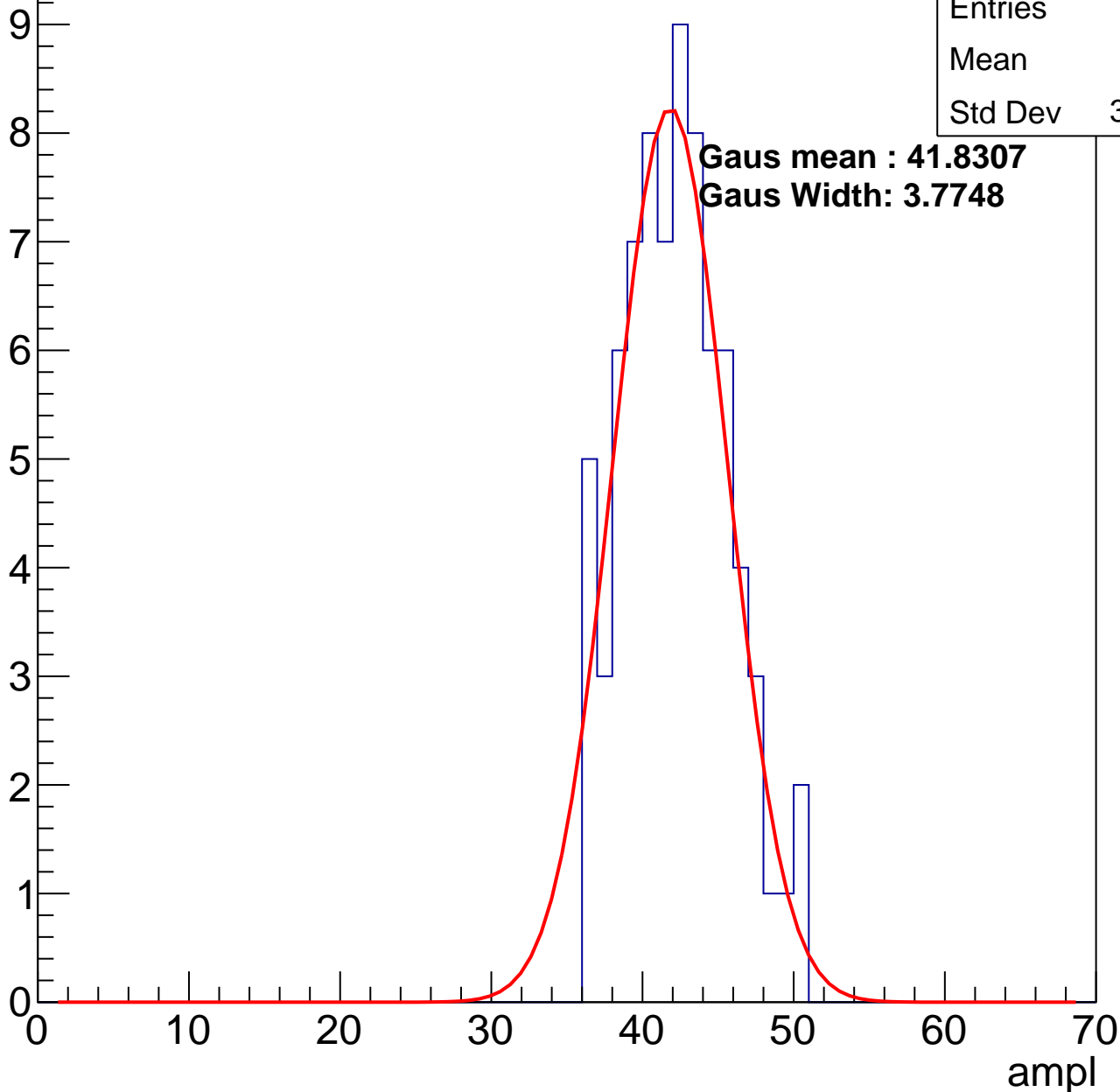
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	41.8
Std Dev	3.418

Gaus mean : 41.8307

Gaus Width: 3.7748

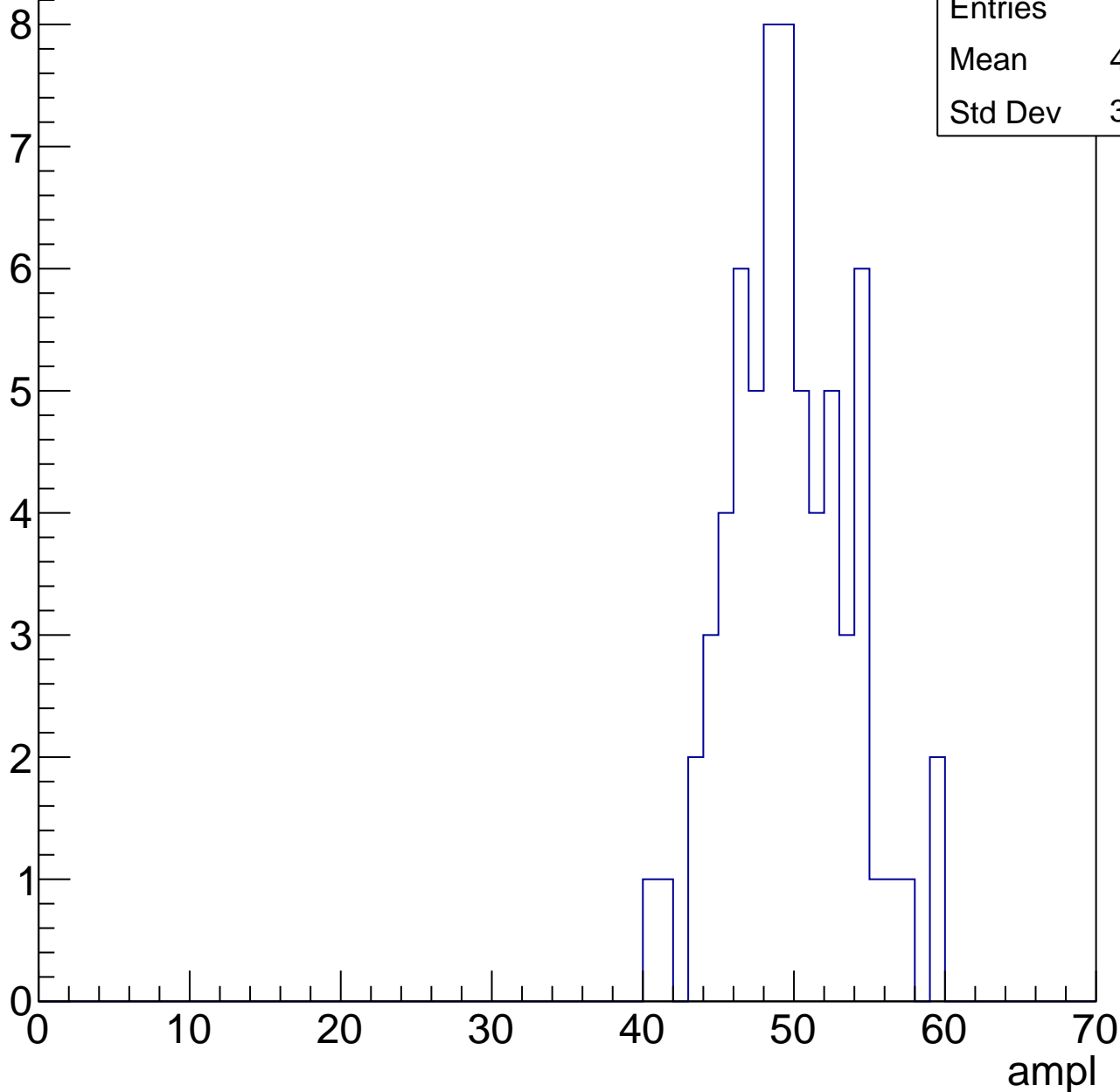


B1L102S, U10-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

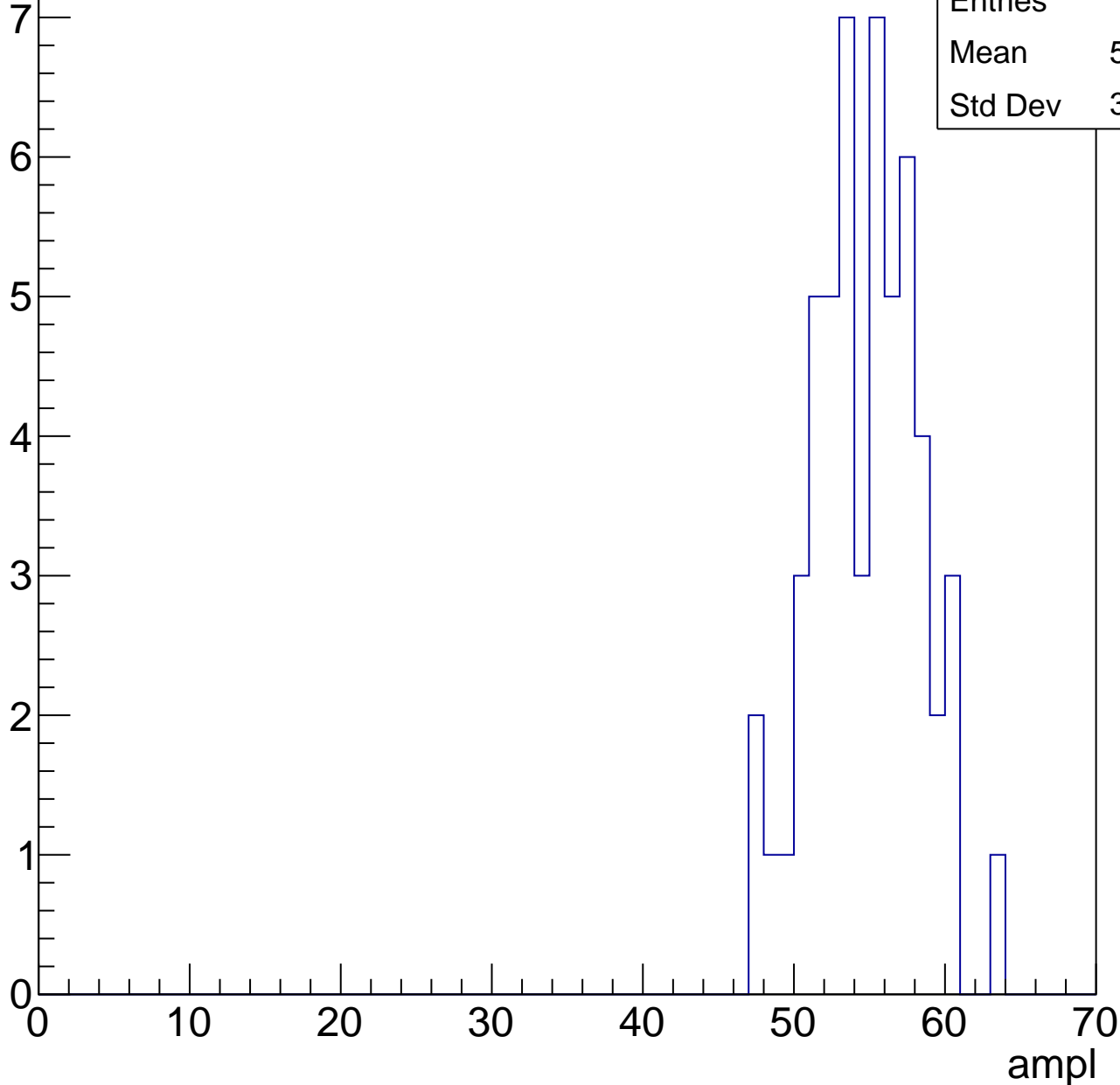
Entries	66
Mean	49.23
Std Dev	3.984



B1L102S, U10-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

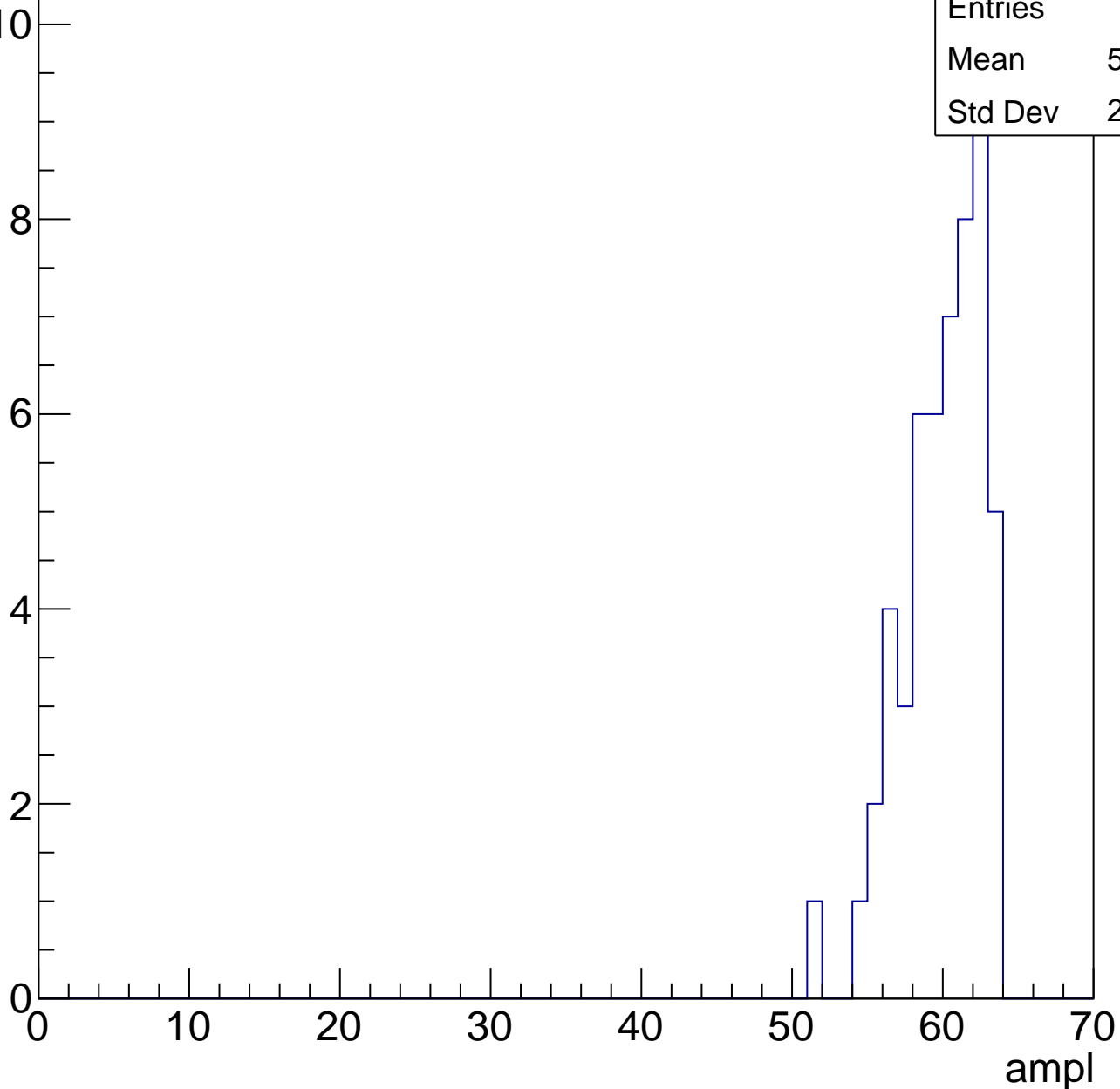


B1L102S, U10-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

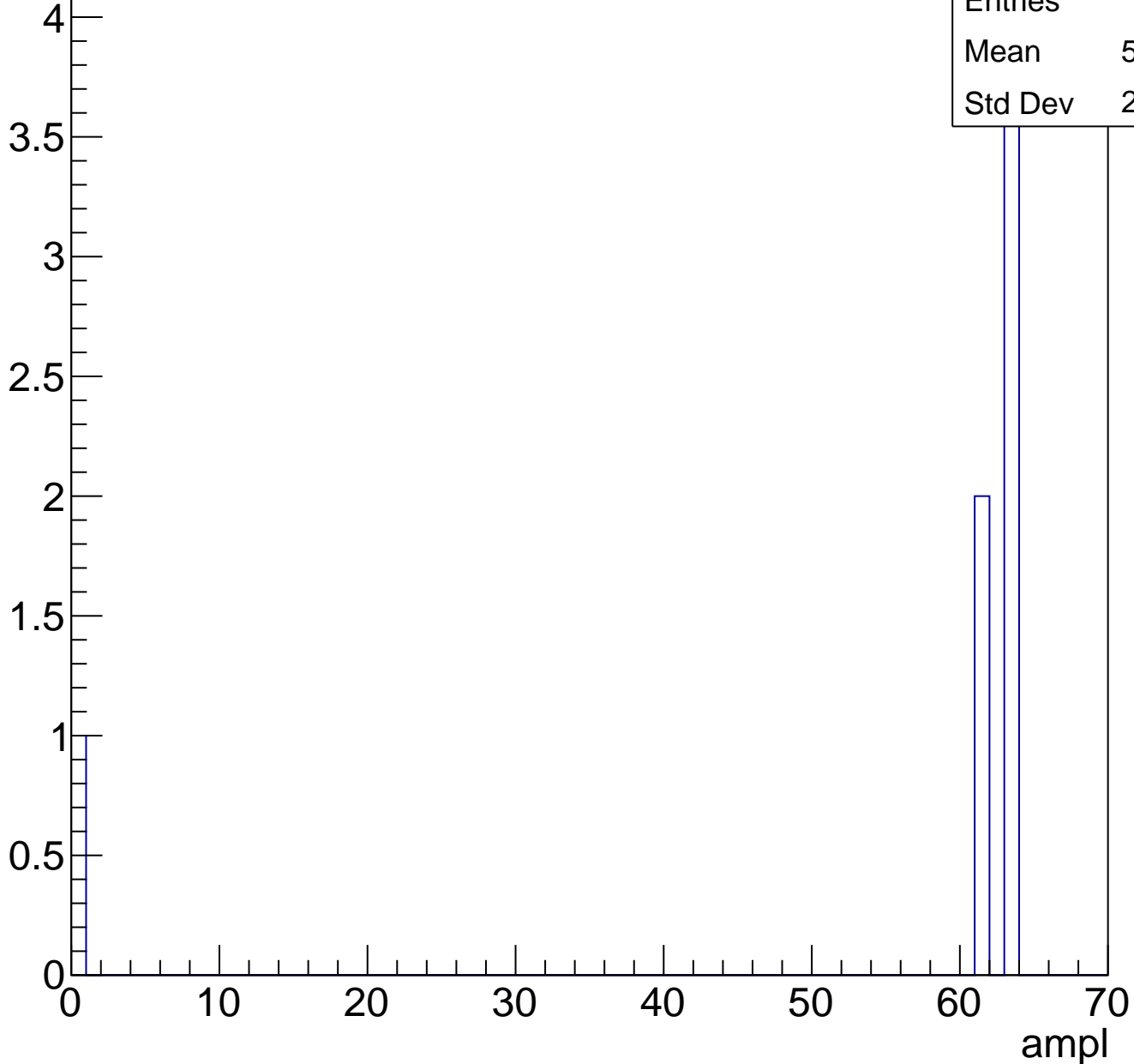
Entries	53
Mean	59.53
Std Dev	2.639



B1L102S, U10-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

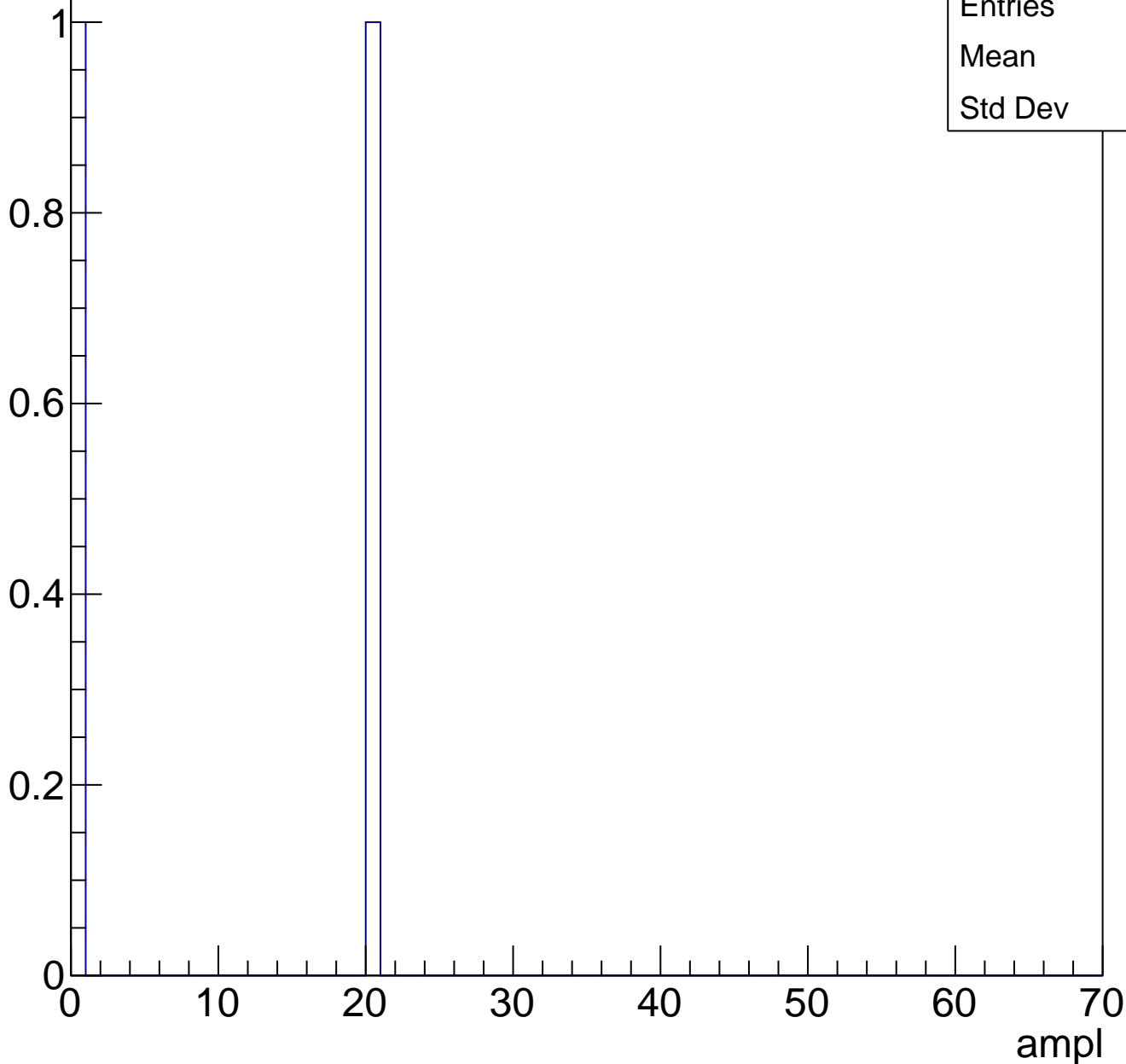
Entry



B1L102S, U10-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch85, adc0

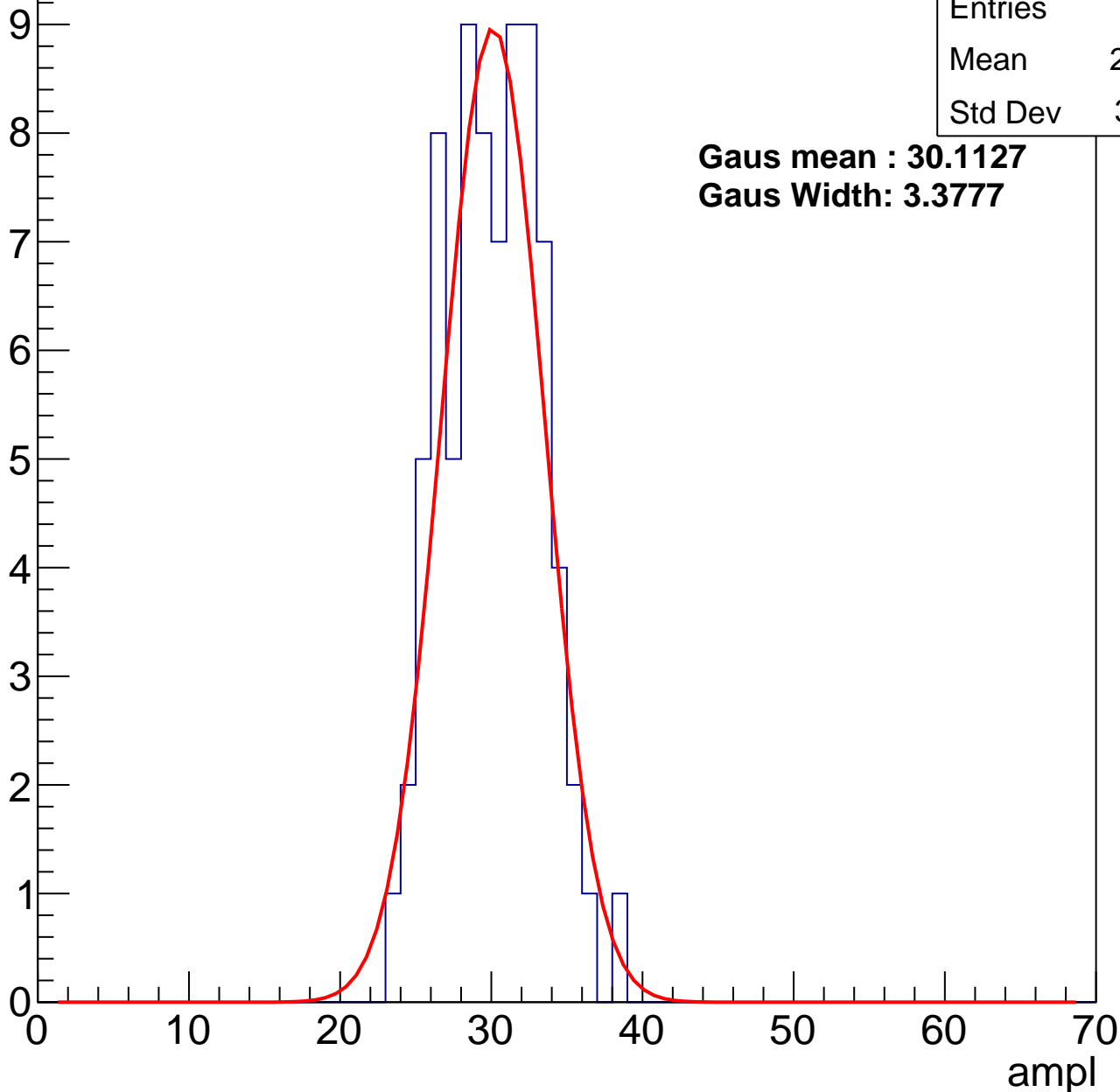
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.63
Std Dev	3.151

Gaus mean : 30.1127

Gaus Width: 3.3777



B1L102S, U10-ch85, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	37.62
Std Dev	3.307

Gaus mean : 37.6174

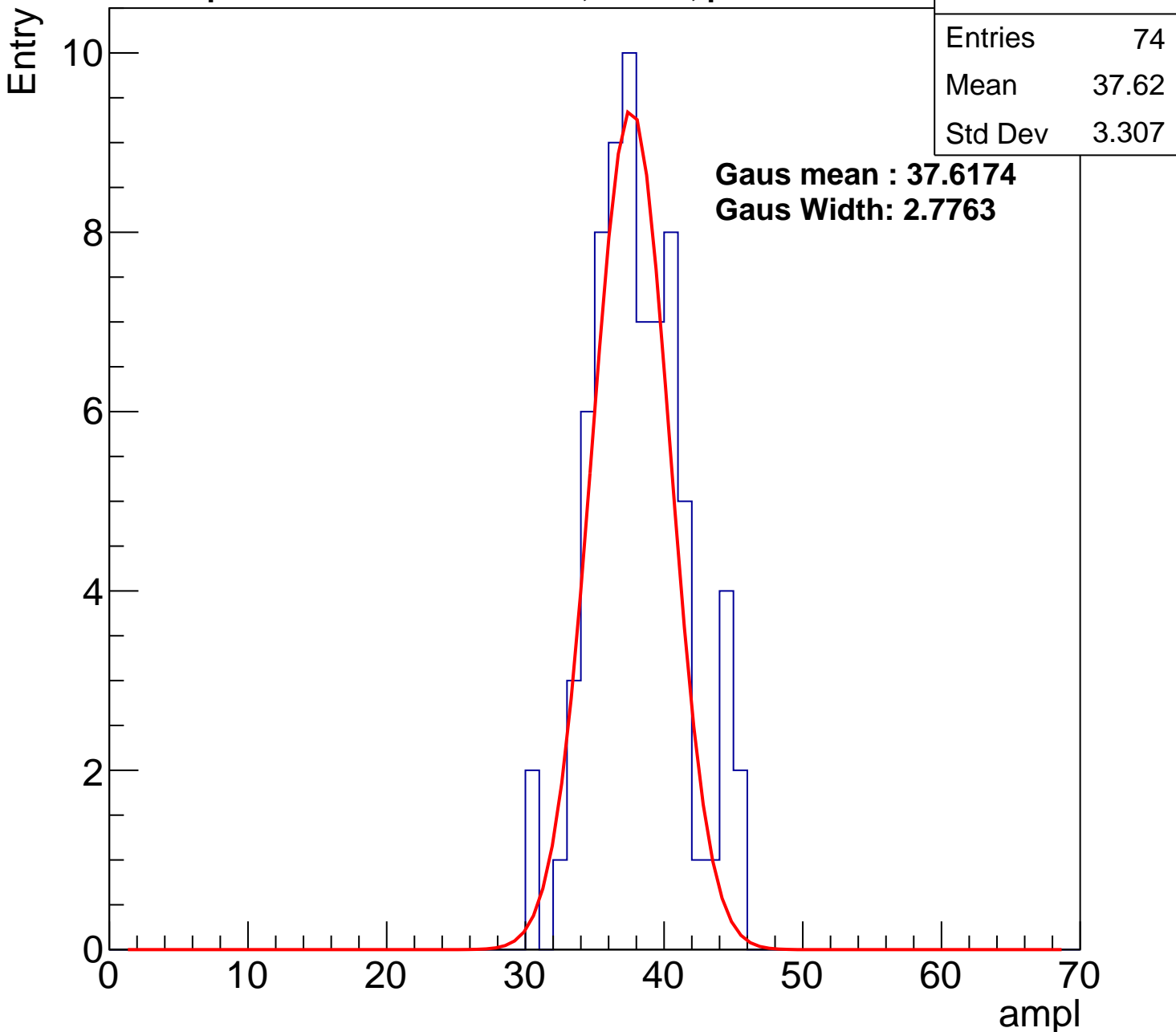
Gaus Width: 2.7763

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch85, adc2

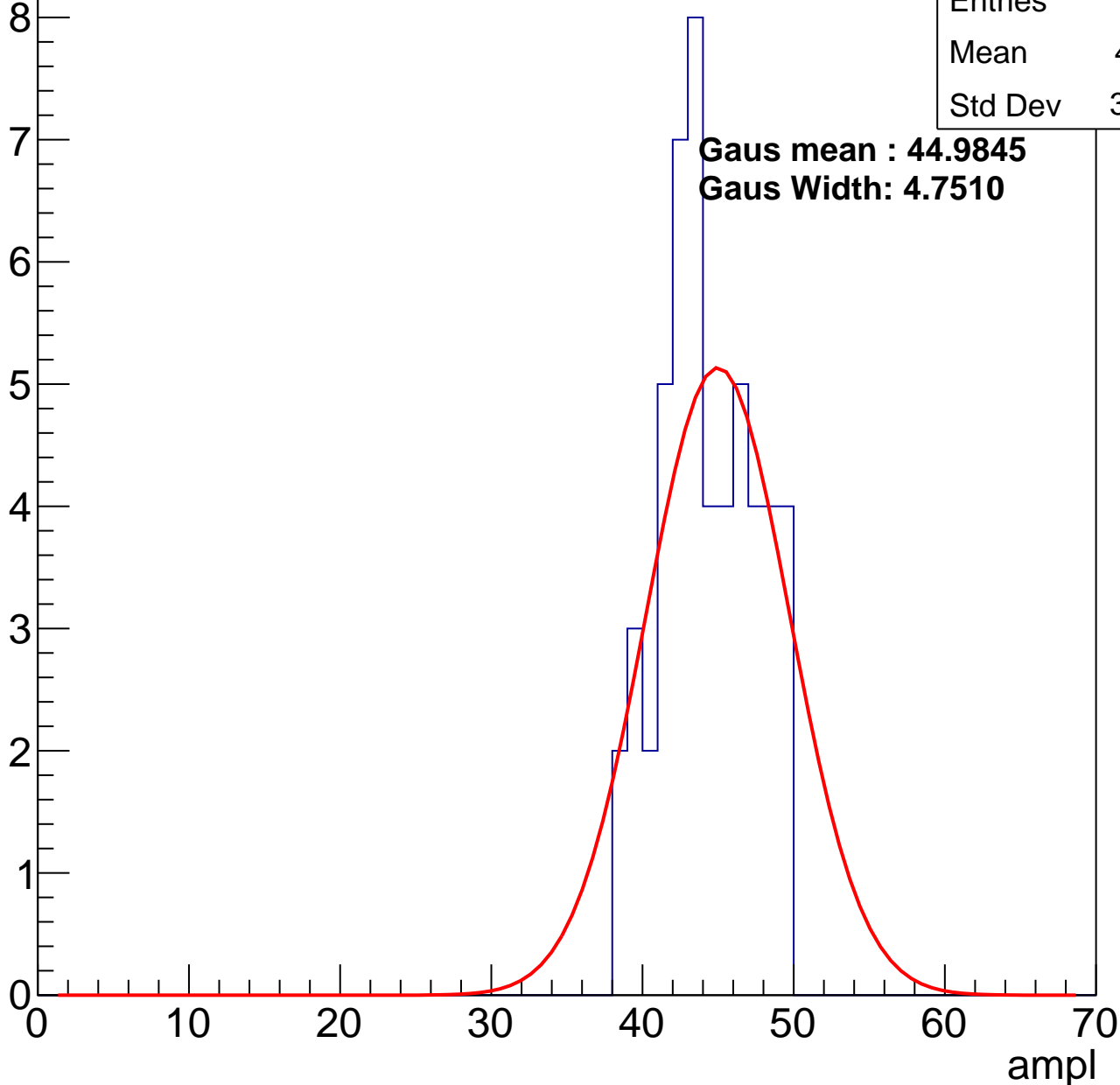
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.81
Std Dev	3.045

Gaus mean : 44.9845

Gaus Width: 4.7510



B1L102S, U10-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	50.29
Std Dev	3.79

Entry

10

8

6

4

2

0

0

10

20

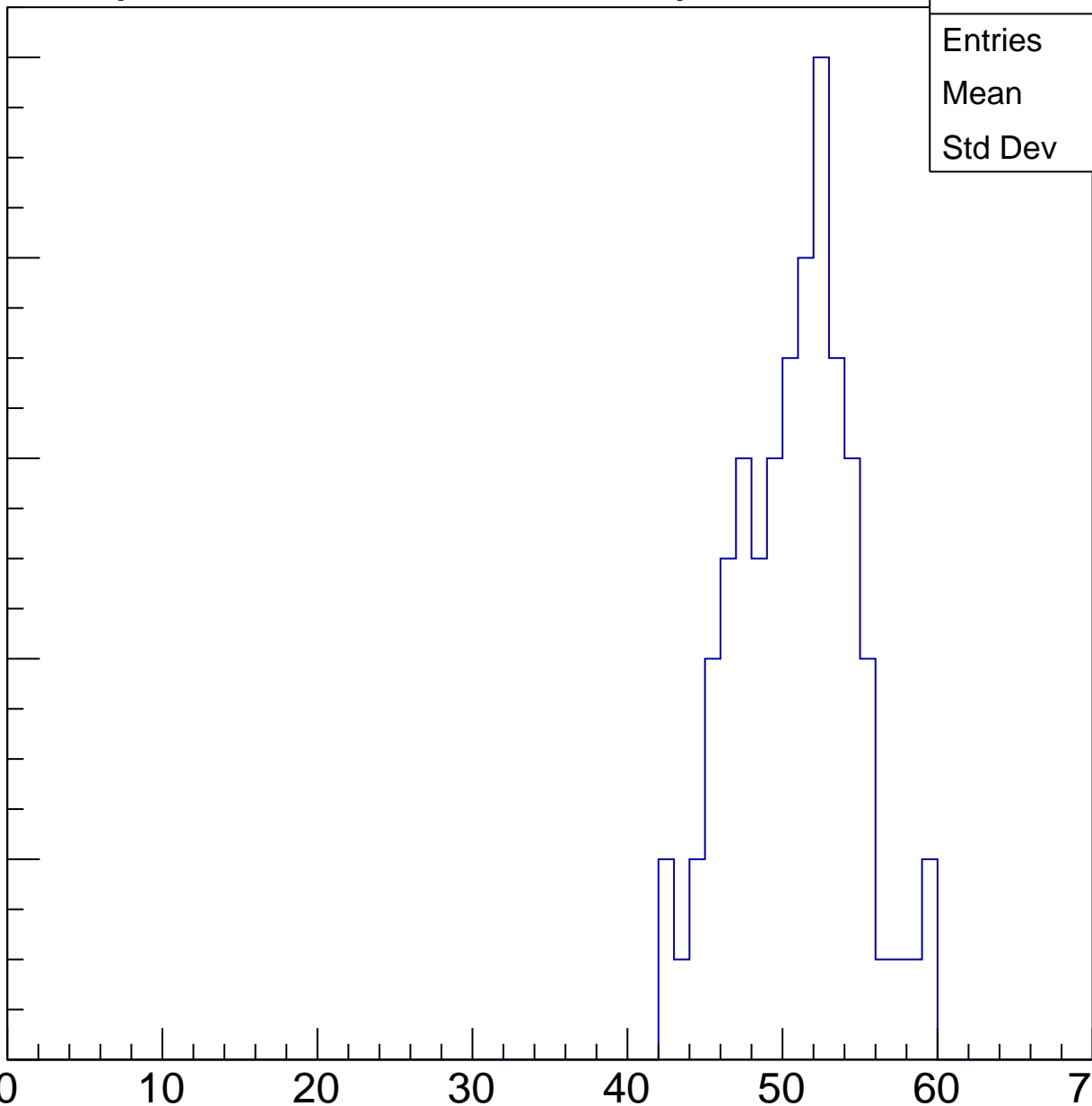
30

40

50

60

ampl

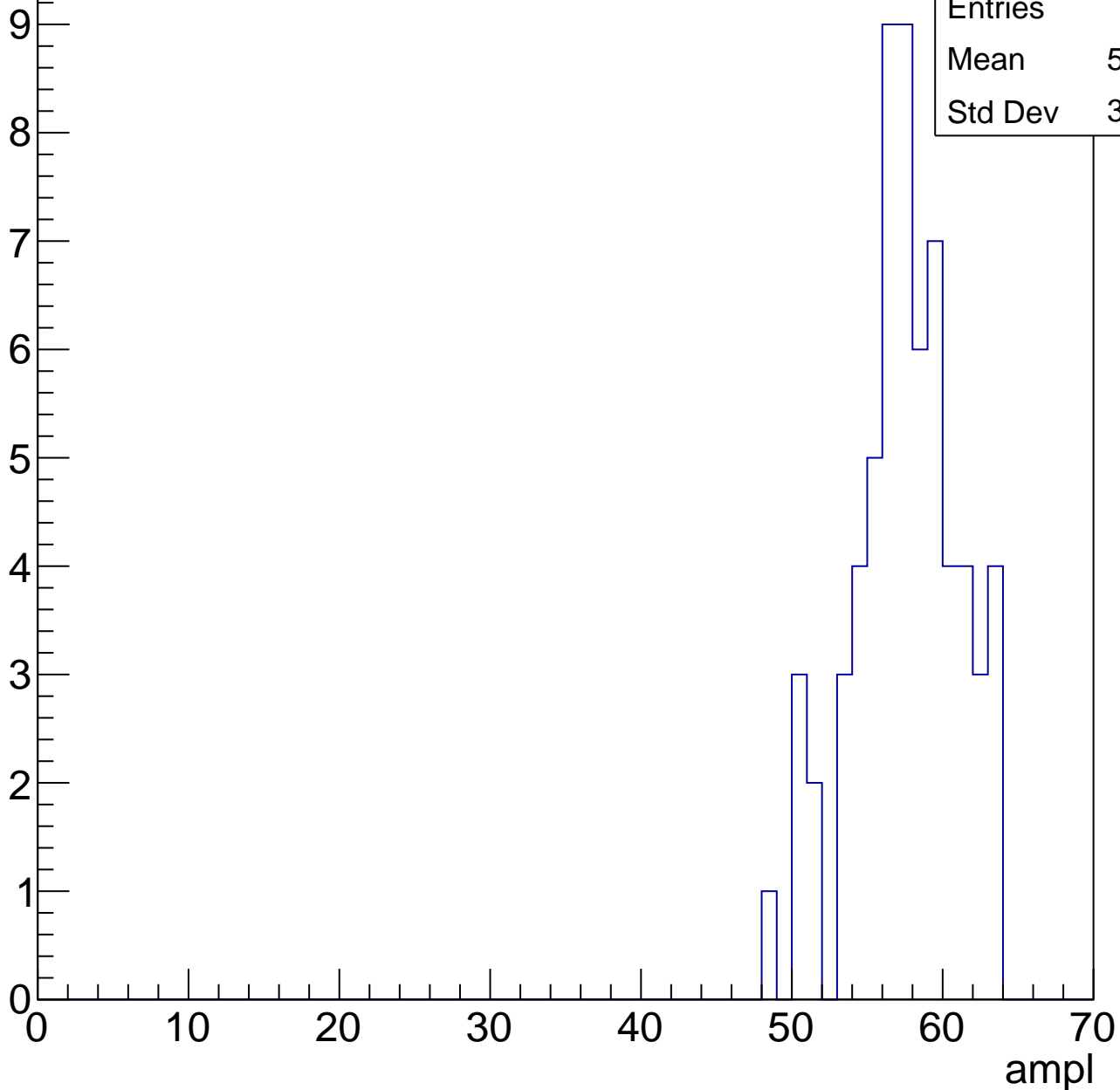


B1L102S, U10-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	57.03
Std Dev	3.459

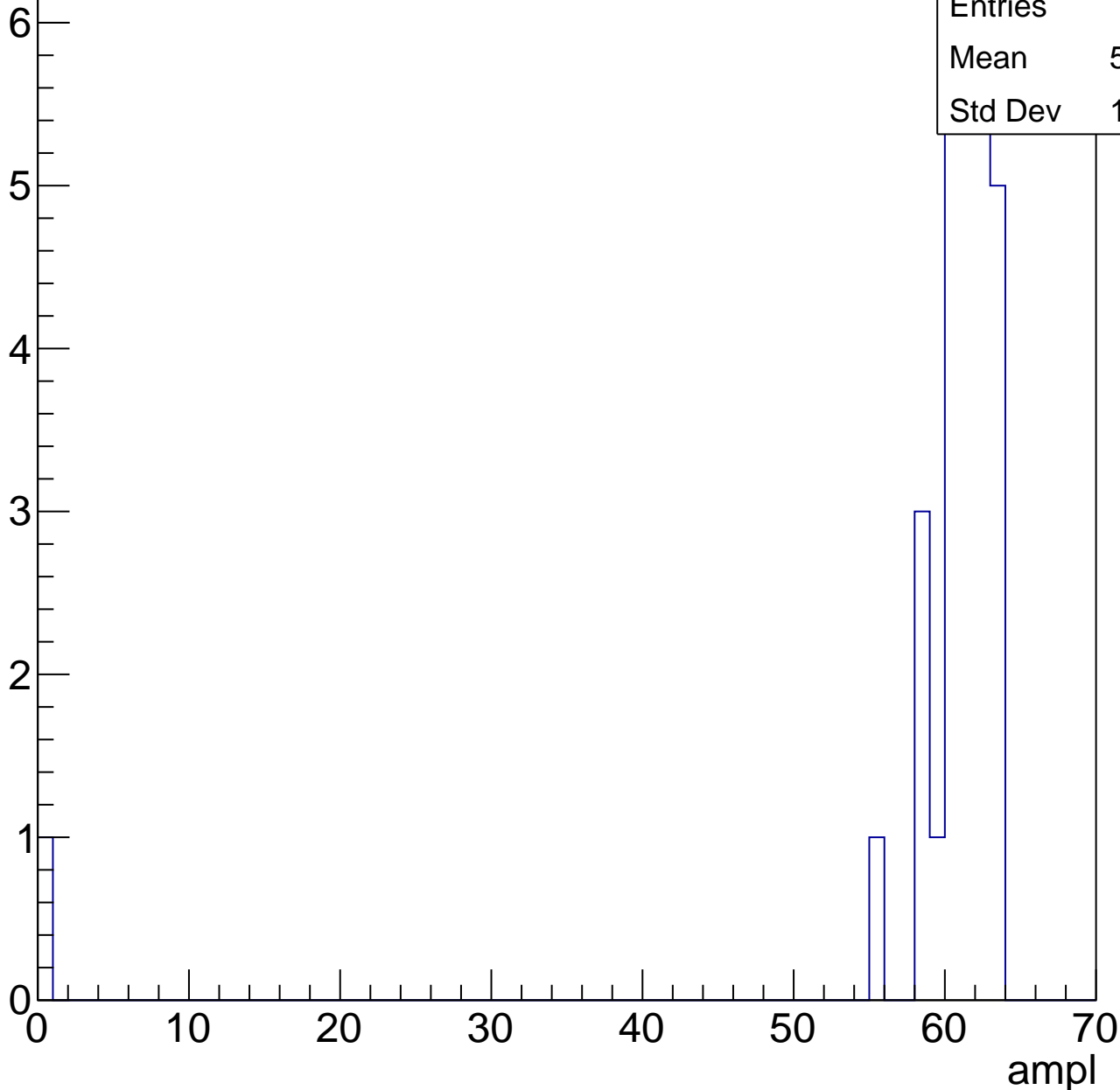


B1L102S, U10-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	58.66
Std Dev	11.24



B1L102S, U10-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch86, adc0

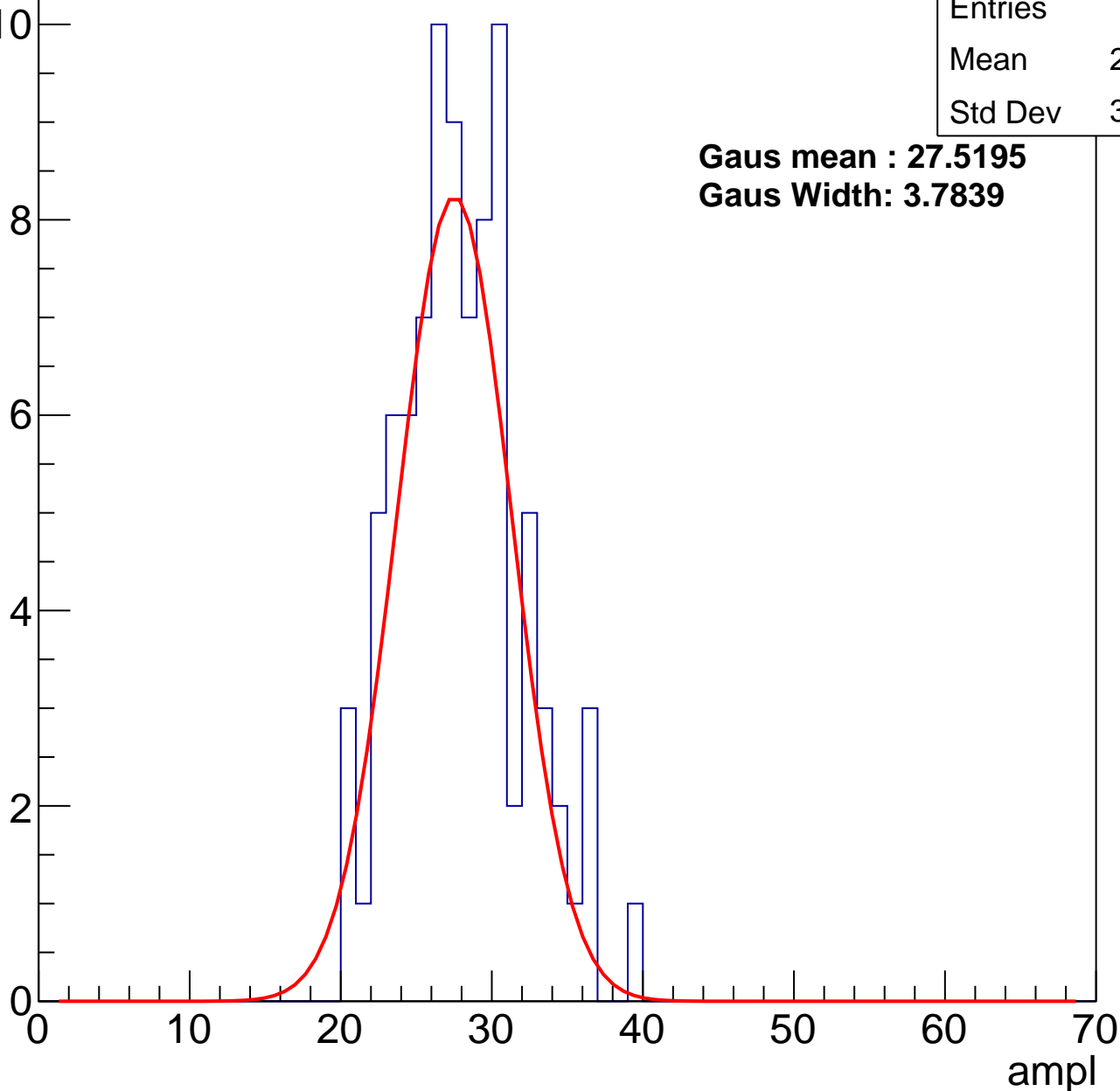
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	27.53
Std Dev	3.986

Gaus mean : 27.5195

Gaus Width: 3.7839



B1L102S, U10-ch86, adc1

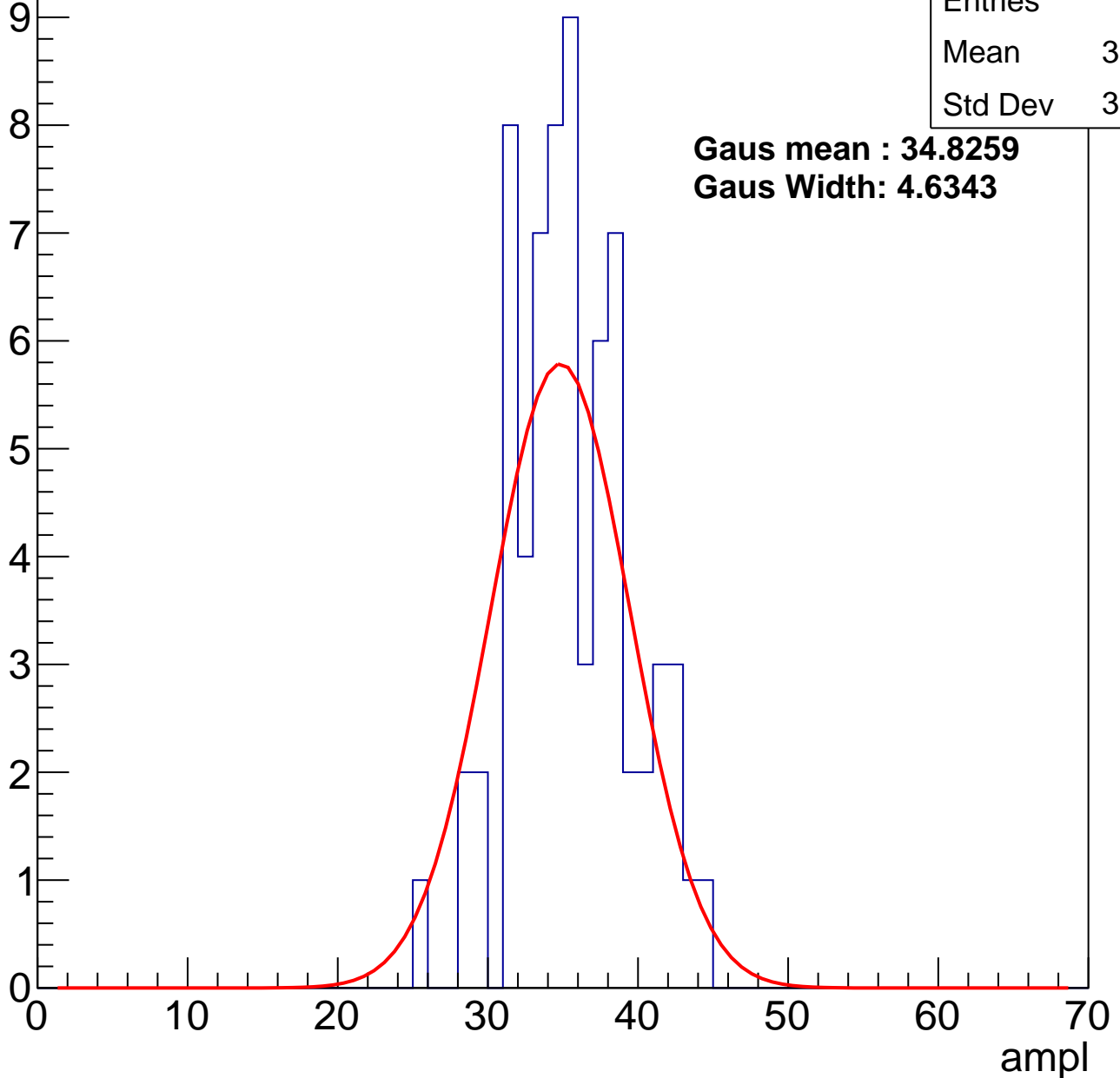
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.12
Std Dev	3.884

Gaus mean : 34.8259

Gaus Width: 4.6343



B1L102S, U10-ch86, adc2

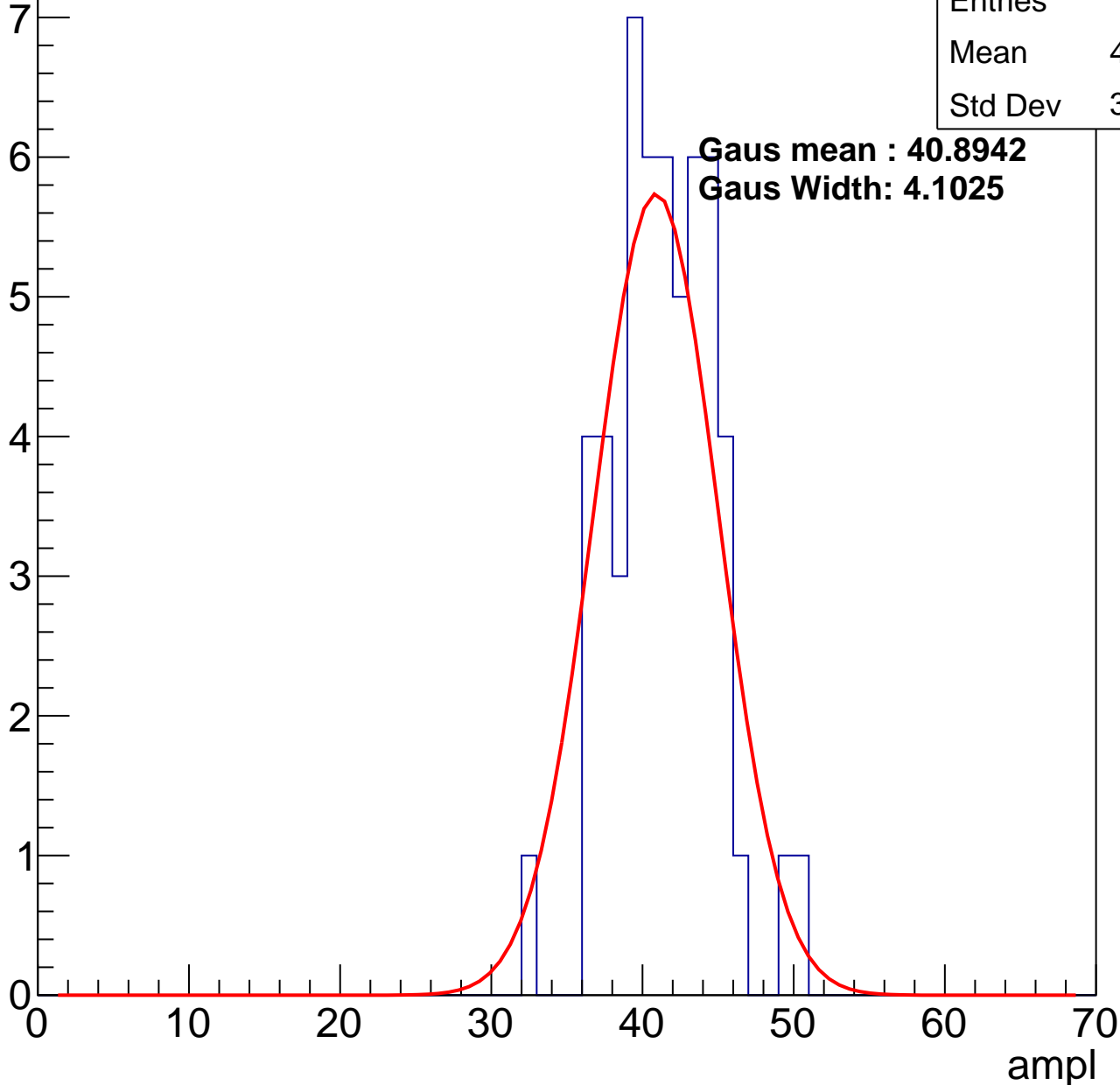
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.98
Std Dev	3.366

Gaus mean : 40.8942

Gaus Width: 4.1025

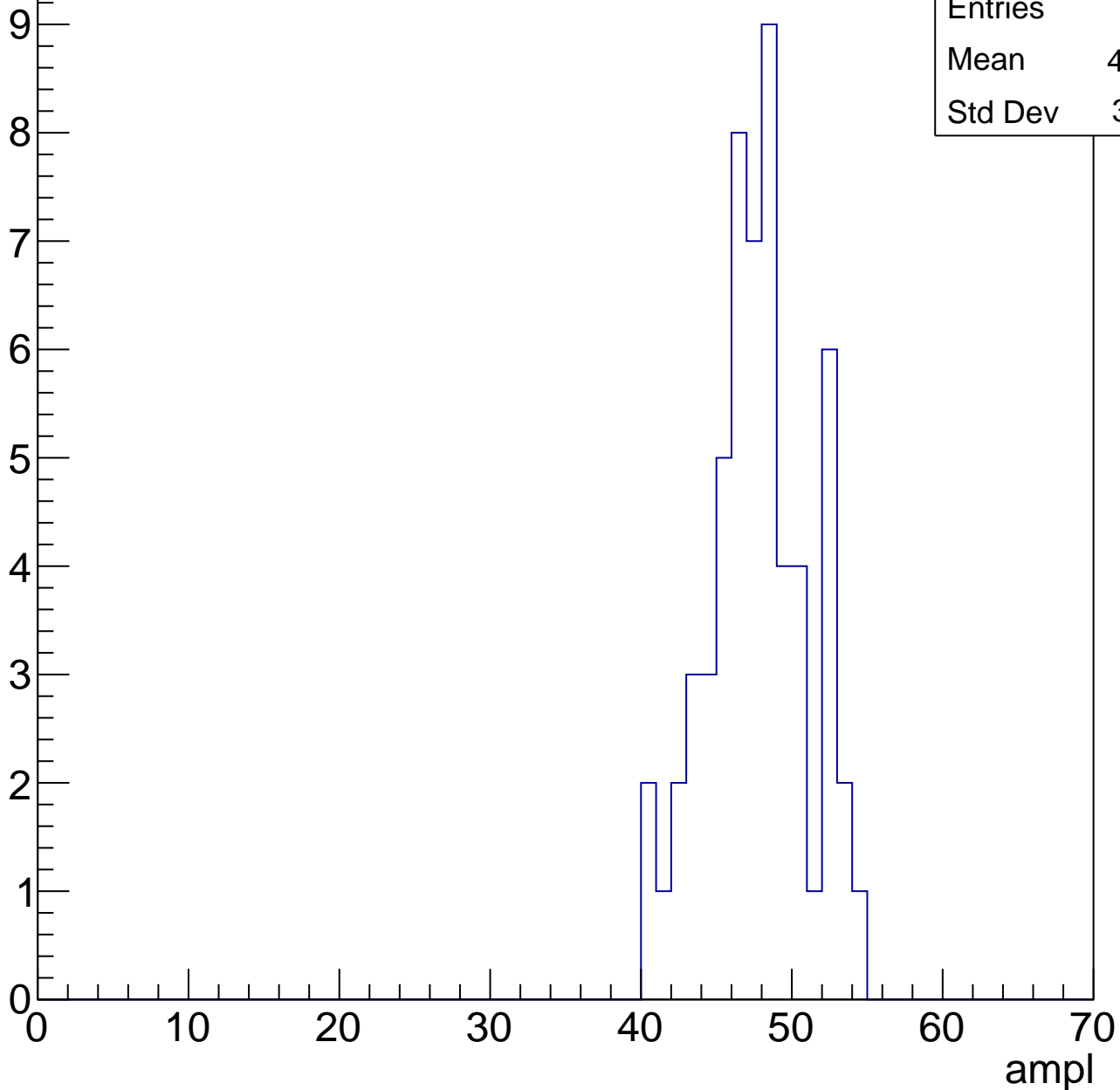


B1L102S, U10-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.22
Std Dev	3.301

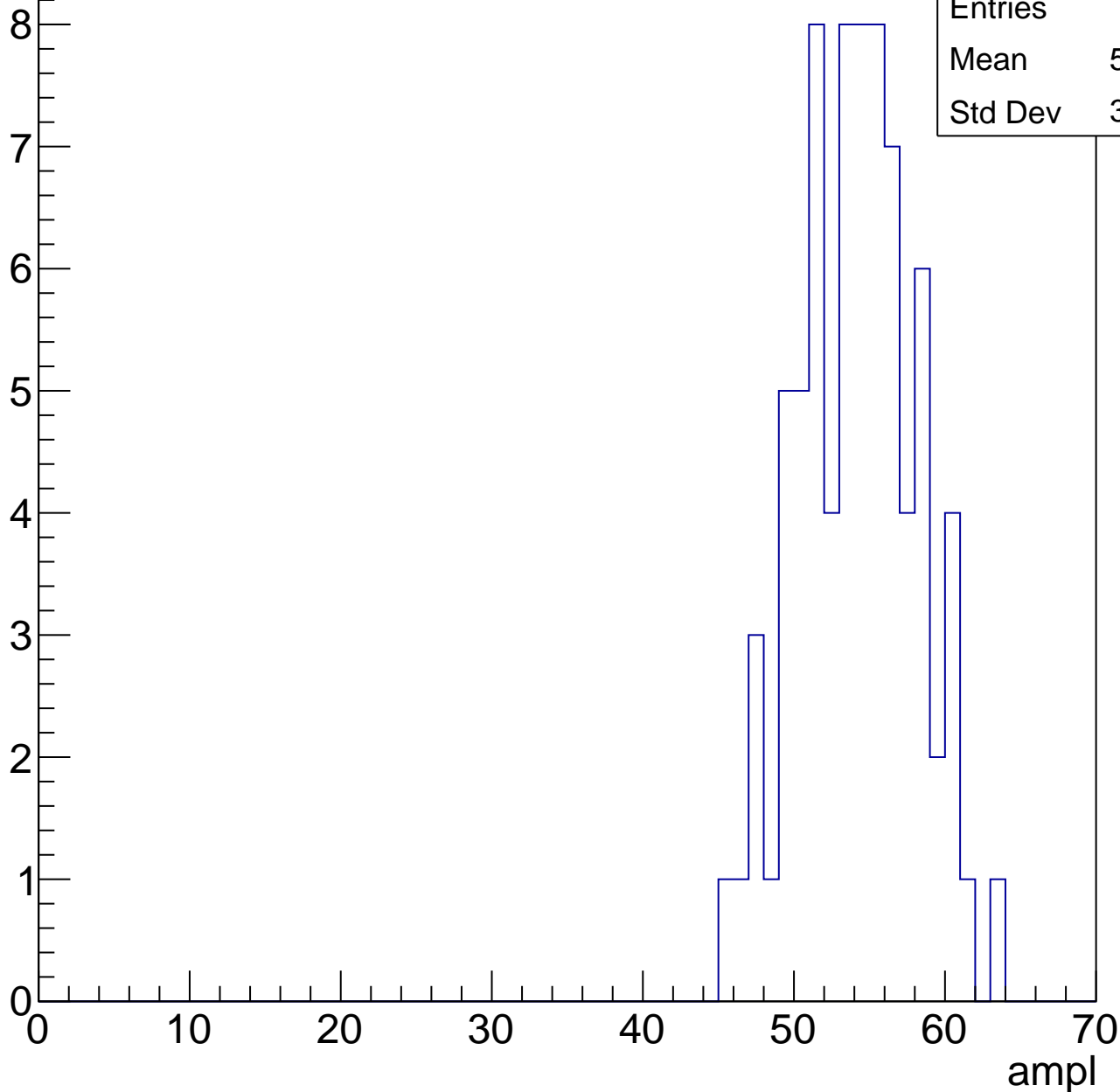


B1L102S, U10-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	53.73
Std Dev	3.806

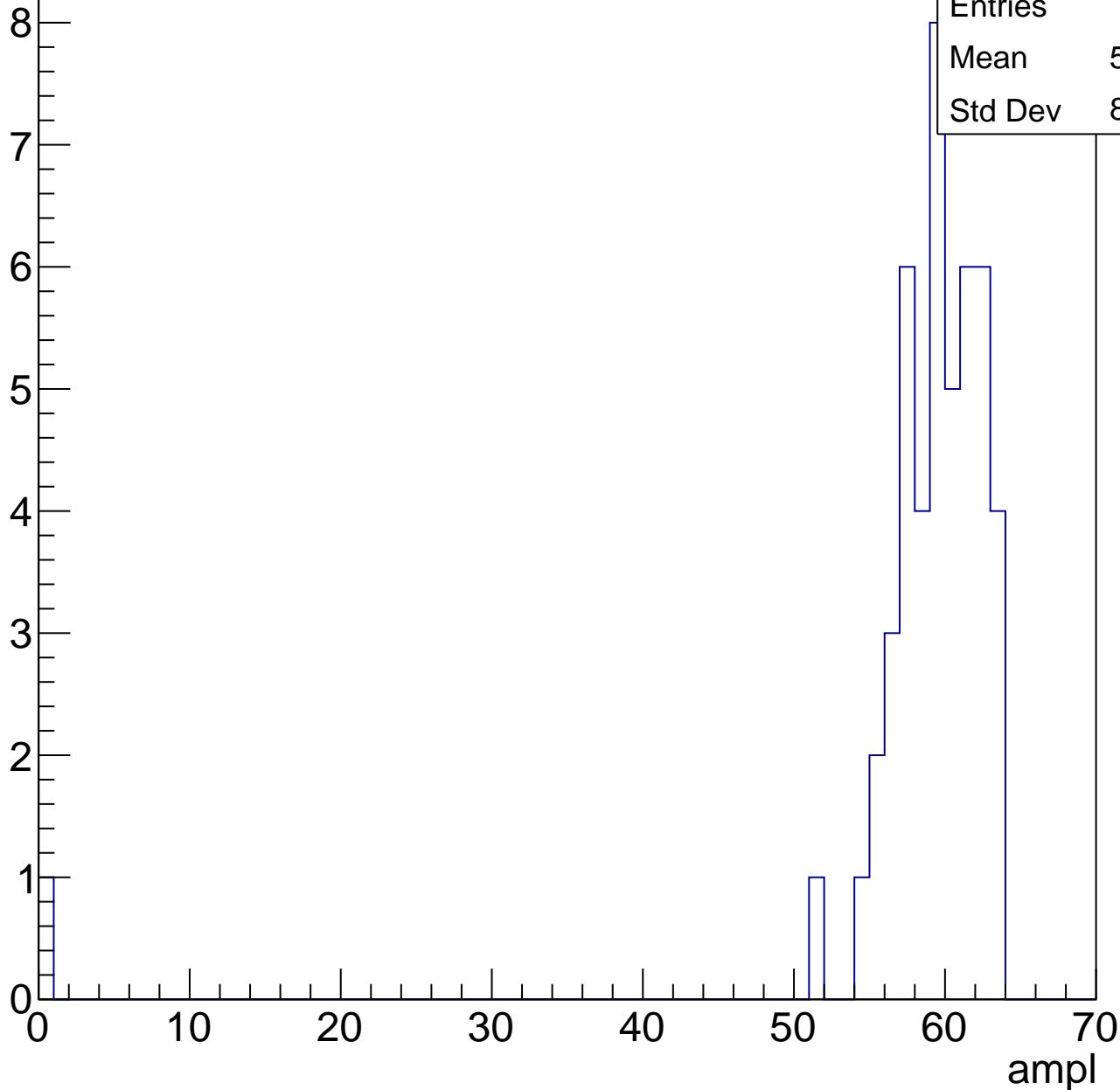


B1L102S, U10-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.85
Std Dev	8.923

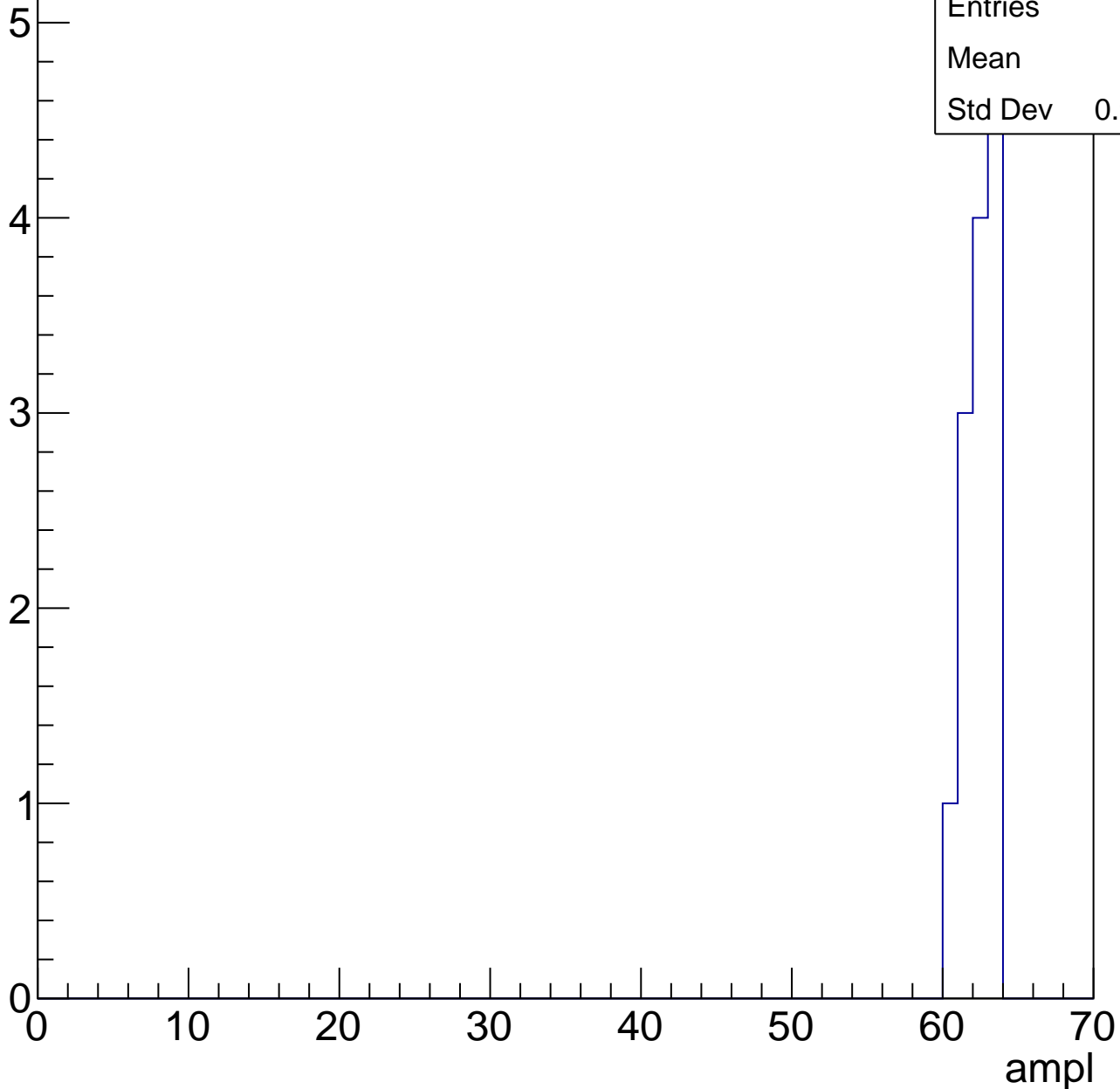


B1L102S, U10-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

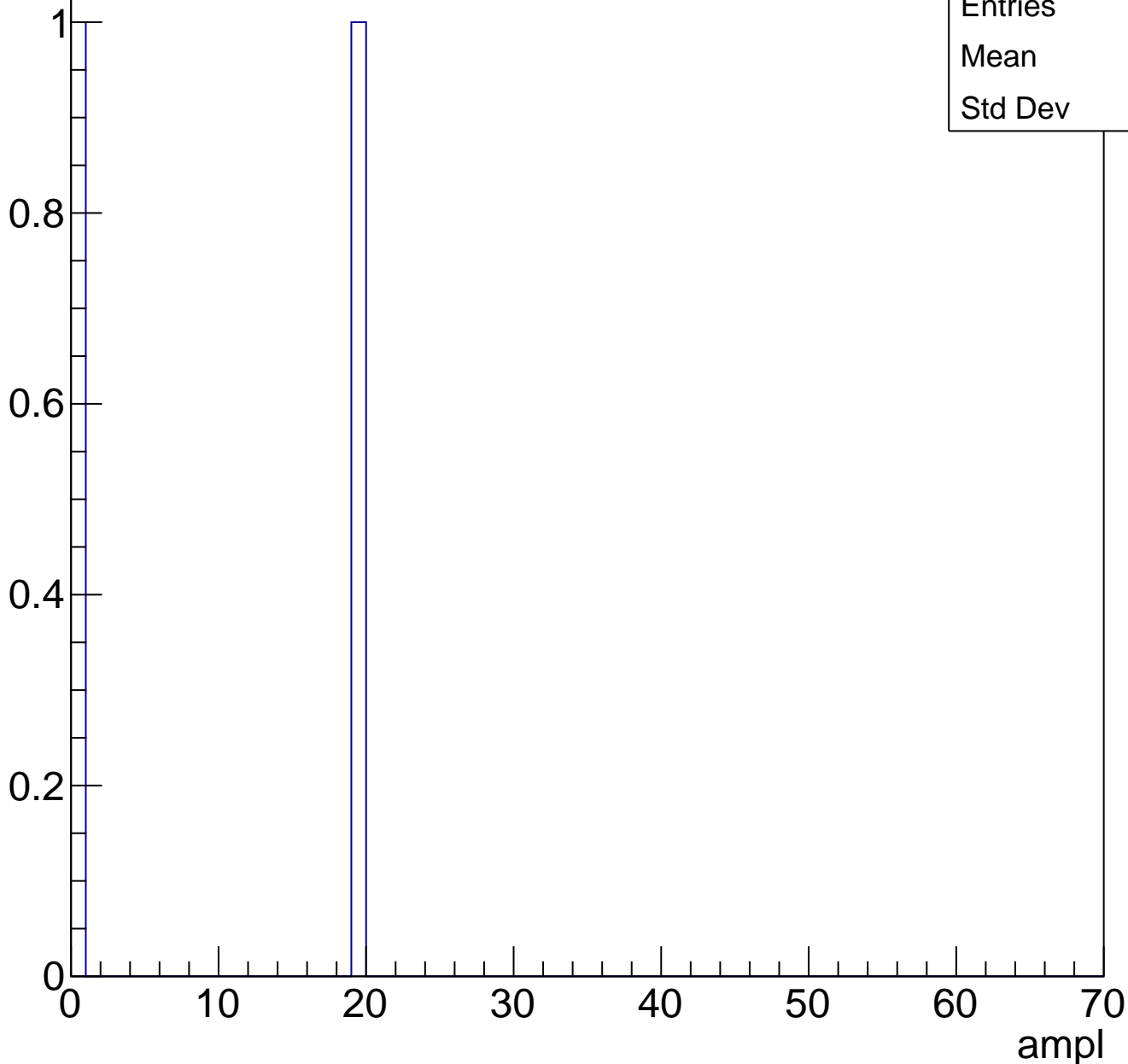
Entries	13
Mean	62
Std Dev	0.9608



B1L102S, U10-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U10-ch87, adc0

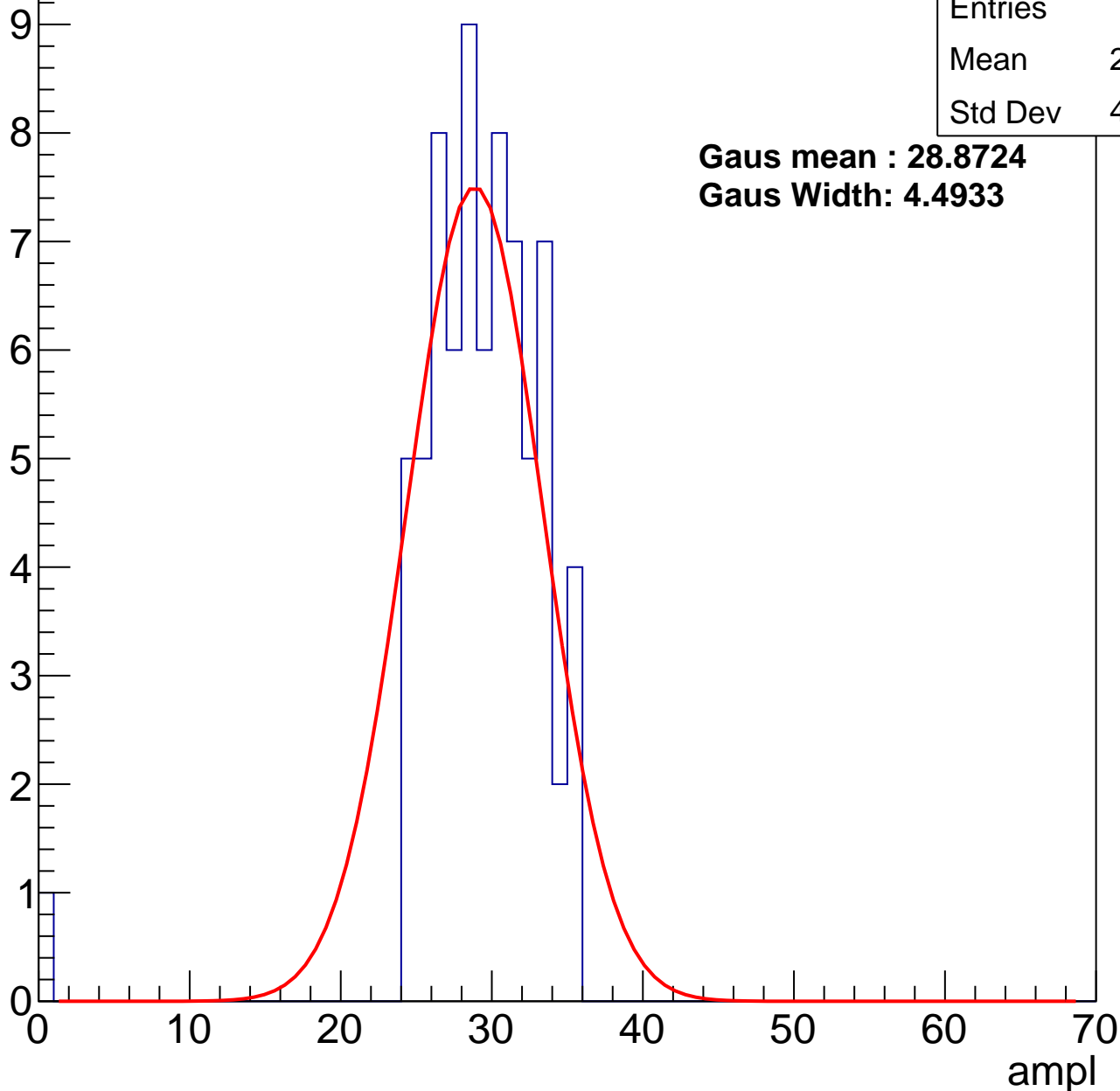
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.73
Std Dev	4.582

Gaus mean : 28.8724

Gaus Width: 4.4933



B1L102S, U10-ch87, adc1

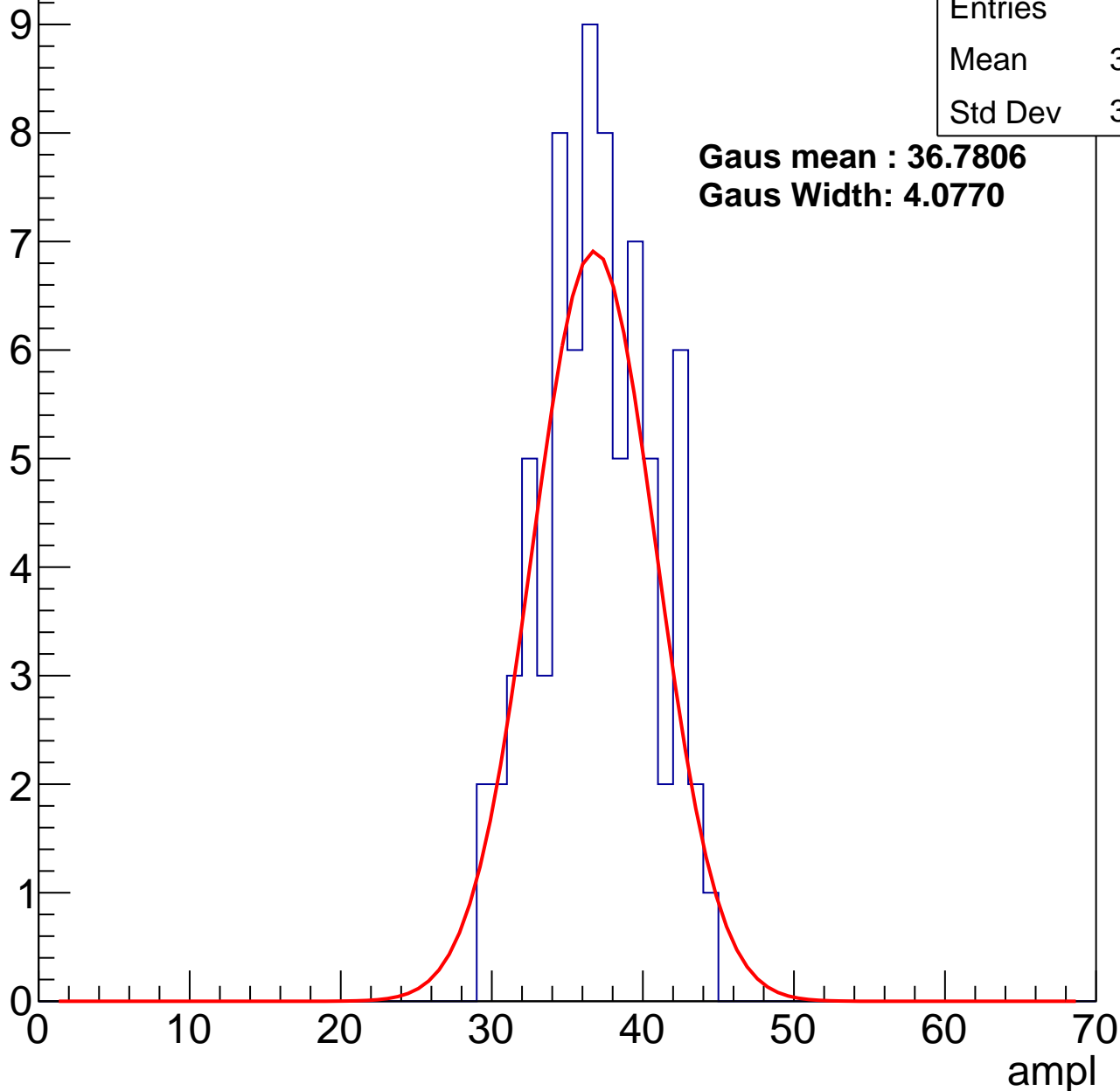
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.47
Std Dev	3.625

Gaus mean : 36.7806

Gaus Width: 4.0770



B1L102S, U10-ch87, adc2

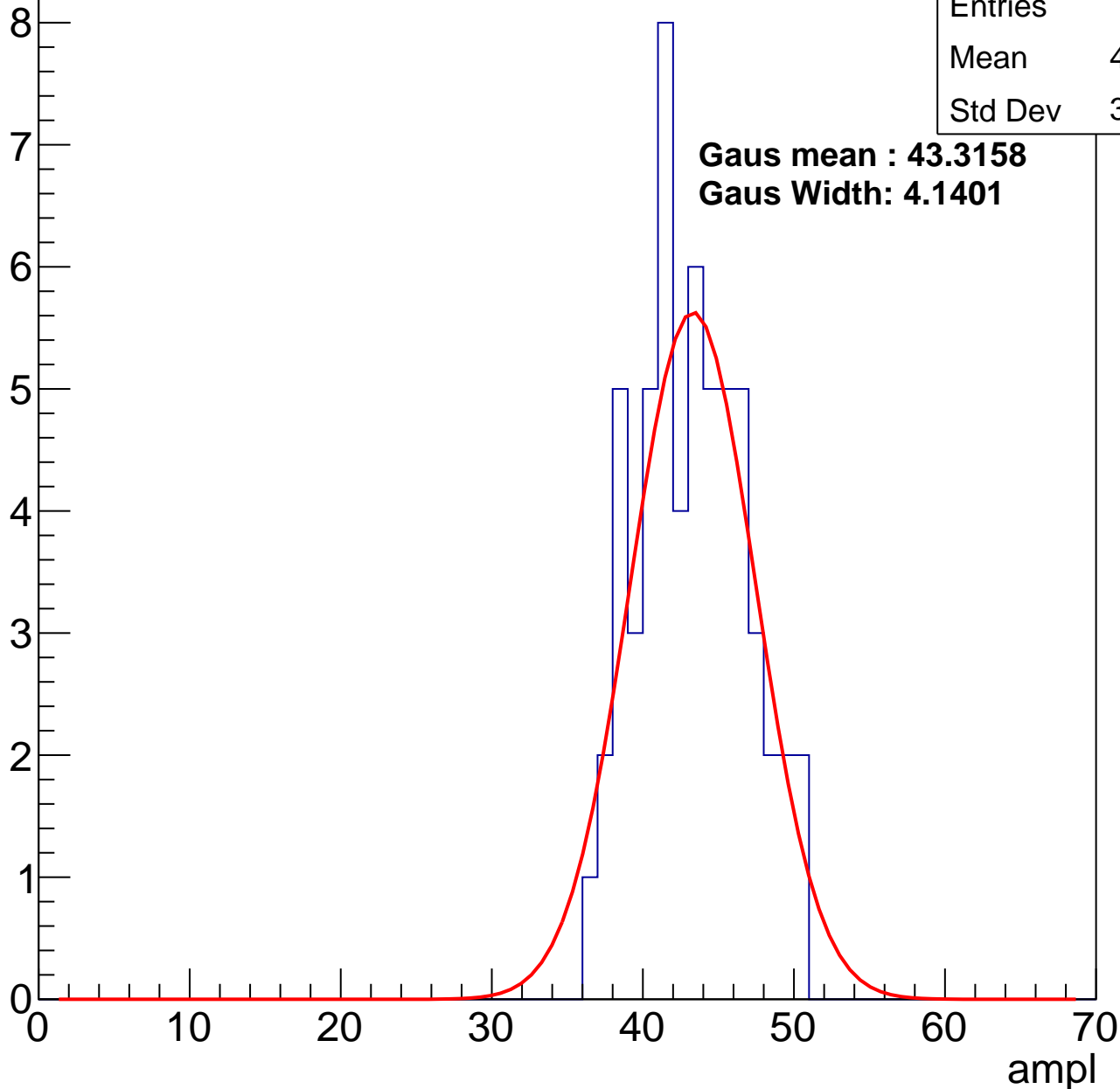
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.78
Std Dev	3.499

Gaus mean : 43.3158

Gaus Width: 4.1401

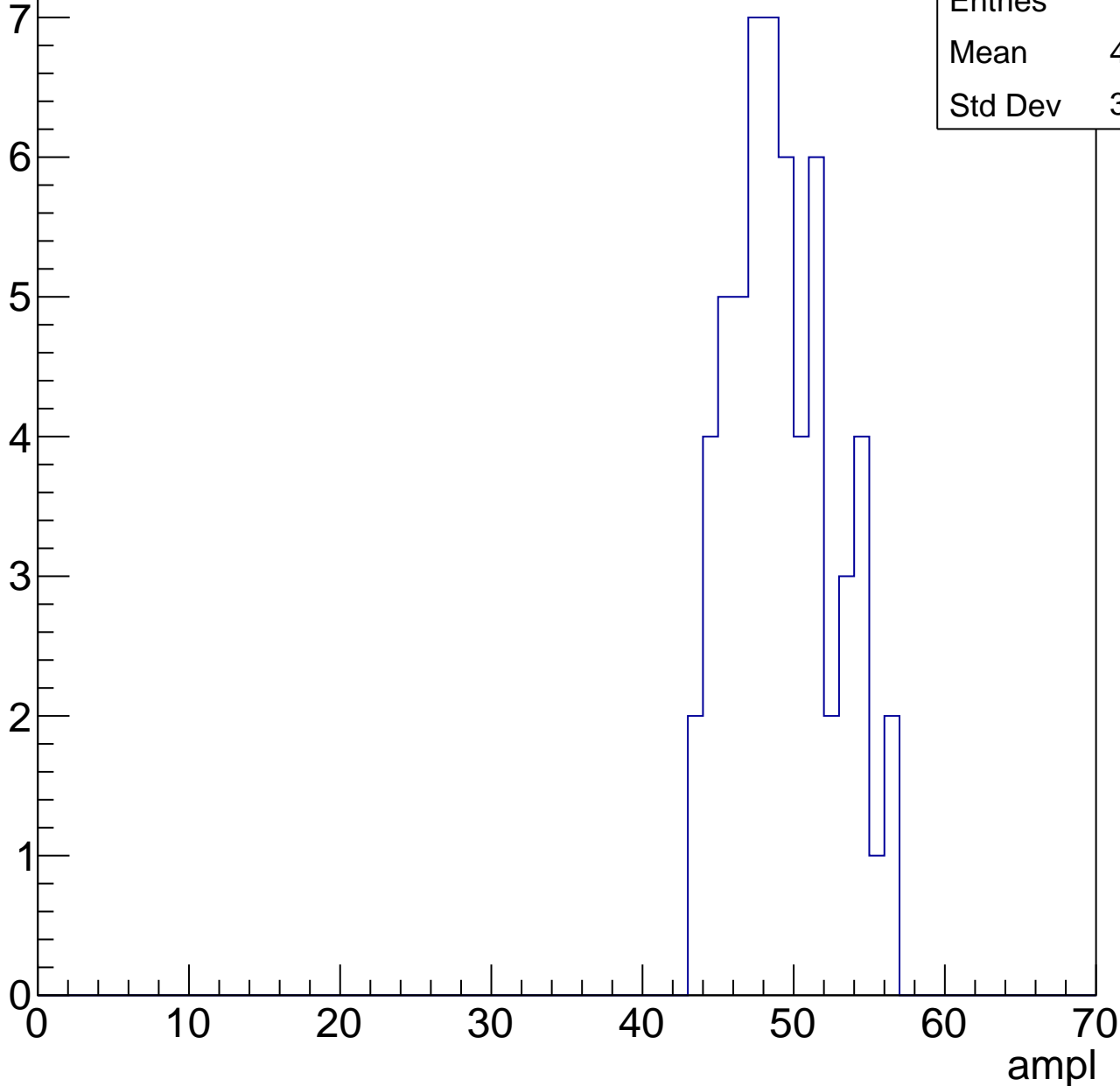


B1L102S, U10-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.76
Std Dev	3.365

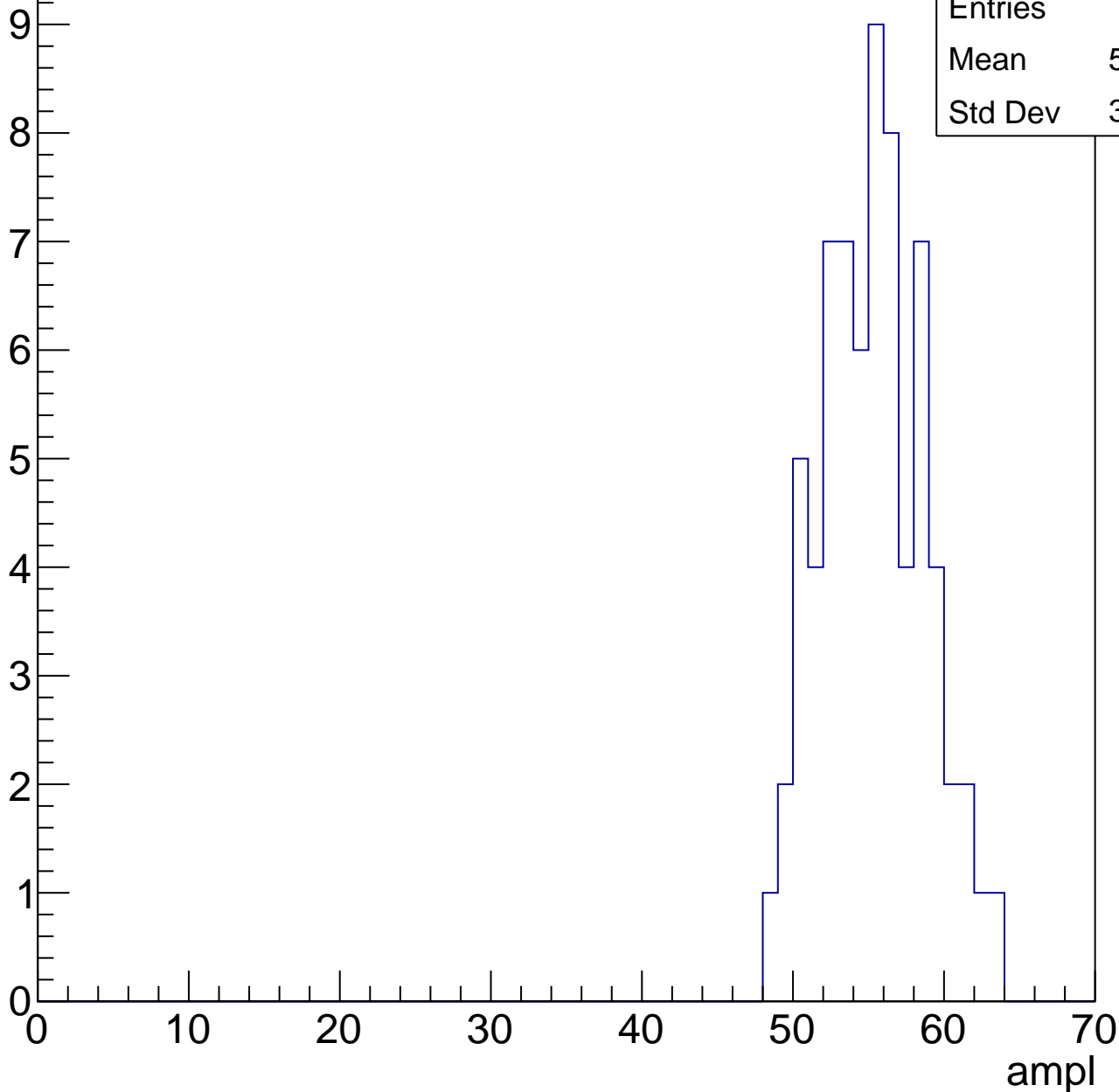


B1L102S, U10-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.84
Std Dev	3.362

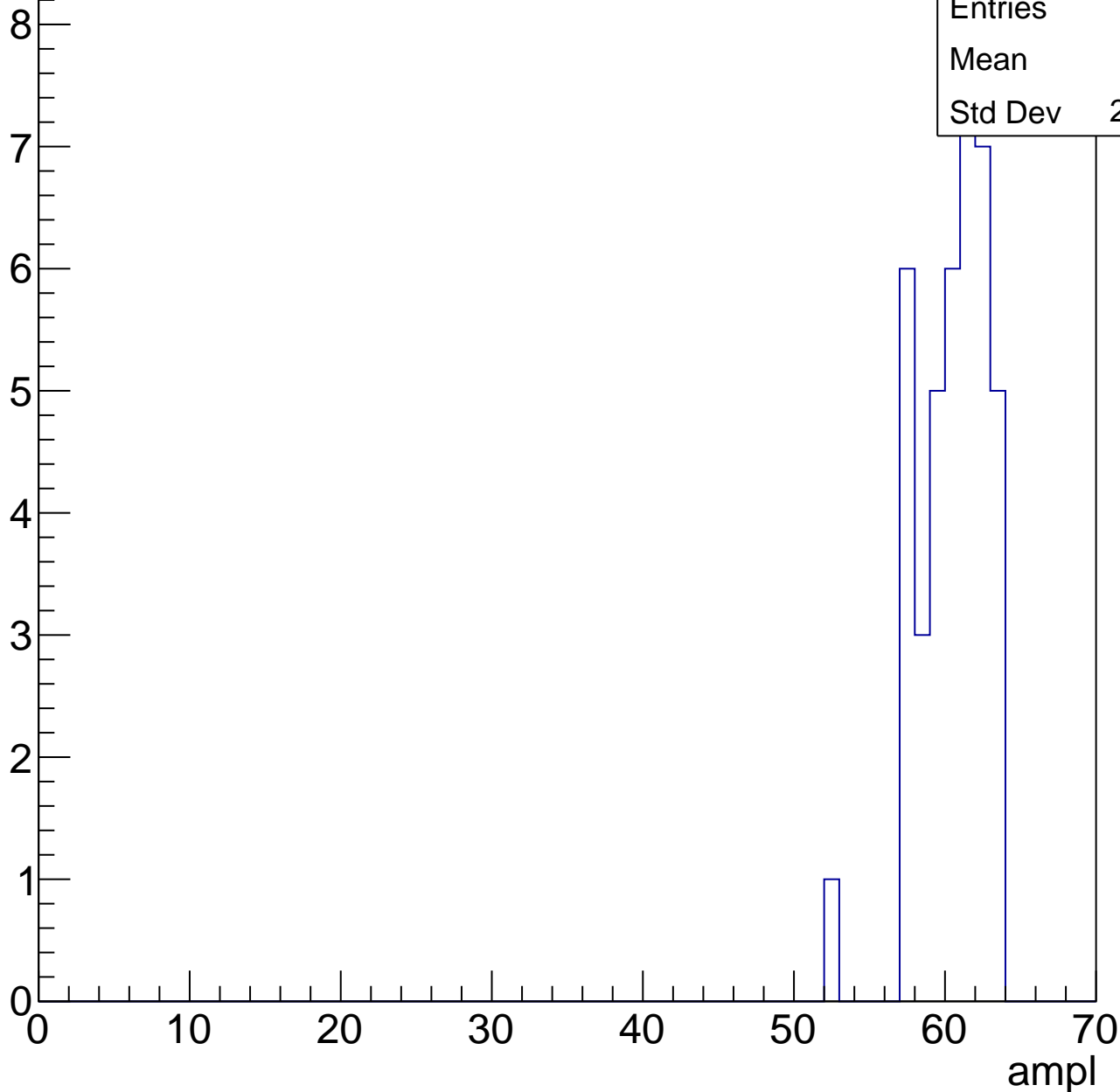


B1L102S, U10-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

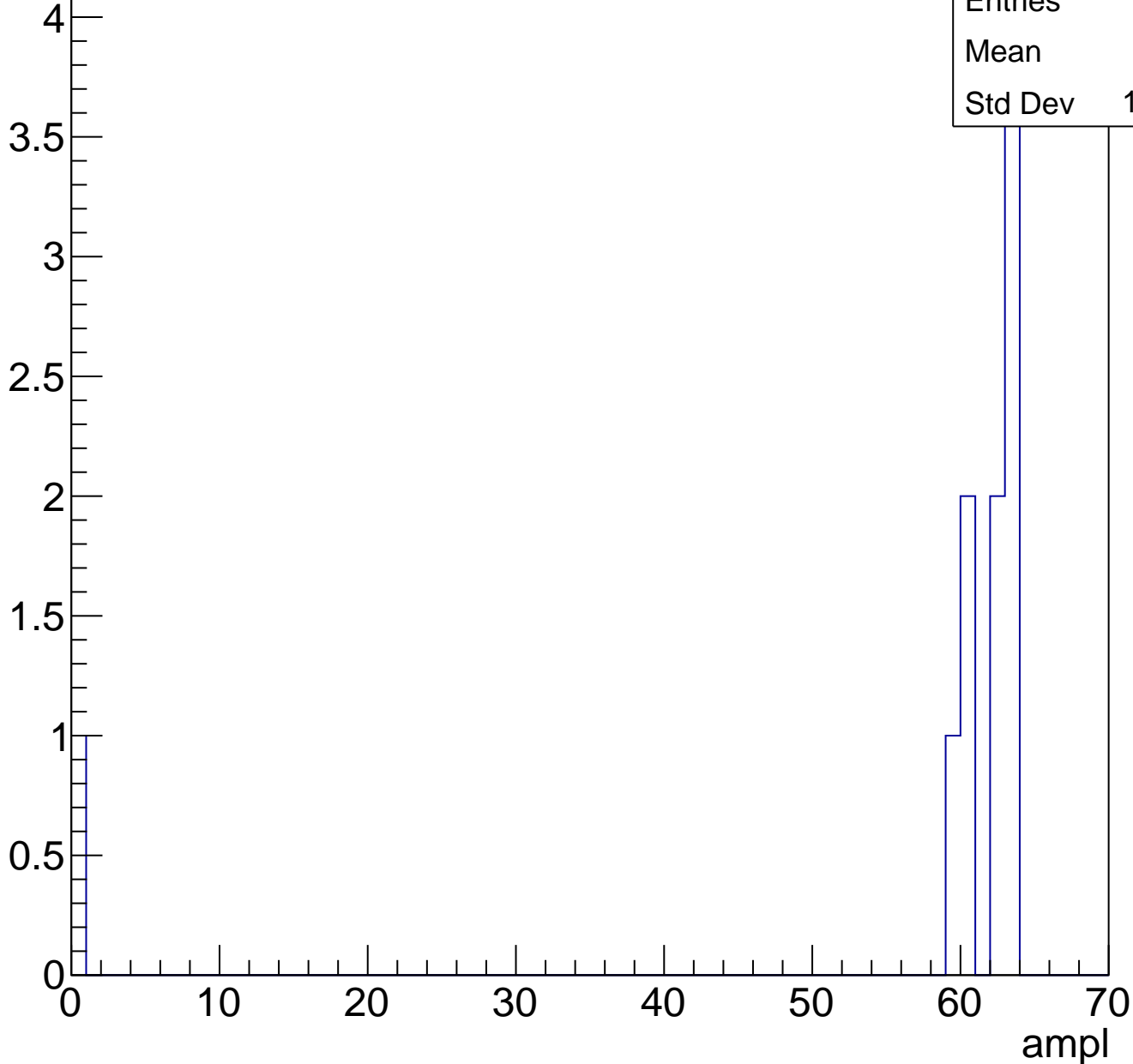
Entries	41
Mean	60
Std Dev	2.295



B1L102S, U10-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L102S, U10-ch88, adc0

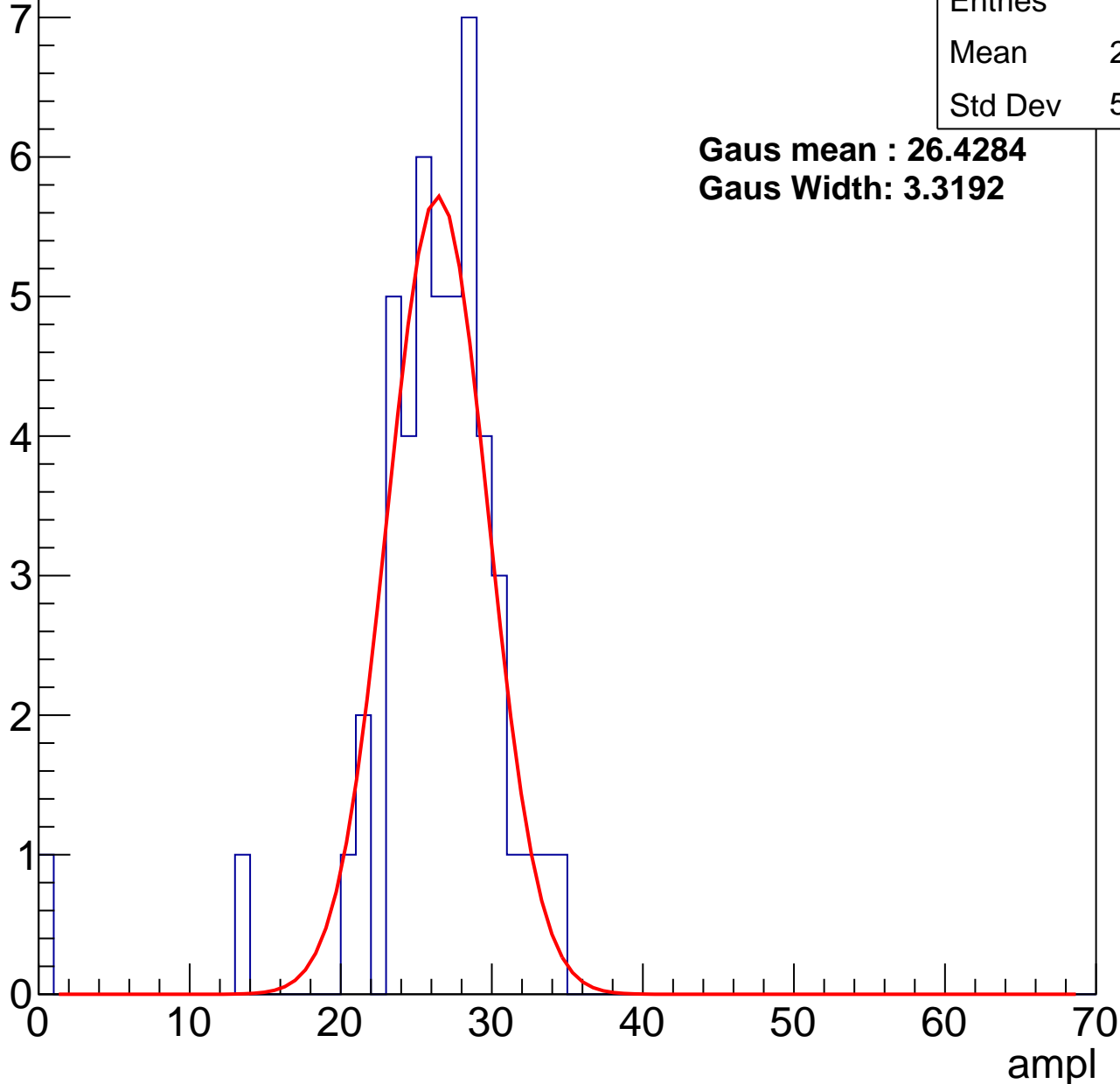
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	25.69
Std Dev	5.173

Gaus mean : 26.4284

Gaus Width: 3.3192



B1L102S, U10-ch88, adc1

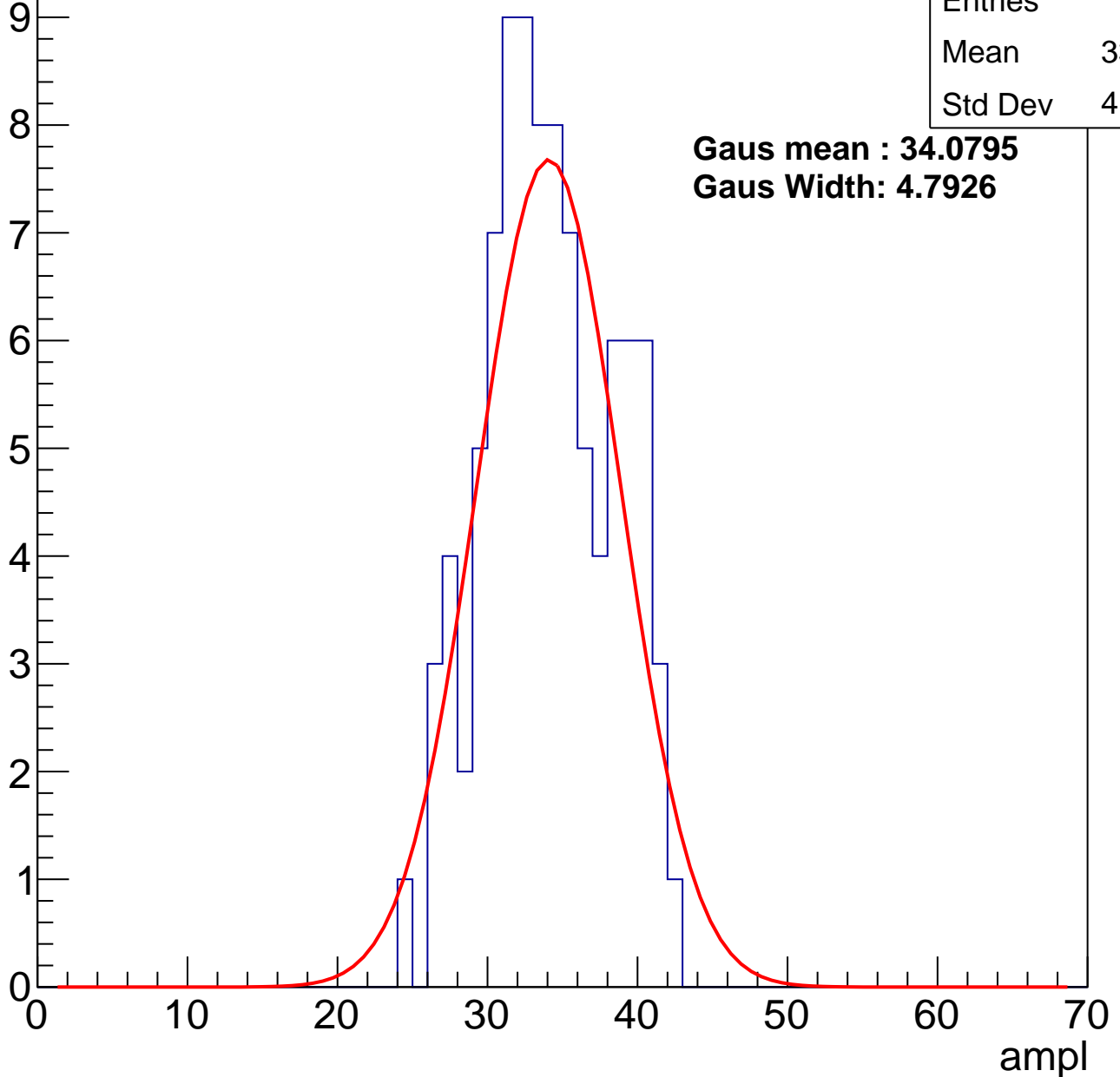
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	33.66
Std Dev	4.186

Gaus mean : 34.0795

Gaus Width: 4.7926



B1L102S, U10-ch88, adc2

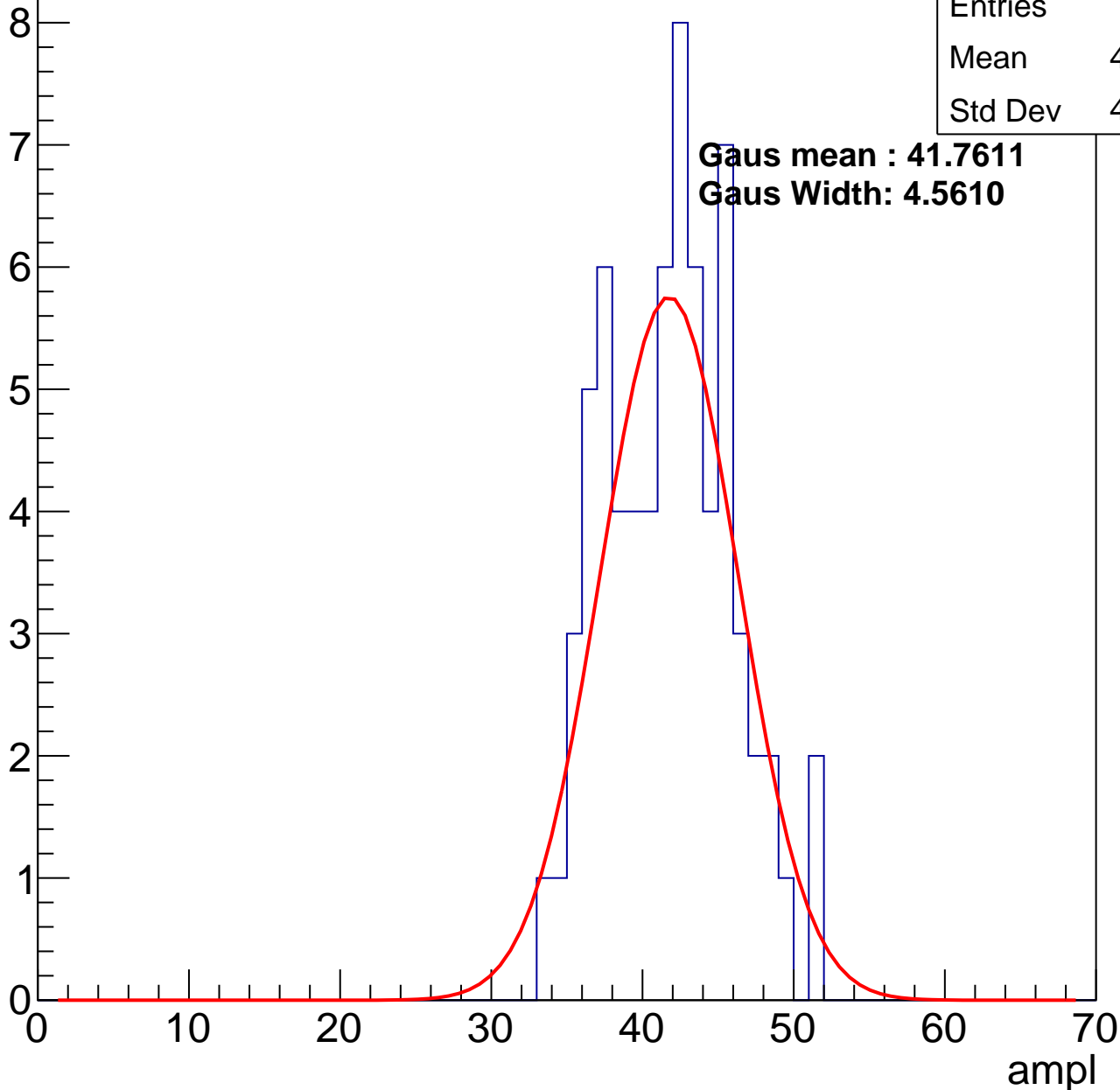
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.33
Std Dev	4.134

Gaus mean : 41.7611

Gaus Width: 4.5610

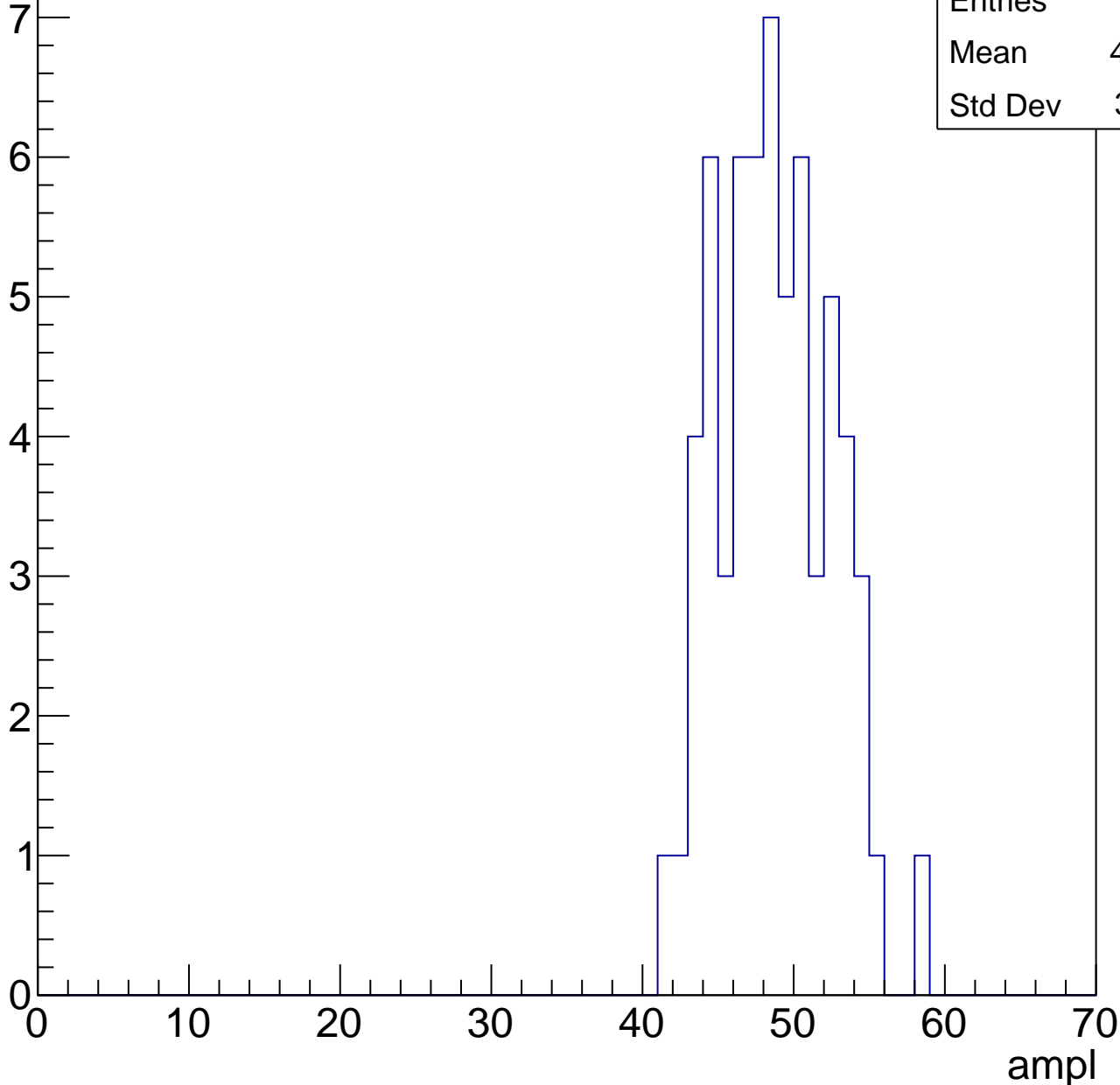


B1L102S, U10-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.27
Std Dev	3.651

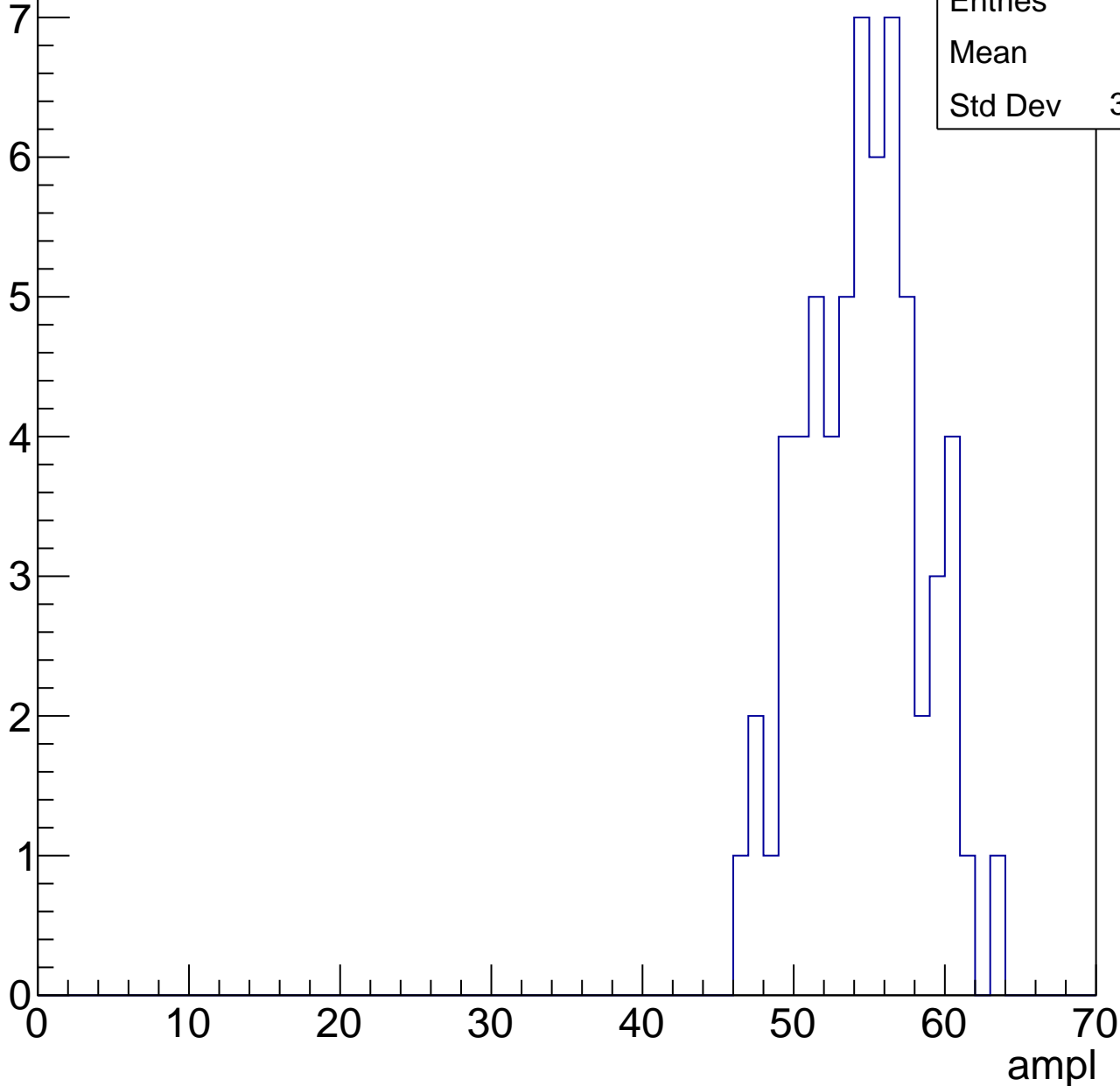


B1L102S, U10-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

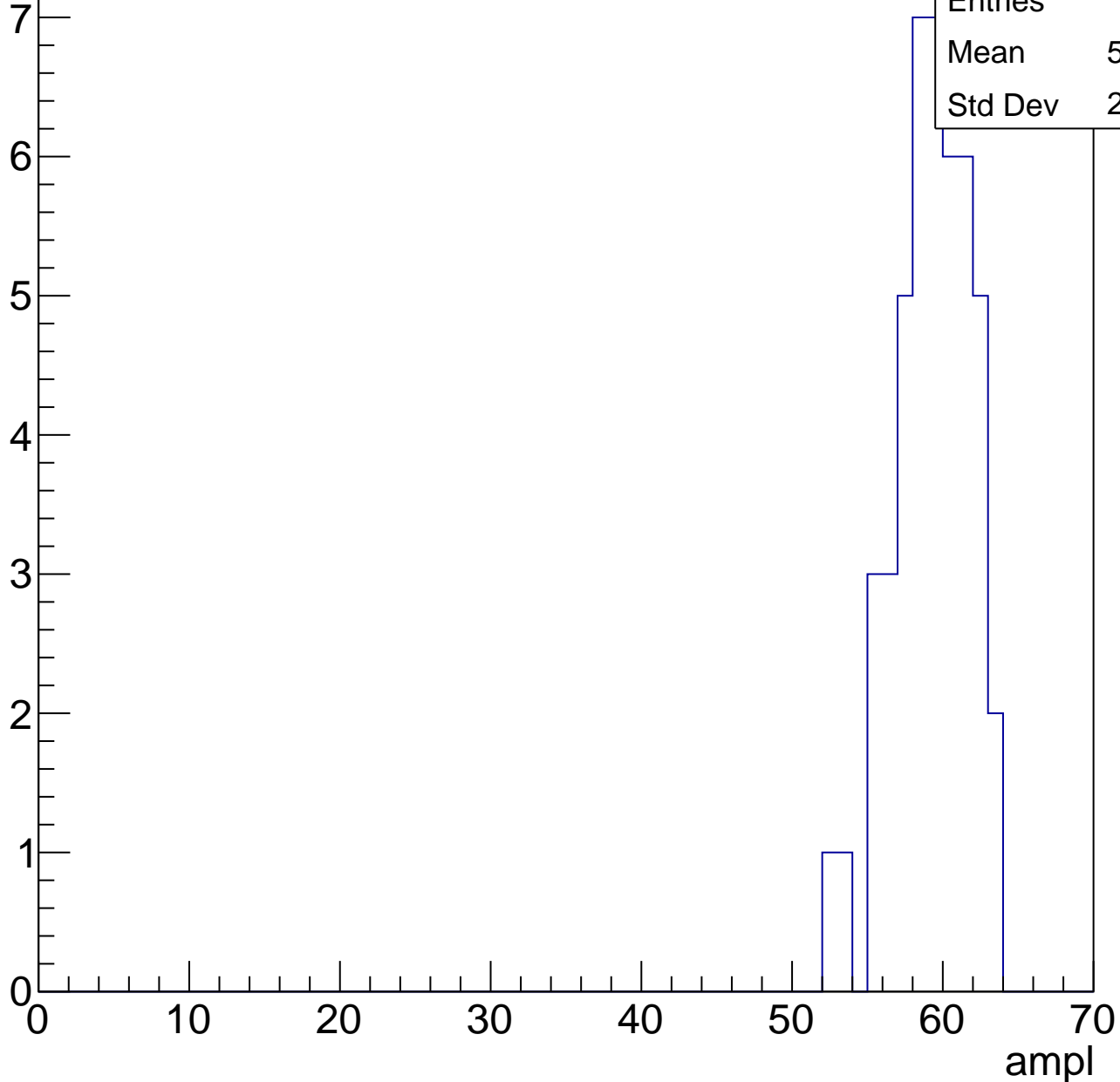
Entries	62
Mean	54.1
Std Dev	3.783



B1L102S, U10-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

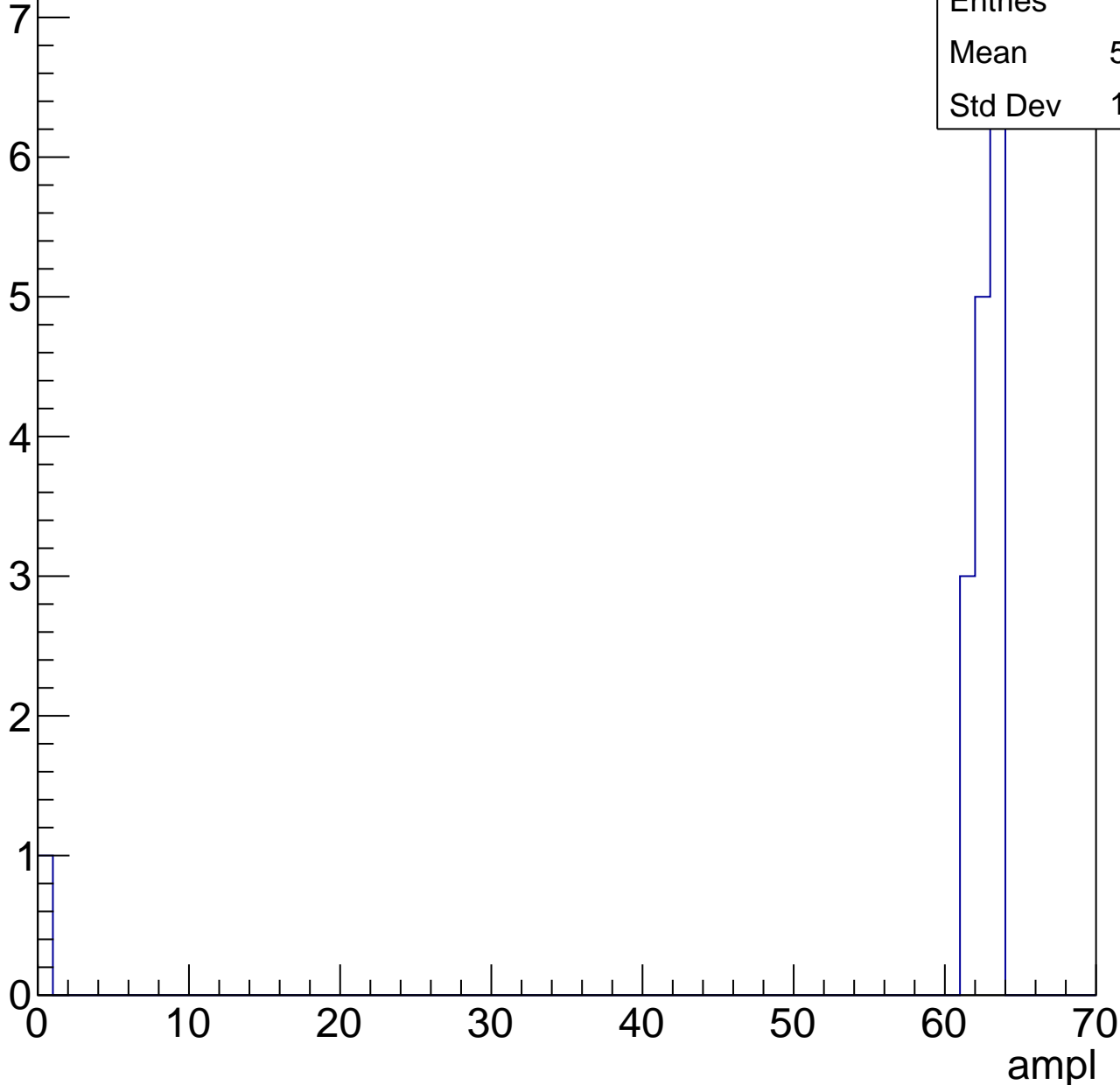


B1L102S, U10-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	58.38
Std Dev	15.09



B1L102S, U10-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U10-ch89, adc0

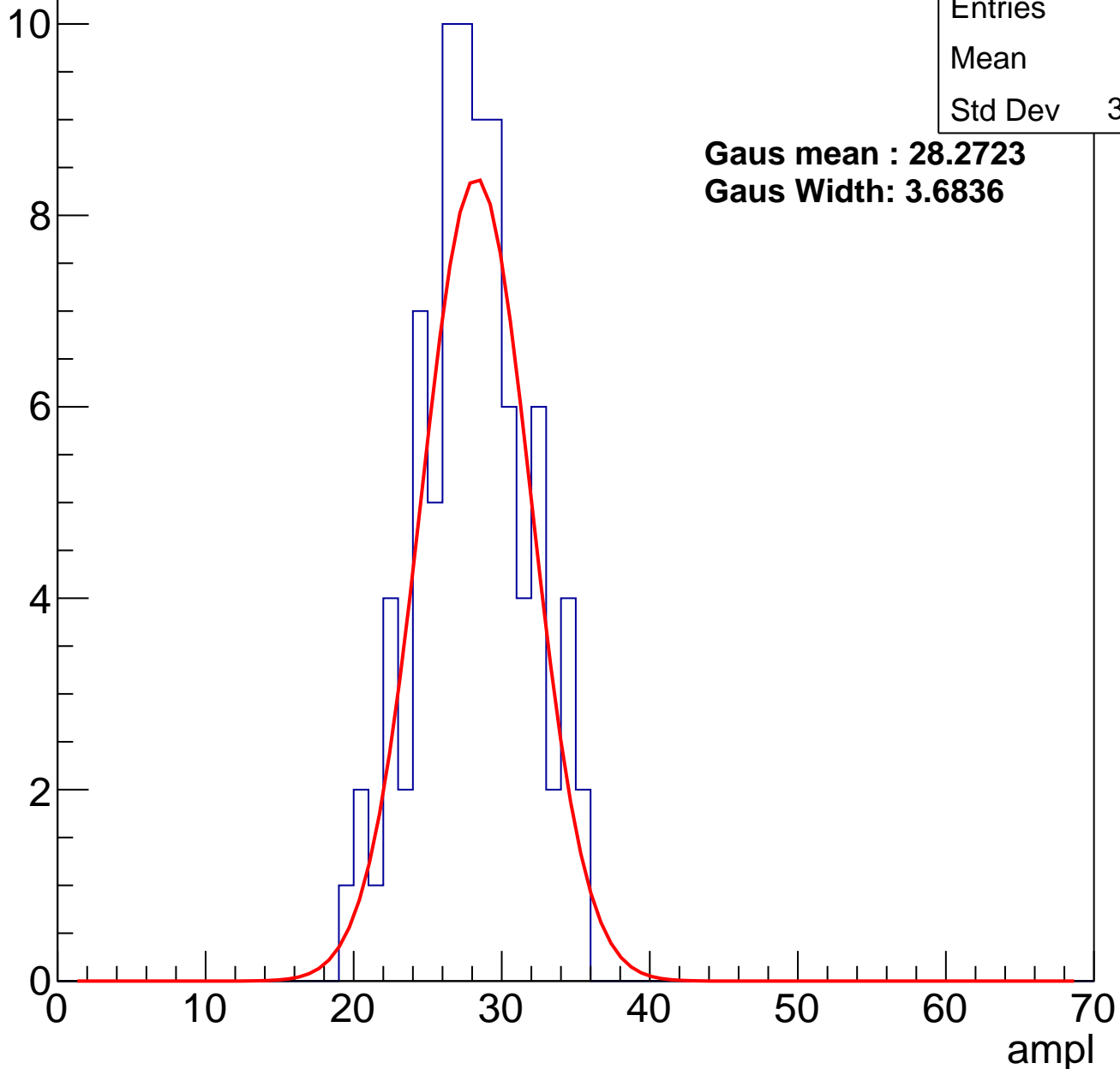
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	27.6
Std Dev	3.622

Gaus mean : 28.2723

Gaus Width: 3.6836

Entry



B1L102S, U10-ch89, adc1

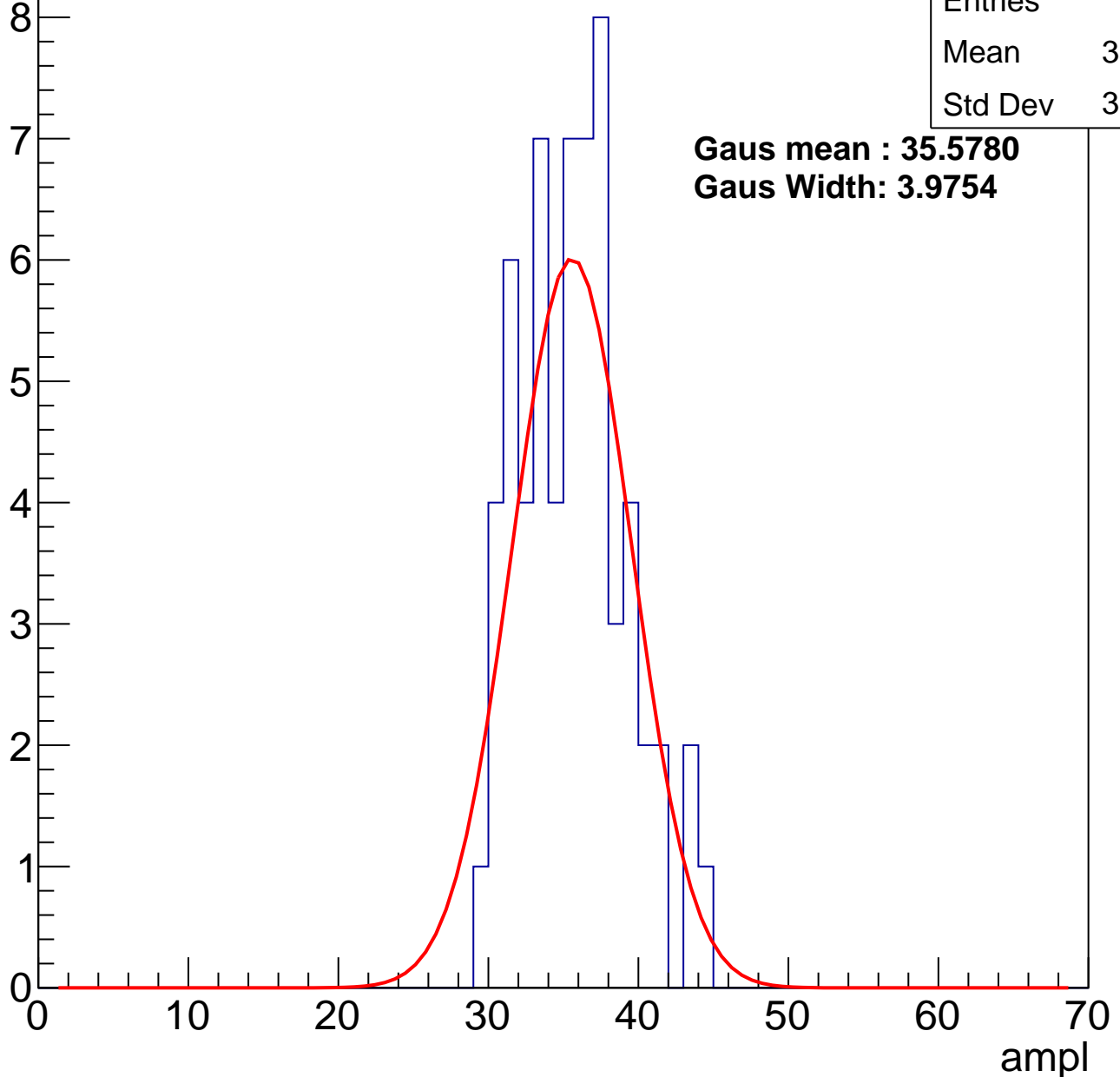
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.24
Std Dev	3.495

Gaus mean : 35.5780

Gaus Width: 3.9754



B1L102S, U10-ch89, adc2

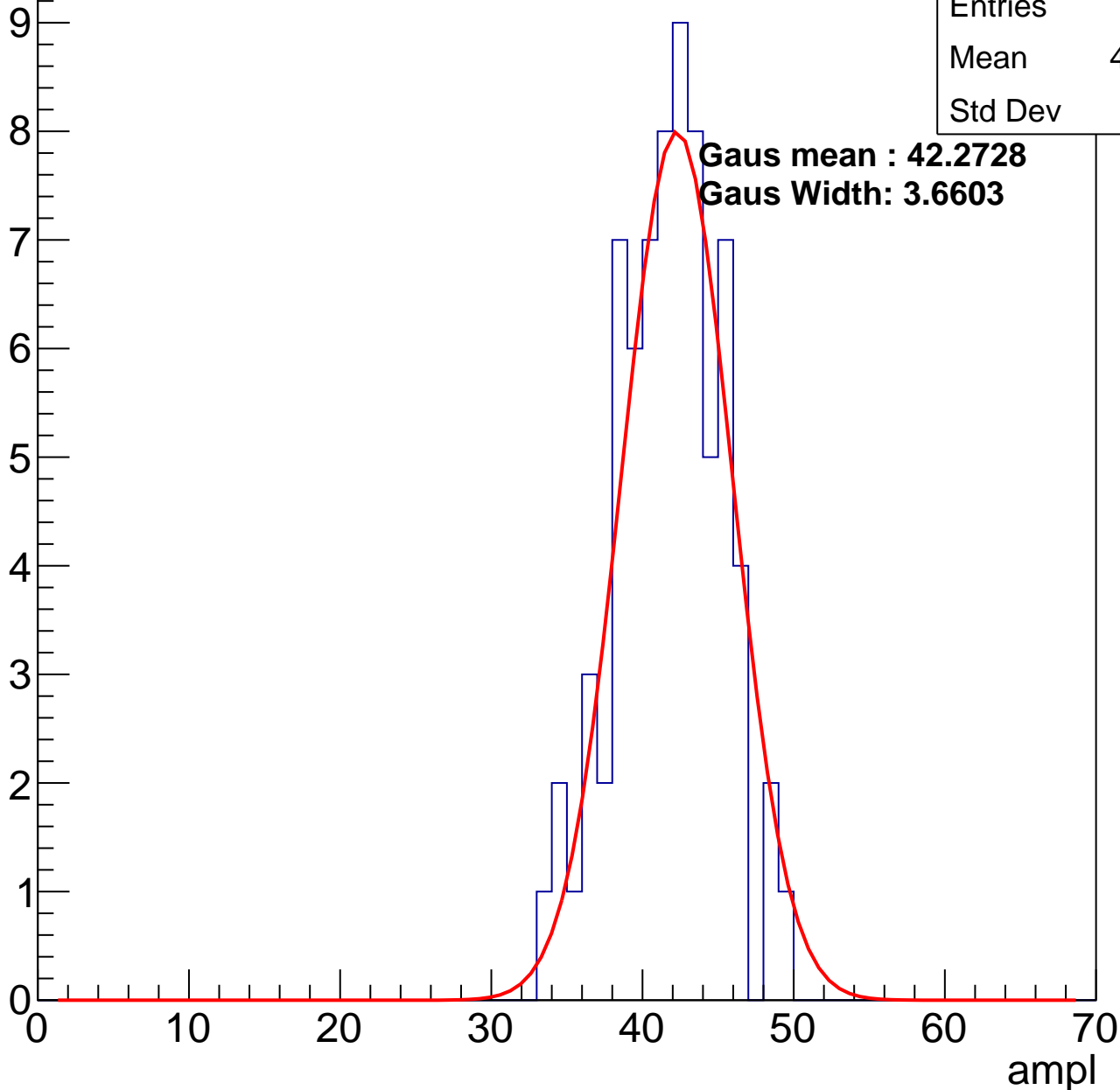
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.26
Std Dev	3.42

Gaus mean : 42.2728

Gaus Width: 3.6603



B1L102S, U10-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	48.97
Std Dev	3.272

Entry

10

8

6

4

2

0

0

10

20

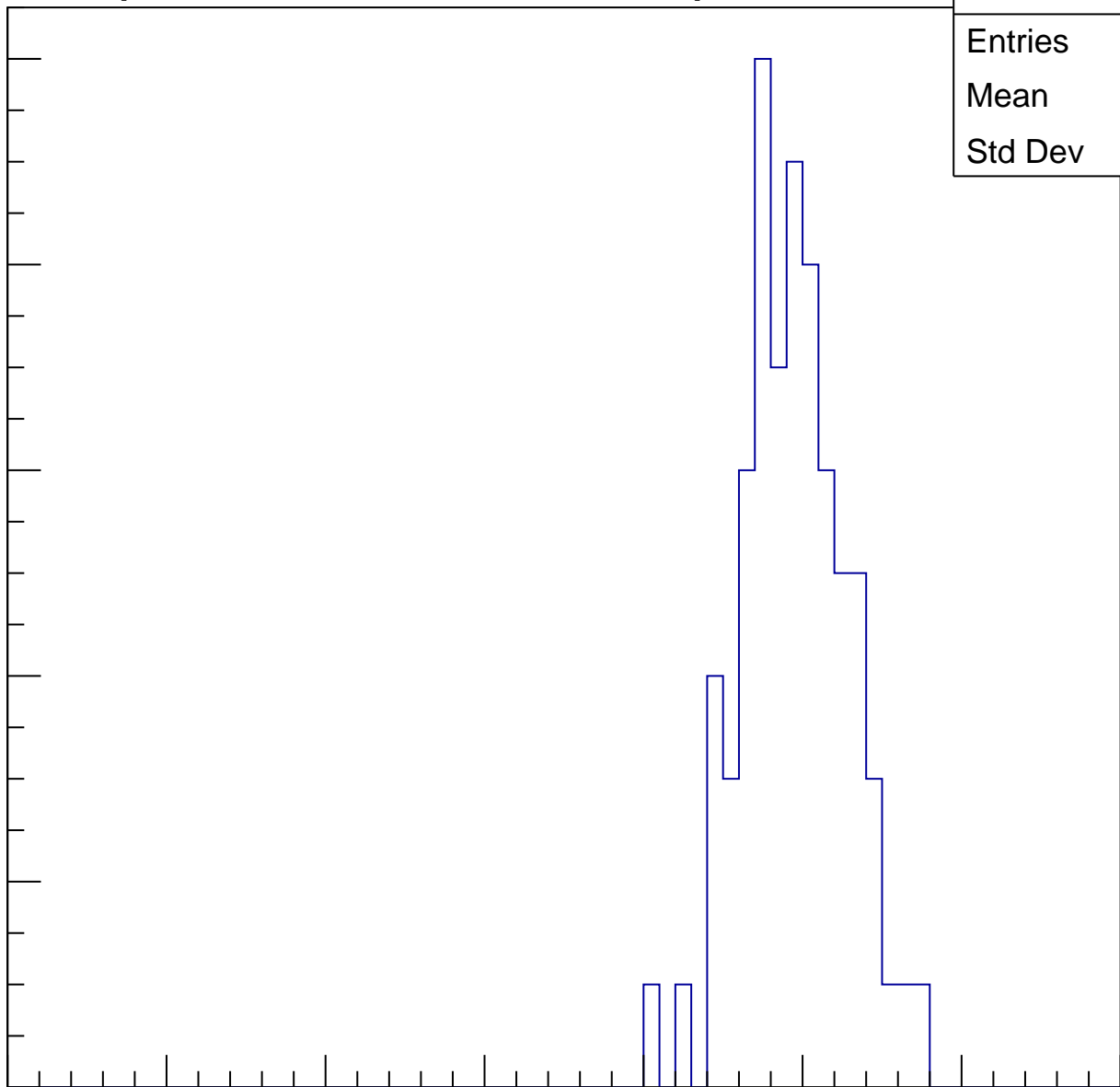
30

40

50

60

ampl

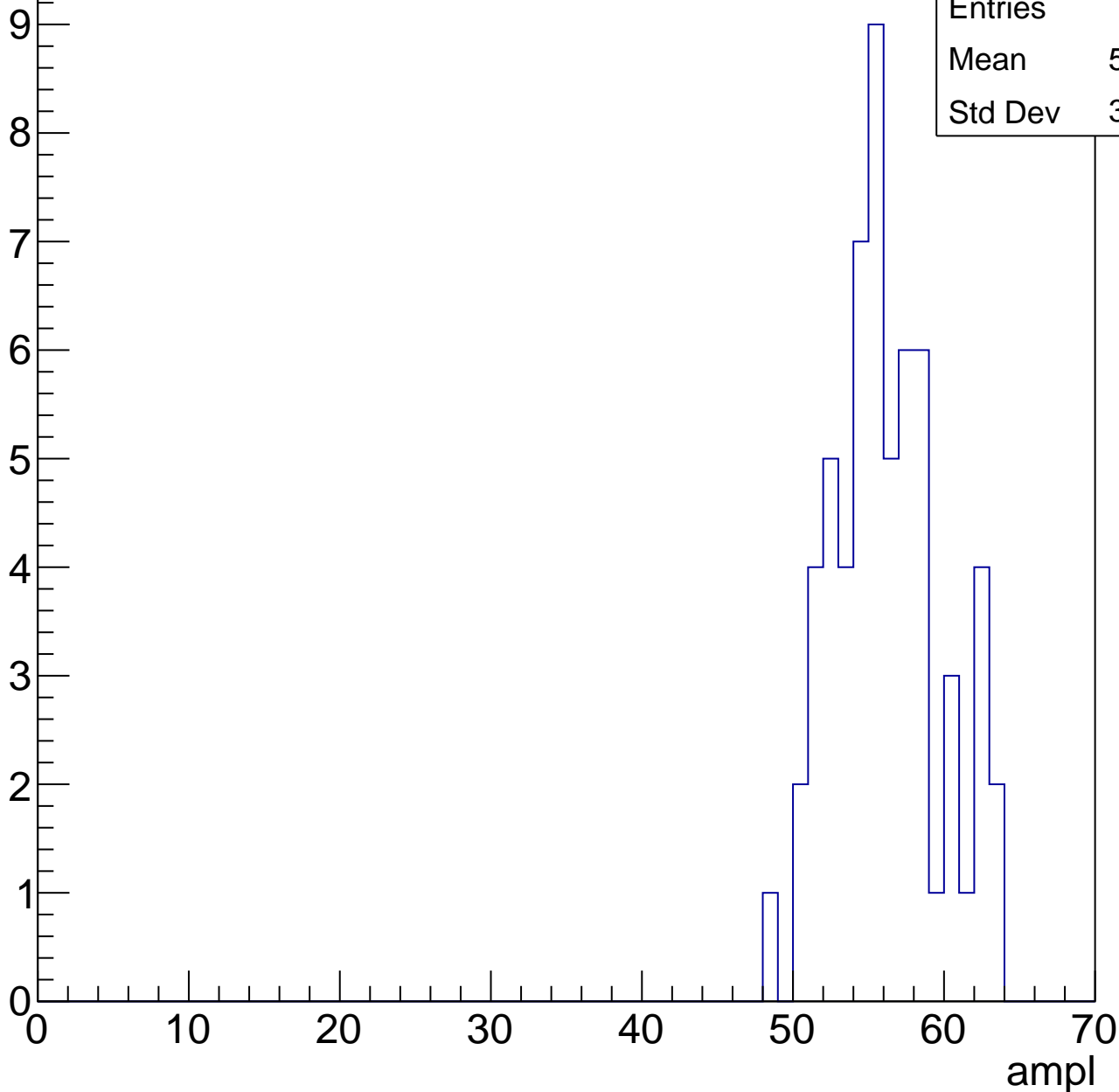


B1L102S, U10-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

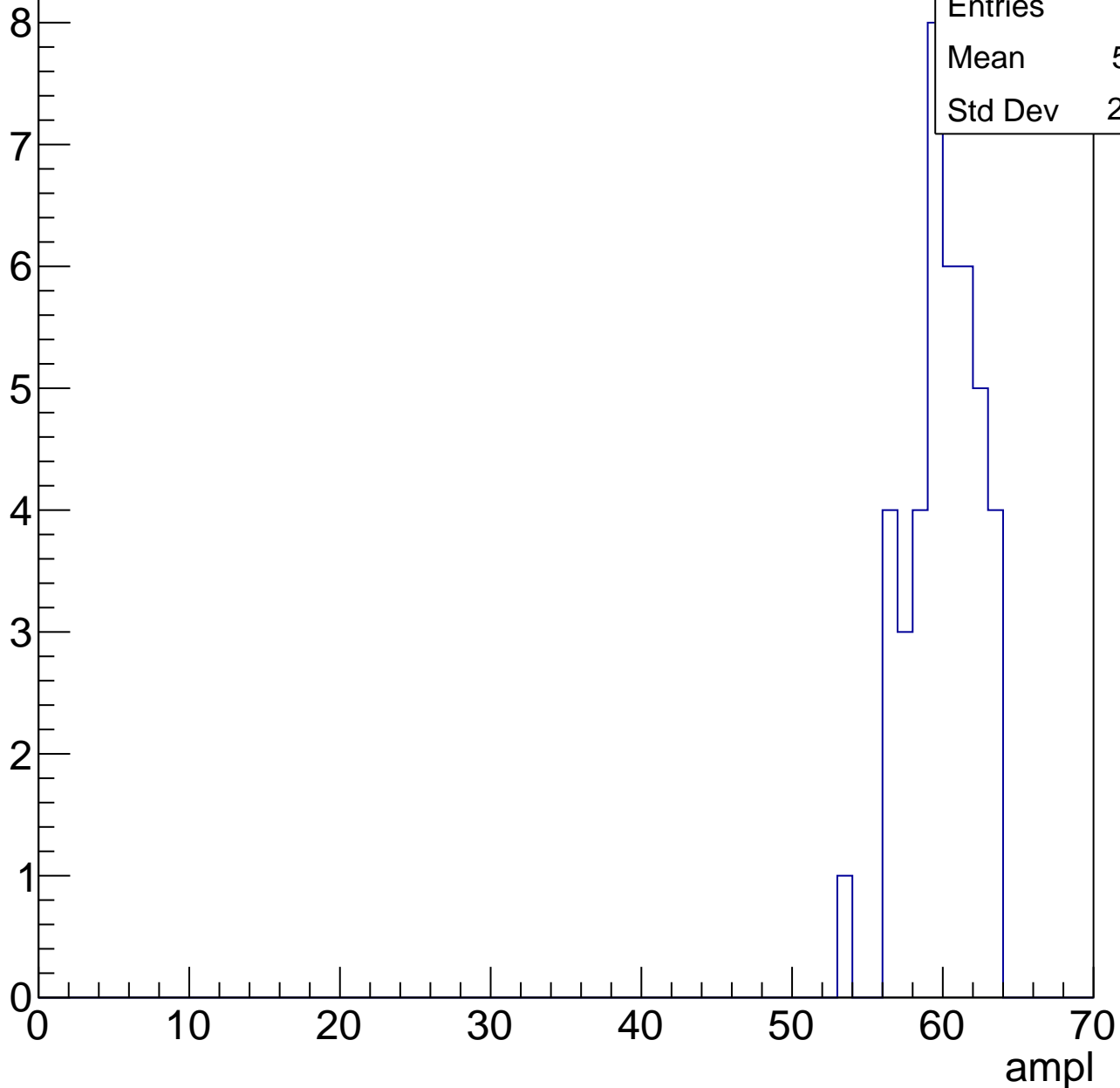
Entries	60
Mean	55.68
Std Dev	3.505



B1L102S, U10-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

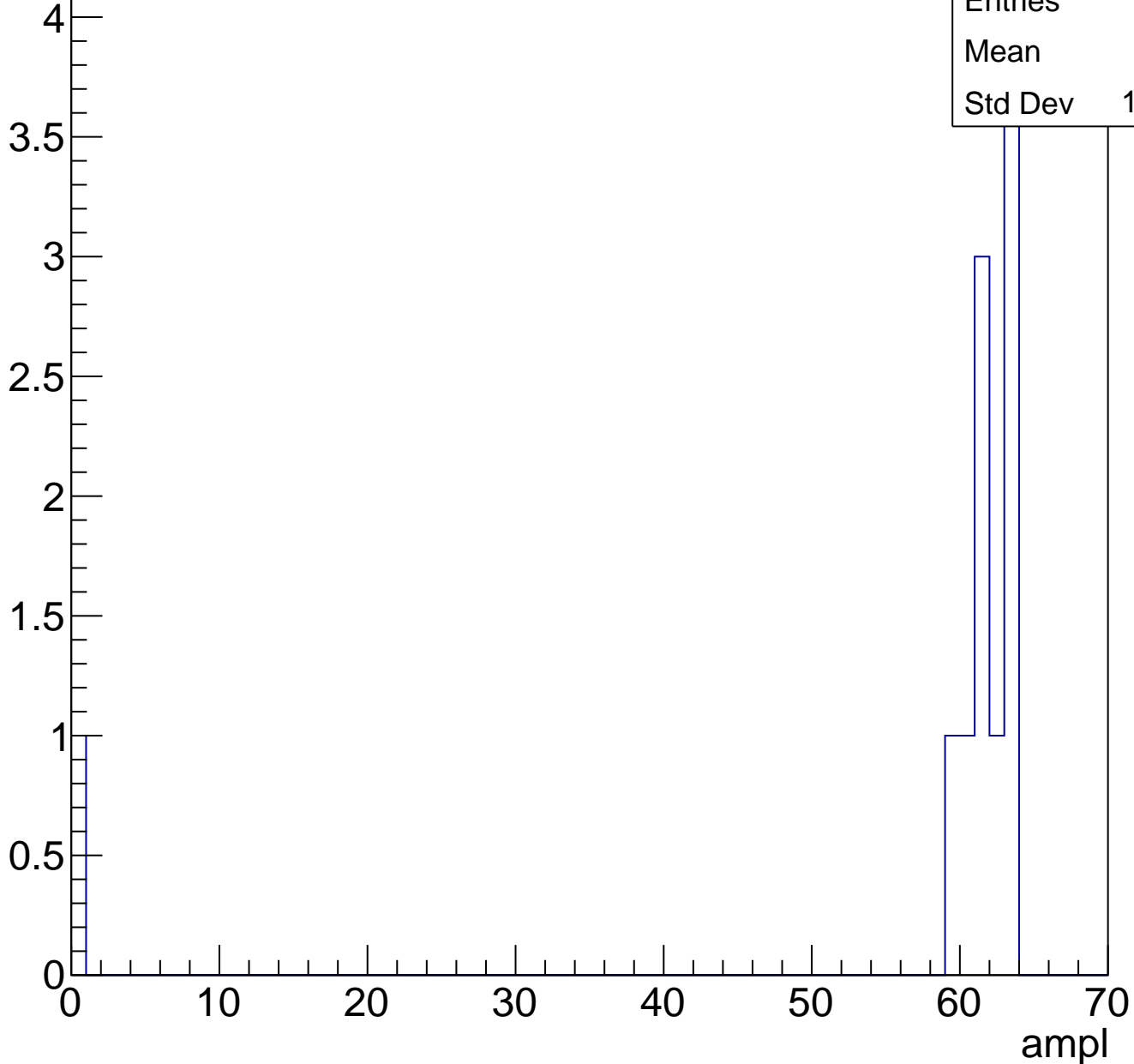


Entries	41
Mean	59.51
Std Dev	2.297

B1L102S, U10-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch90, adc0

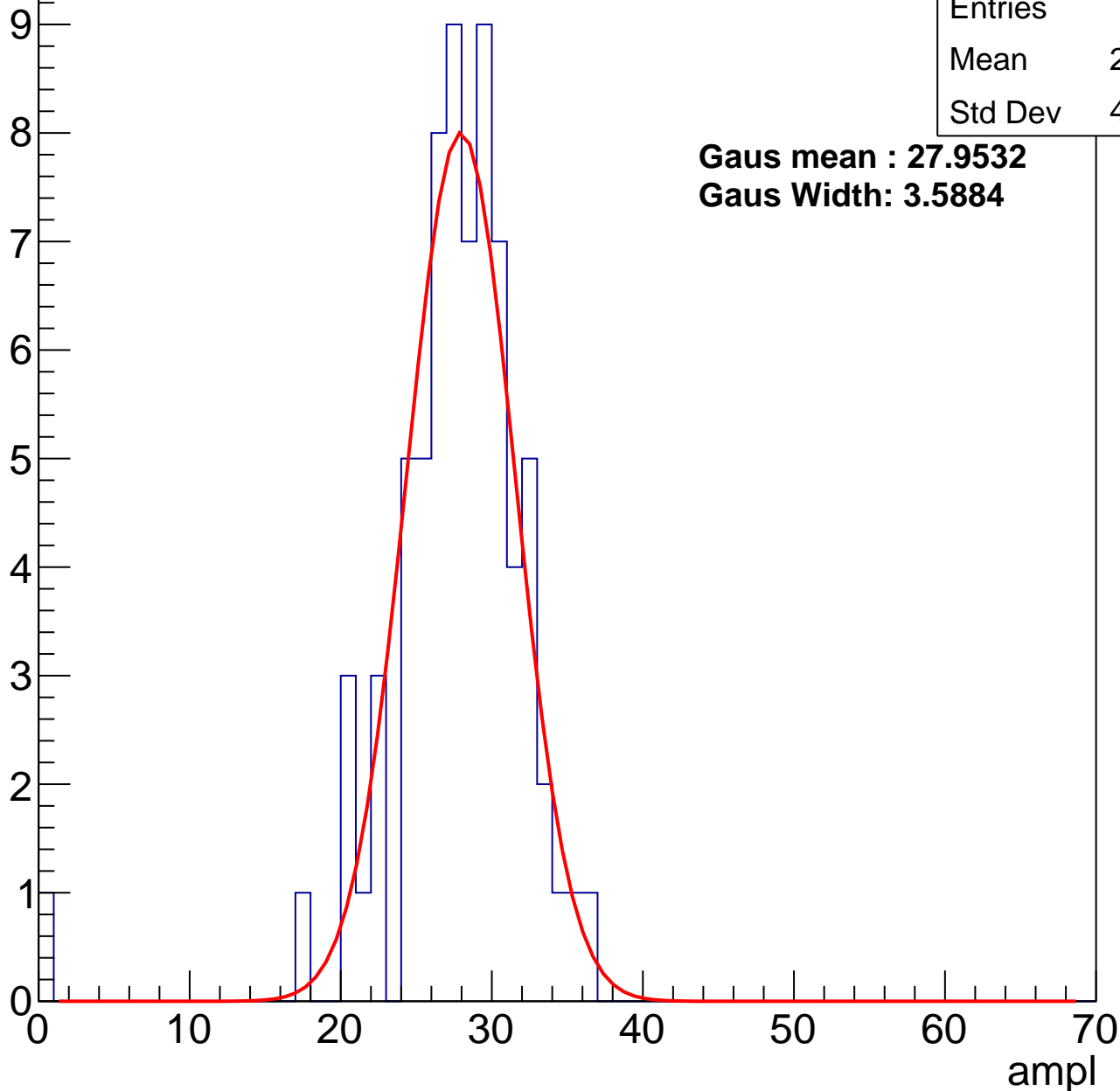
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.15
Std Dev	4.853

Gaus mean : 27.9532

Gaus Width: 3.5884



B1L102S, U10-ch90, adc1

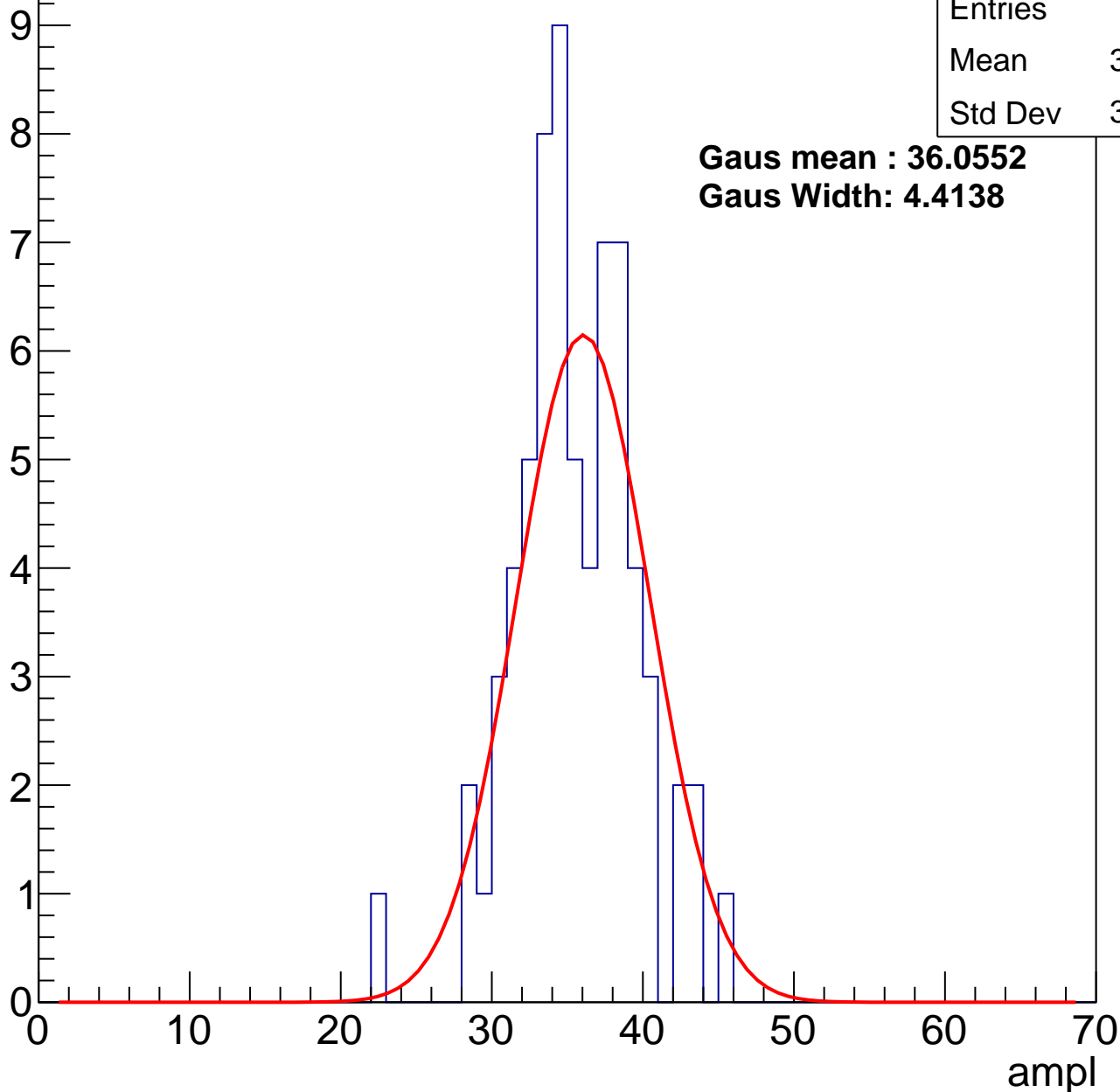
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.09
Std Dev	3.999

Gaus mean : 36.0552

Gaus Width: 4.4138



B1L102S, U10-ch90, adc2

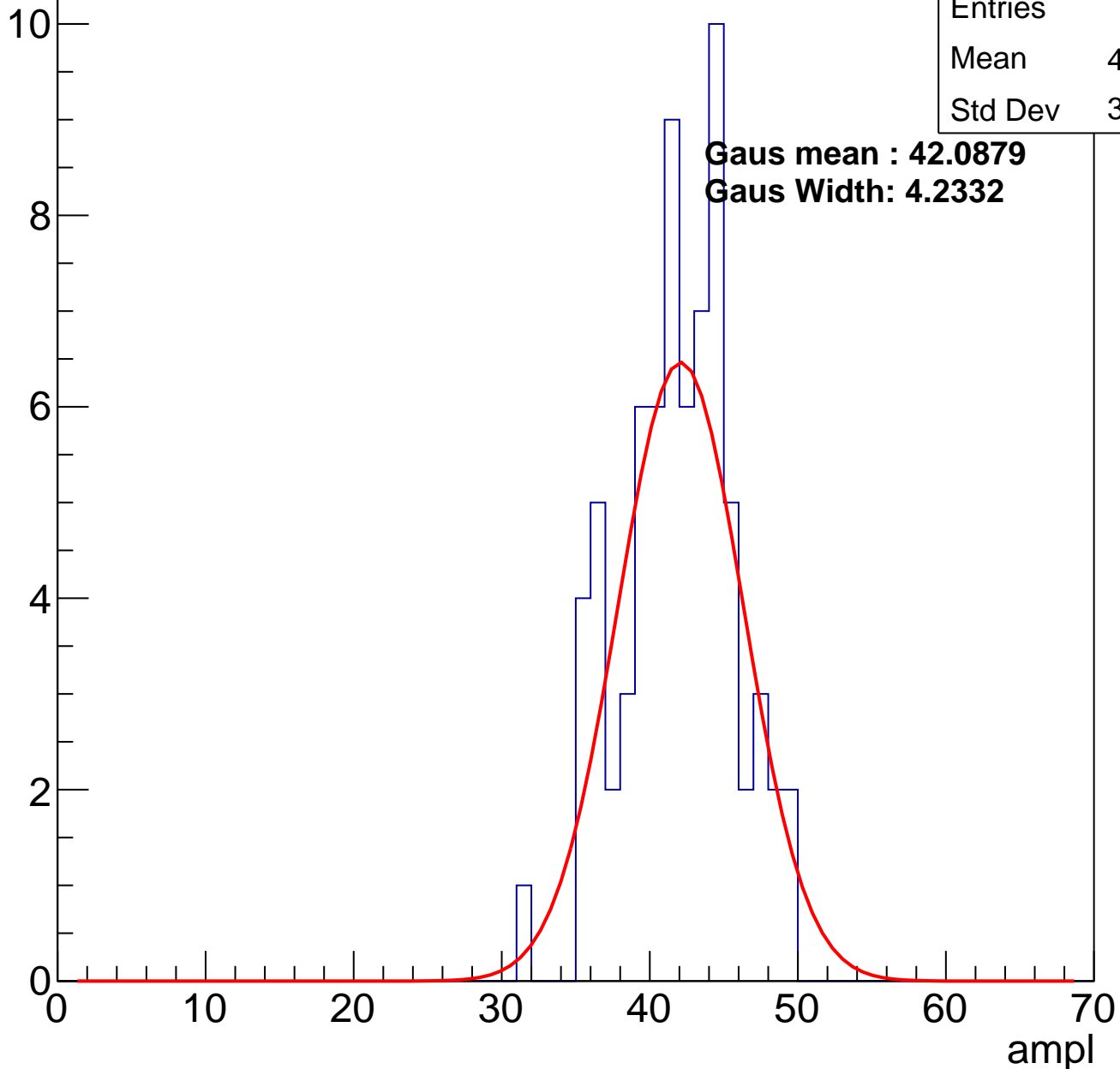
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	41.47
Std Dev	3.757

Gaus mean : 42.0879

Gaus Width: 4.2332

Entry

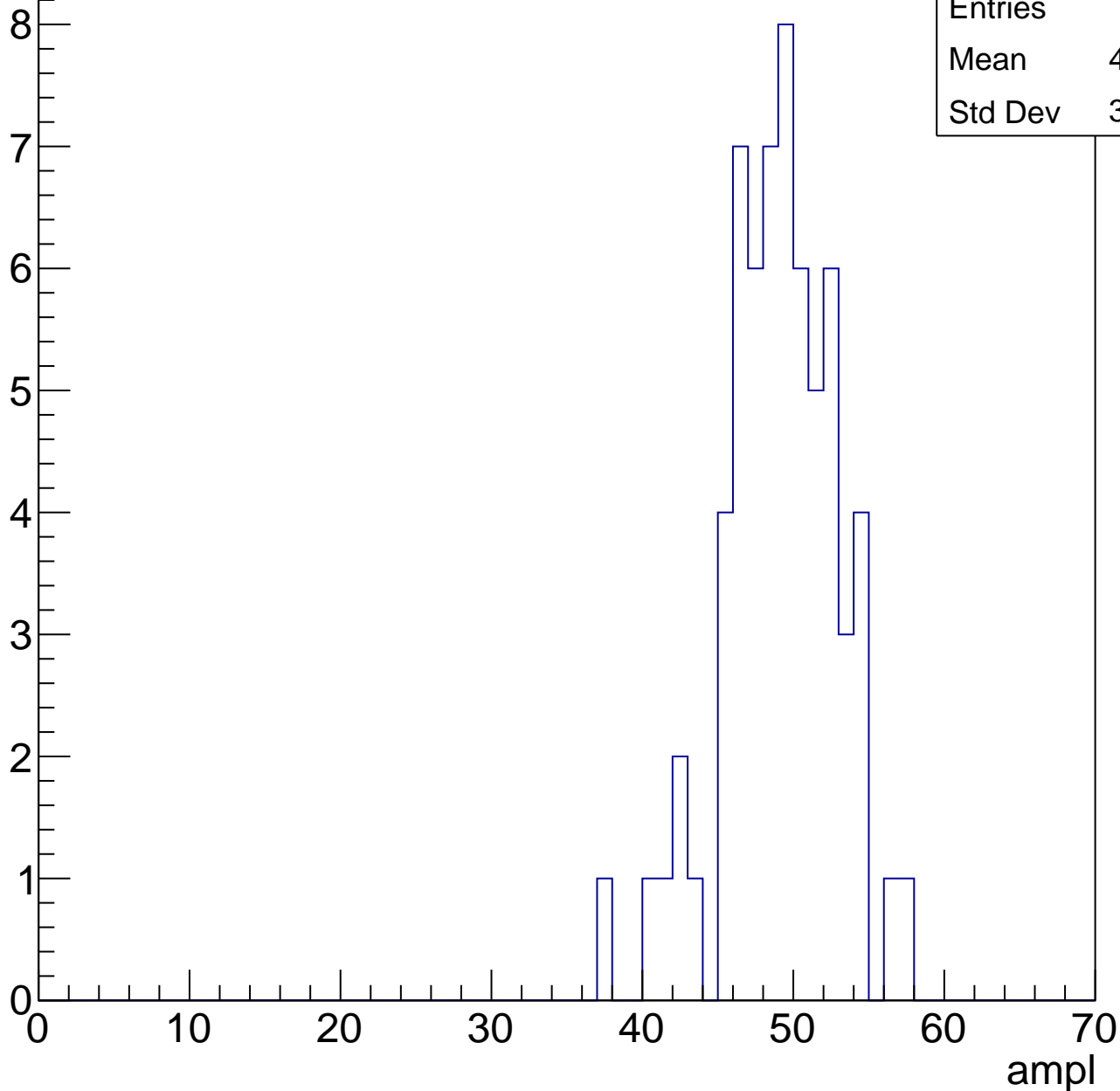


B1L102S, U10-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.62
Std Dev	3.777

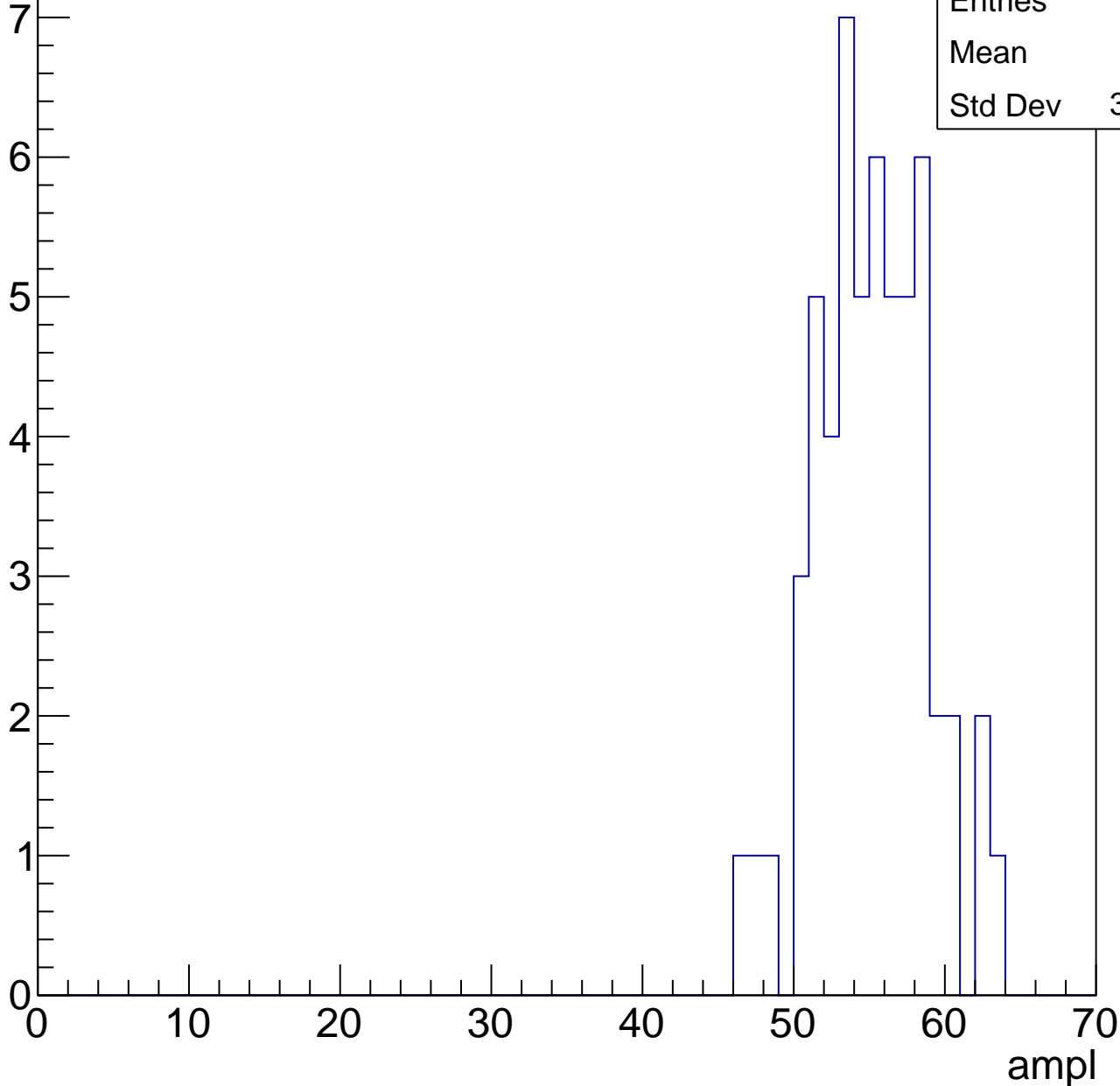


B1L102S, U10-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.7
Std Dev	3.625



B1L102S, U10-ch90, adc5

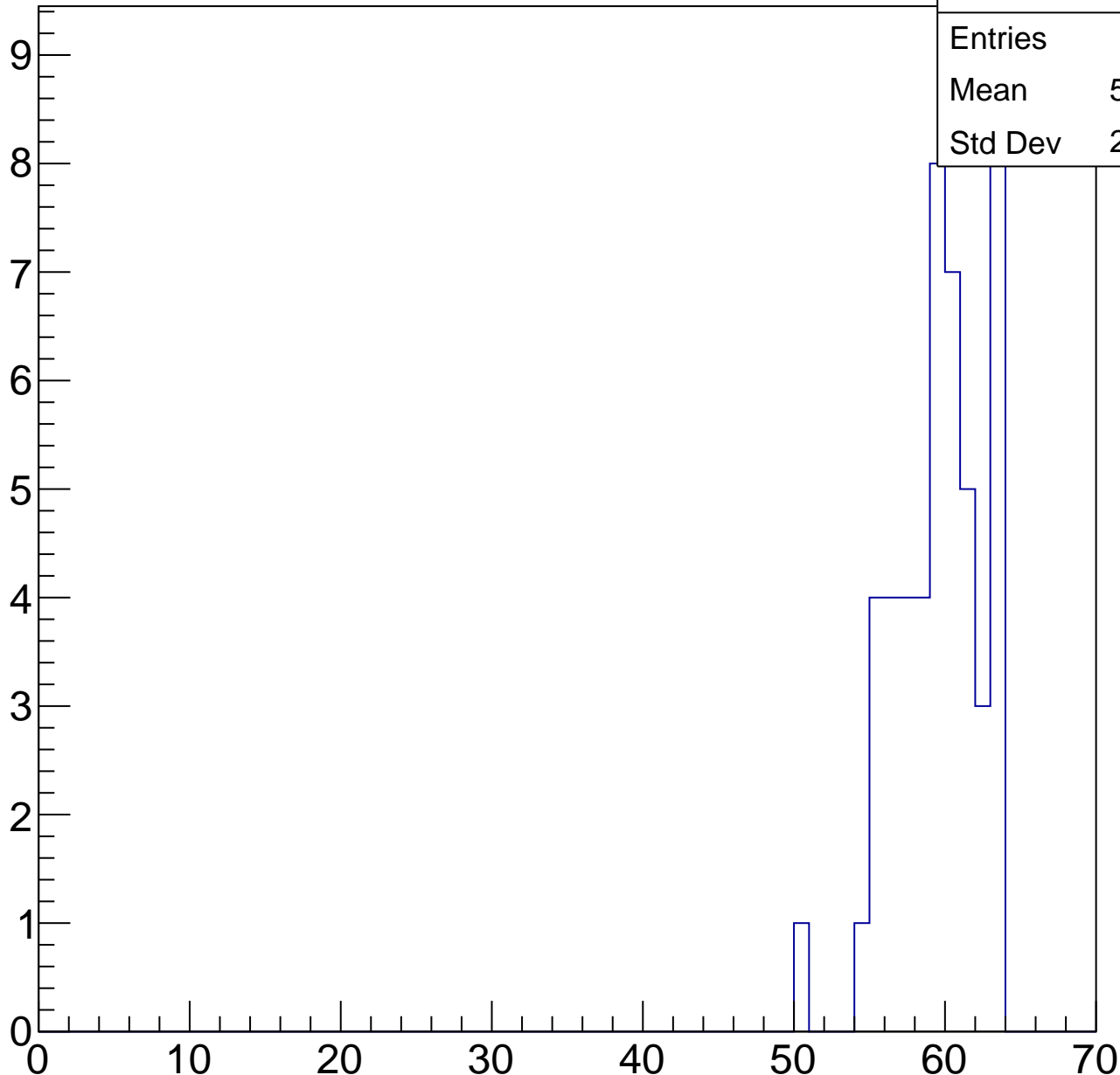
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.16
Std Dev	2.908

ampl

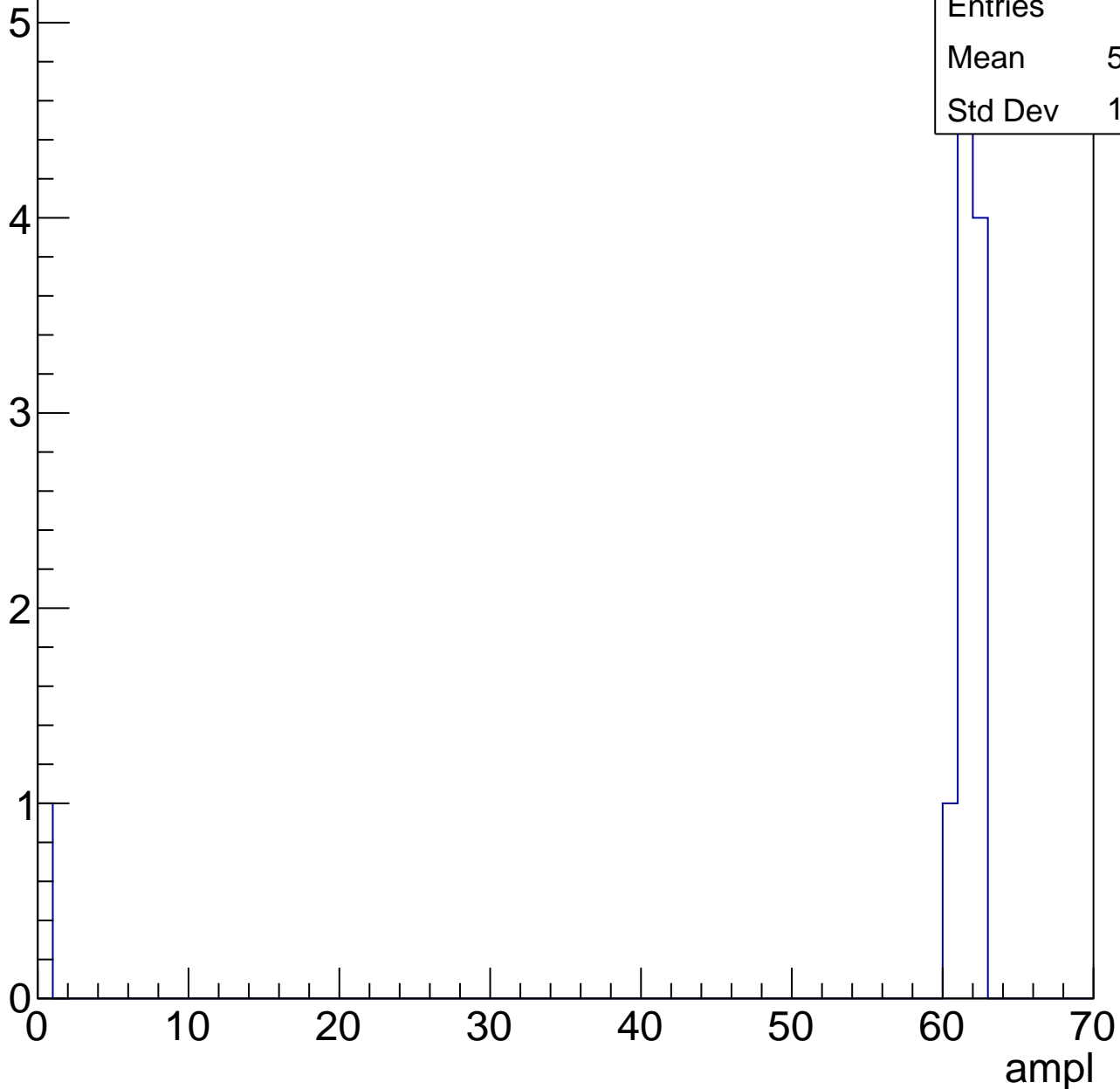


B1L102S, U10-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	55.73
Std Dev	17.63



B1L102S, U10-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch91, adc0

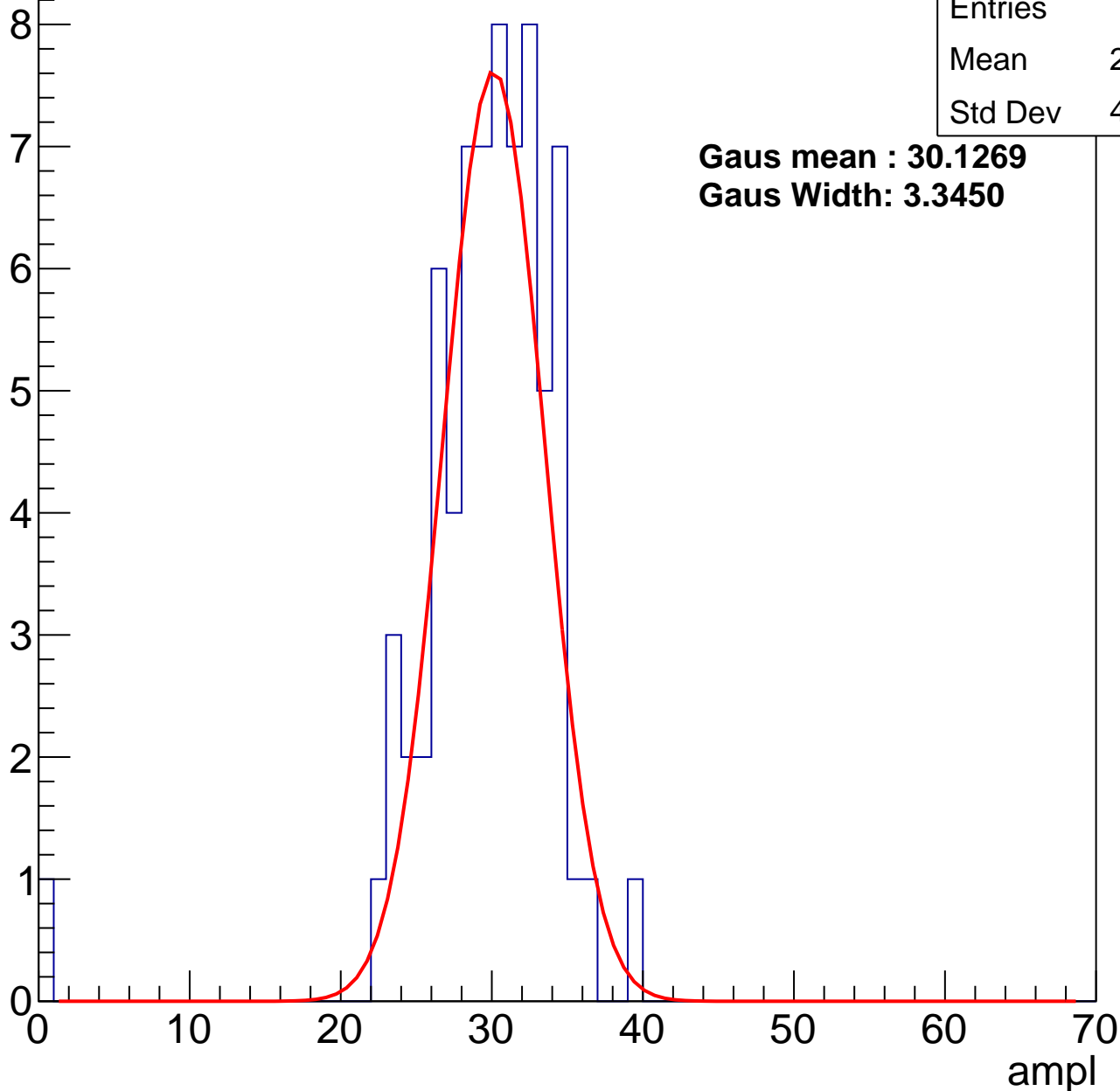
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.27
Std Dev	4.893

Gaus mean : 30.1269

Gaus Width: 3.3450



B1L102S, U10-ch91, adc1

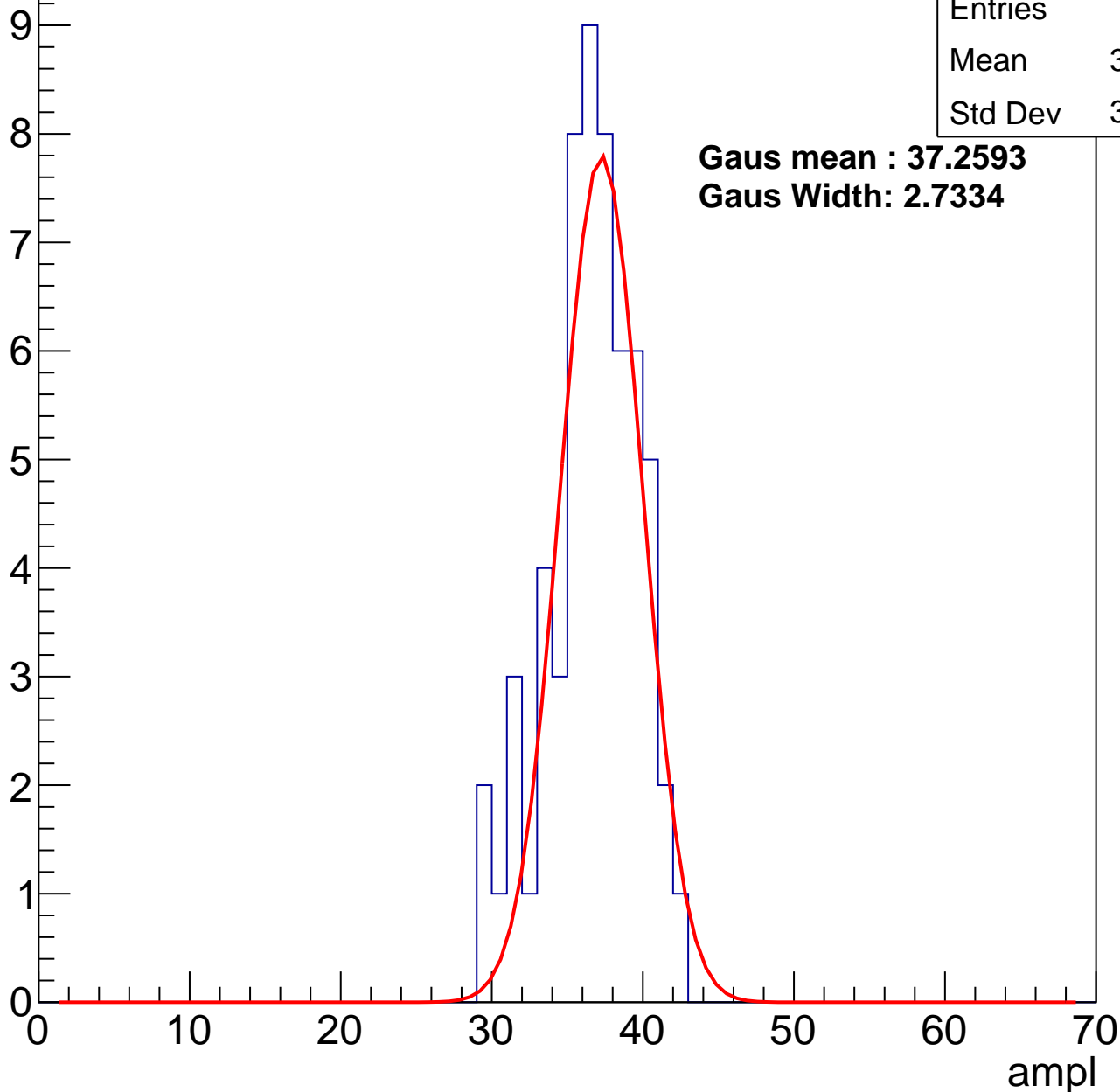
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.15
Std Dev	3.002

Gaus mean : 37.2593

Gaus Width: 2.7334



B1L102S, U10-ch91, adc2

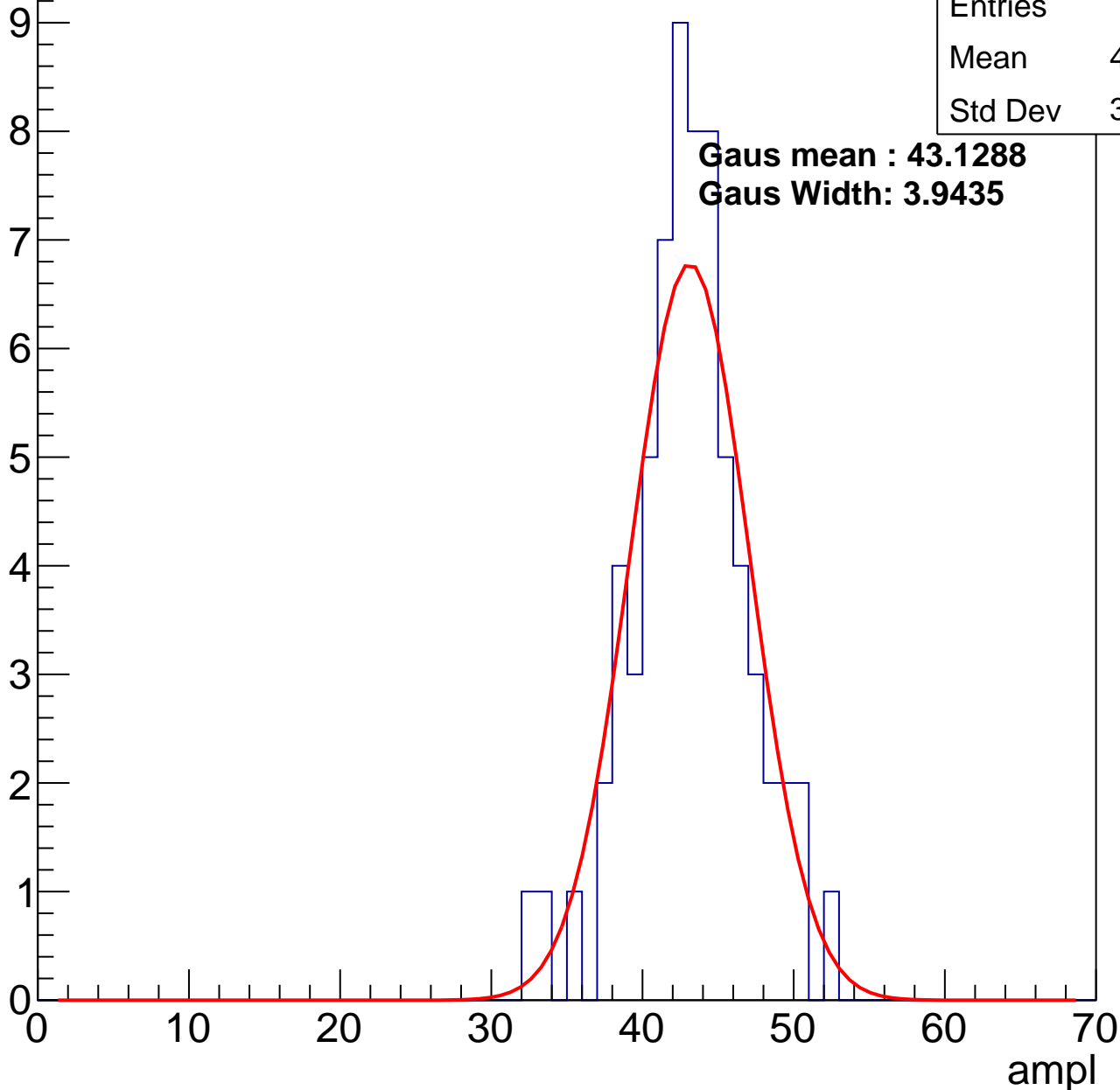
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.65
Std Dev	3.819

Gaus mean : 43.1288

Gaus Width: 3.9435

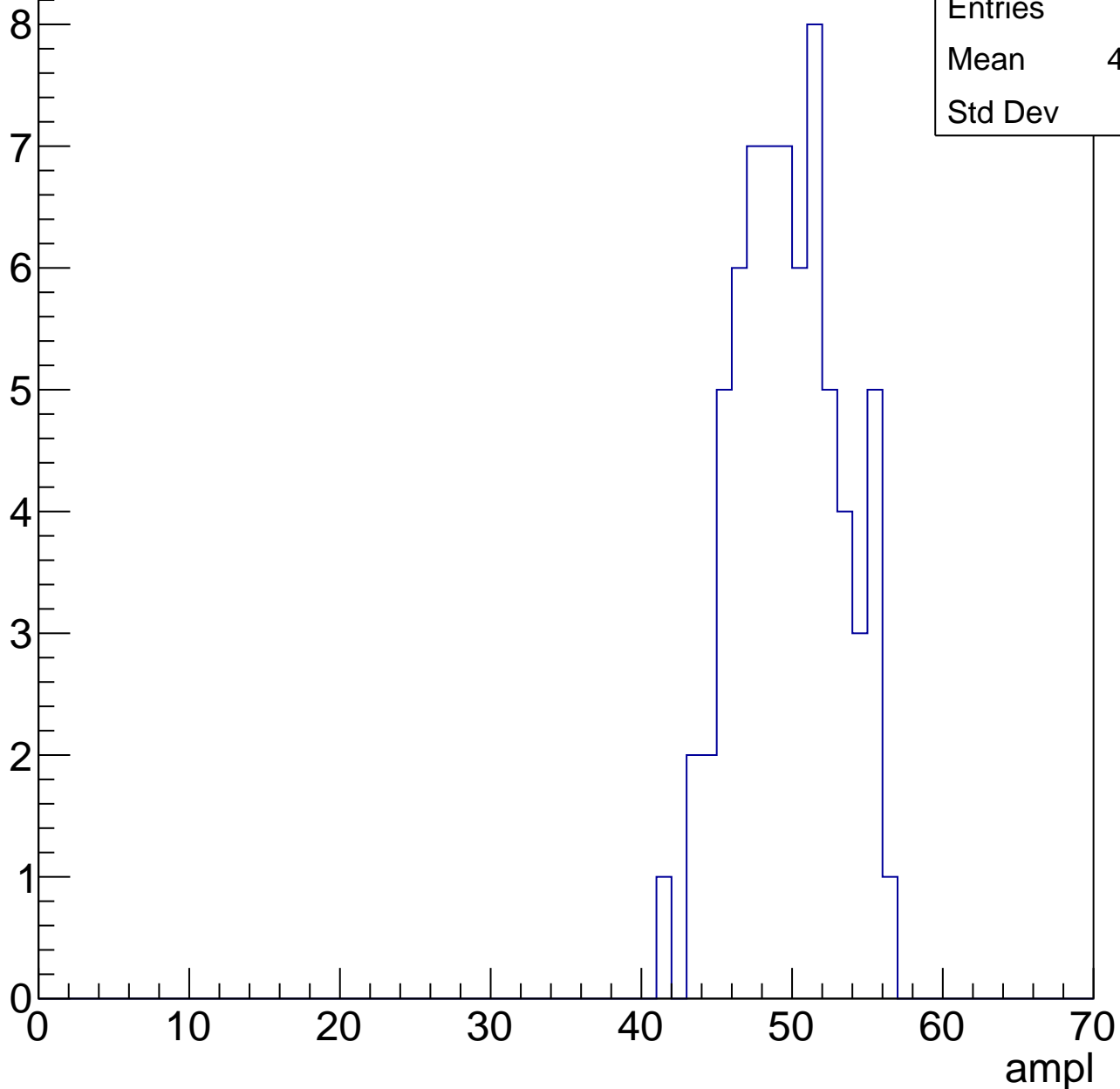


B1L102S, U10-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

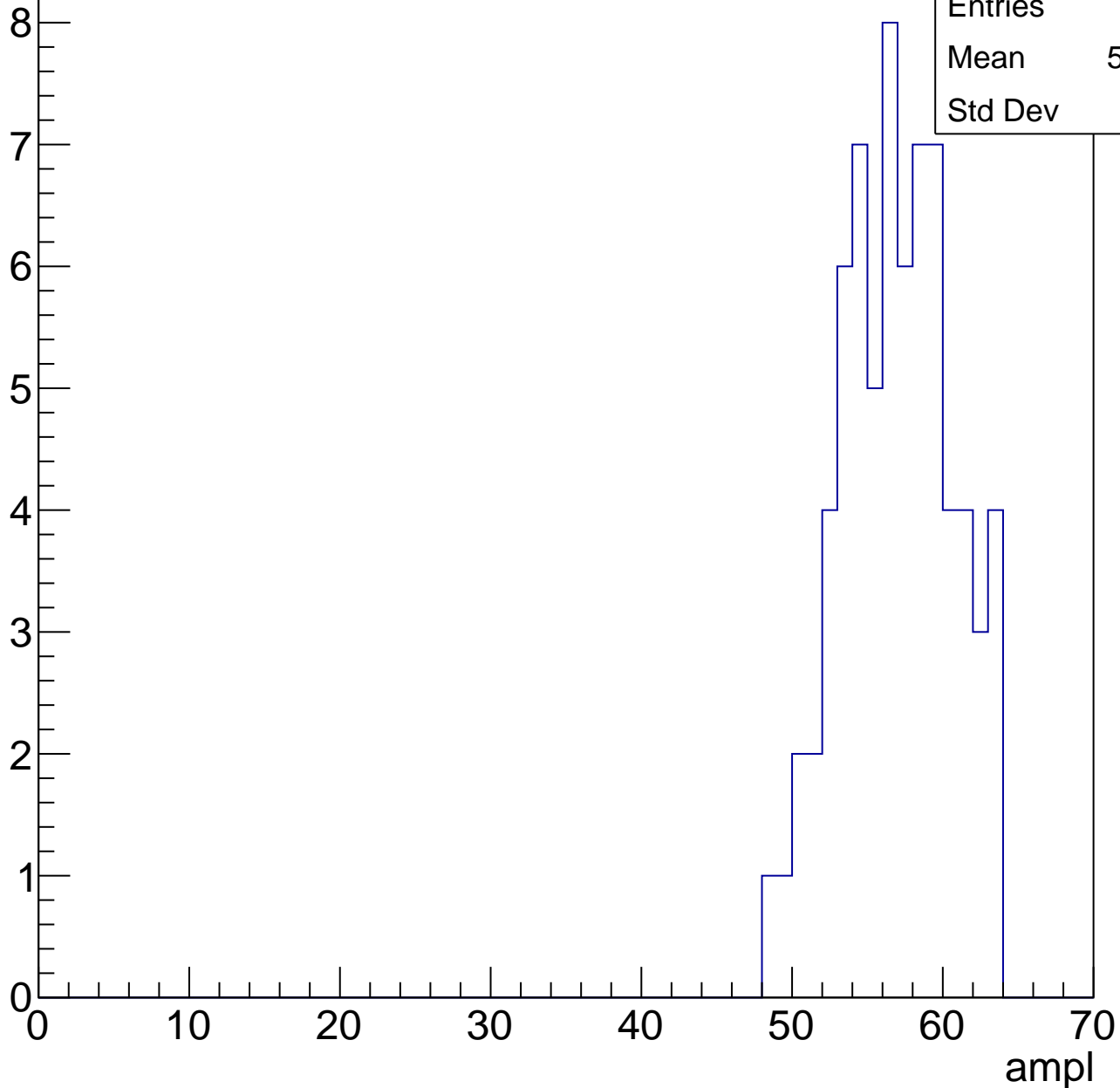
Entries	69
Mean	49.23
Std Dev	3.41



B1L102S, U10-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

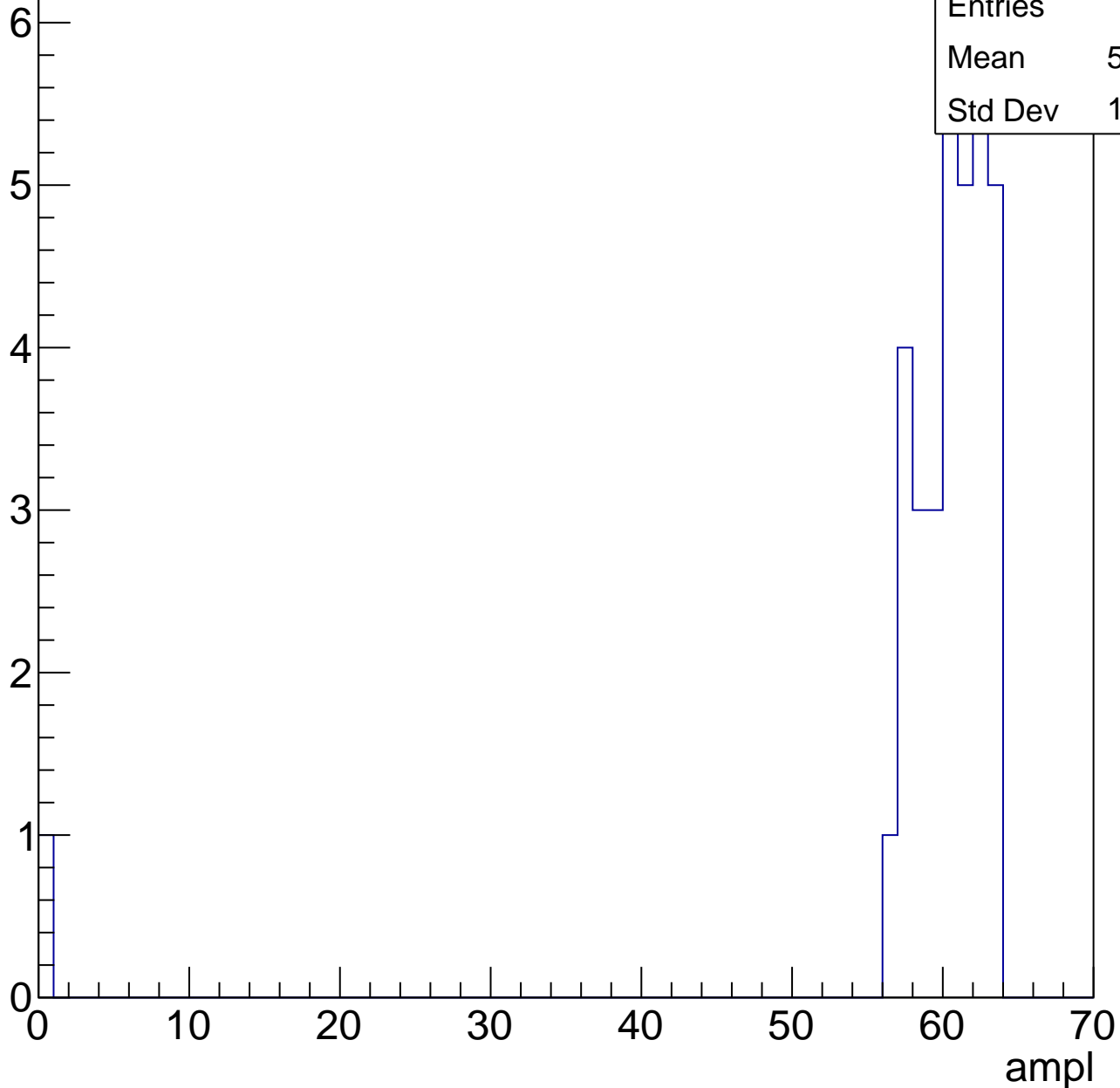


Entries	71
Mean	56.46
Std Dev	3.63

B1L102S, U10-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U10-ch92, adc0

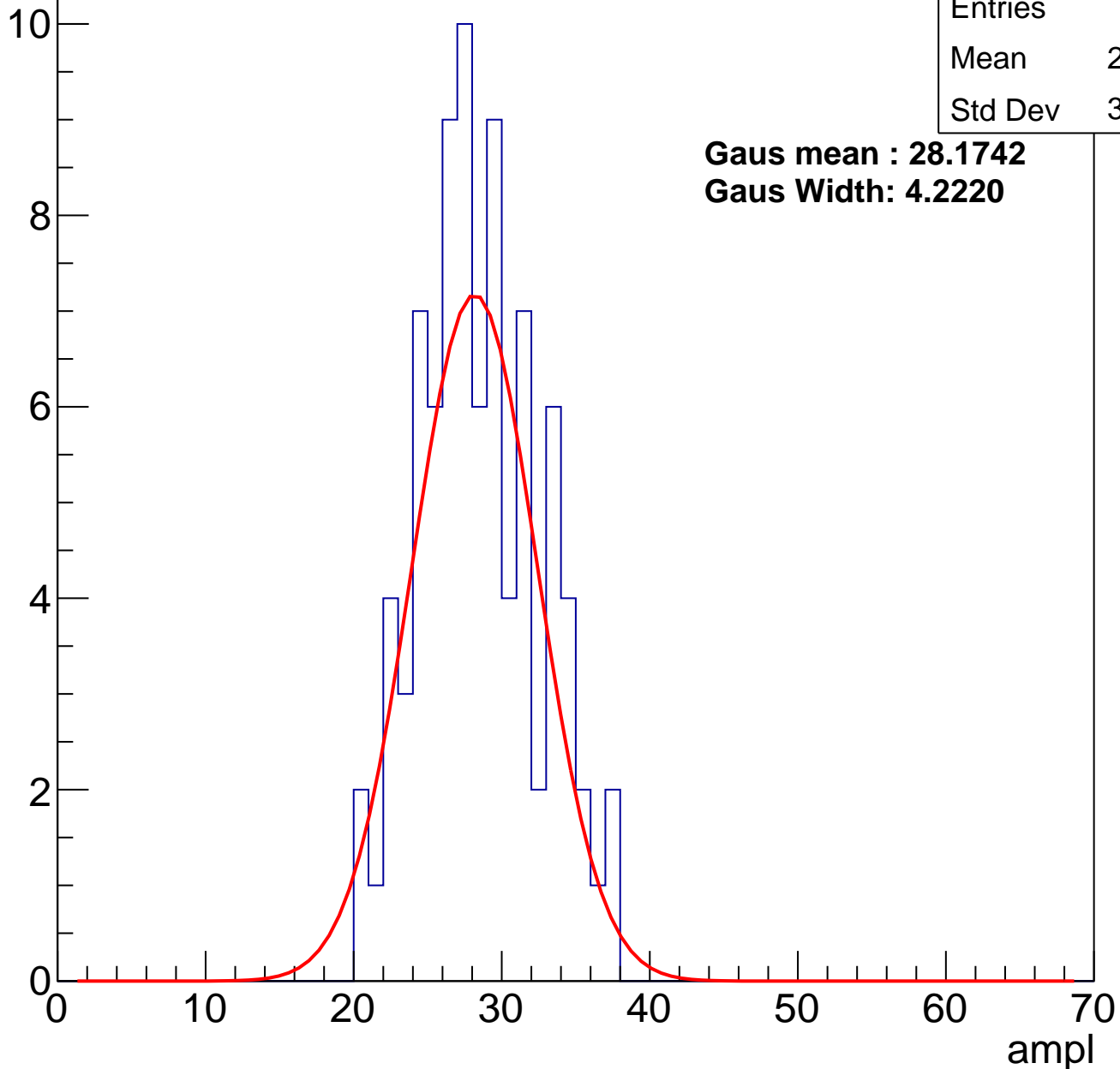
calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	28.05
Std Dev	3.979

Gaus mean : 28.1742

Gaus Width: 4.2220

Entry



B1L102S, U10-ch92, adc1

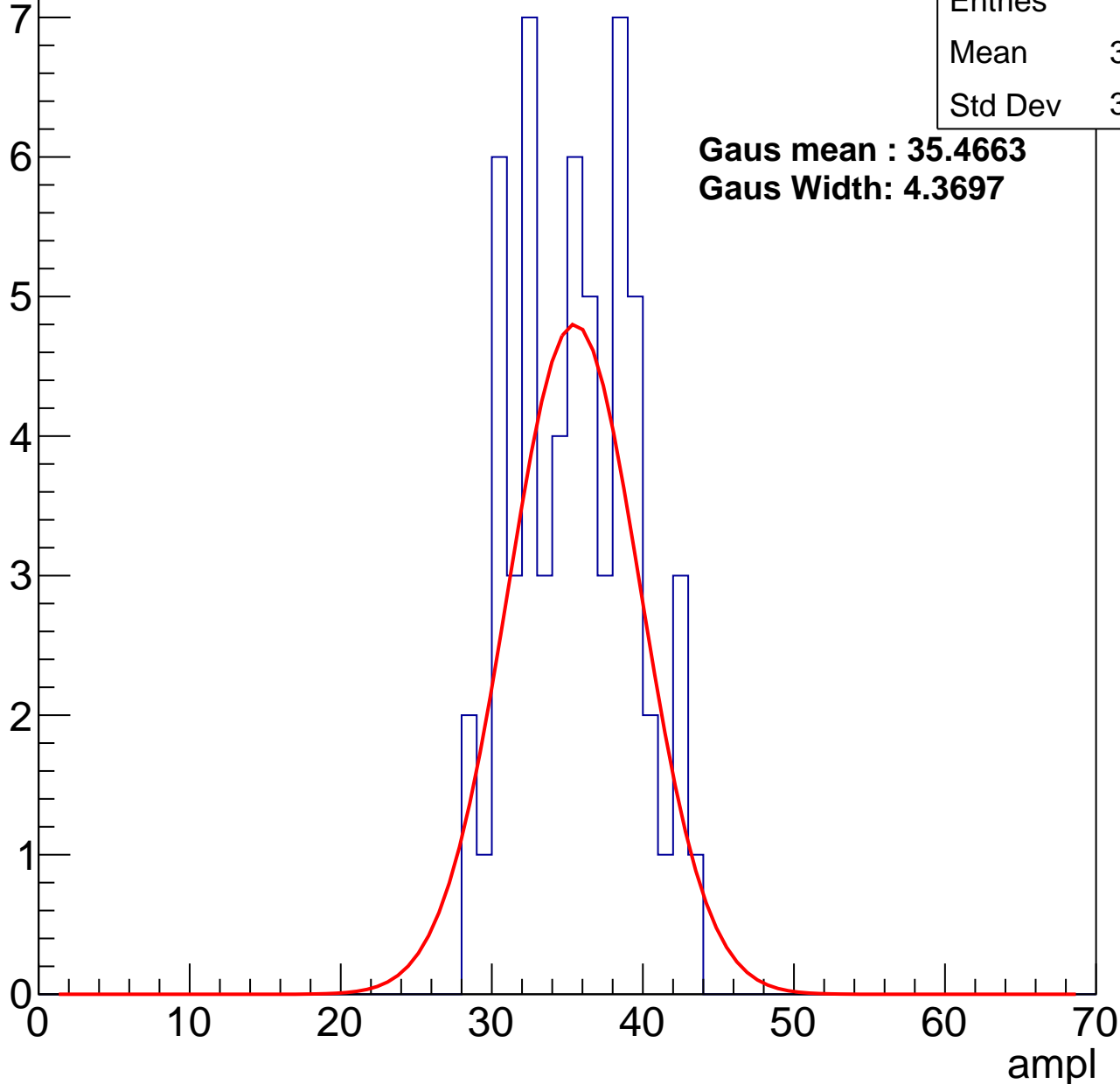
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.07
Std Dev	3.835

Gaus mean : 35.4663

Gaus Width: 4.3697



B1L102S, U10-ch92, adc2

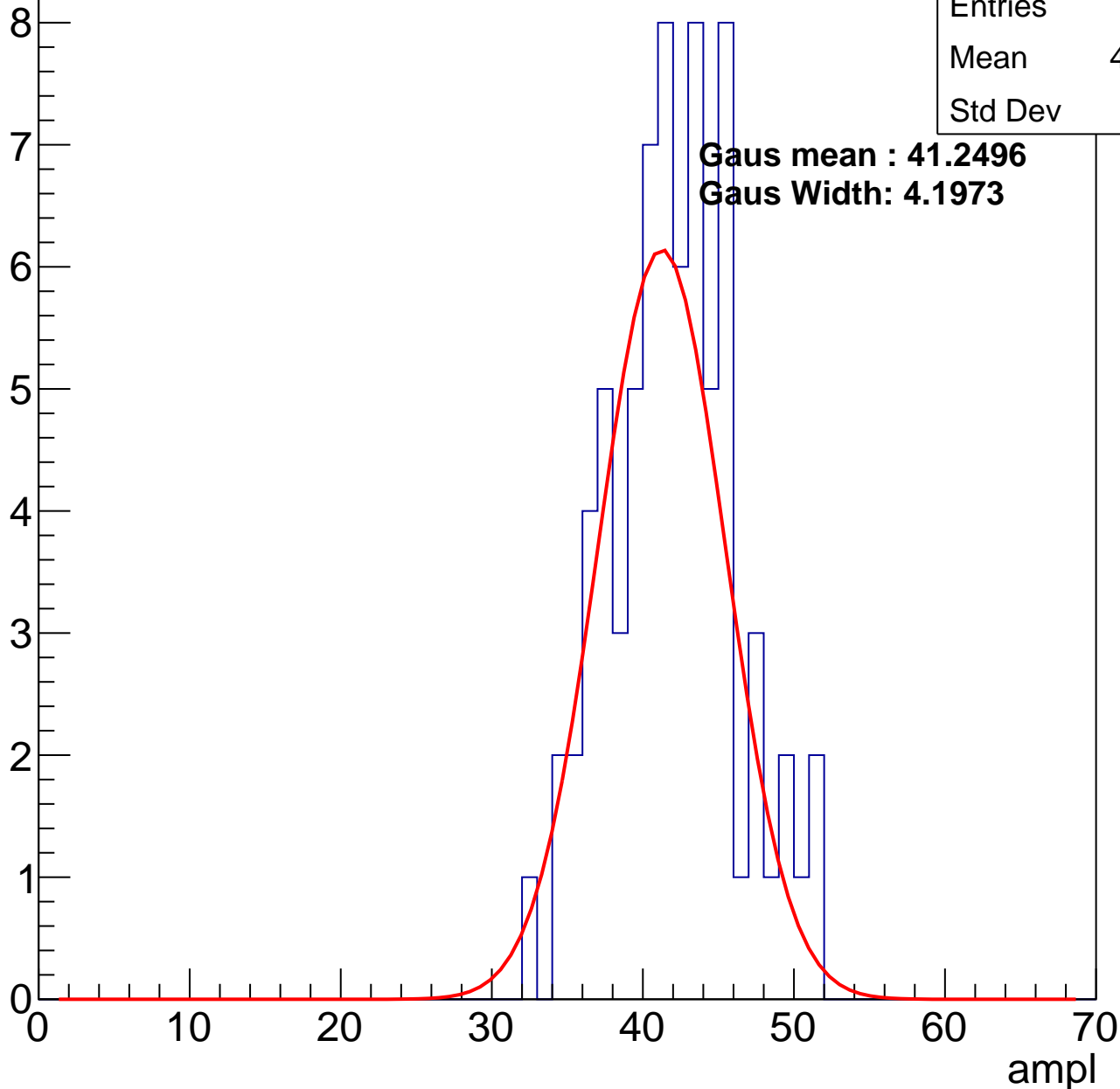
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.58
Std Dev	4.13

Gaus mean : 41.2496

Gaus Width: 4.1973

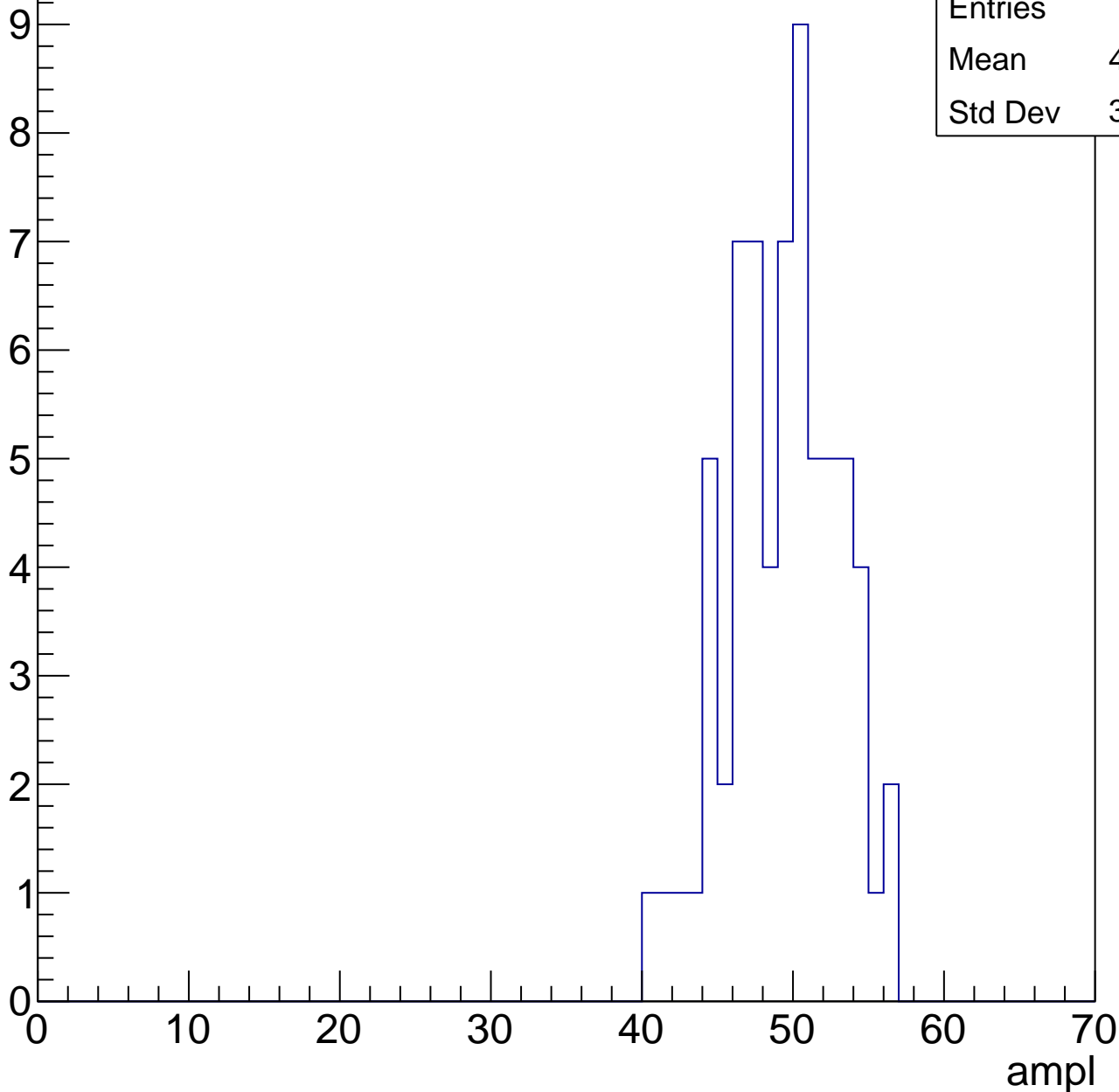


B1L102S, U10-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.88
Std Dev	3.602

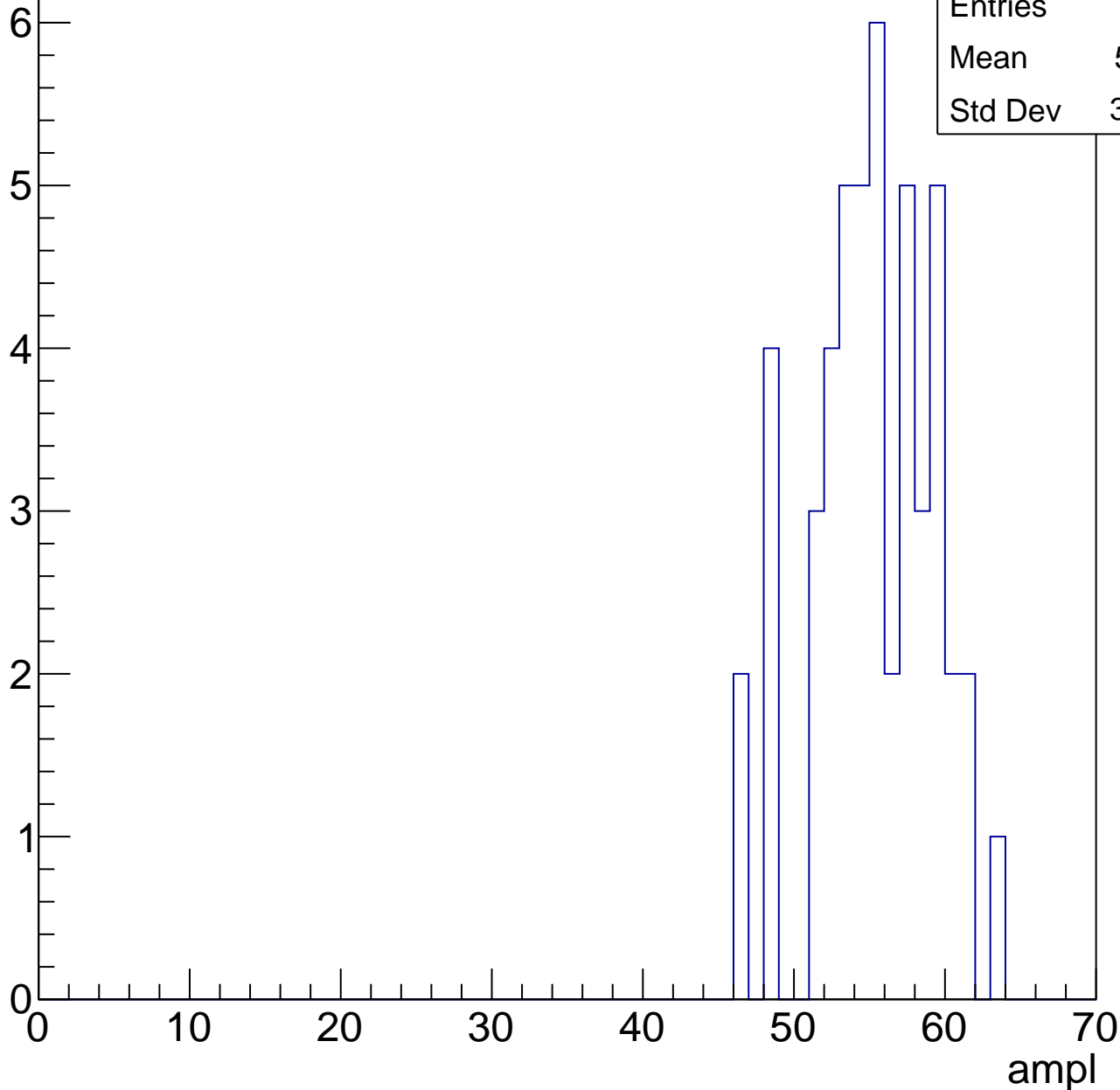


B1L102S, U10-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.71
Std Dev	3.985

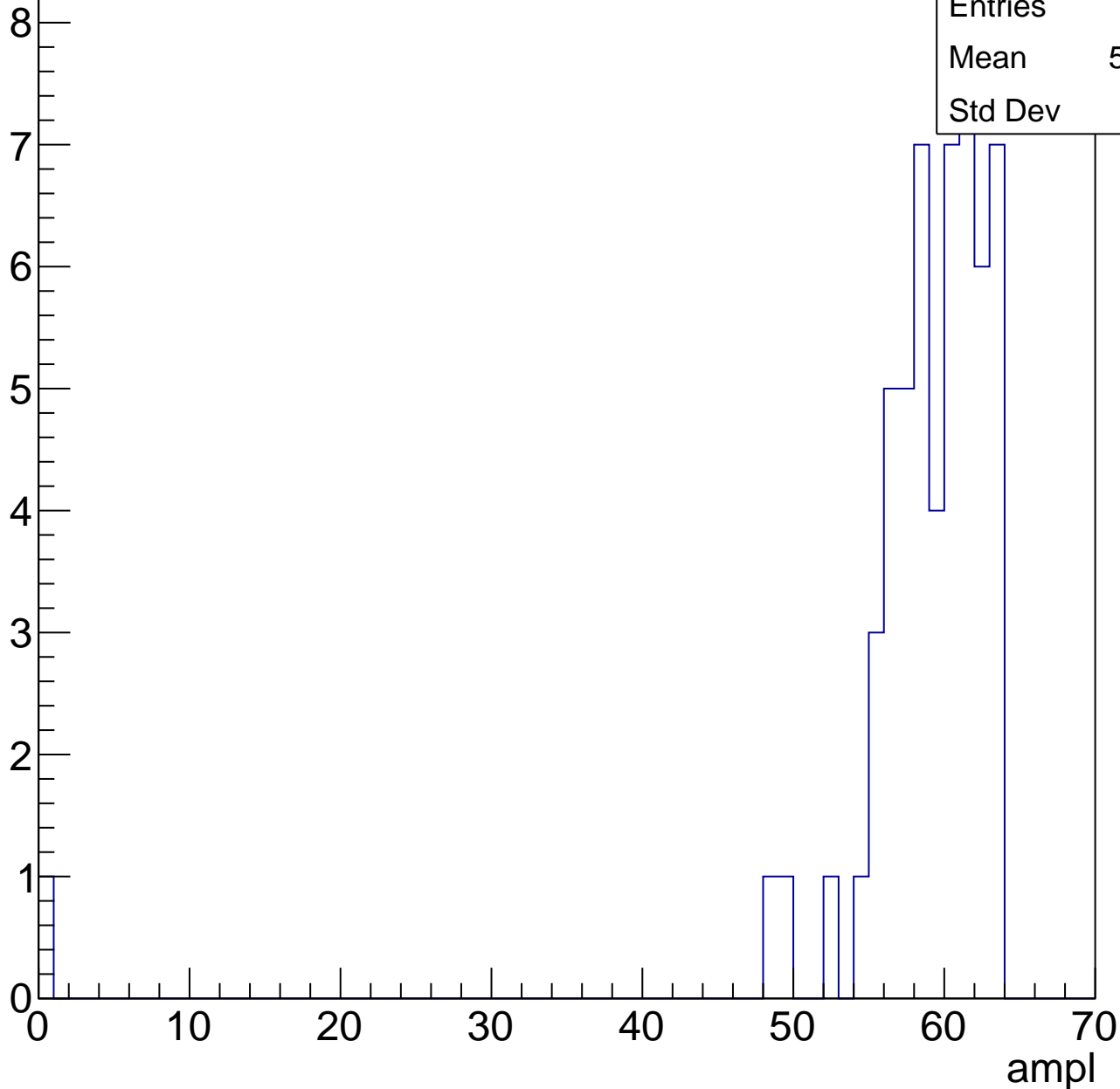


B1L102S, U10-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

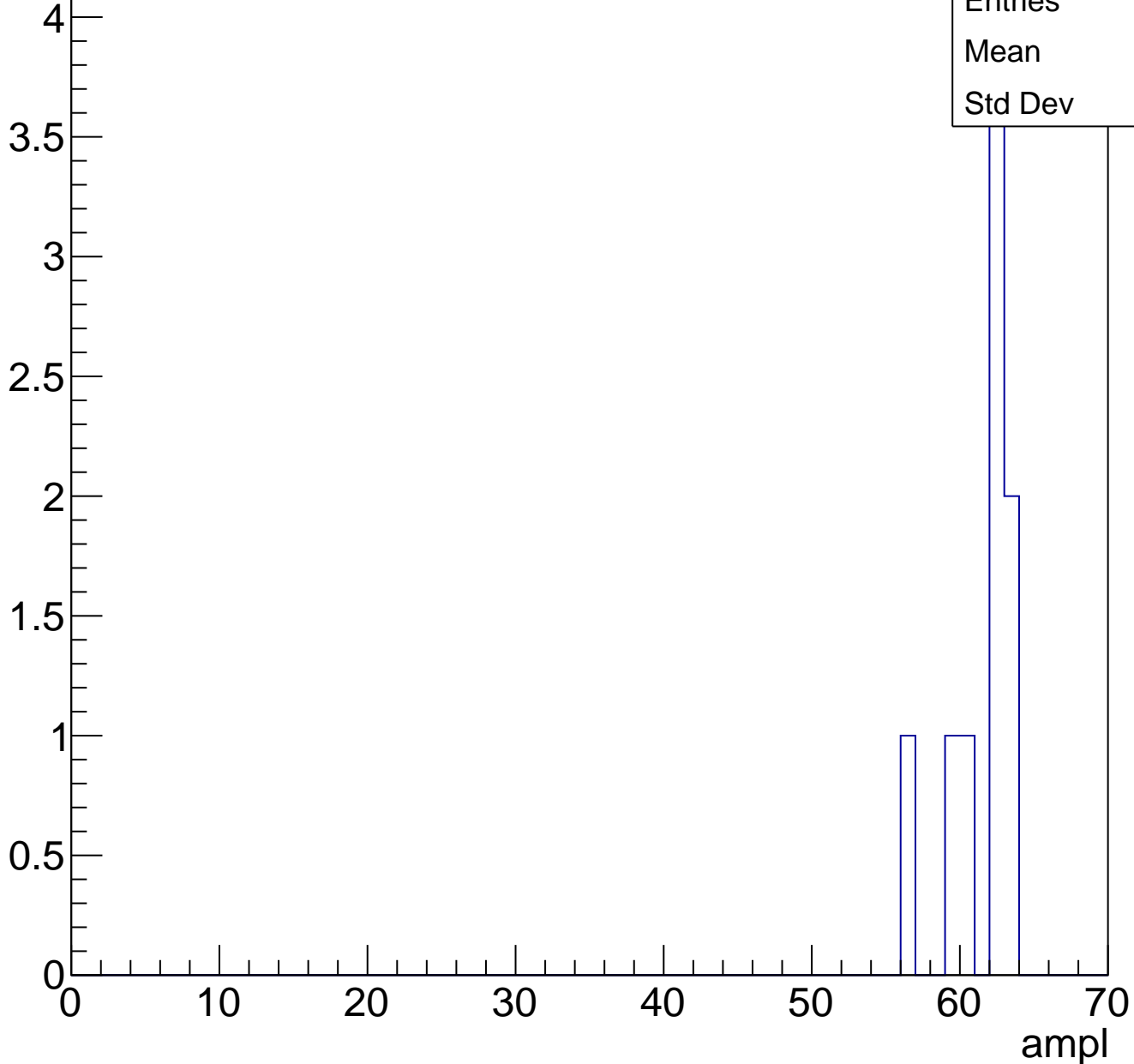
Entries	57
Mean	57.82
Std Dev	8.4



B1L102S, U10-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch93, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	27.52
Std Dev	5.652

Gaus mean : 29.1245

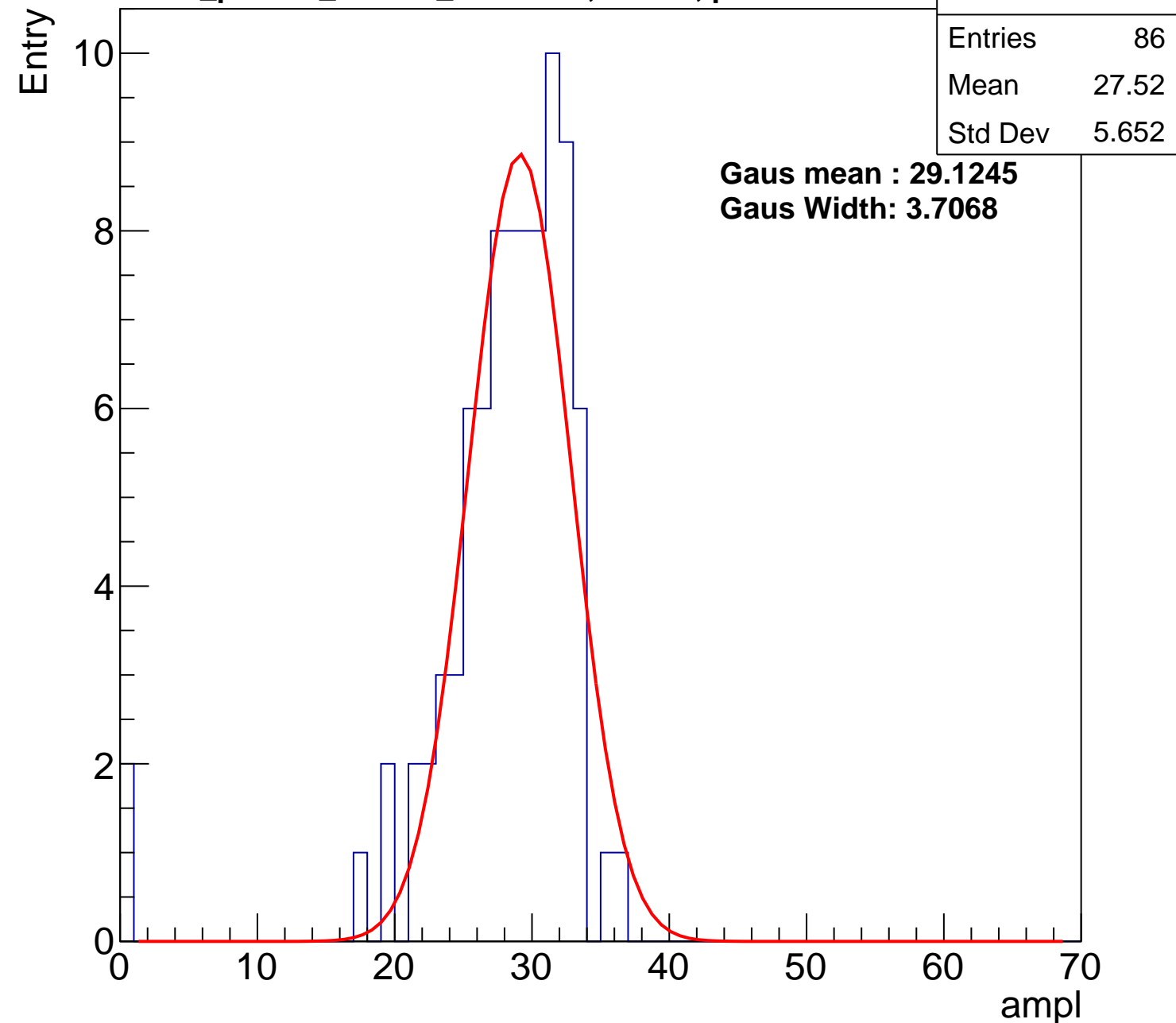
Gaus Width: 3.7068

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



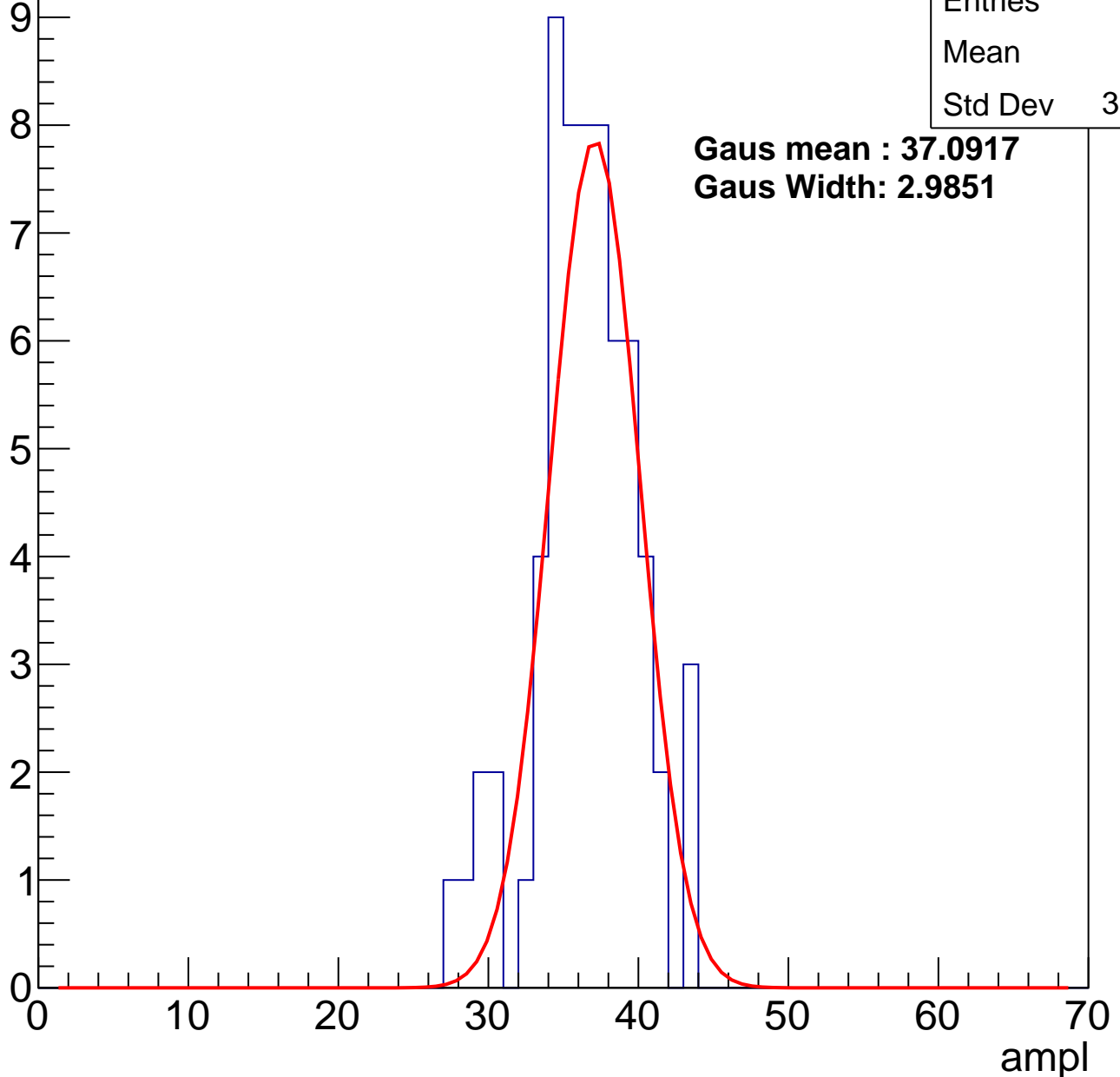
B1L102S, U10-ch93, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36
Std Dev	3.415

Gaus mean : 37.0917
Gaus Width: 2.9851



B1L102S, U10-ch93, adc2

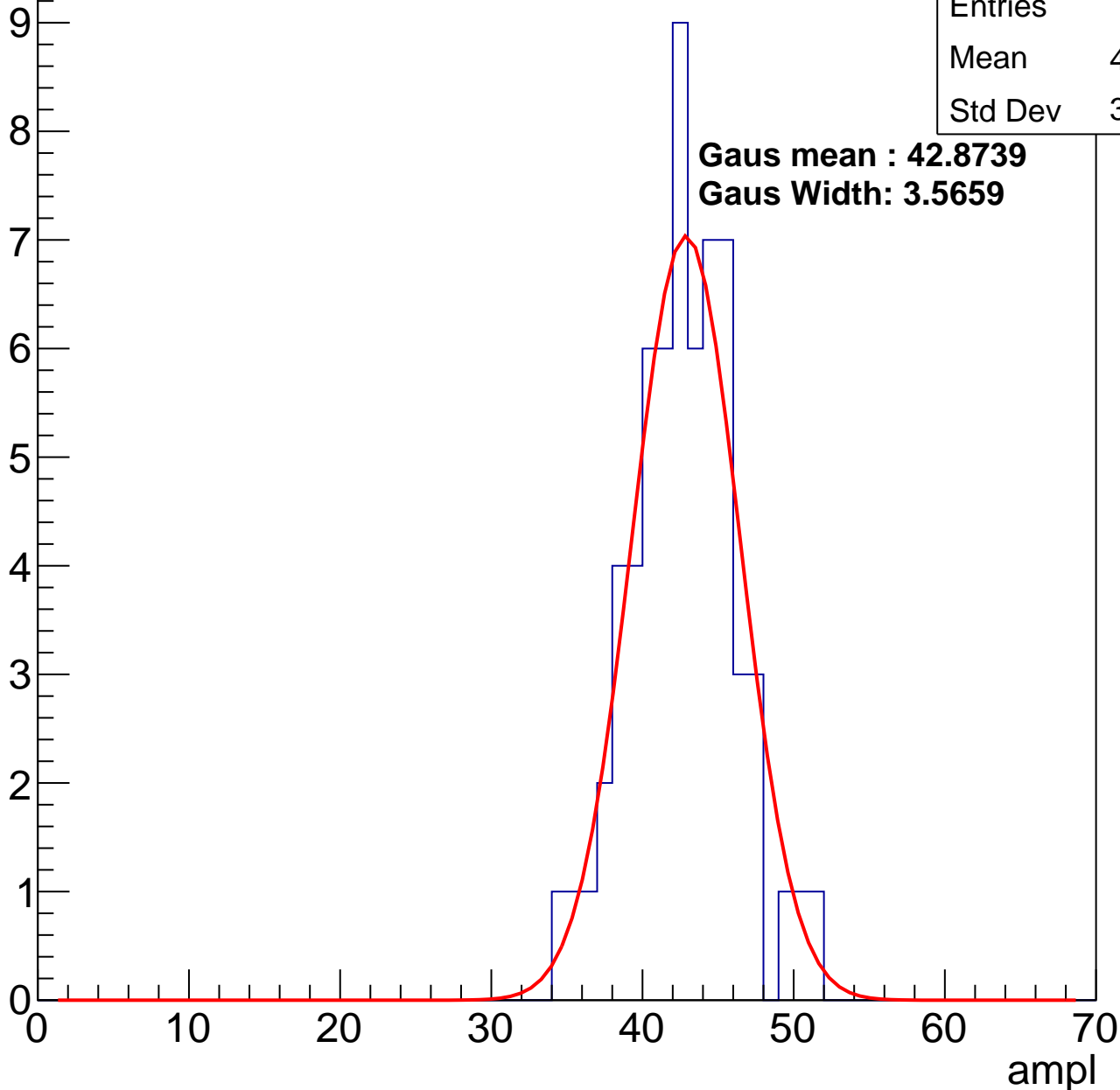
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.24
Std Dev	3.426

Gaus mean : 42.8739

Gaus Width: 3.5659



B1L102S, U10-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	49.32
Std Dev	3.76

Entry

10

8

6

4

2

0

0

10

20

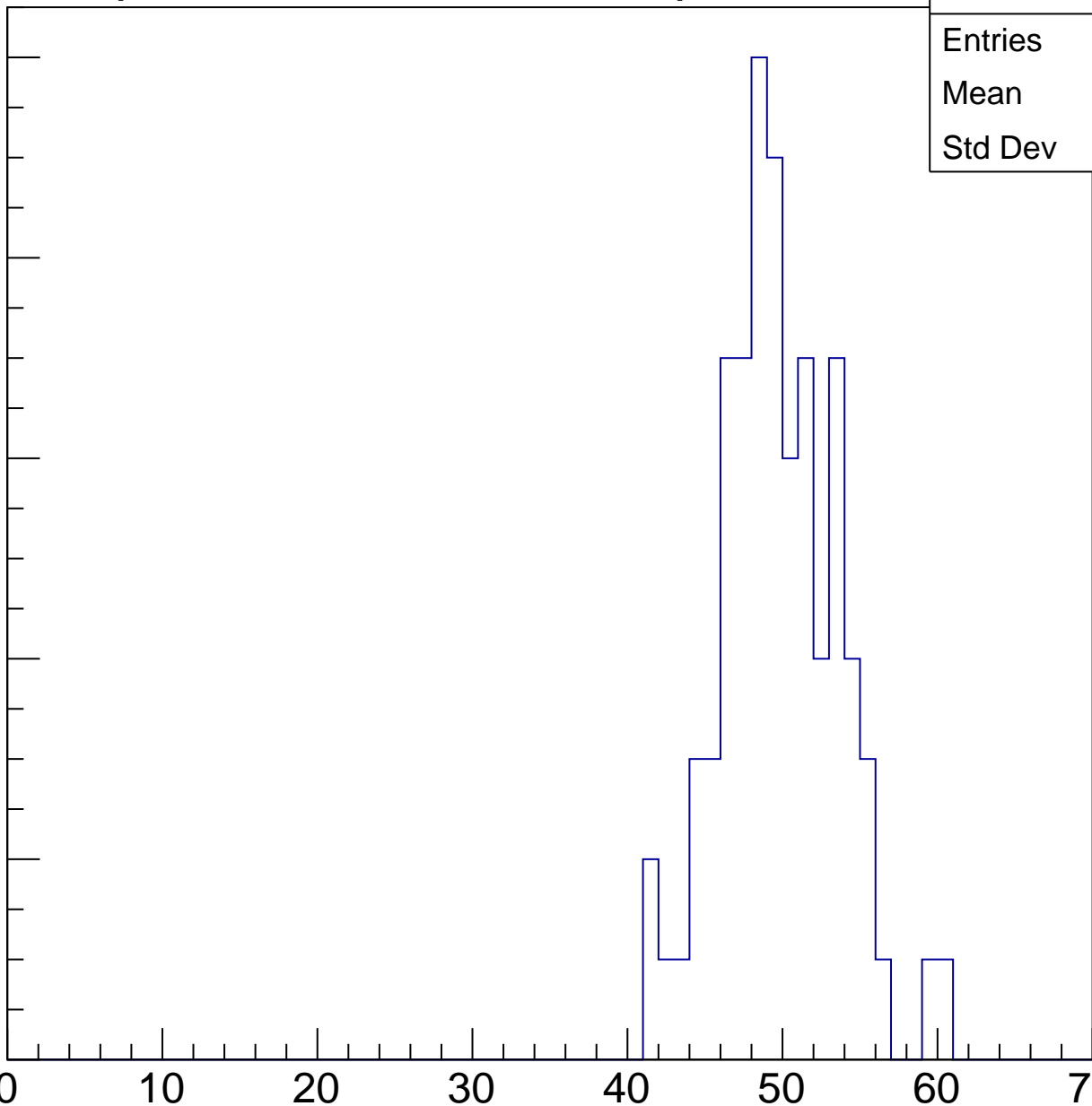
30

40

50

60

ampl

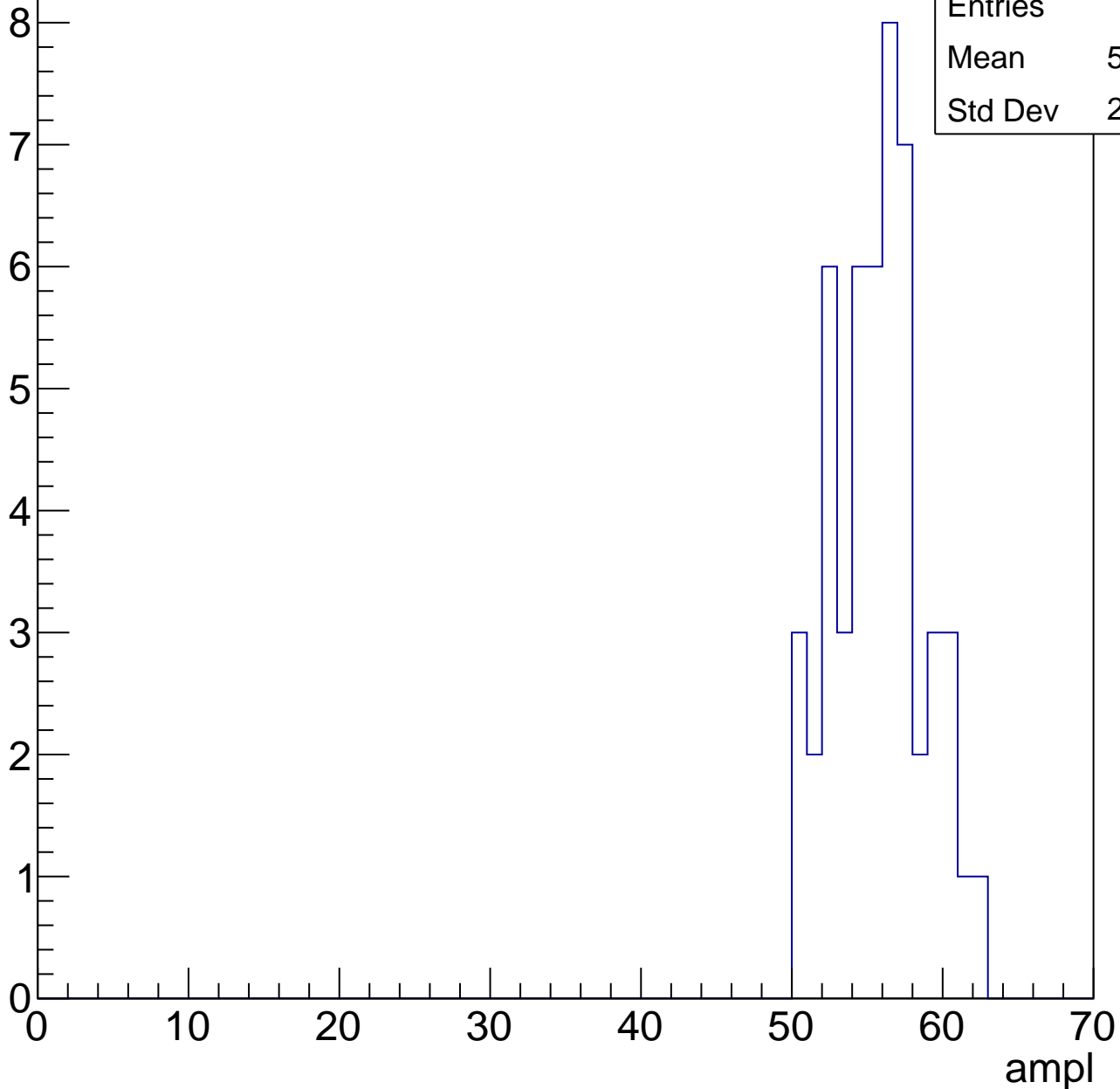


B1L102S, U10-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

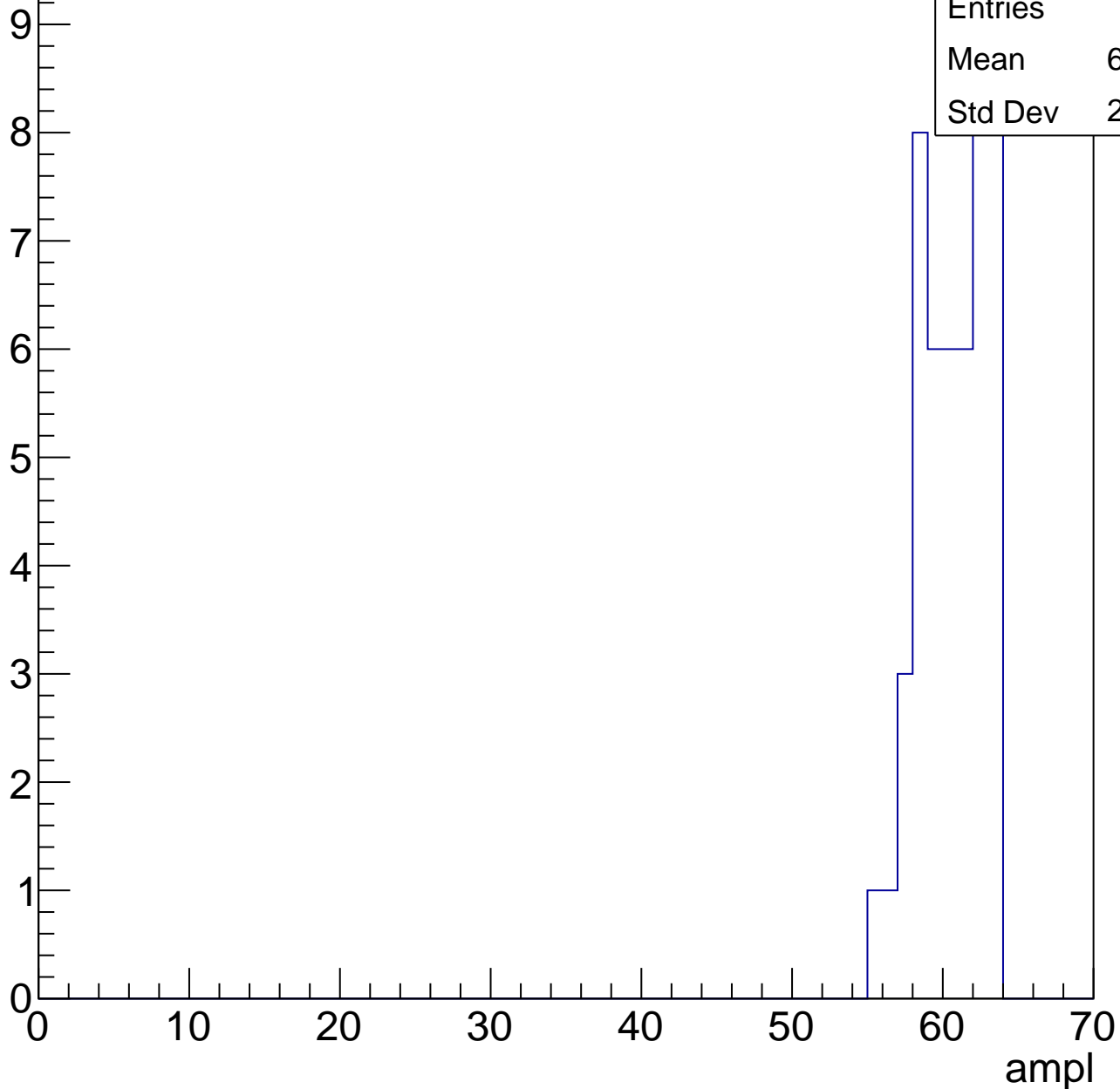
Entries	51
Mean	55.29
Std Dev	2.926



B1L102S, U10-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

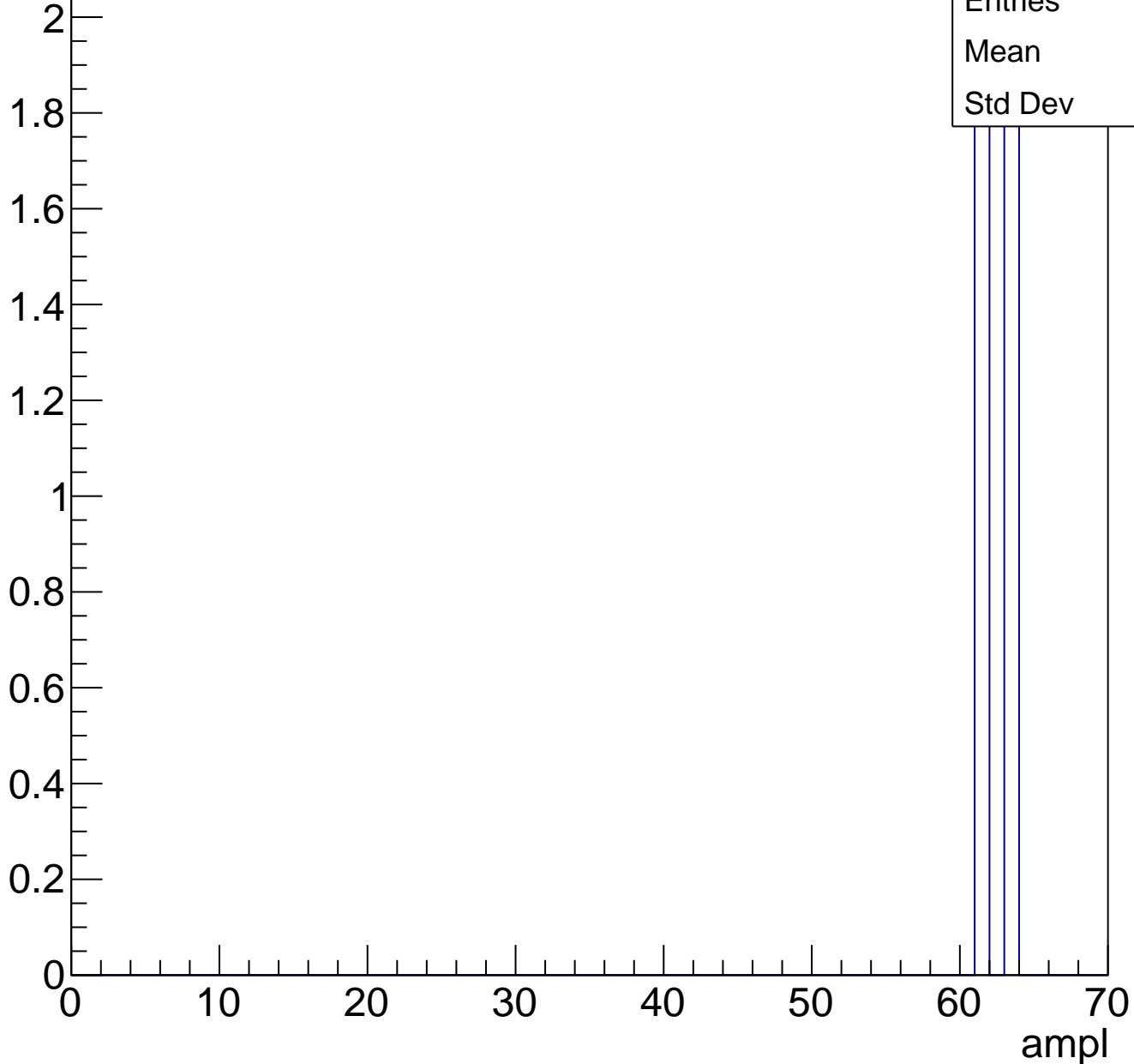


Entries	48
Mean	60.17
Std Dev	2.134

B1L102S, U10-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch94, adc0

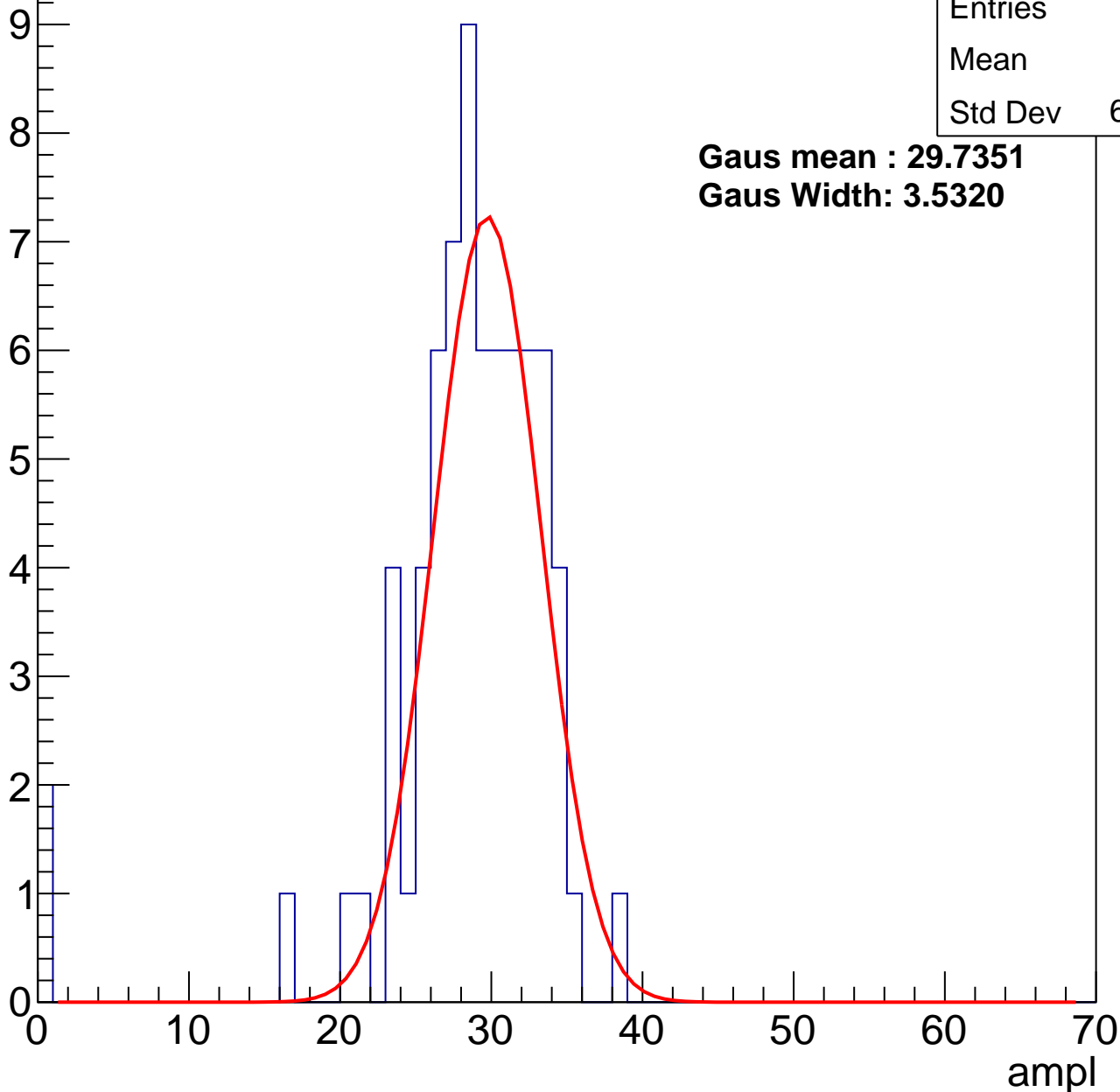
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.9
Std Dev	6.044

Gaus mean : 29.7351

Gaus Width: 3.5320



B1L102S, U10-ch94, adc1

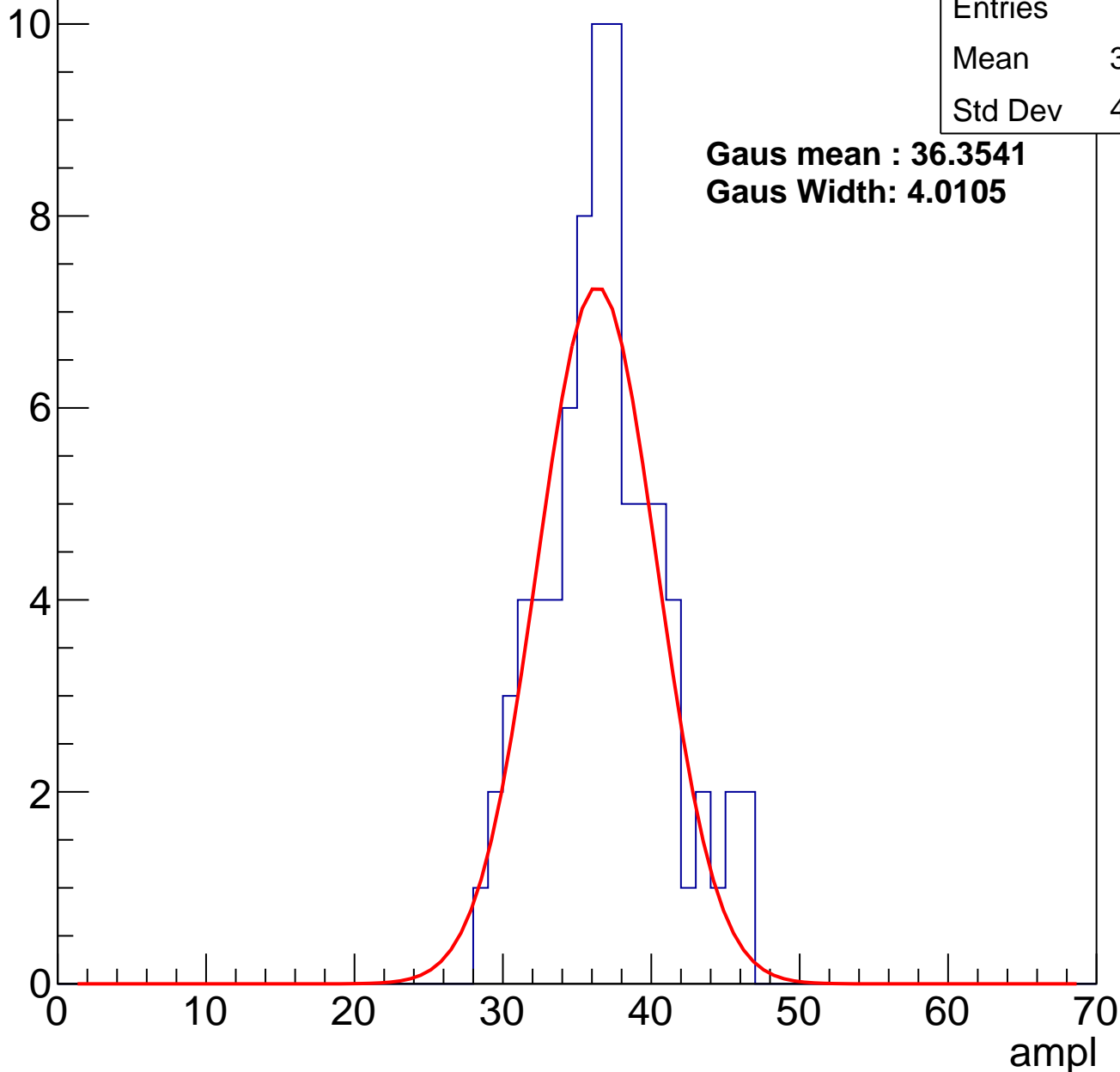
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	36.42
Std Dev	4.062

Gaus mean : 36.3541

Gaus Width: 4.0105

Entry



B1L102S, U10-ch94, adc2

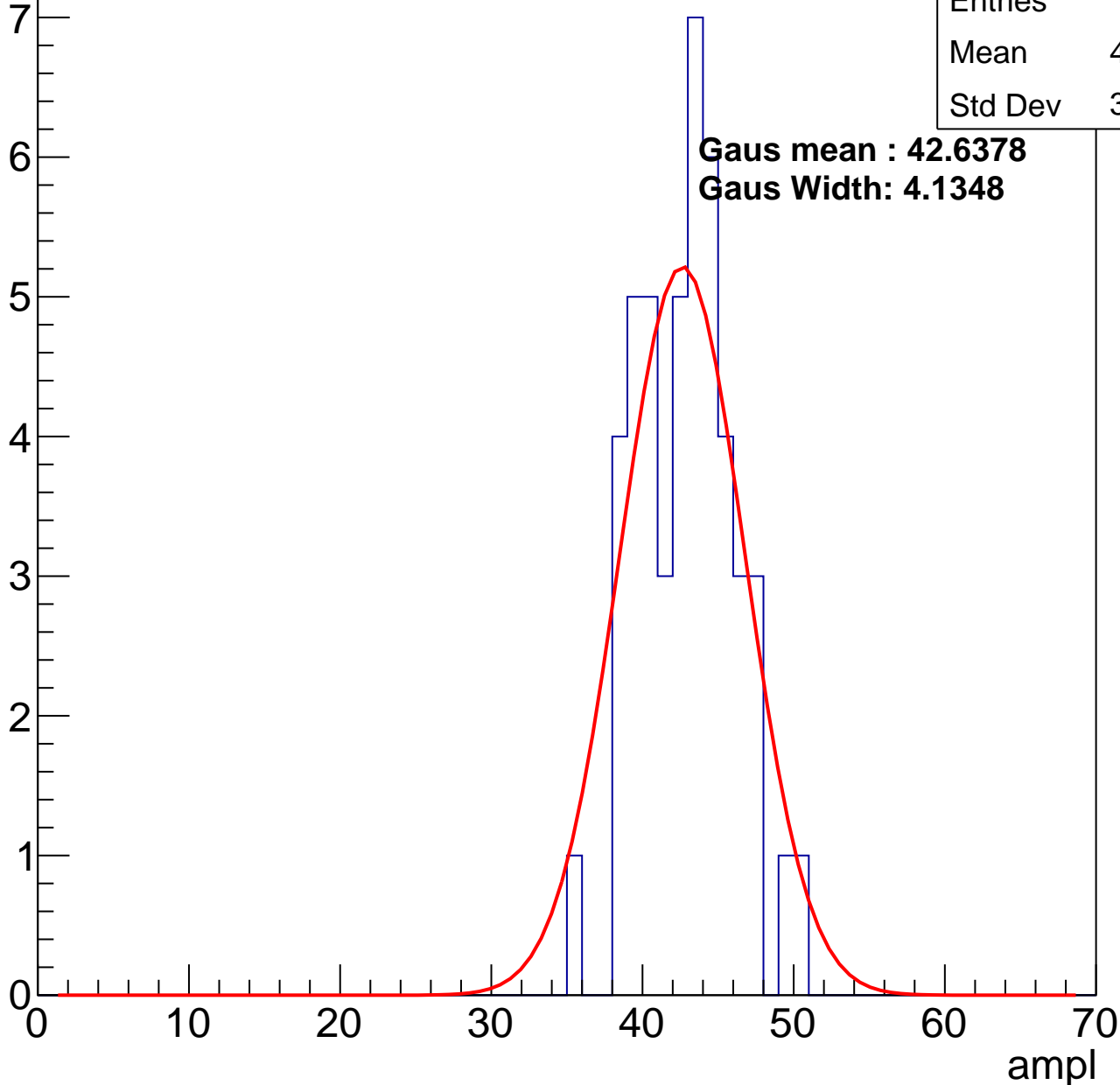
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.46
Std Dev	3.142

Gaus mean : 42.6378

Gaus Width: 4.1348

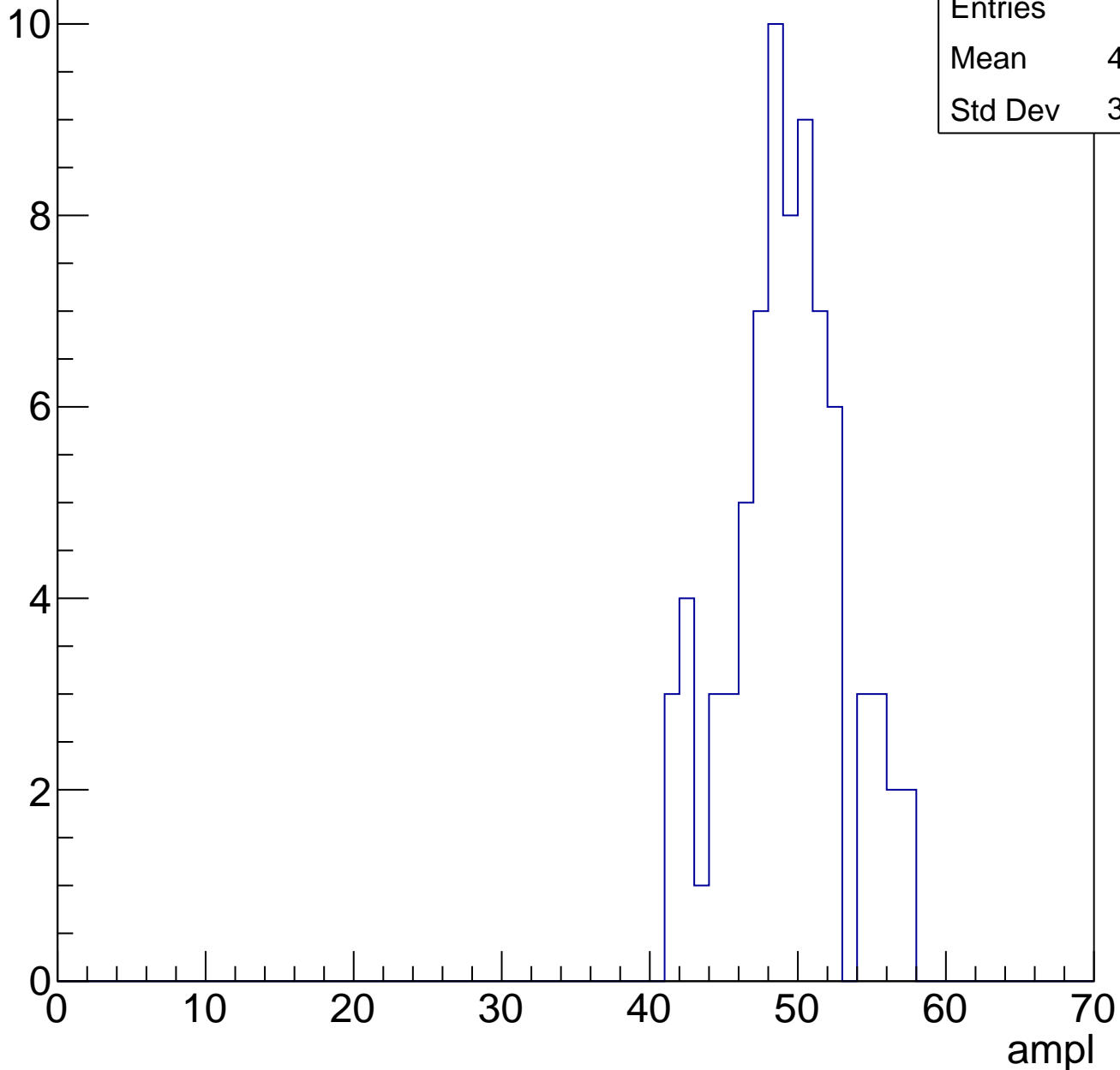


B1L102S, U10-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	48.74
Std Dev	3.847

Entry

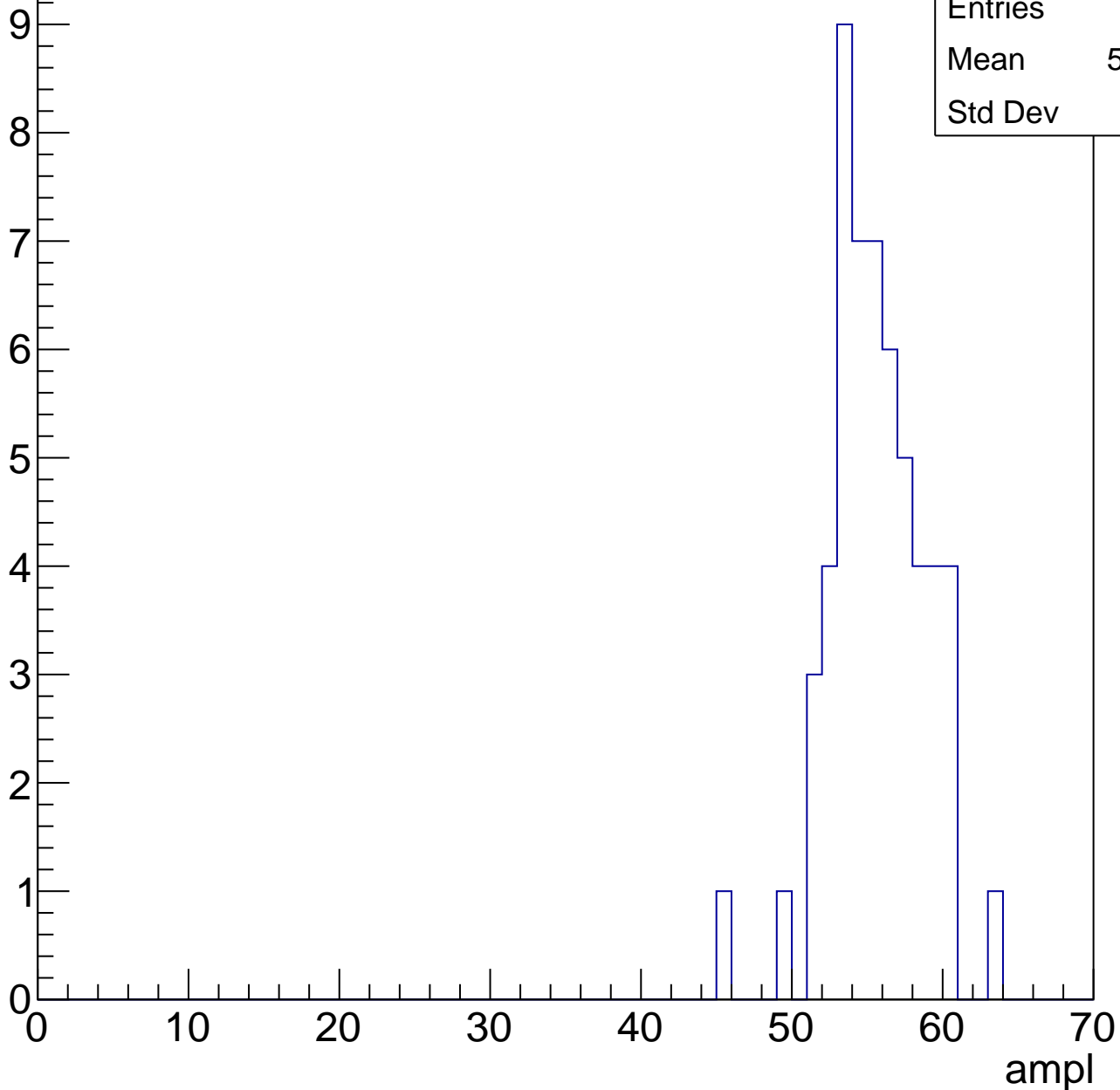


B1L102S, U10-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.12
Std Dev	3.14

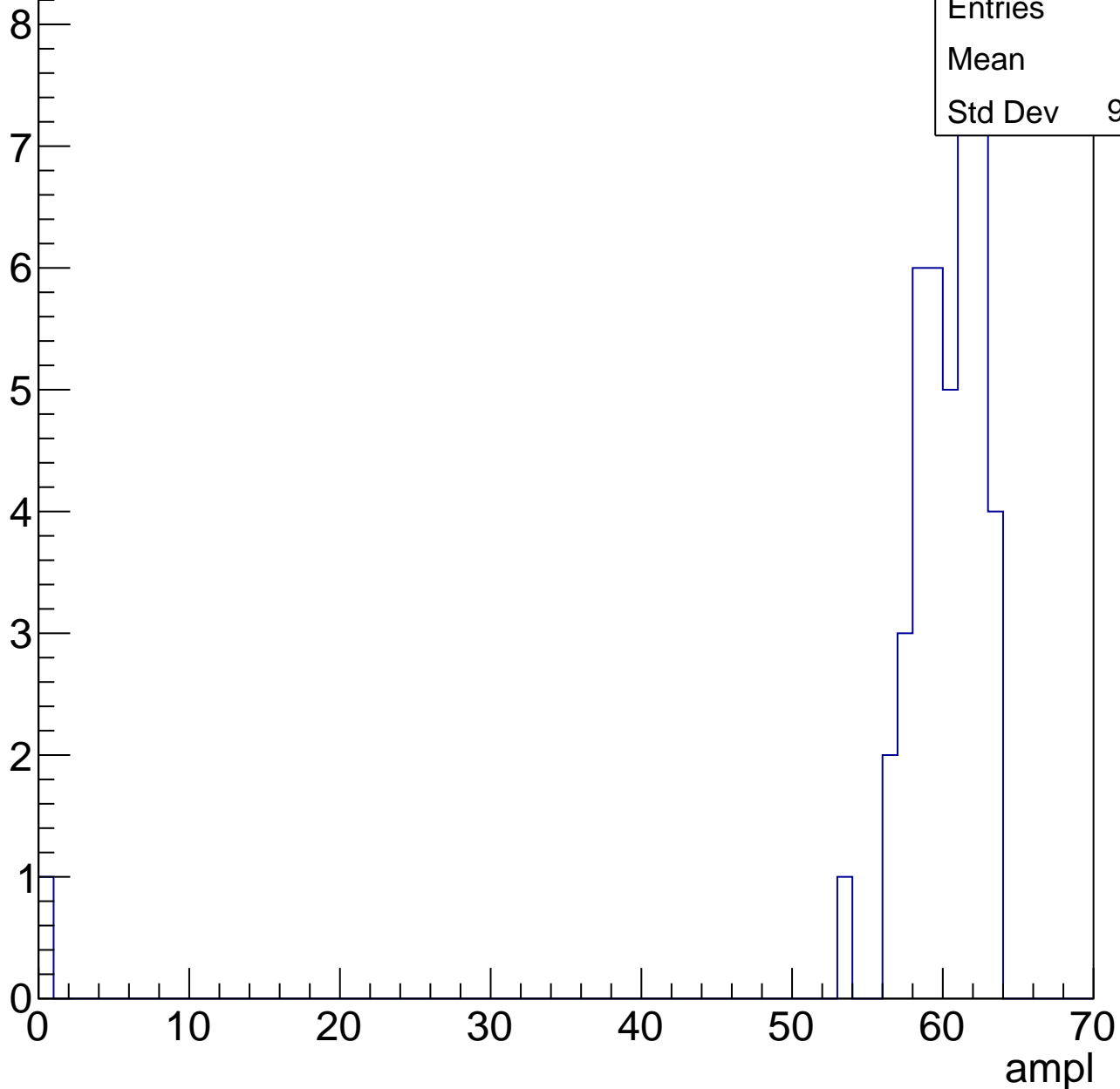


B1L102S, U10-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.5
Std Dev	9.189

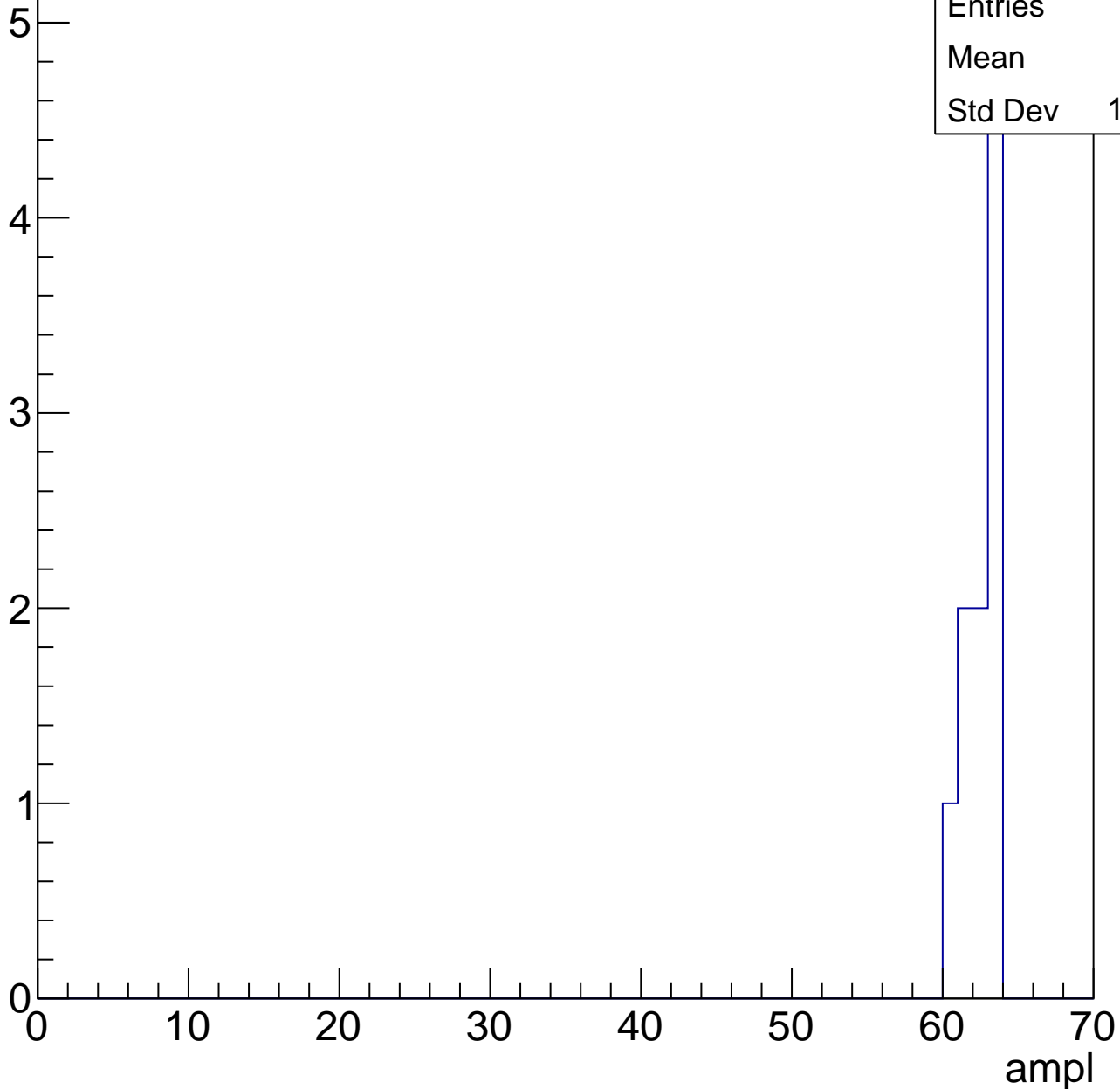


B1L102S, U10-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.1
Std Dev	1.044



B1L102S, U10-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch95, adc0

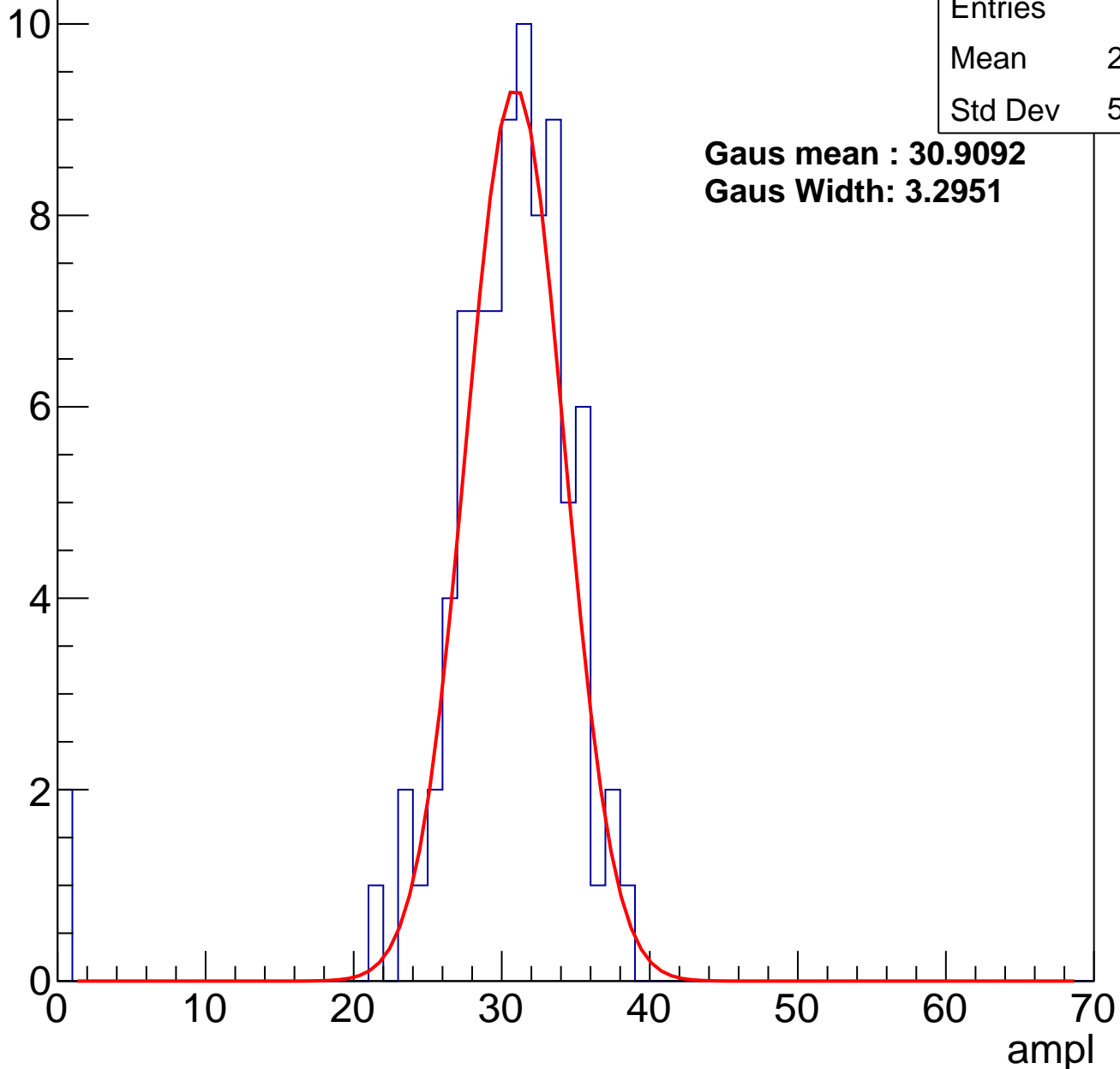
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	29.69
Std Dev	5.742

Gaus mean : 30.9092

Gaus Width: 3.2951

Entry



B1L102S, U10-ch95, adc1

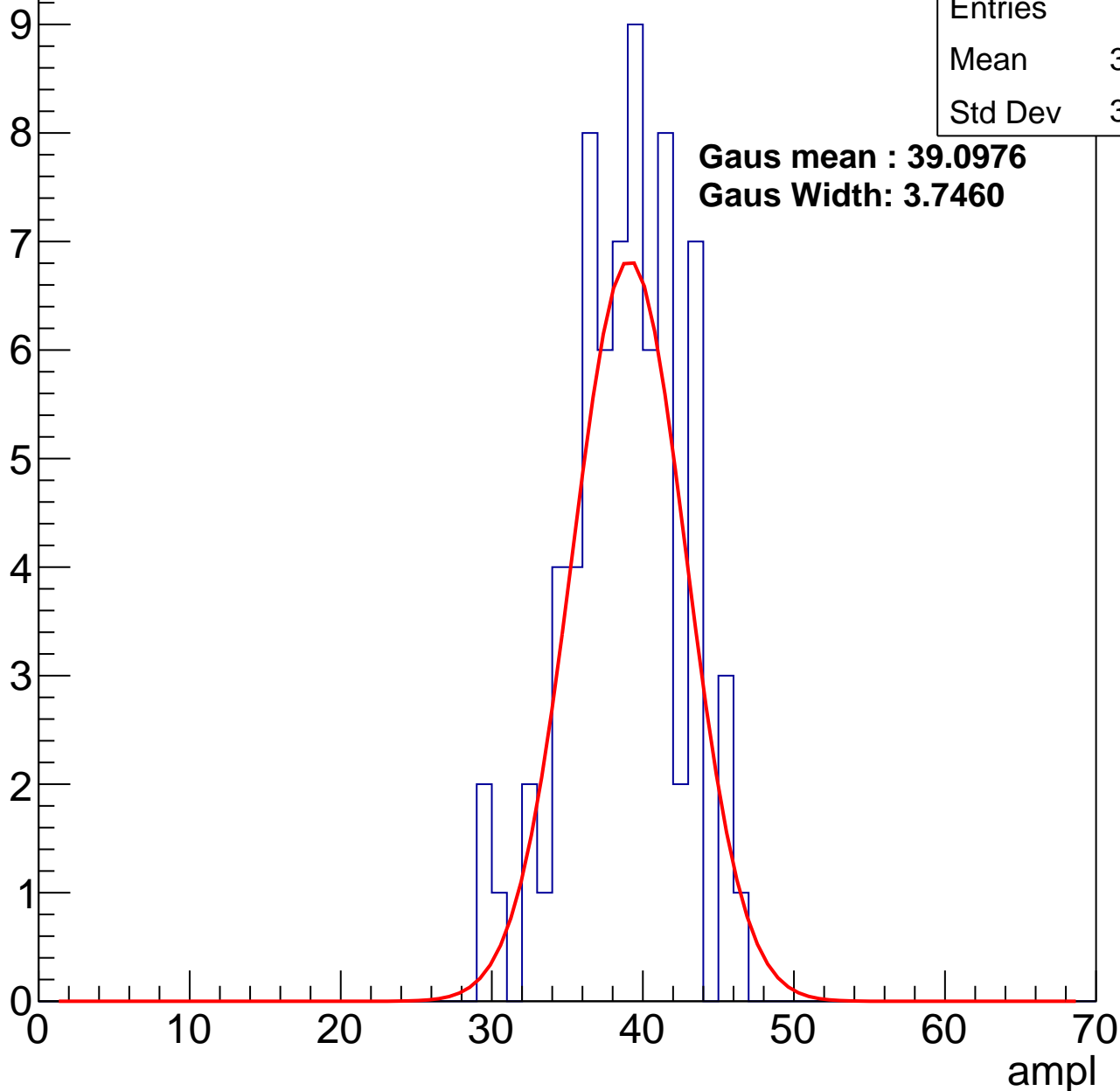
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	38.34
Std Dev	3.707

Gaus mean : 39.0976

Gaus Width: 3.7460



B1L102S, U10-ch95, adc2

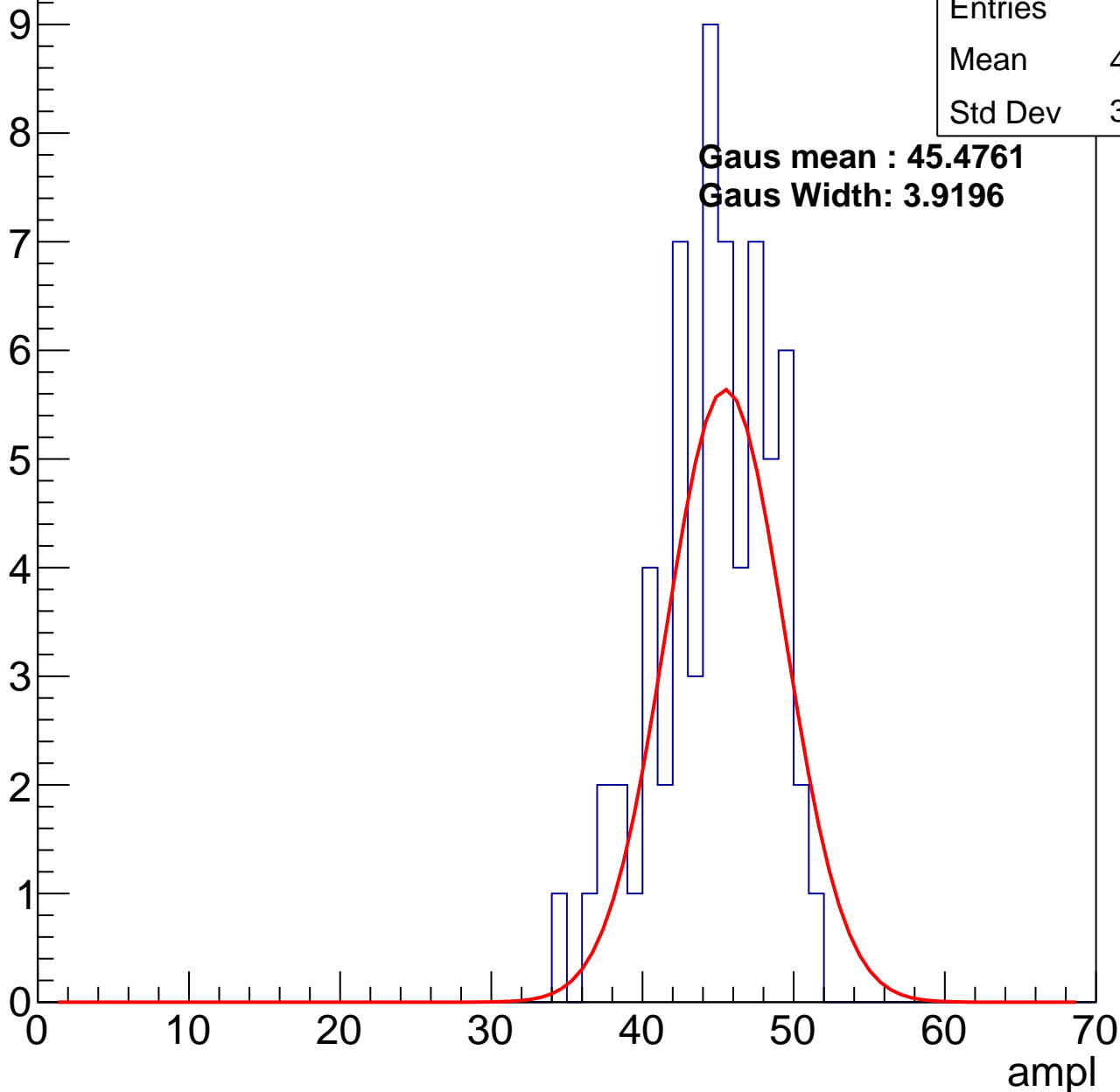
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	44.27
Std Dev	3.759

Gaus mean : 45.4761

Gaus Width: 3.9196

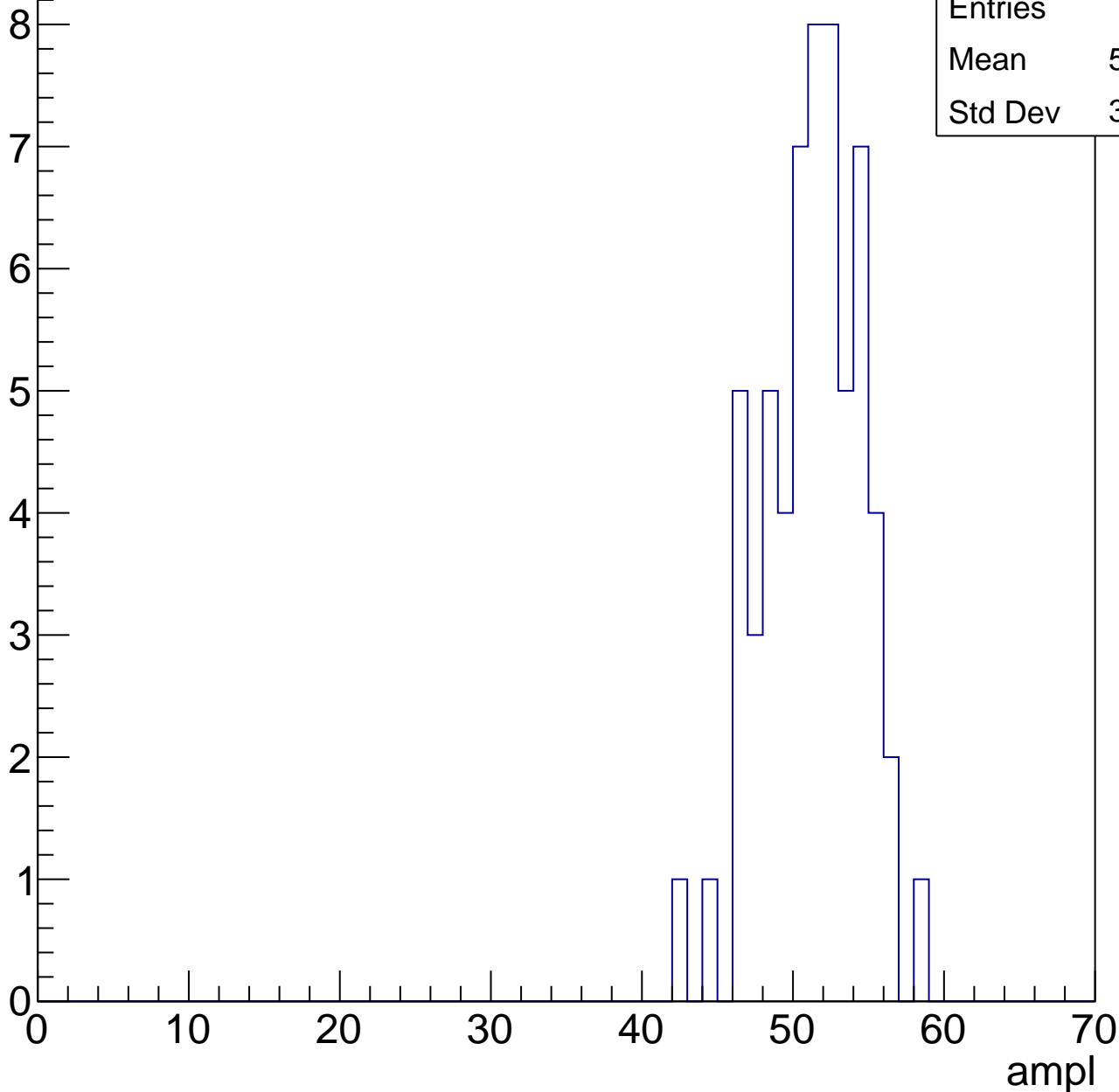


B1L102S, U10-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.82
Std Dev	3.196

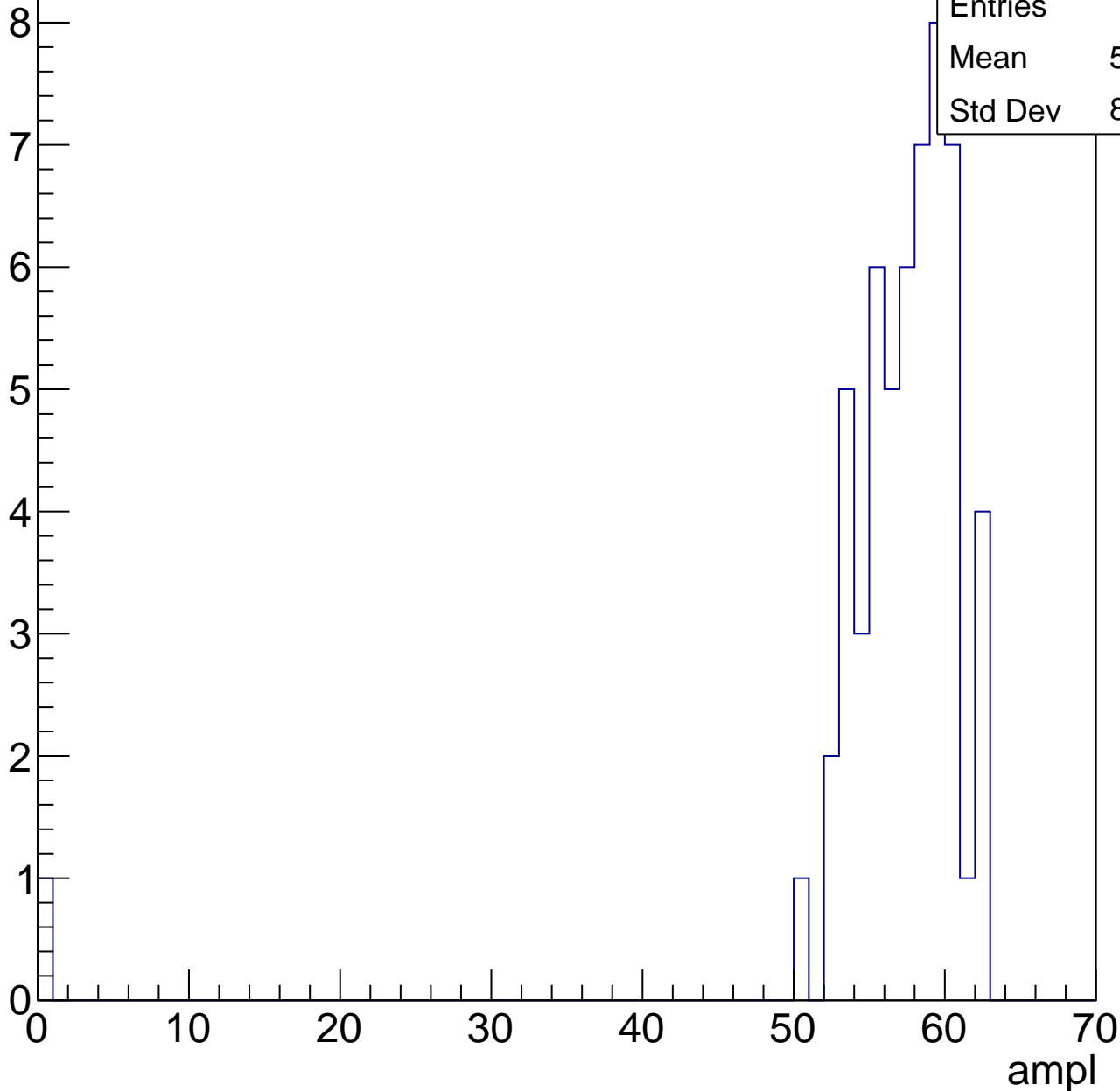


B1L102S, U10-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.07
Std Dev	8.077

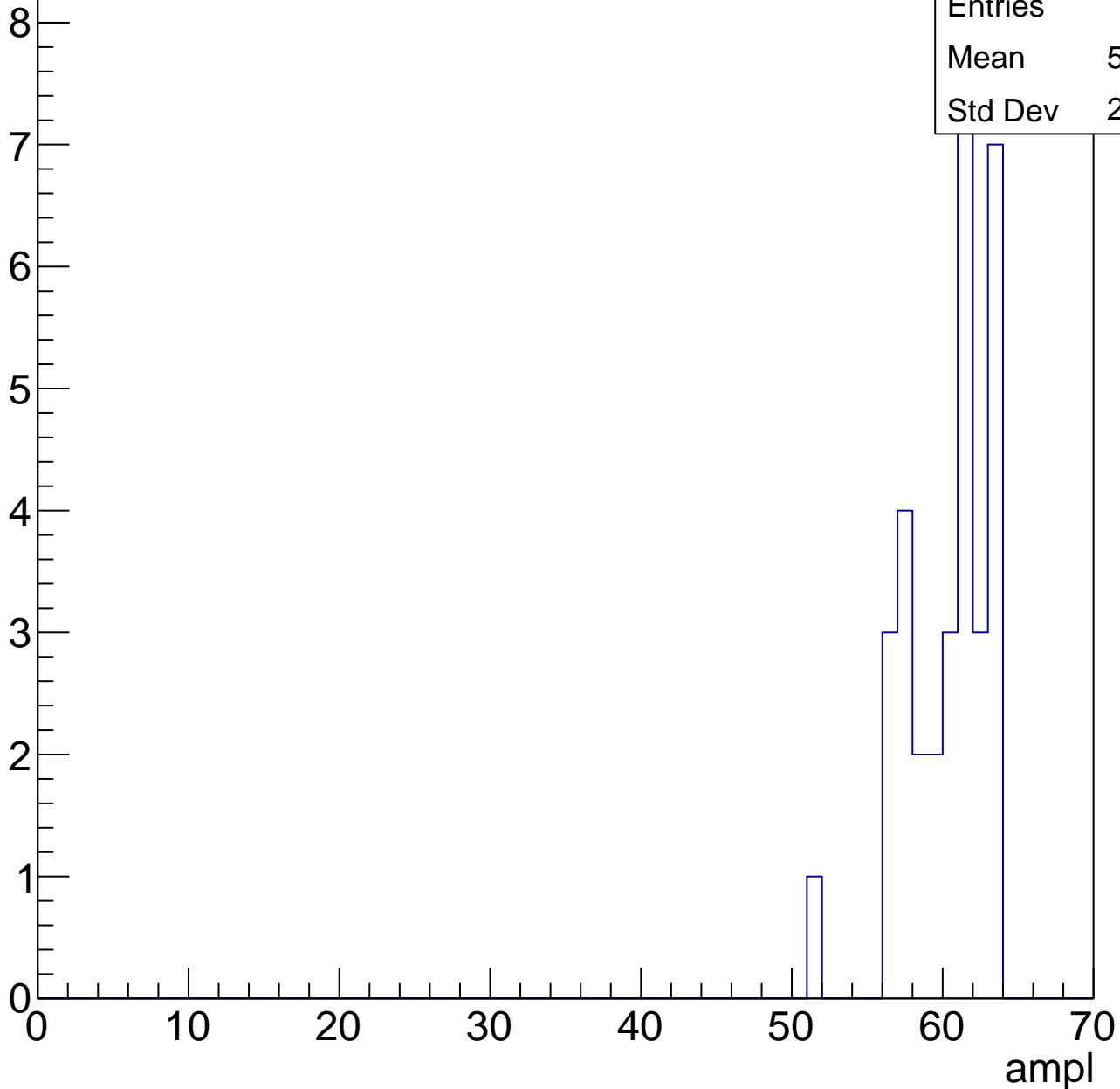


B1L102S, U10-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	59.88
Std Dev	2.793



B1L102S, U10-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



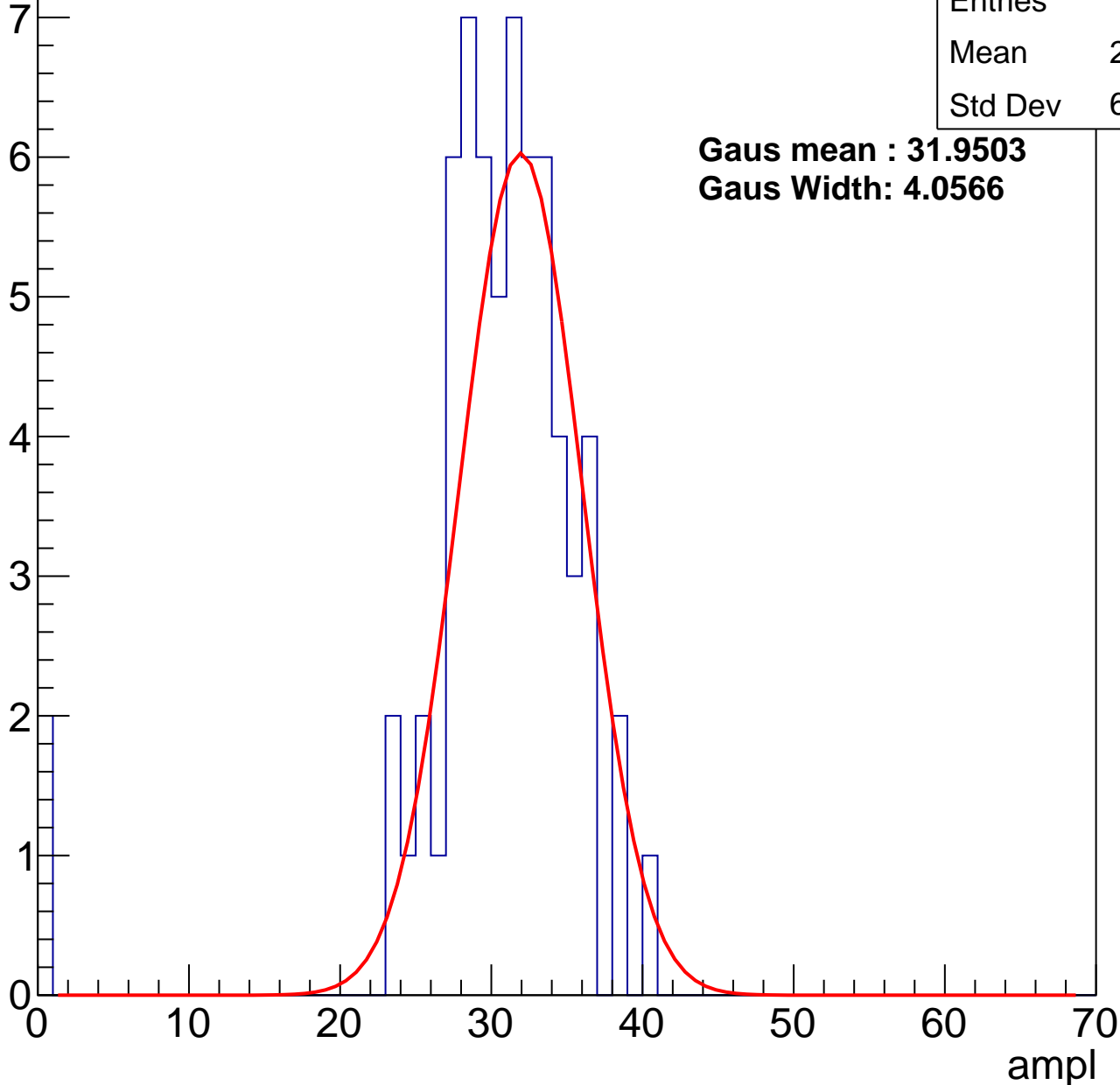
B1L102S, U10-ch96, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.78
Std Dev	6.422

Gaus mean : 31.9503
Gaus Width: 4.0566



B1L102S, U10-ch96, adc1

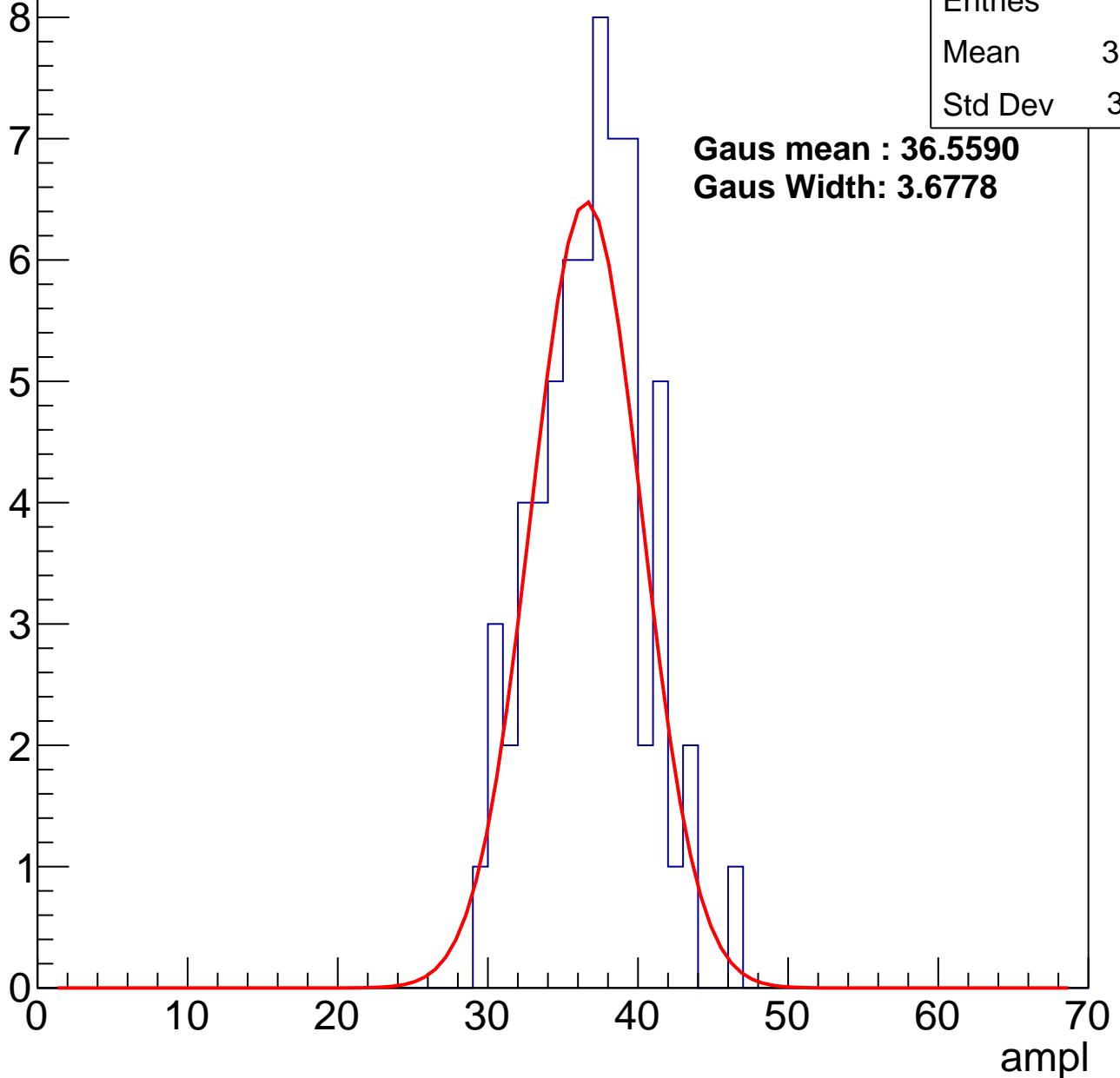
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.42
Std Dev	3.561

Gaus mean : 36.5590

Gaus Width: 3.6778



B1L102S, U10-ch96, adc2

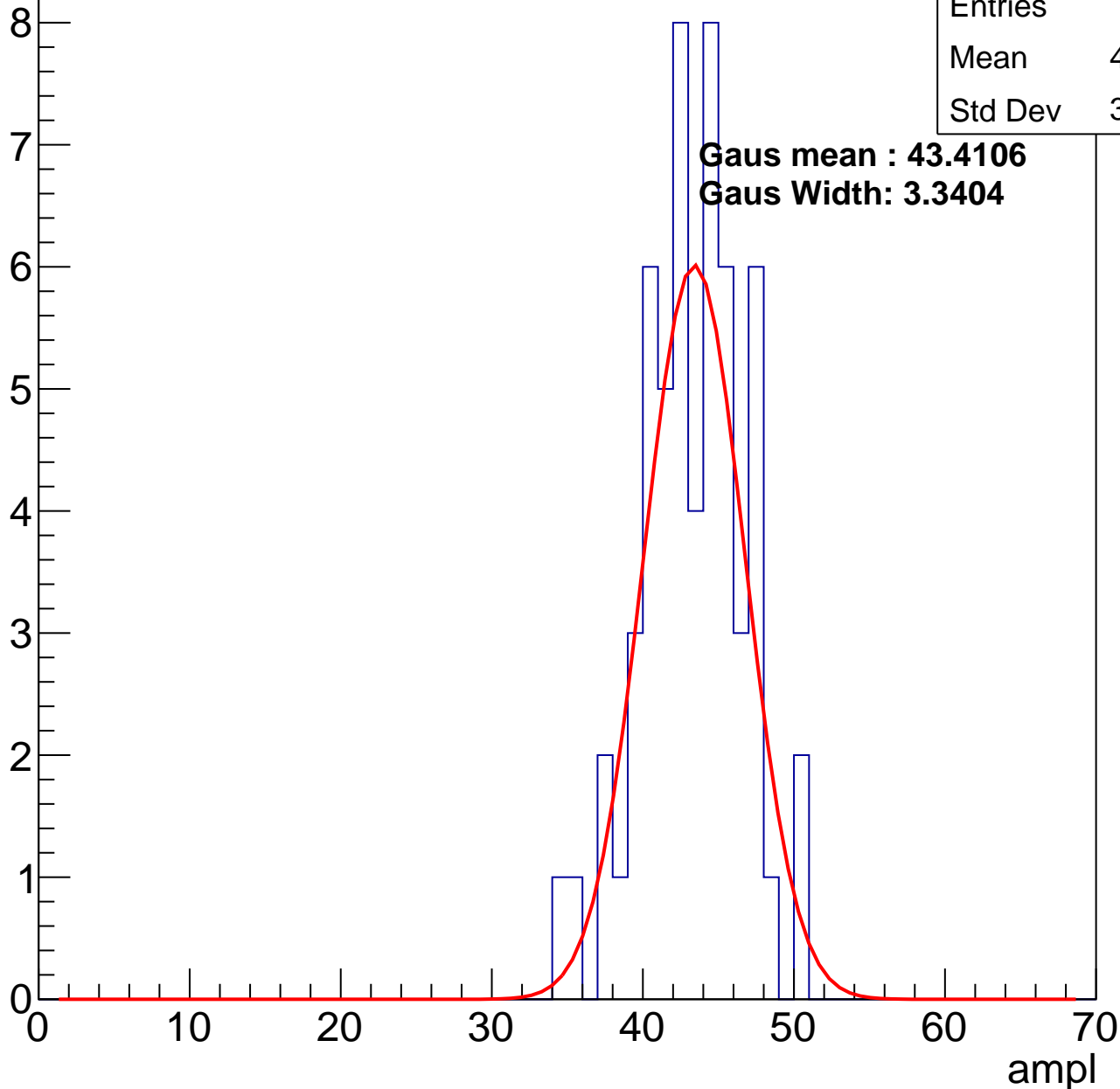
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.82
Std Dev	3.372

Gaus mean : 43.4106

Gaus Width: 3.3404

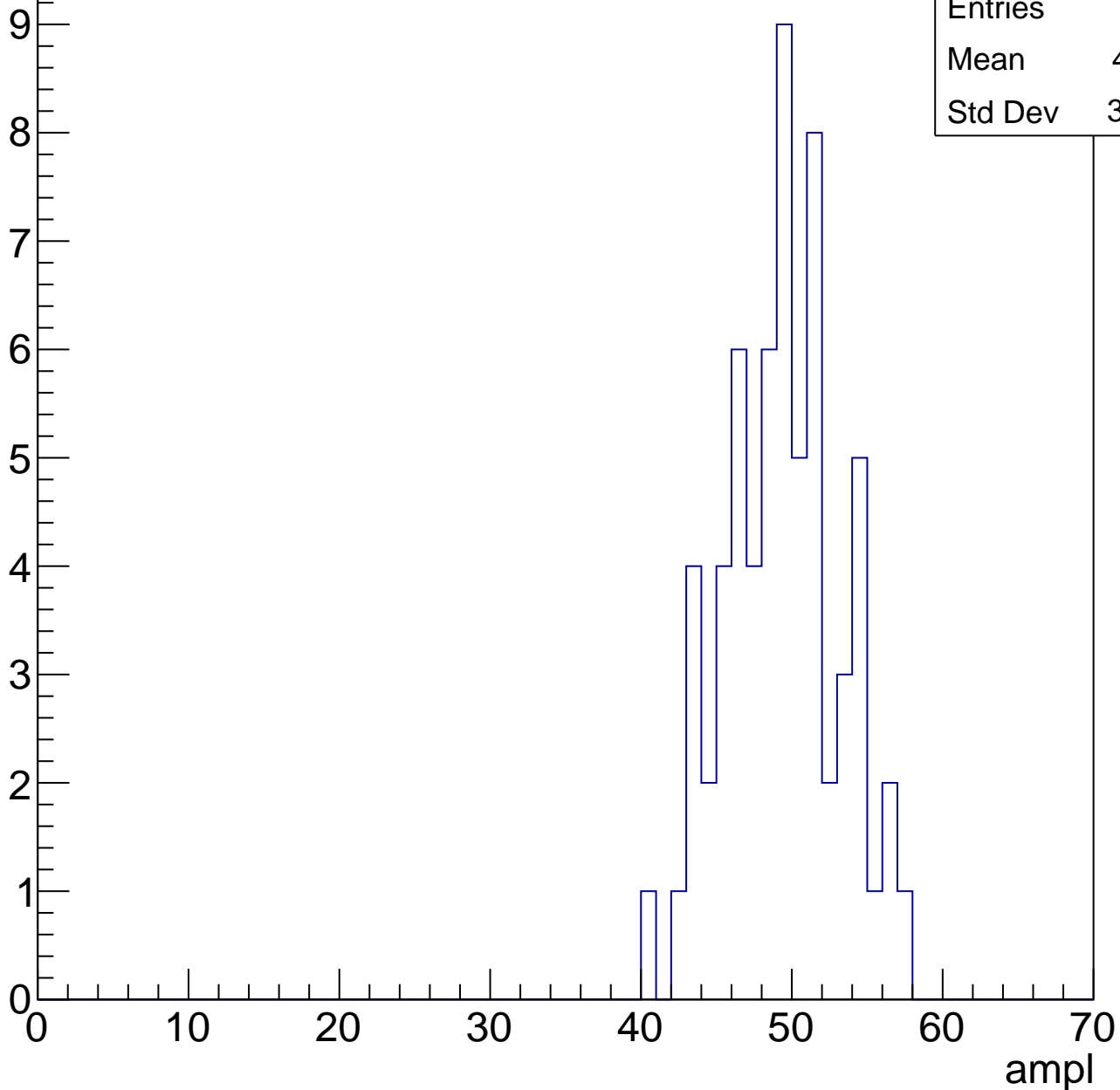


B1L102S, U10-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

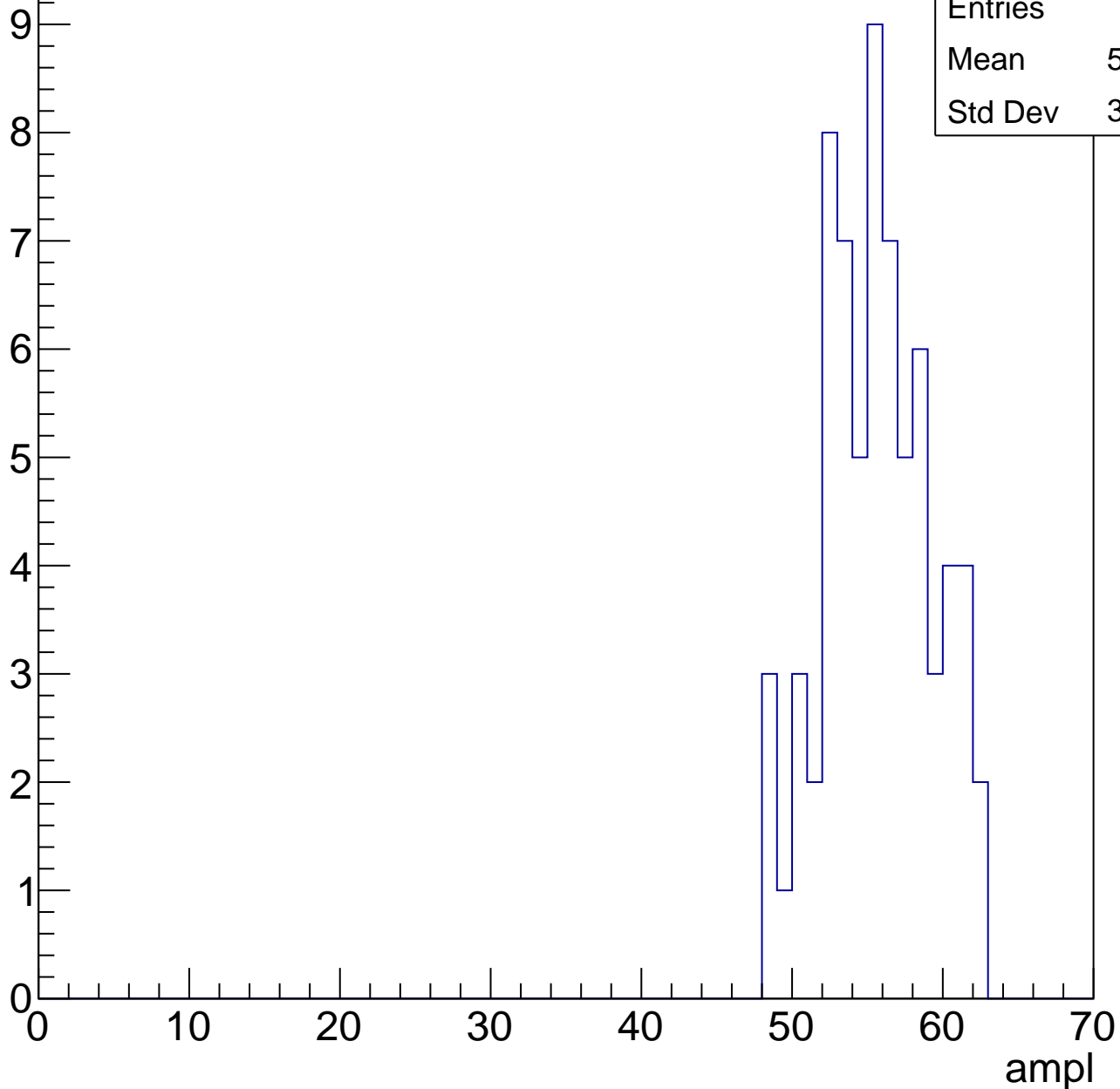
Entries	64
Mean	48.91
Std Dev	3.728



B1L102S, U10-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

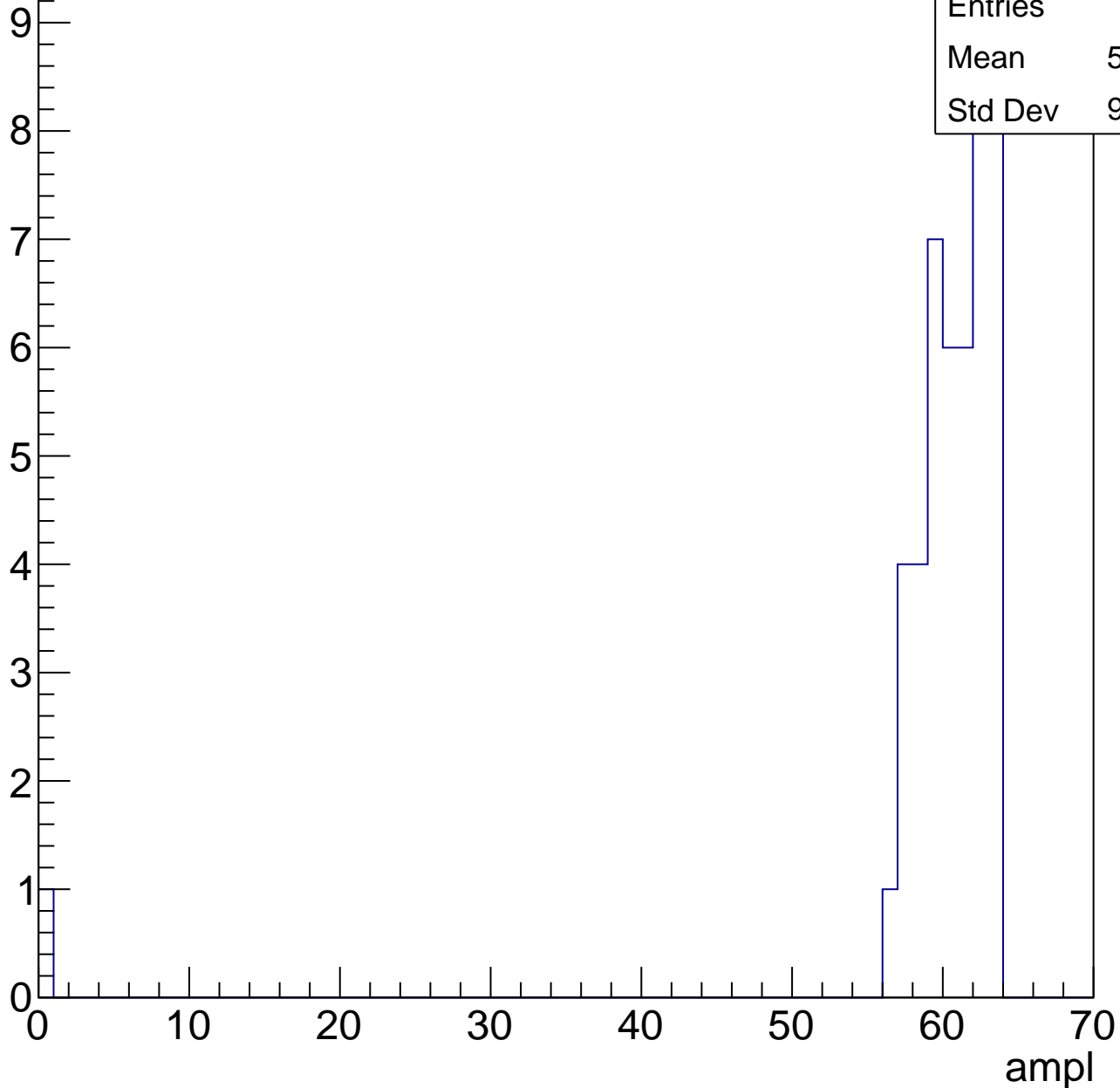
Entry



B1L102S, U10-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L102S, U10-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch97, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	28.84
Std Dev	4.866

Gaus mean : 29.6739

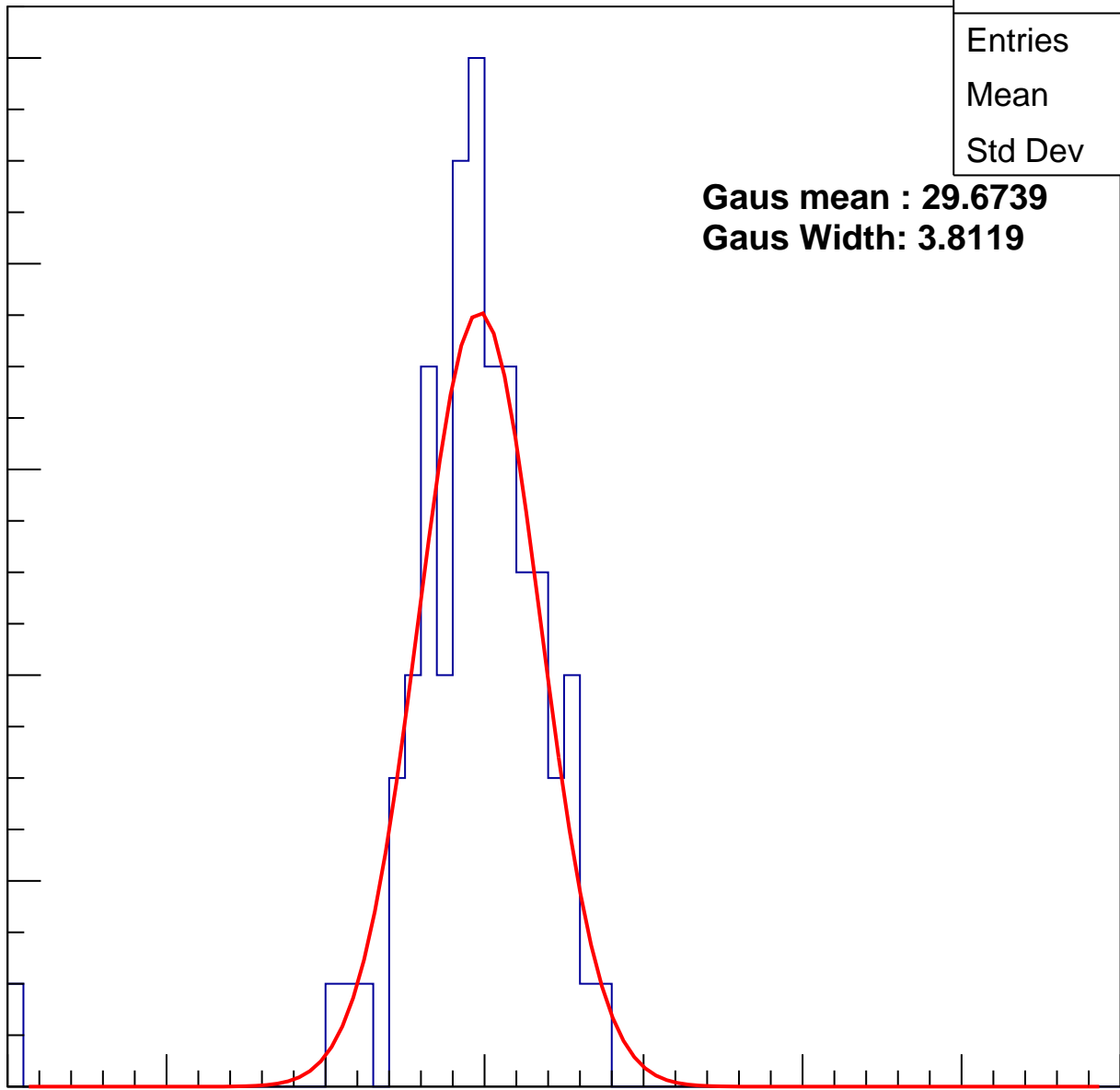
Gaus Width: 3.8119

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch97, adc1

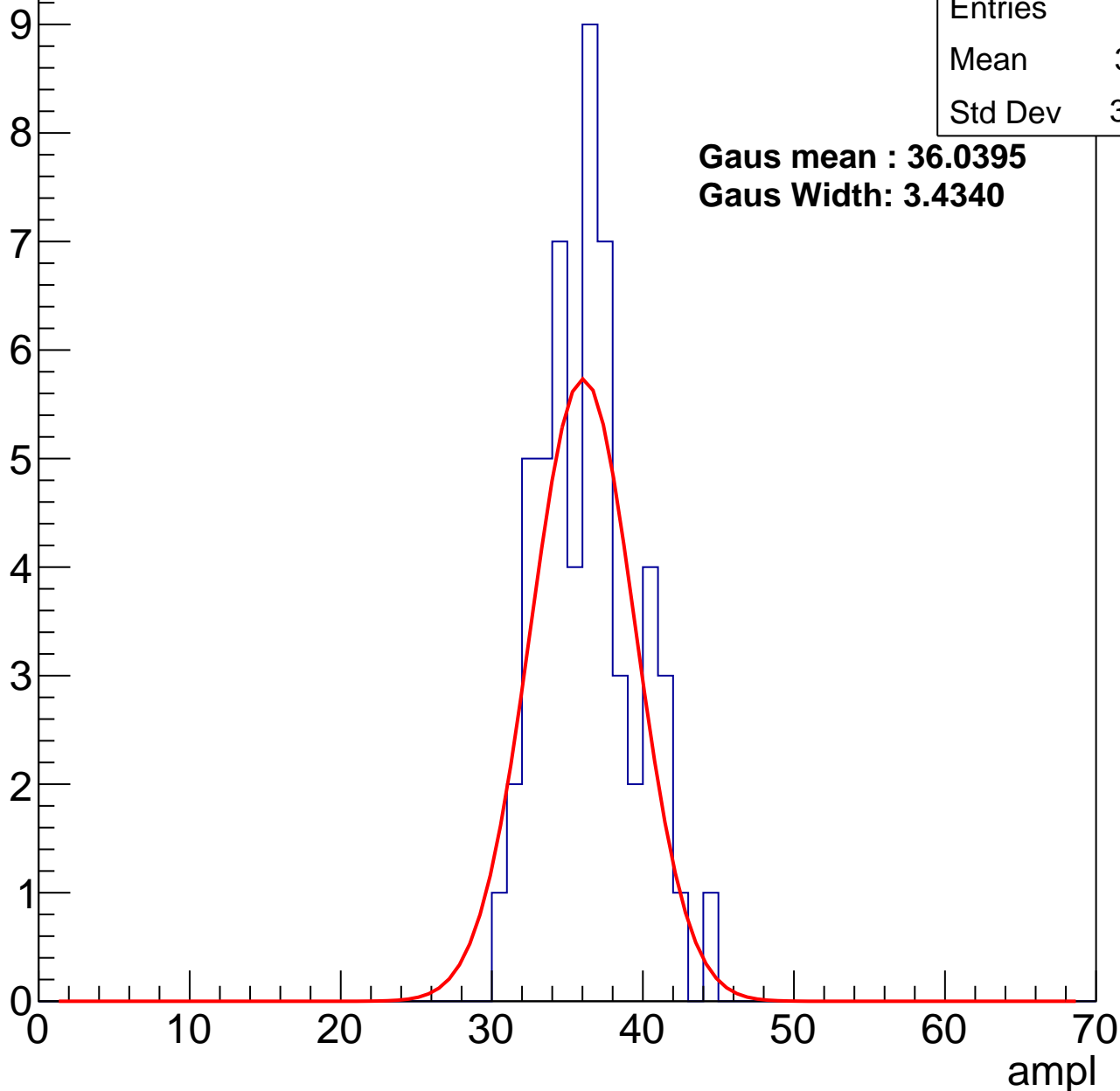
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.91
Std Dev	3.099

Gaus mean : 36.0395

Gaus Width: 3.4340



B1L102S, U10-ch97, adc2

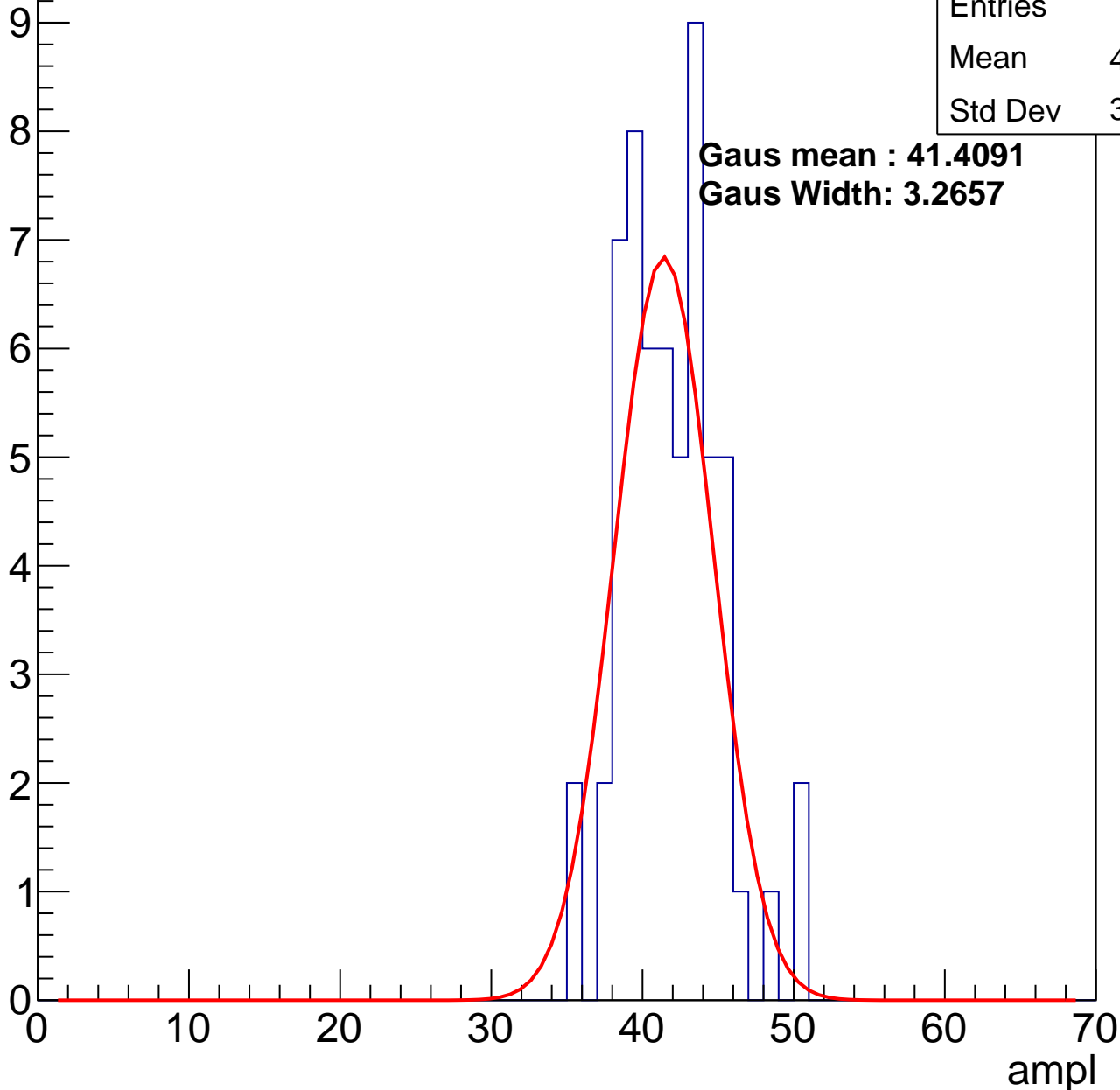
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.42
Std Dev	3.179

Gaus mean : 41.4091

Gaus Width: 3.2657

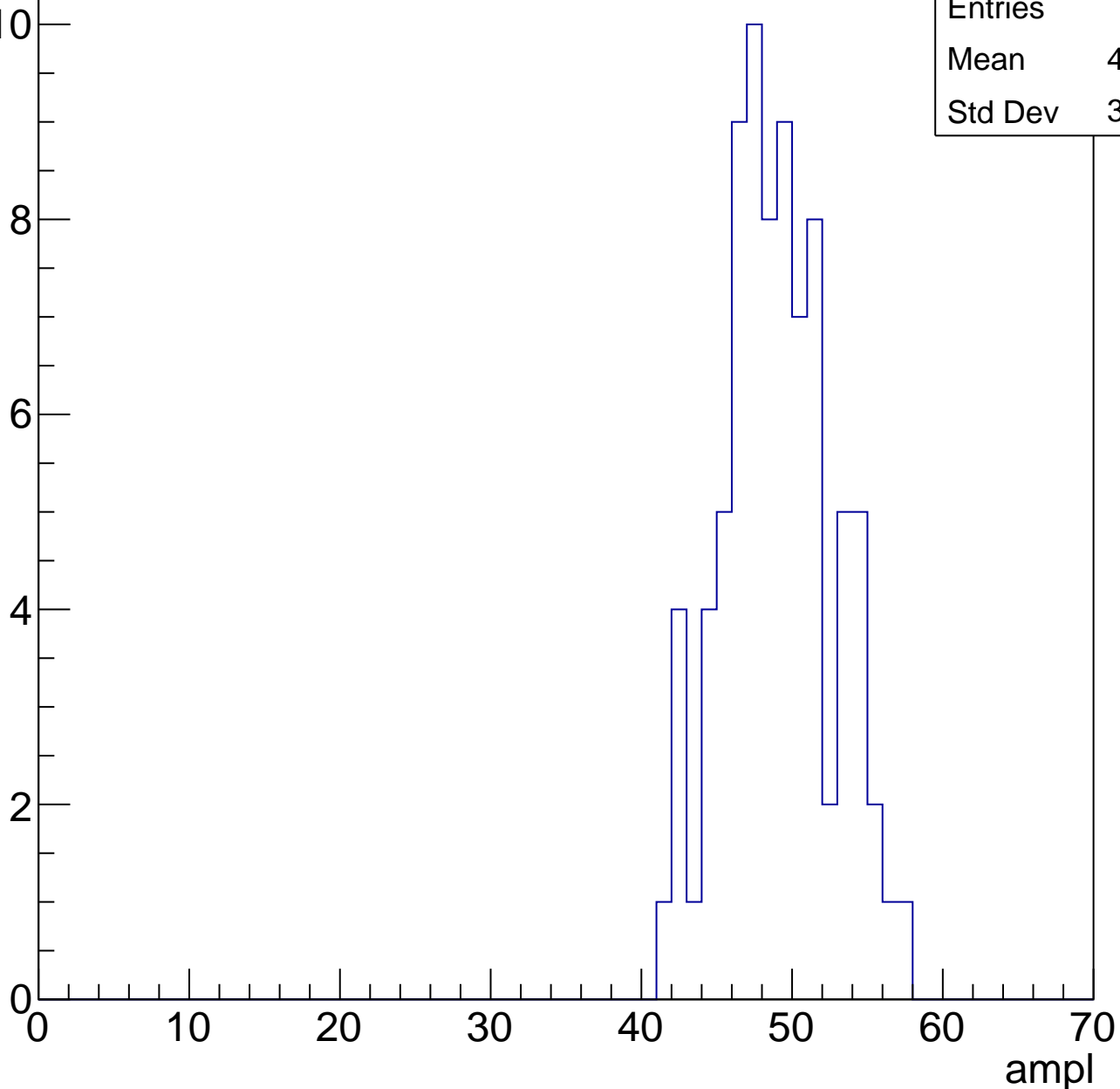


B1L102S, U10-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

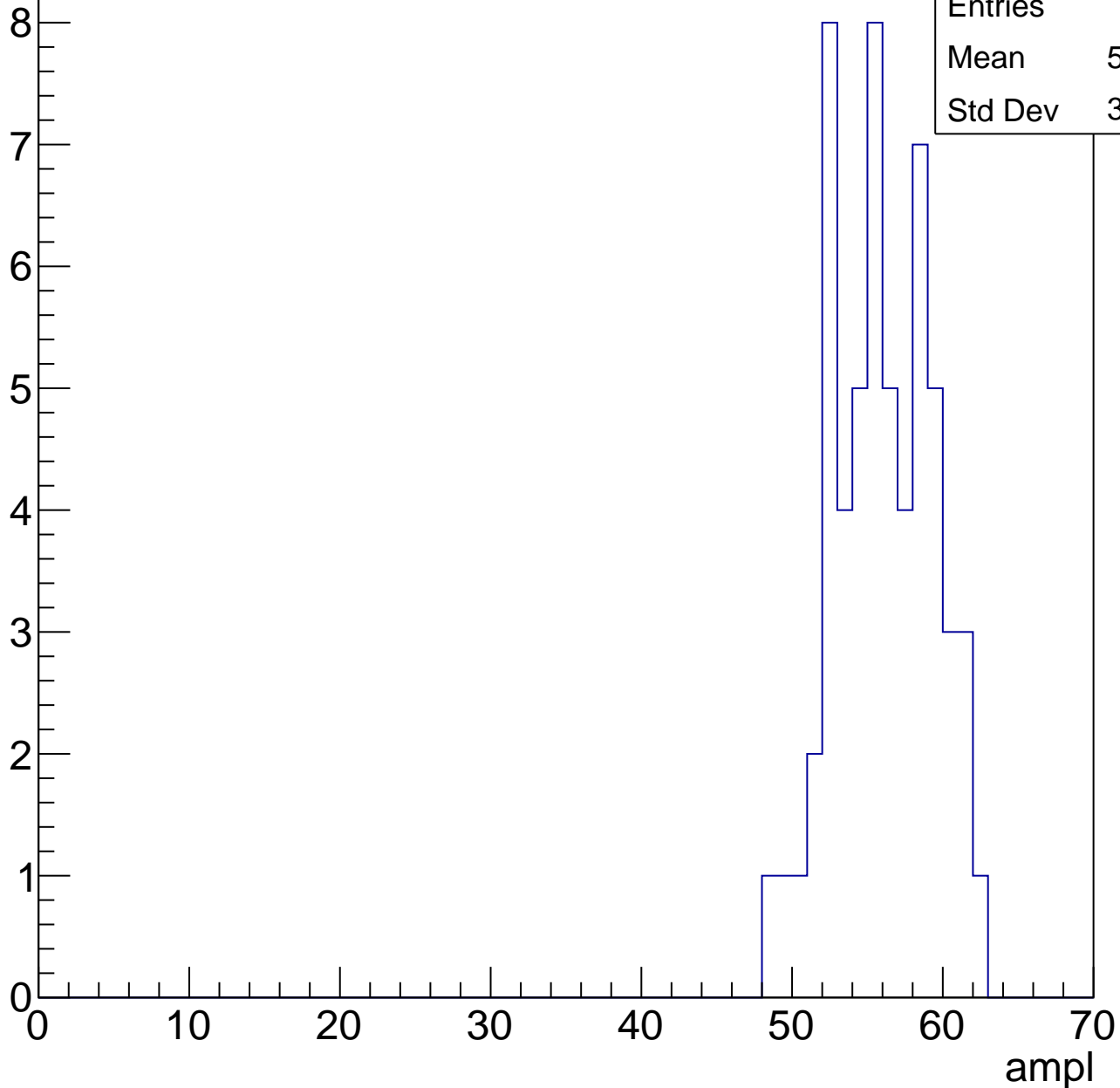
Entries	82
Mean	48.56
Std Dev	3.555



B1L102S, U10-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



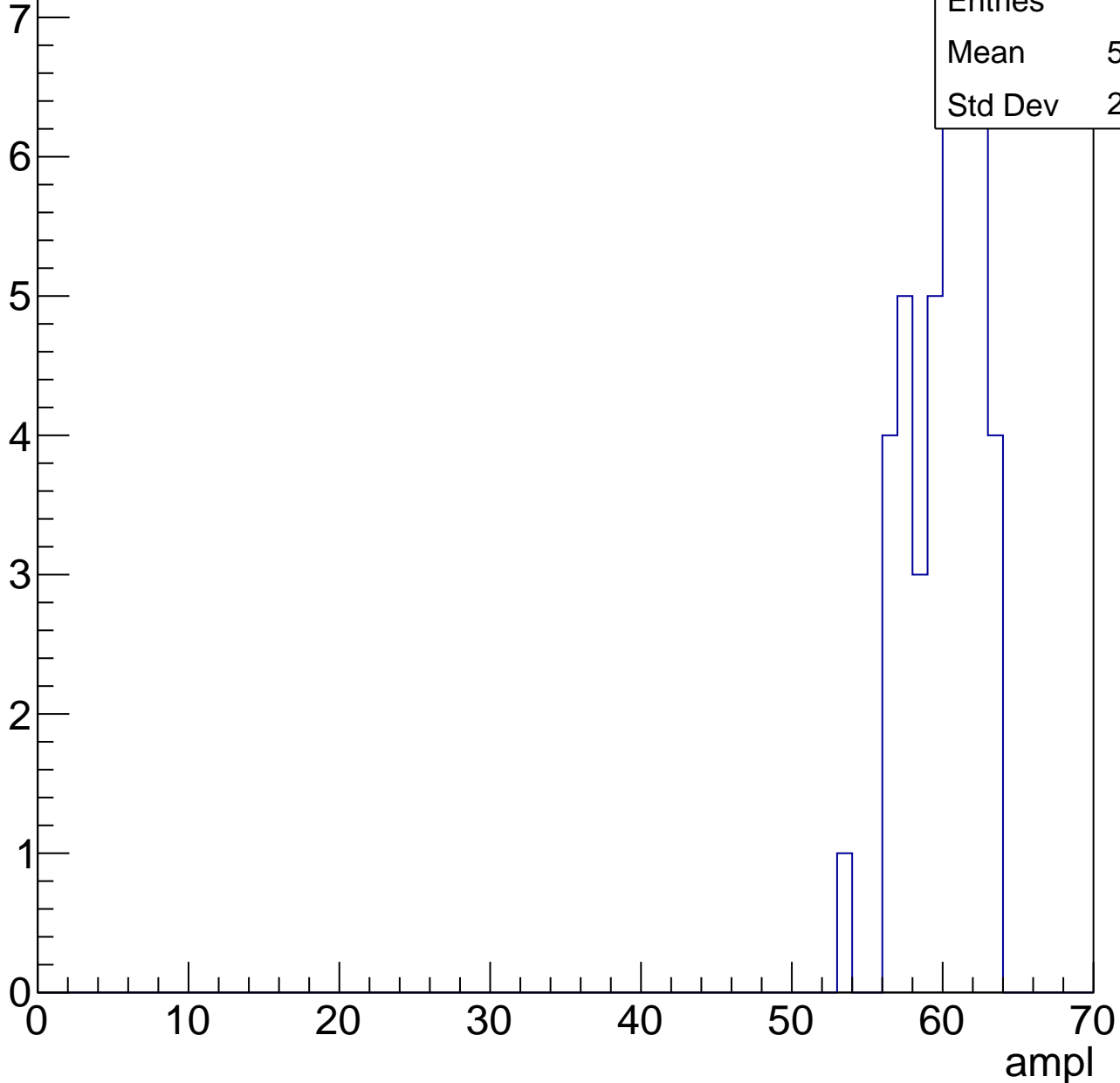
Entries	58
Mean	55.53
Std Dev	3.255

B1L102S, U10-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.63
Std Dev	2.363

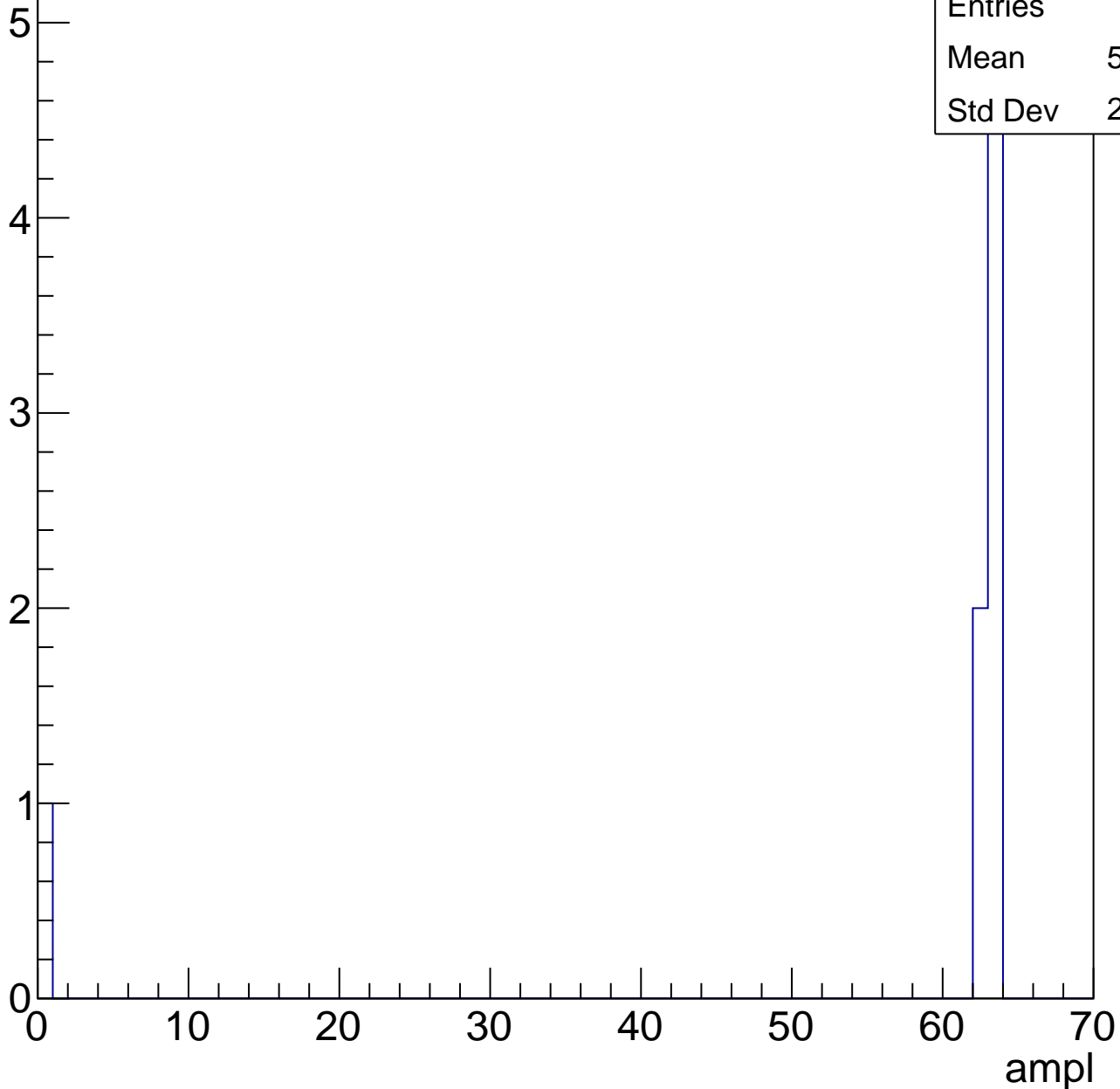


B1L102S, U10-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B1L102S, U10-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch98, adc0

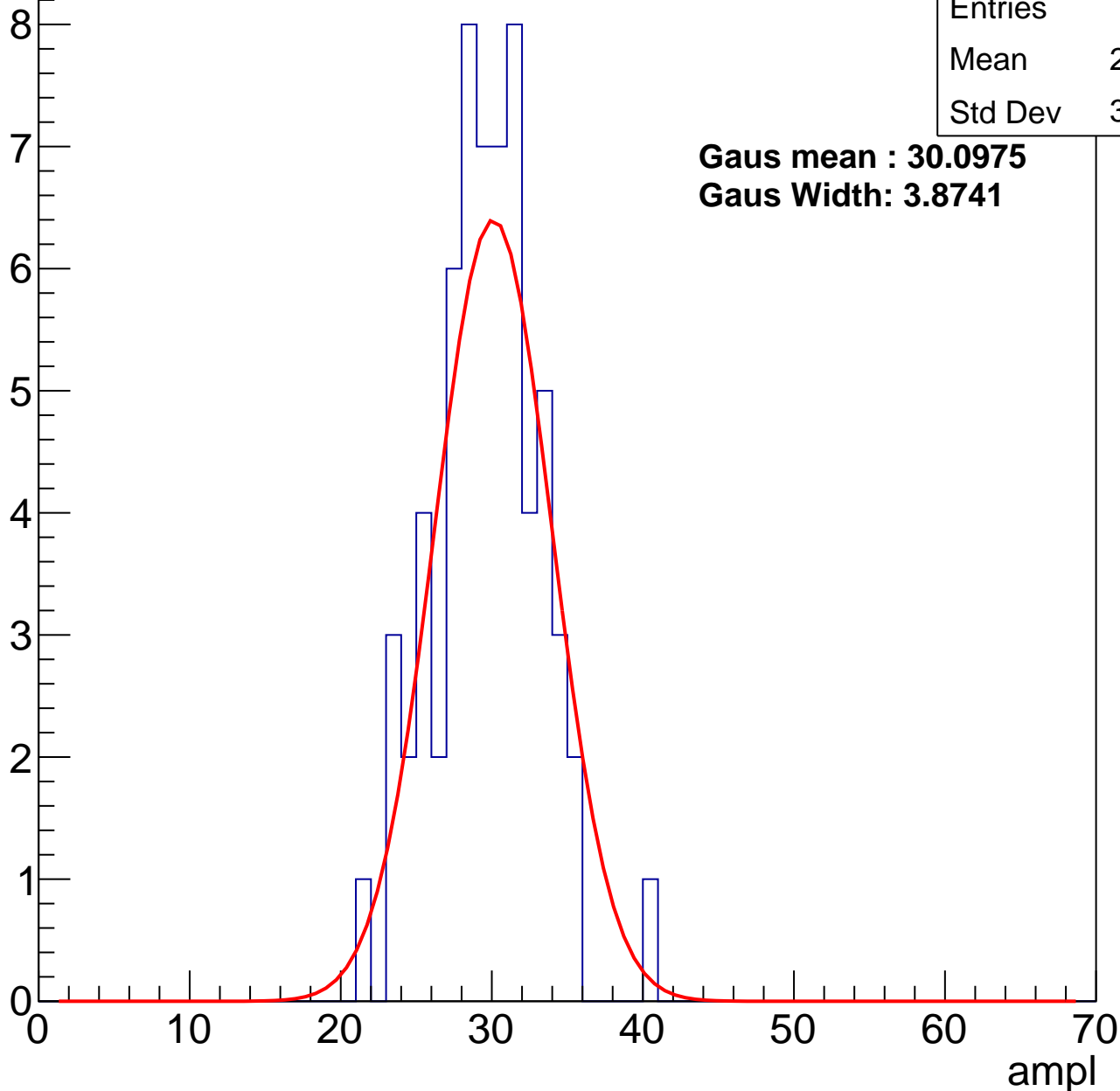
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.24
Std Dev	3.463

Gaus mean : 30.0975

Gaus Width: 3.8741



B1L102S, U10-ch98, adc1

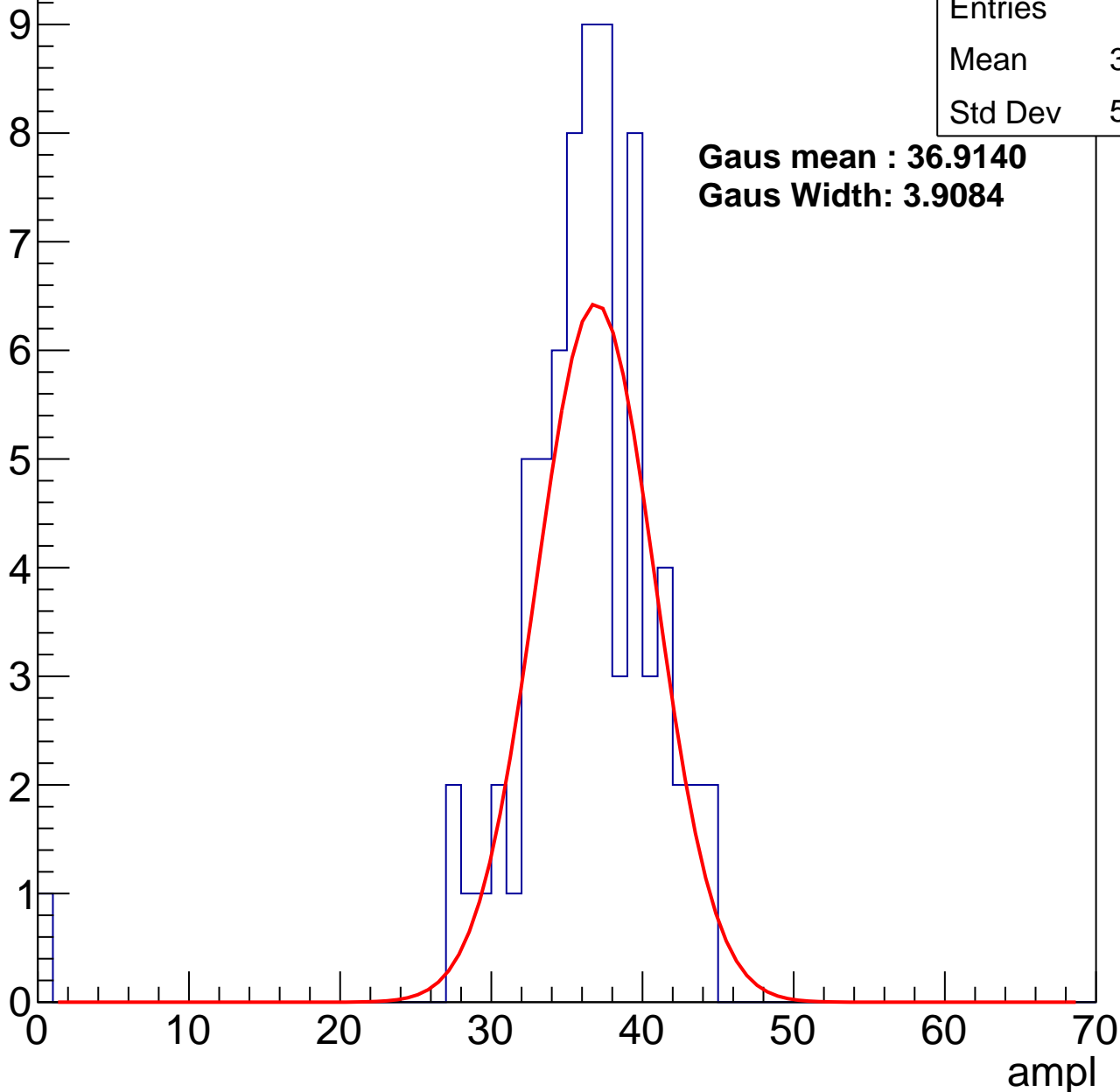
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.62
Std Dev	5.639

Gaus mean : 36.9140

Gaus Width: 3.9084



B1L102S, U10-ch98, adc2

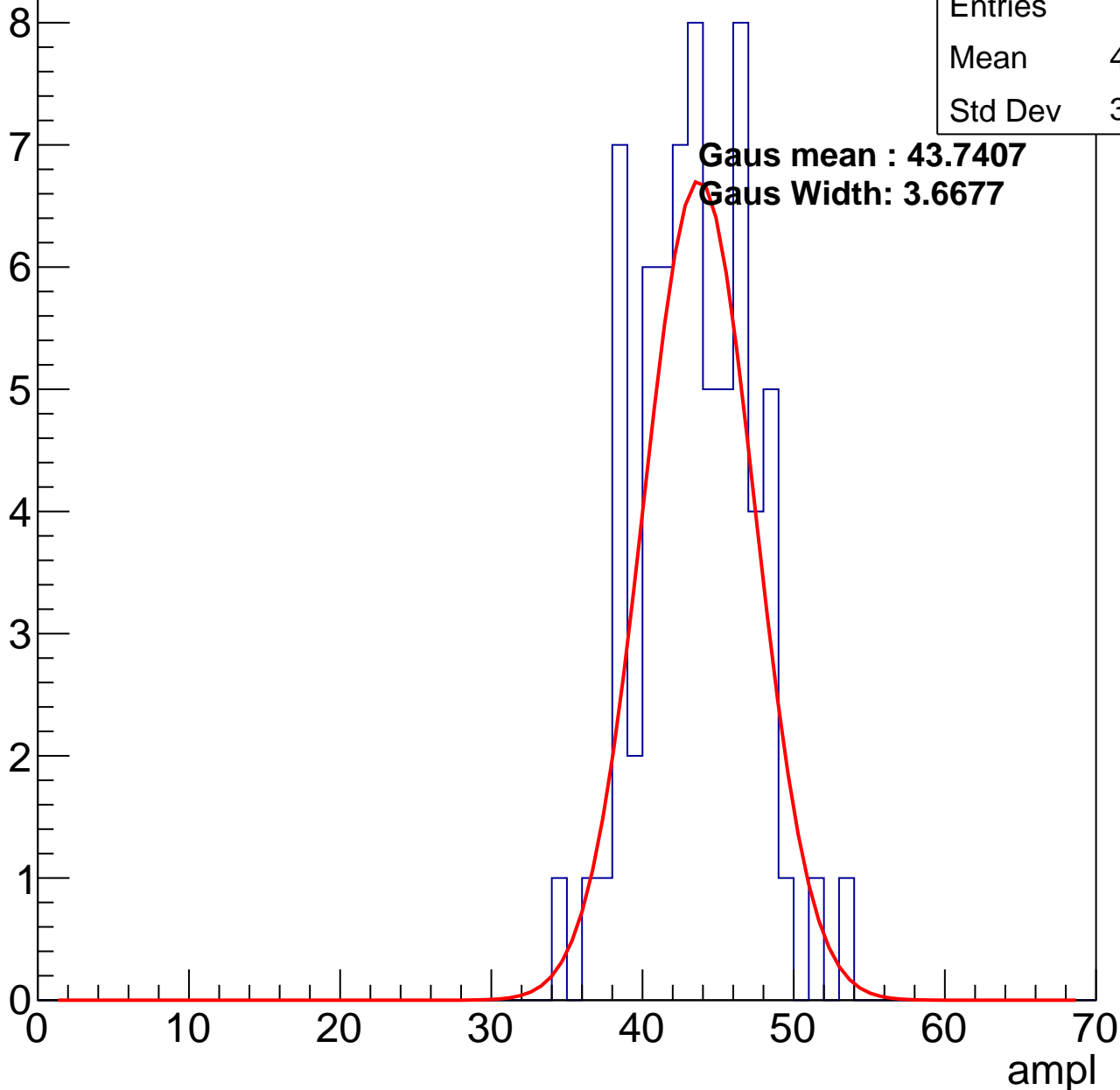
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.03
Std Dev	3.699

Gaus mean : 43.7407

Gaus Width: 3.6677

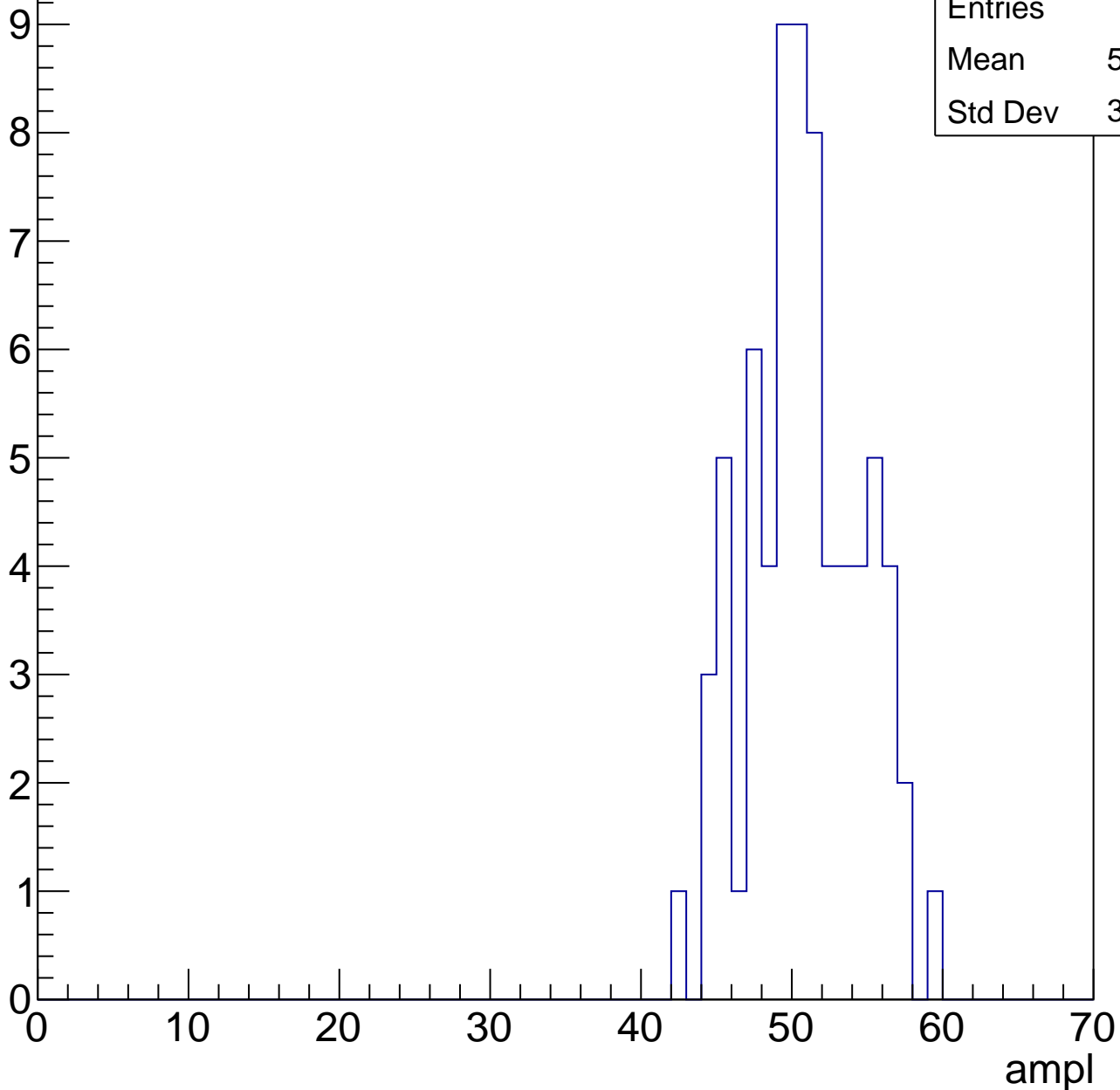


B1L102S, U10-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.37
Std Dev	3.692

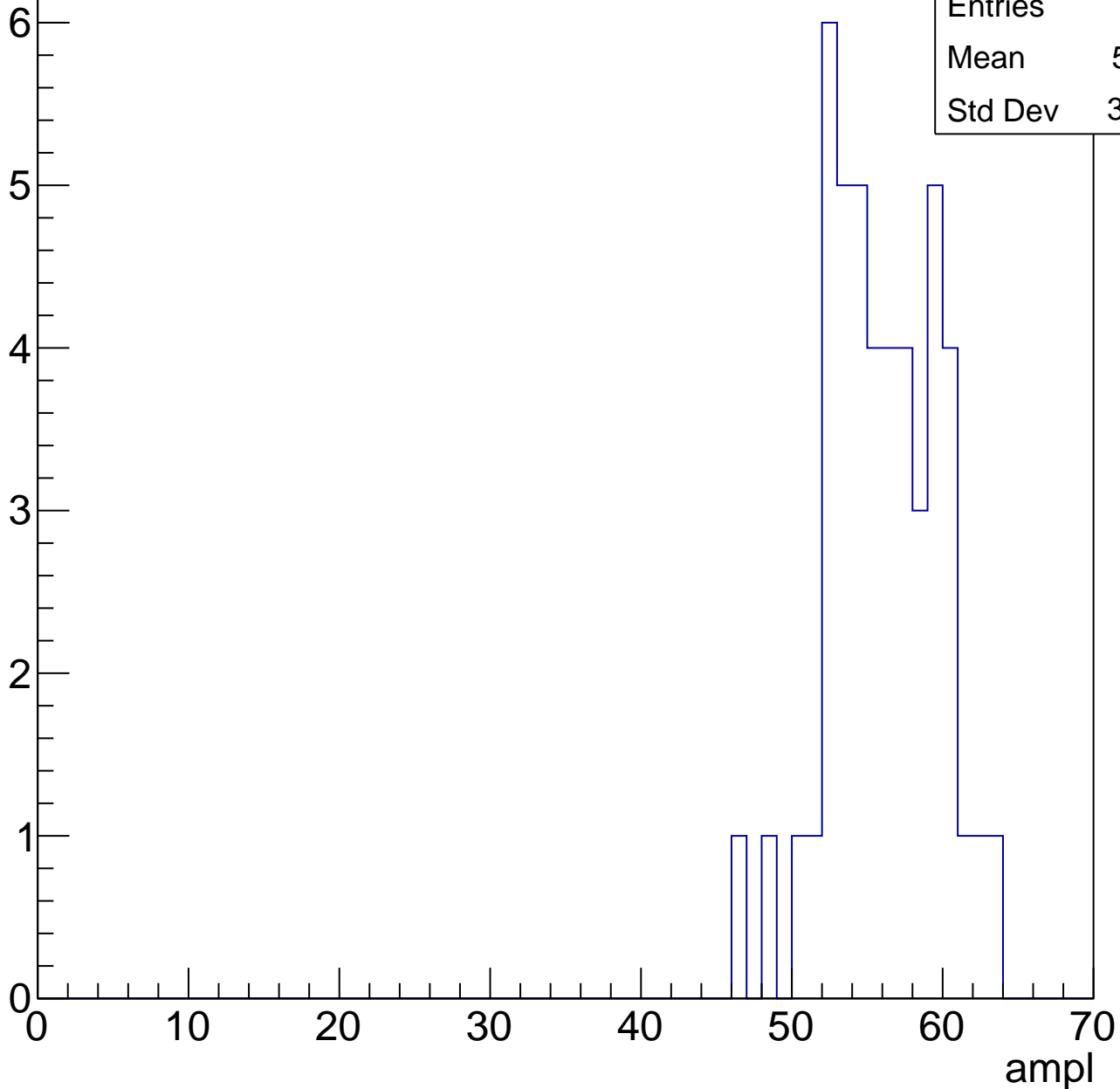


B1L102S, U10-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.51
Std Dev	3.614

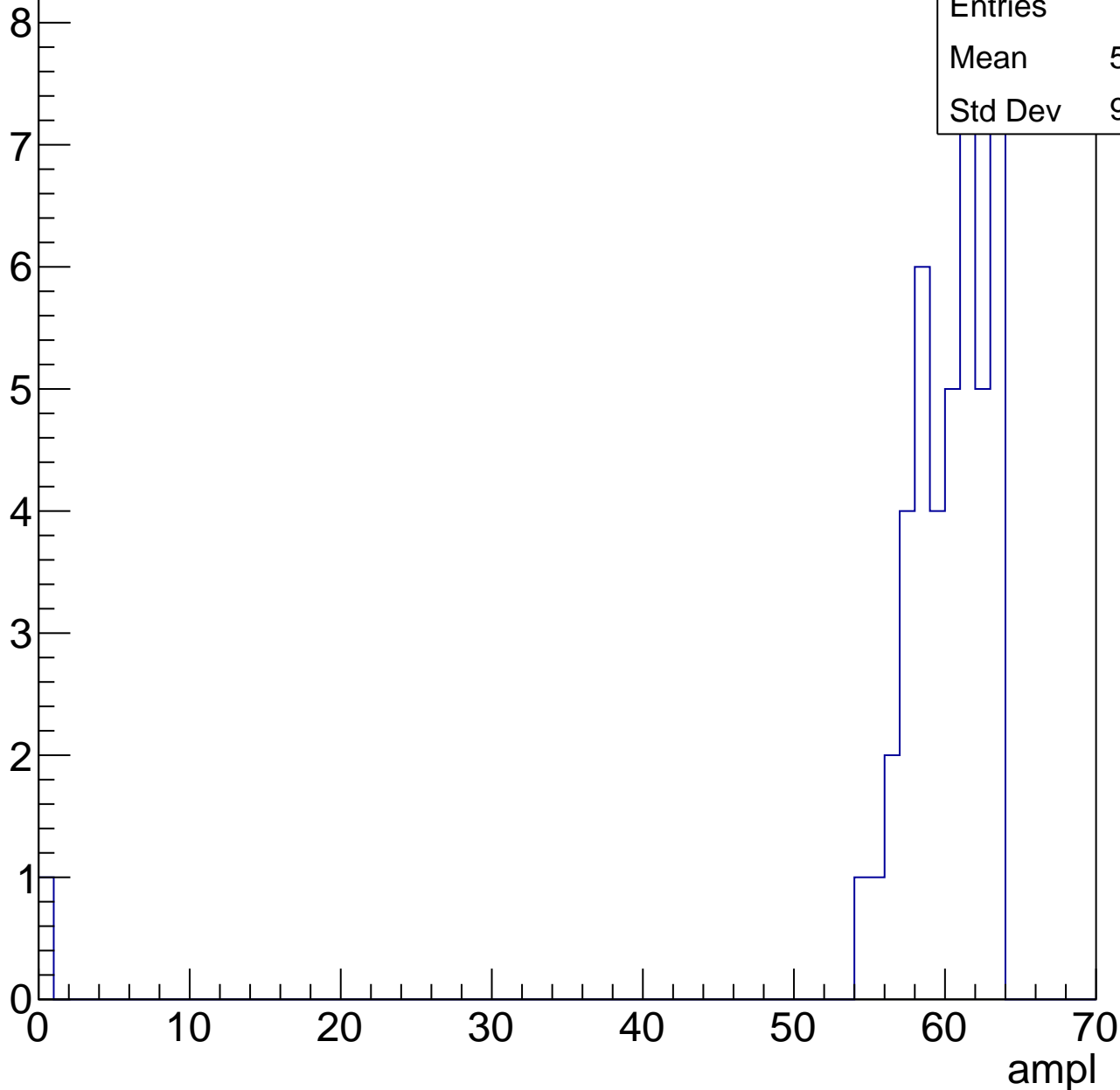


B1L102S, U10-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

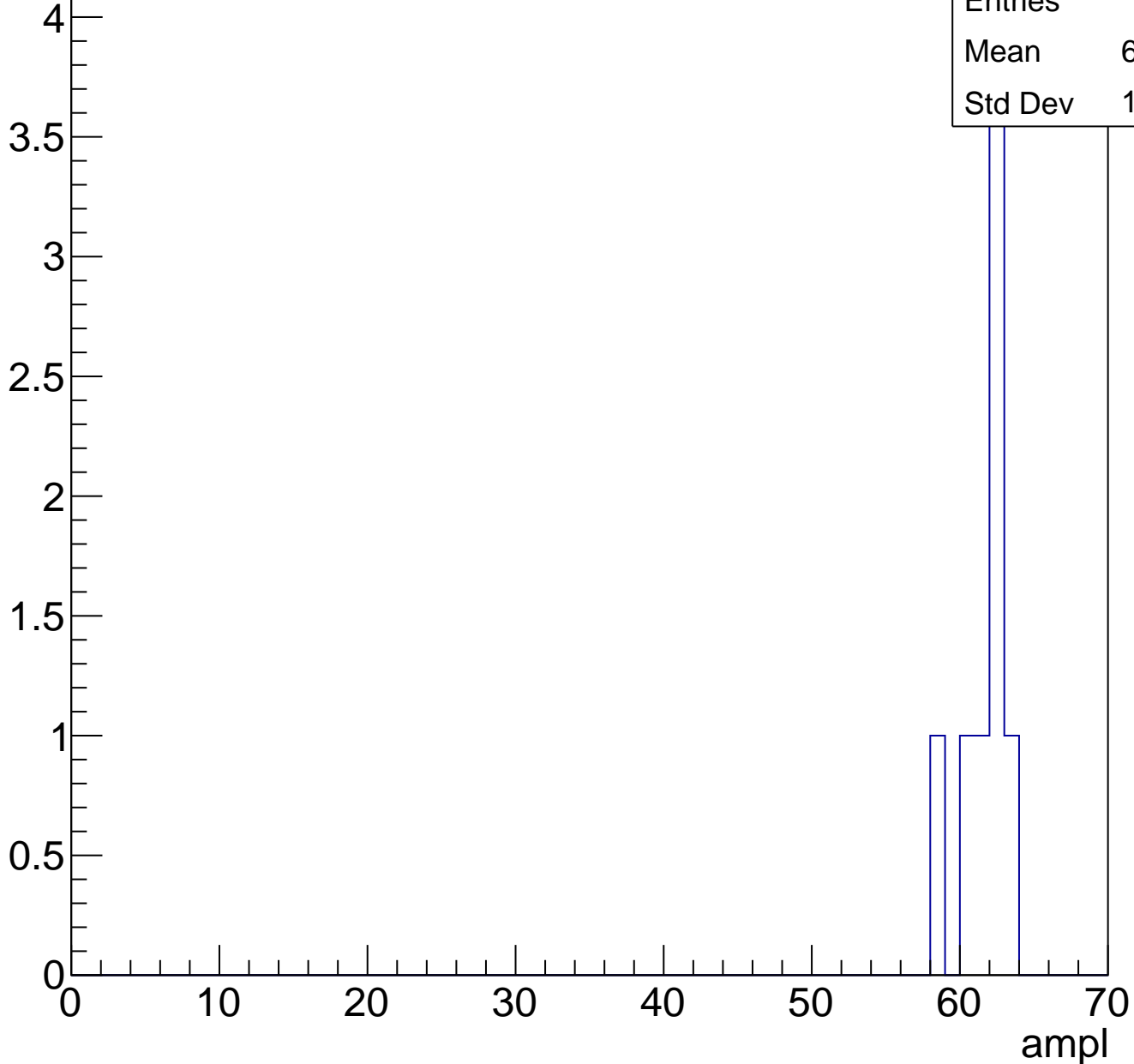
Entries	45
Mean	58.56
Std Dev	9.145



B1L102S, U10-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch99, adc0

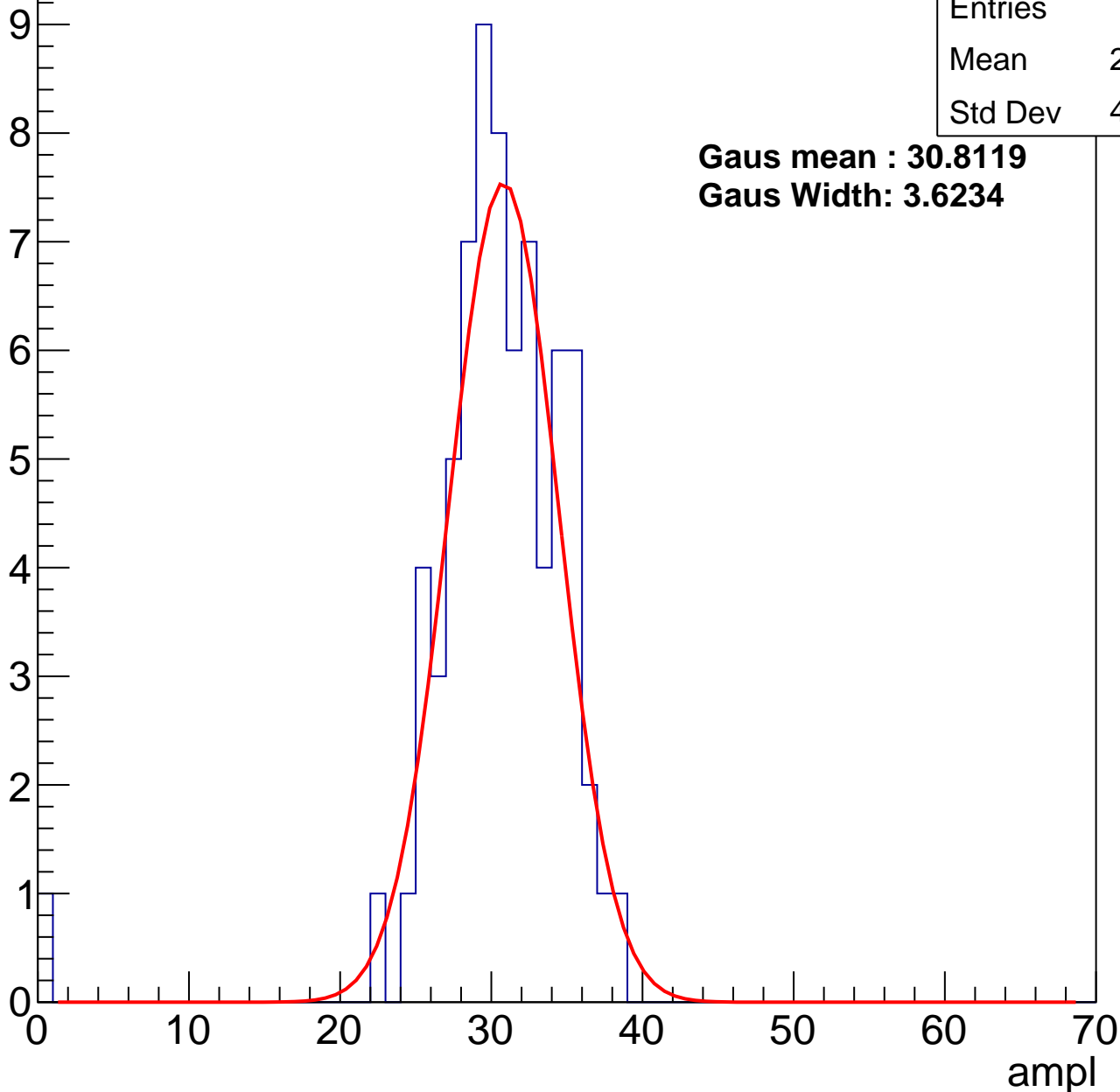
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.99
Std Dev	4.903

Gaus mean : 30.8119

Gaus Width: 3.6234



B1L102S, U10-ch99, adc1

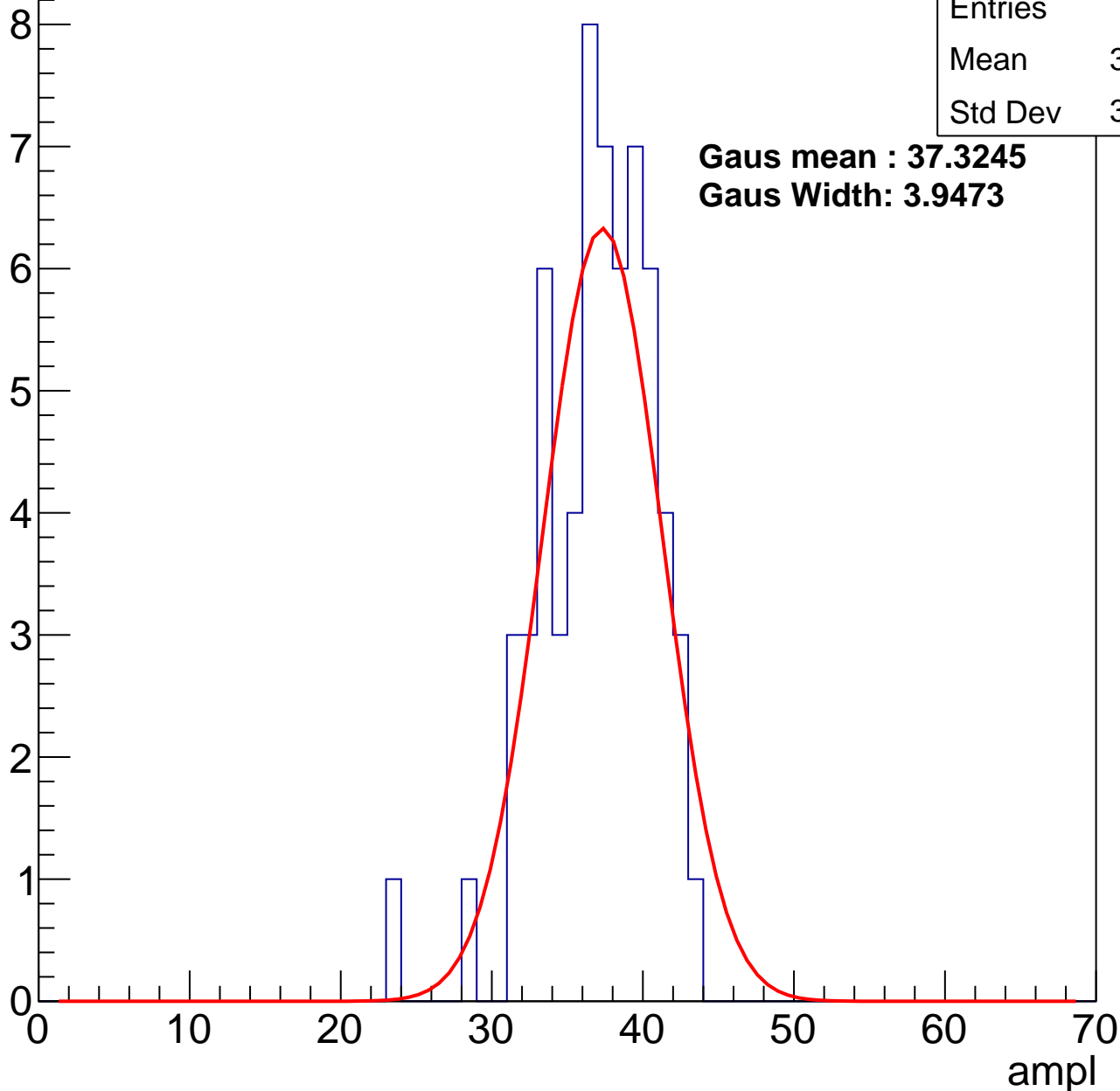
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.52
Std Dev	3.685

Gaus mean : 37.3245

Gaus Width: 3.9473



B1L102S, U10-ch99, adc2

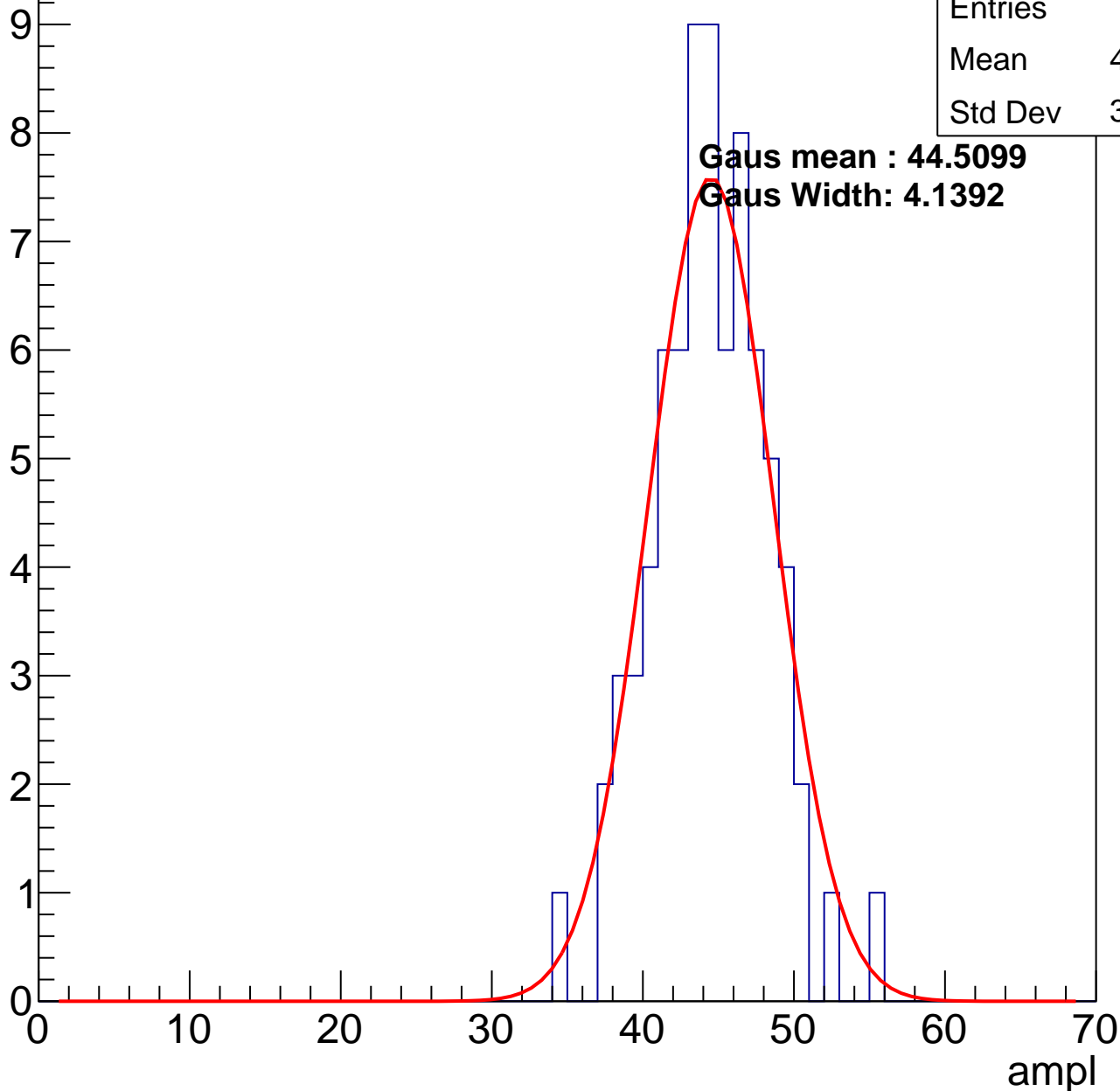
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.99
Std Dev	3.736

Gaus mean : 44.5099

Gaus Width: 4.1392

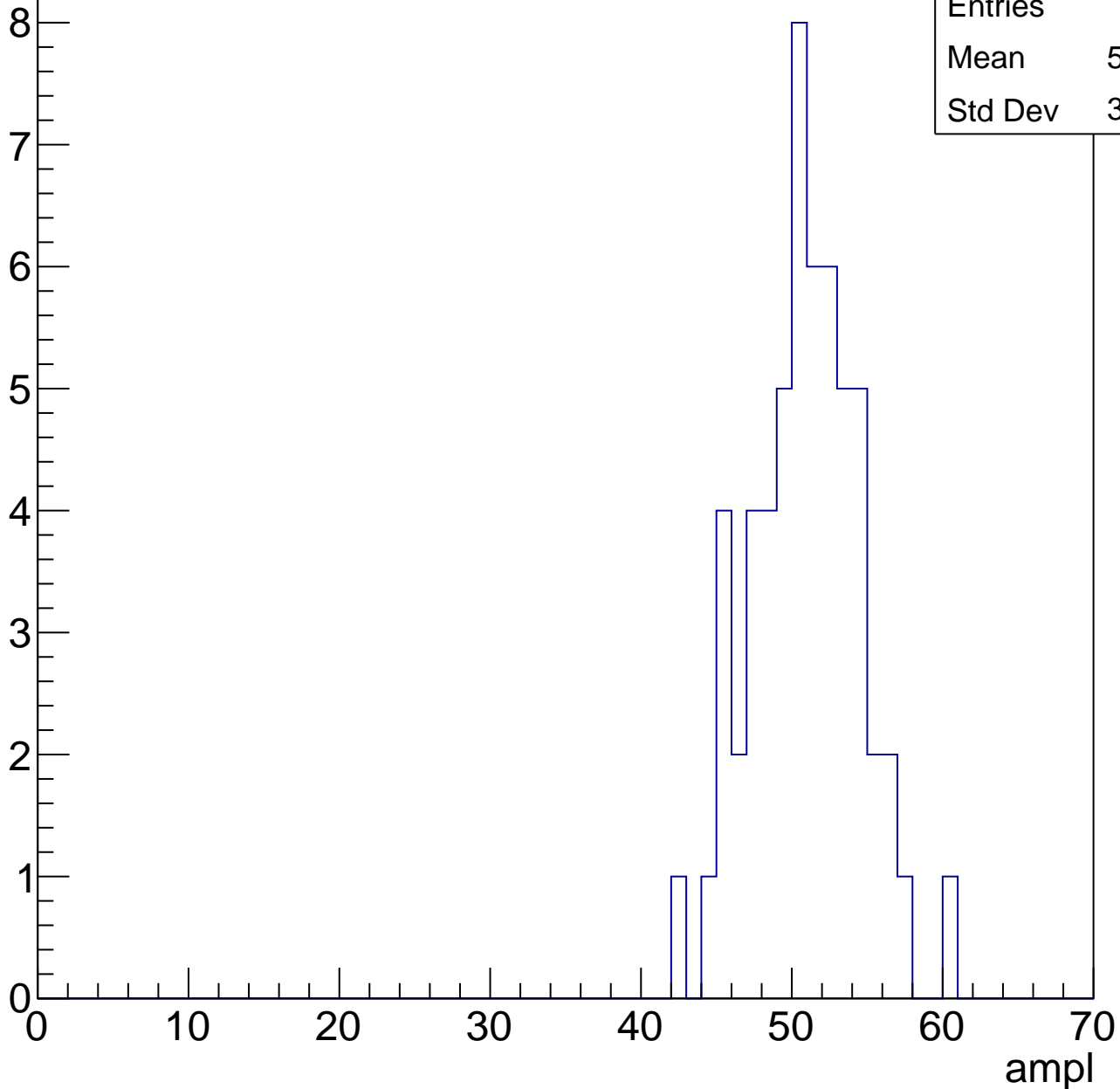


B1L102S, U10-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	50.44
Std Dev	3.514

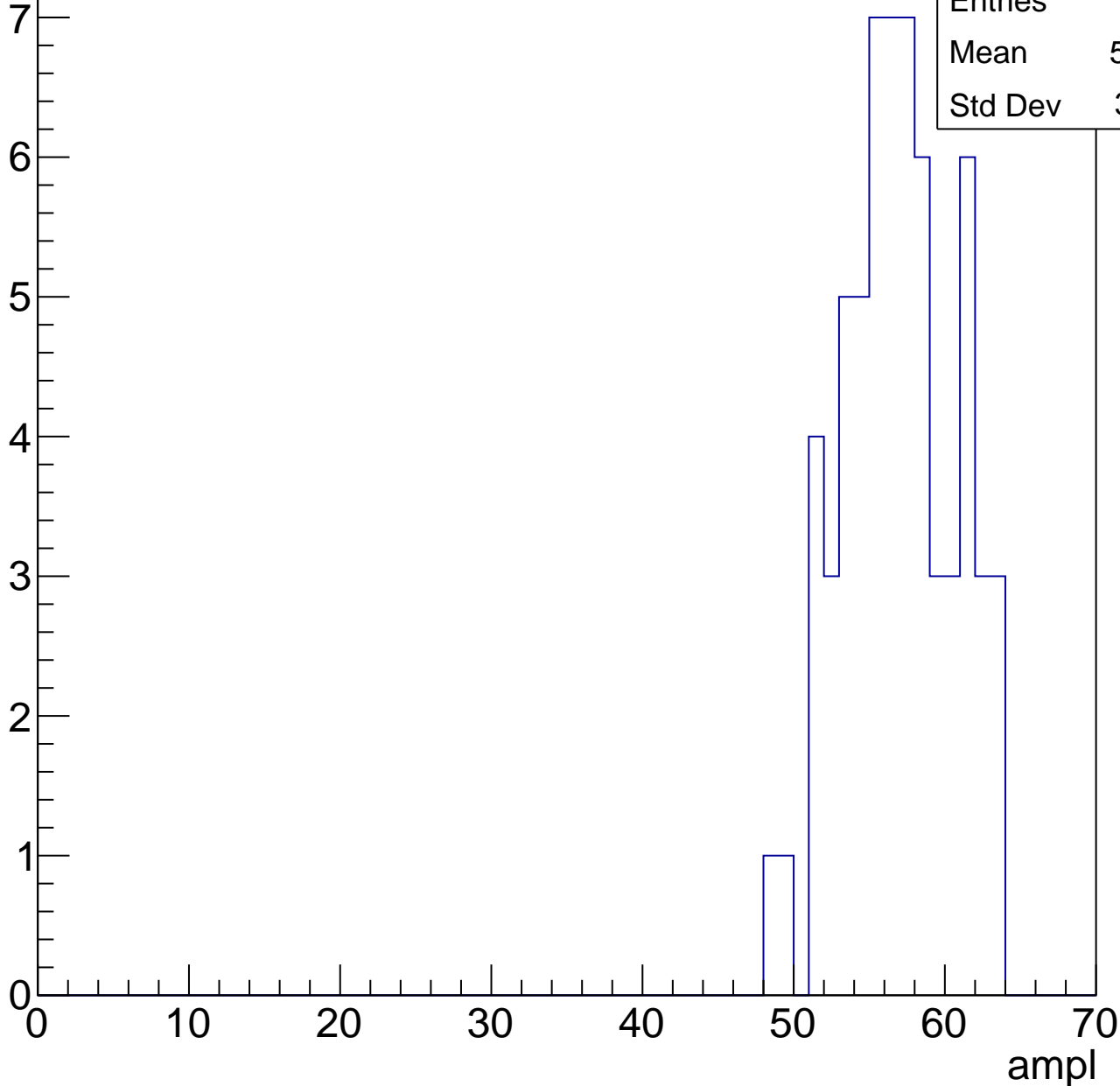


B1L102S, U10-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

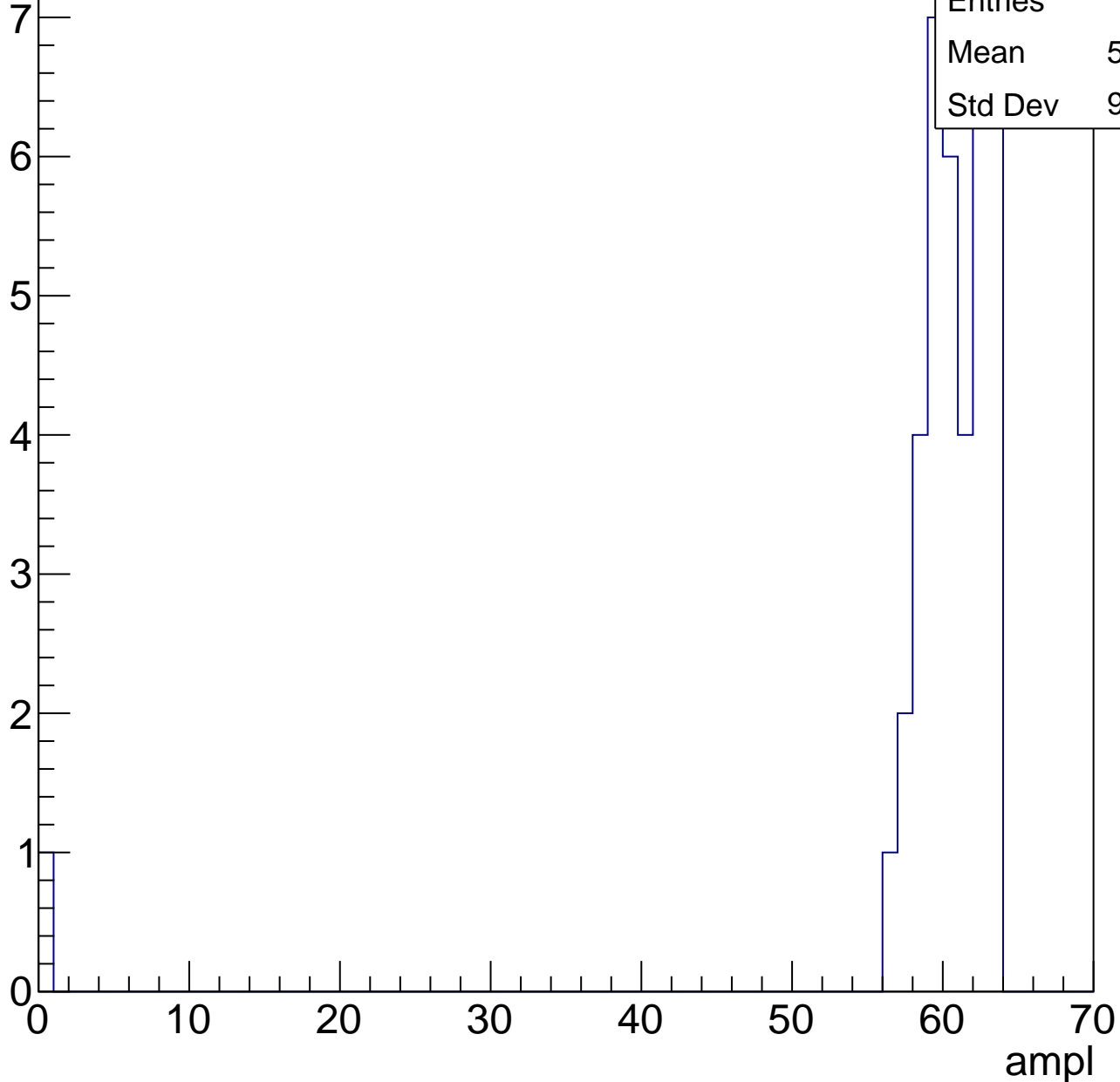
Entries	64
Mean	56.47
Std Dev	3.601



B1L102S, U10-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch100, adc0

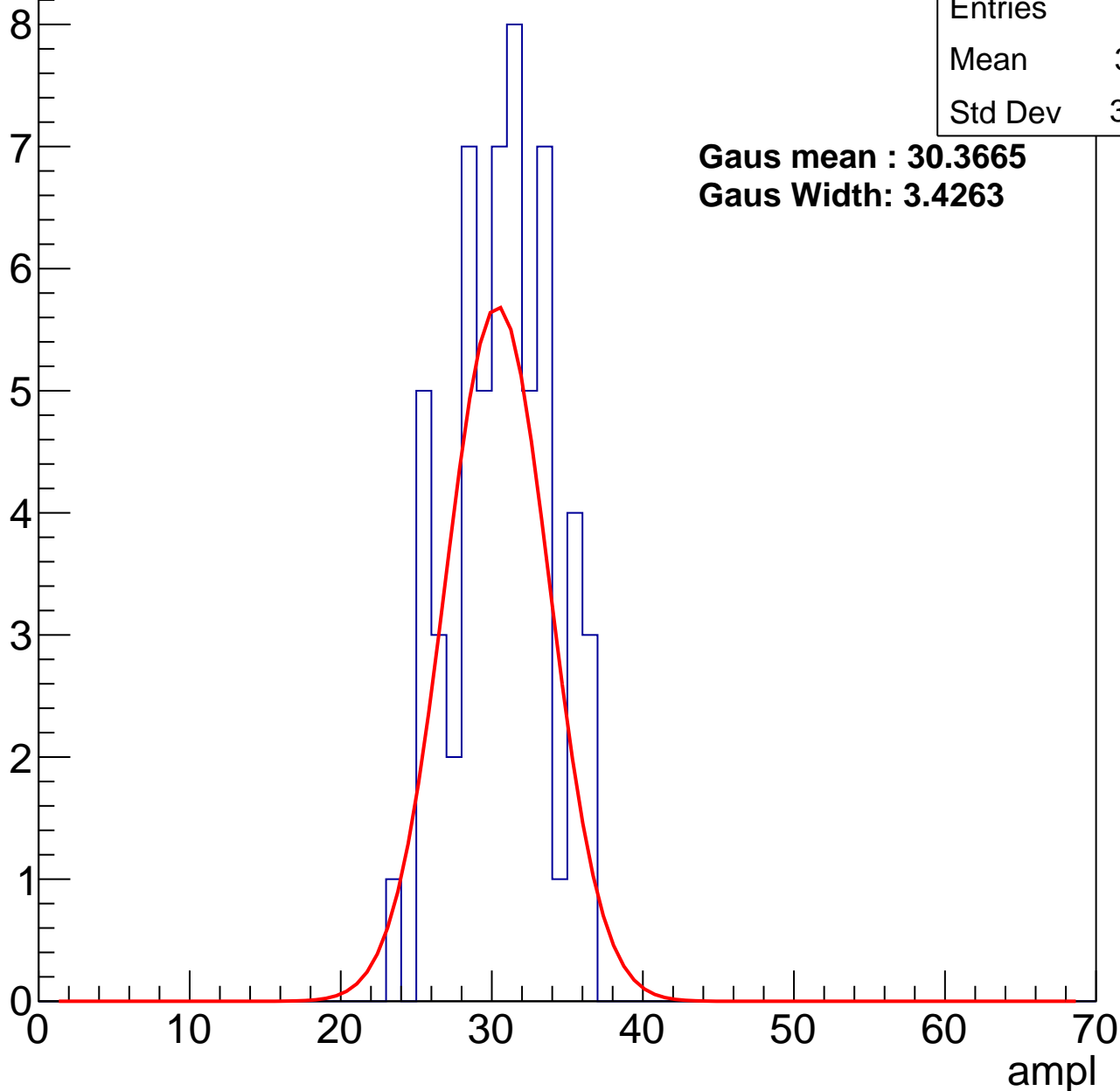
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	30.21
Std Dev	3.177

Gaus mean : 30.3665

Gaus Width: 3.4263



B1L102S, U10-ch100, adc1

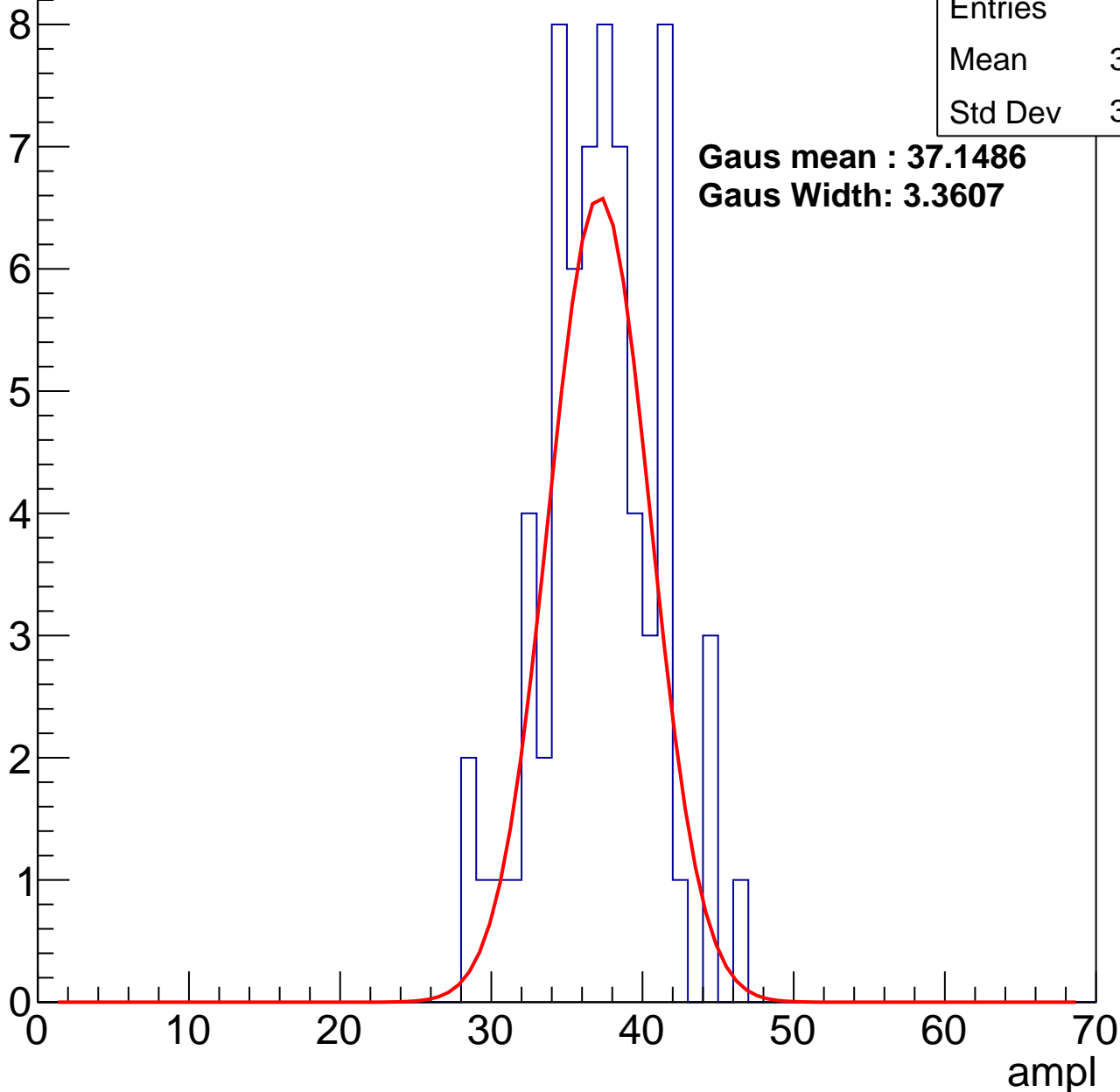
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.72
Std Dev	3.808

Gaus mean : 37.1486

Gaus Width: 3.3607



B1L102S, U10-ch100, adc2

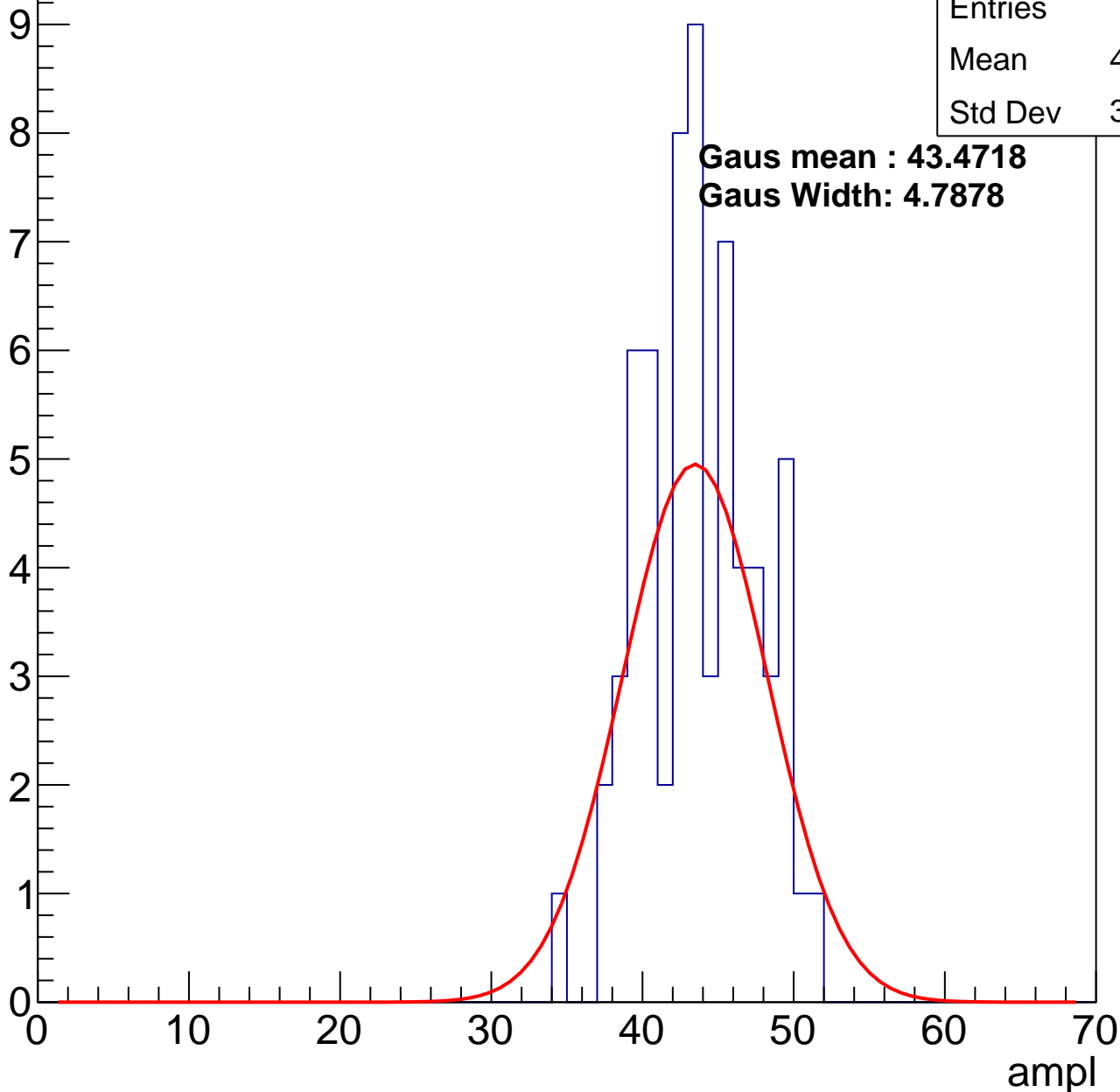
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.23
Std Dev	3.699

Gaus mean : 43.4718

Gaus Width: 4.7878

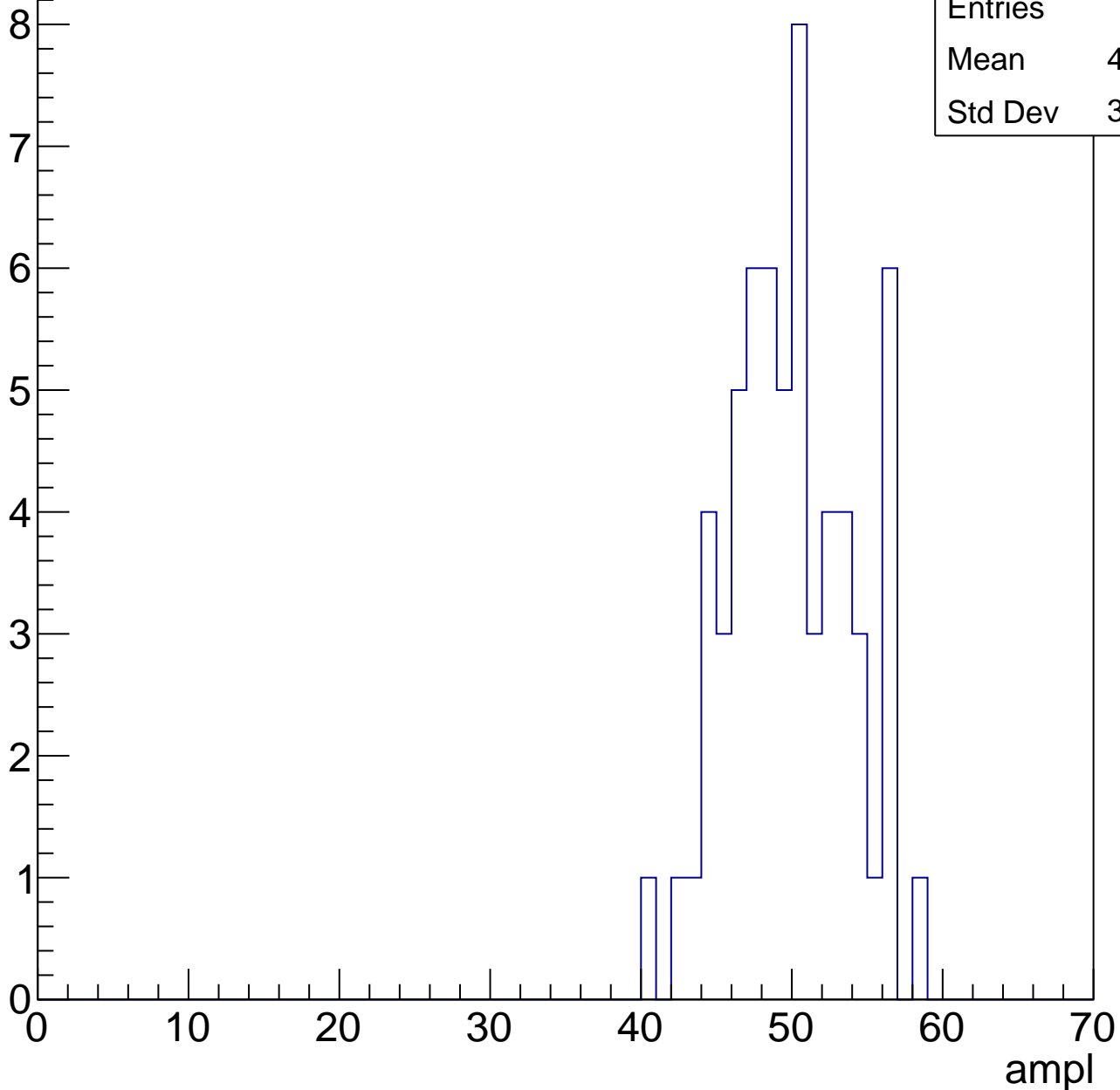


B1L102S, U10-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

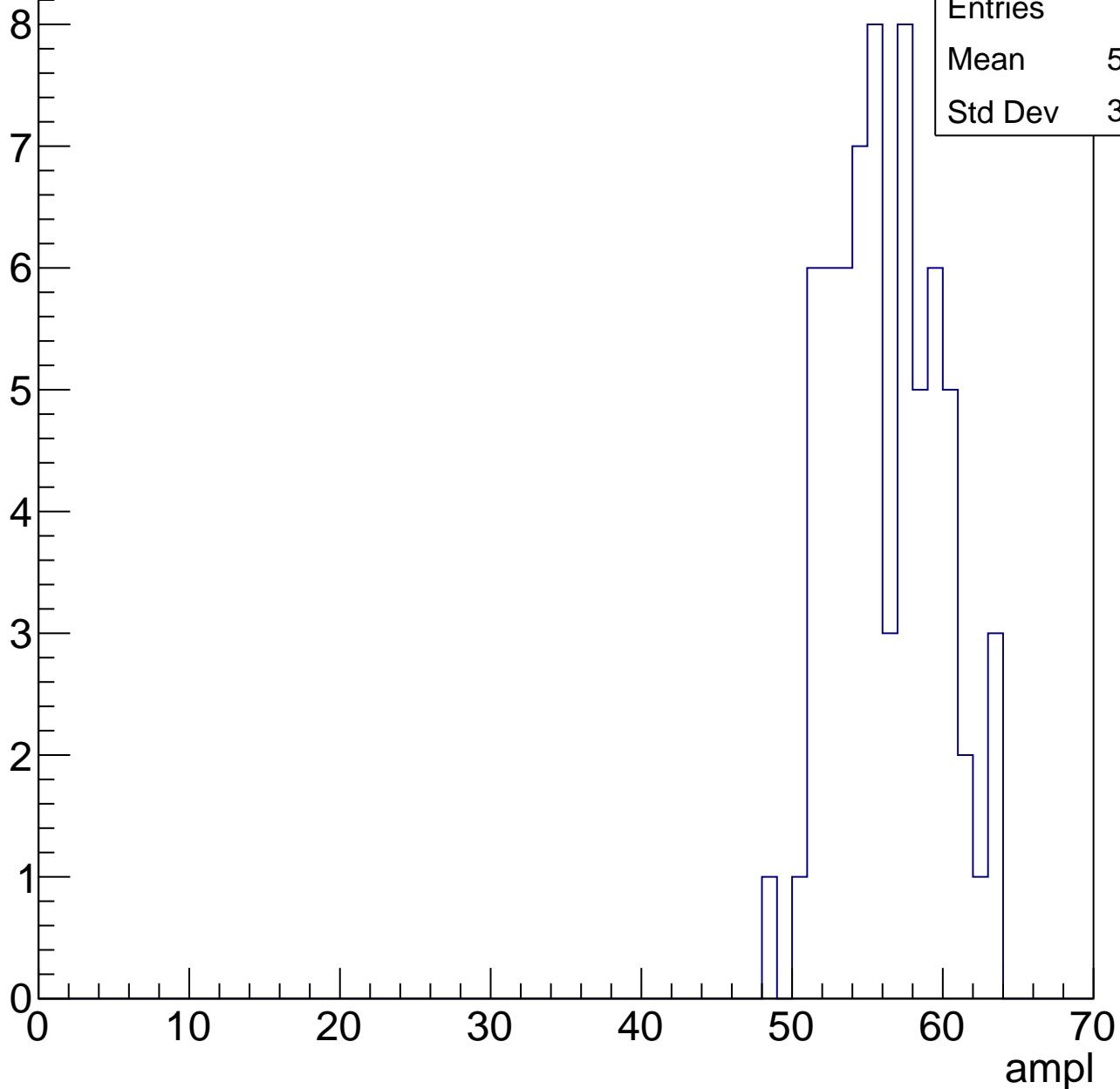
Entries	62
Mean	49.44
Std Dev	3.982



B1L102S, U10-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



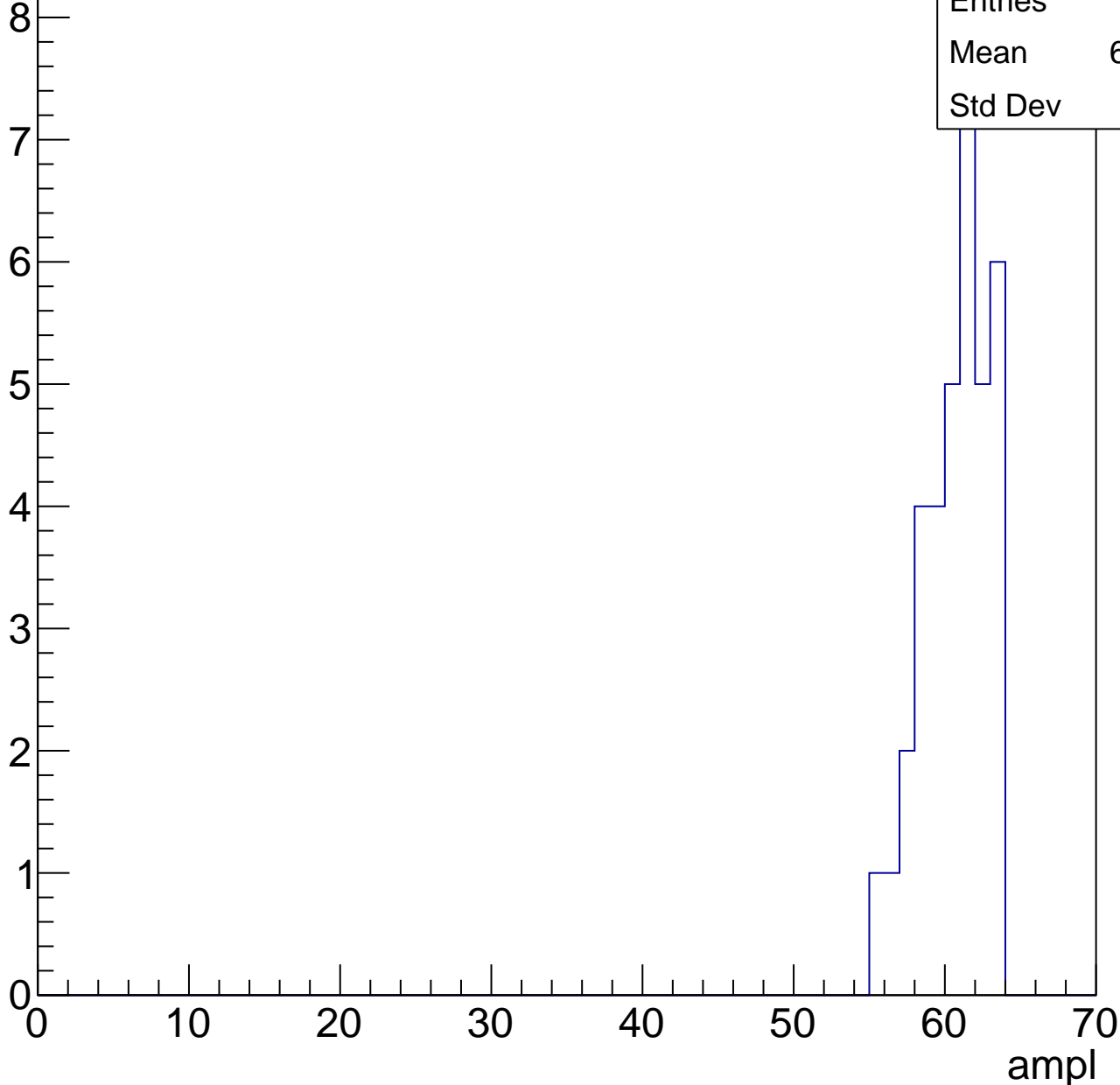
Entries	68
Mean	55.78
Std Dev	3.489

B1L102S, U10-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

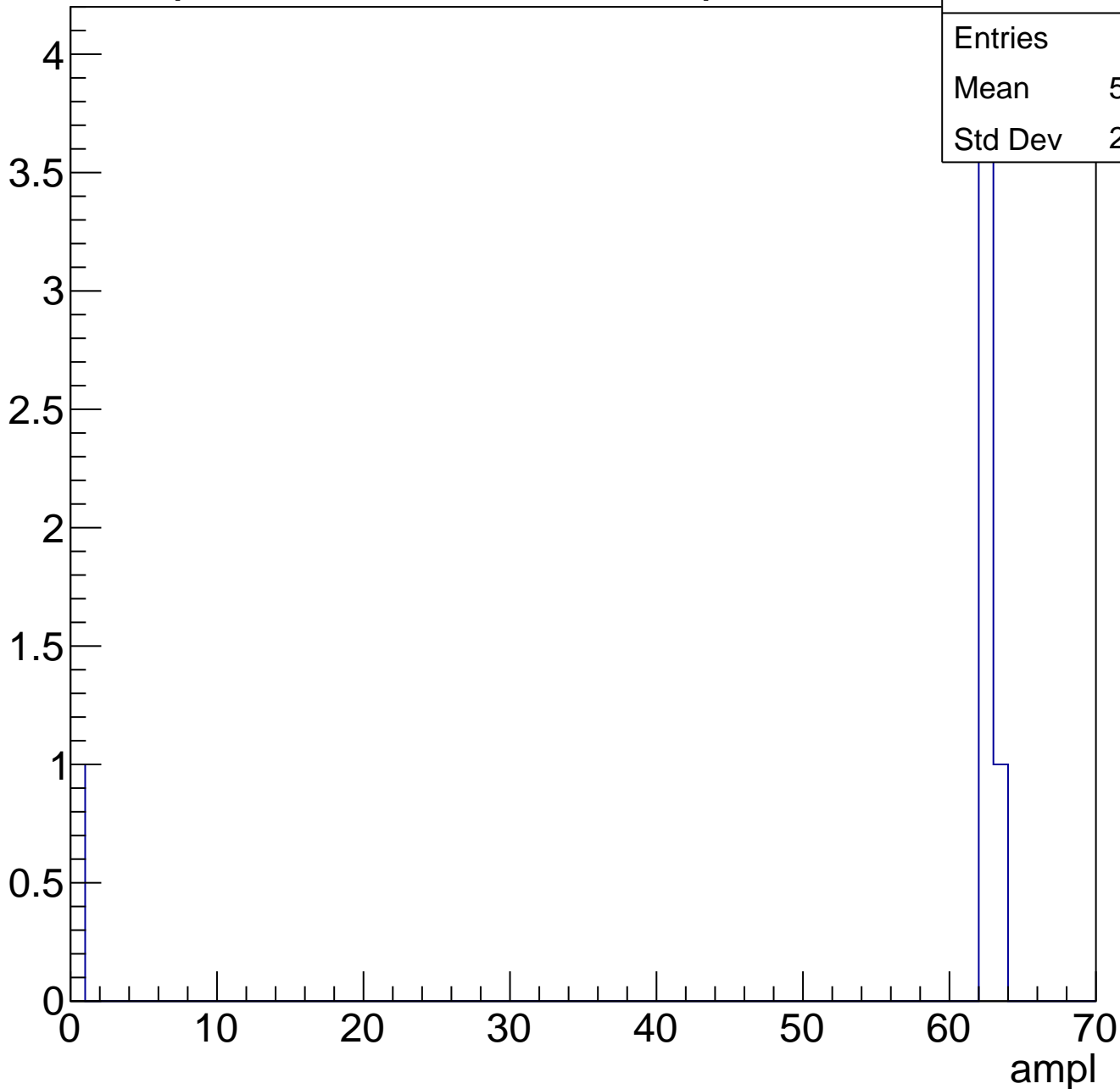
Entries	36
Mean	60.25
Std Dev	2.1



B1L102S, U10-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch101, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	28.76
Std Dev	4.938

Gaus mean : 29.9645

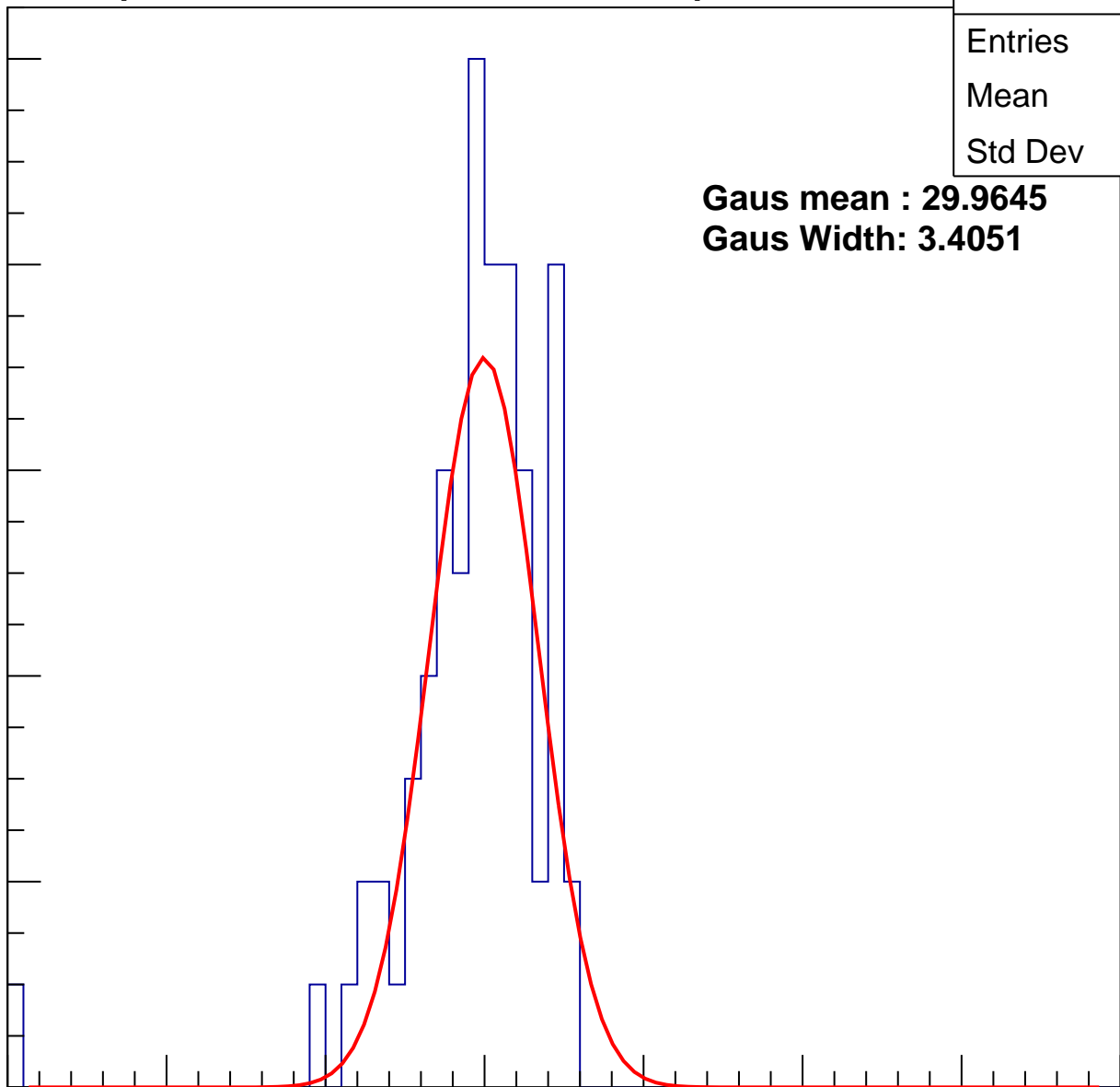
Gaus Width: 3.4051

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch101, adc1

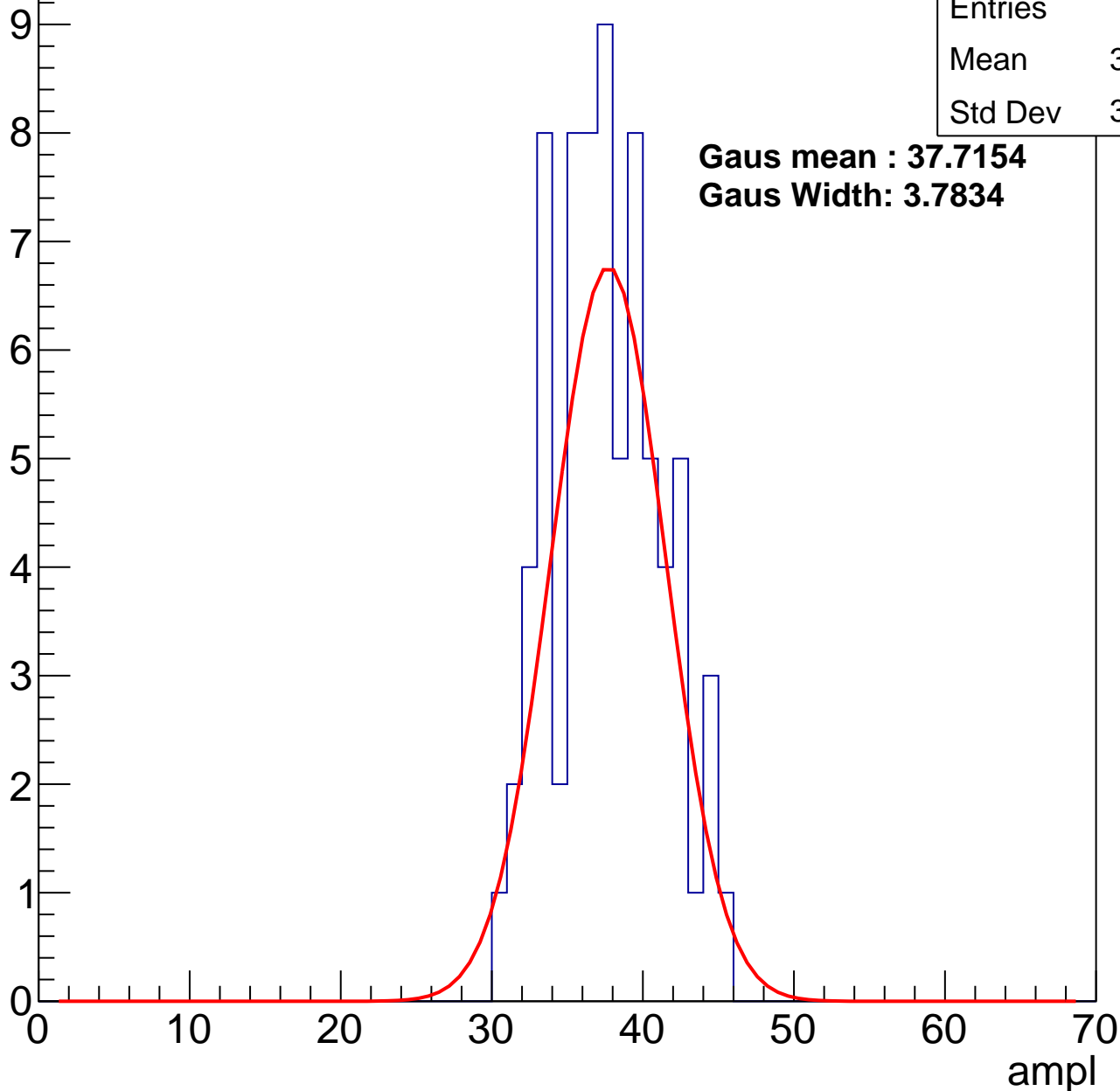
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.15
Std Dev	3.532

Gaus mean : 37.7154

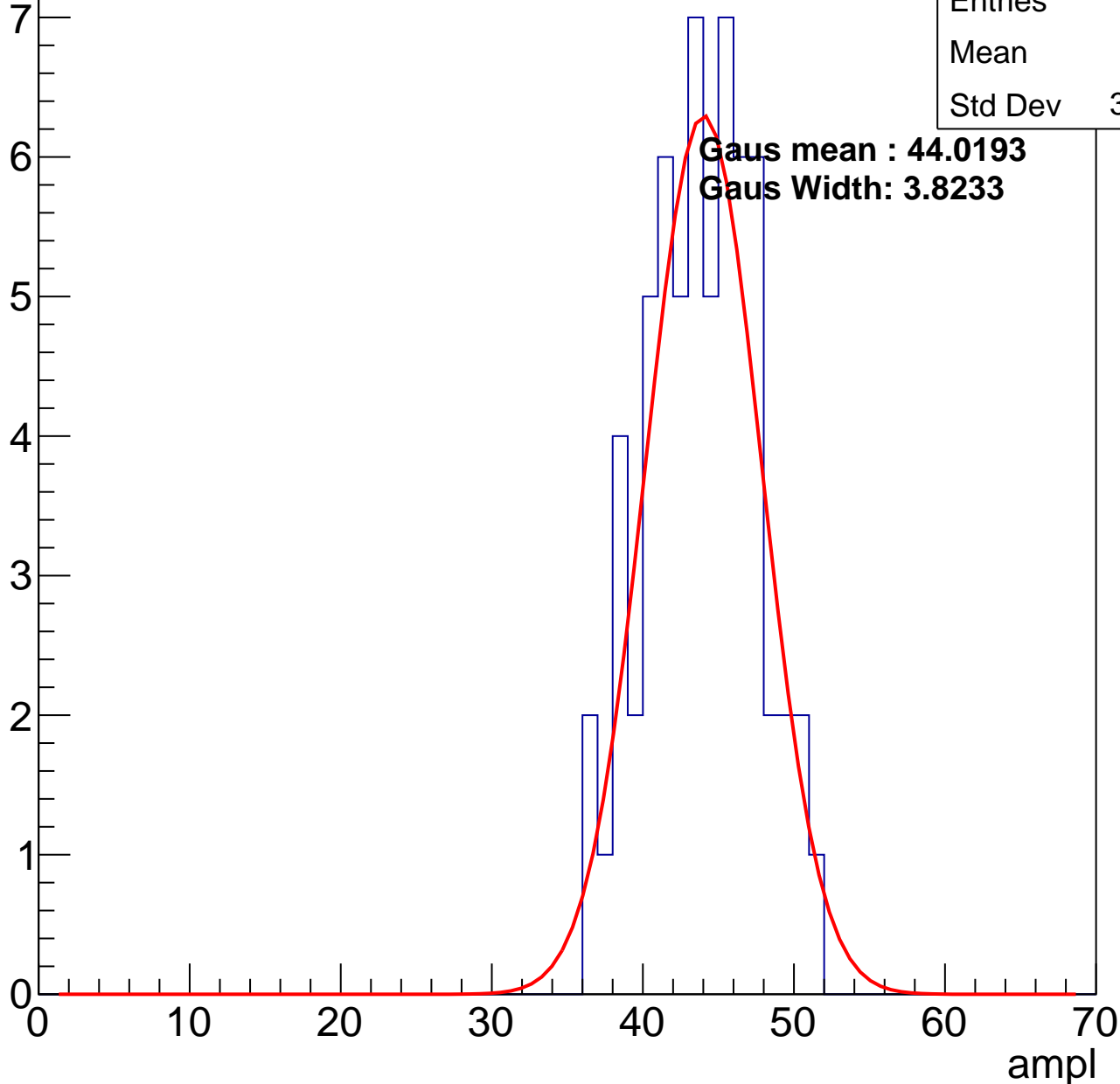
Gaus Width: 3.7834



B1L102S, U10-ch101, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

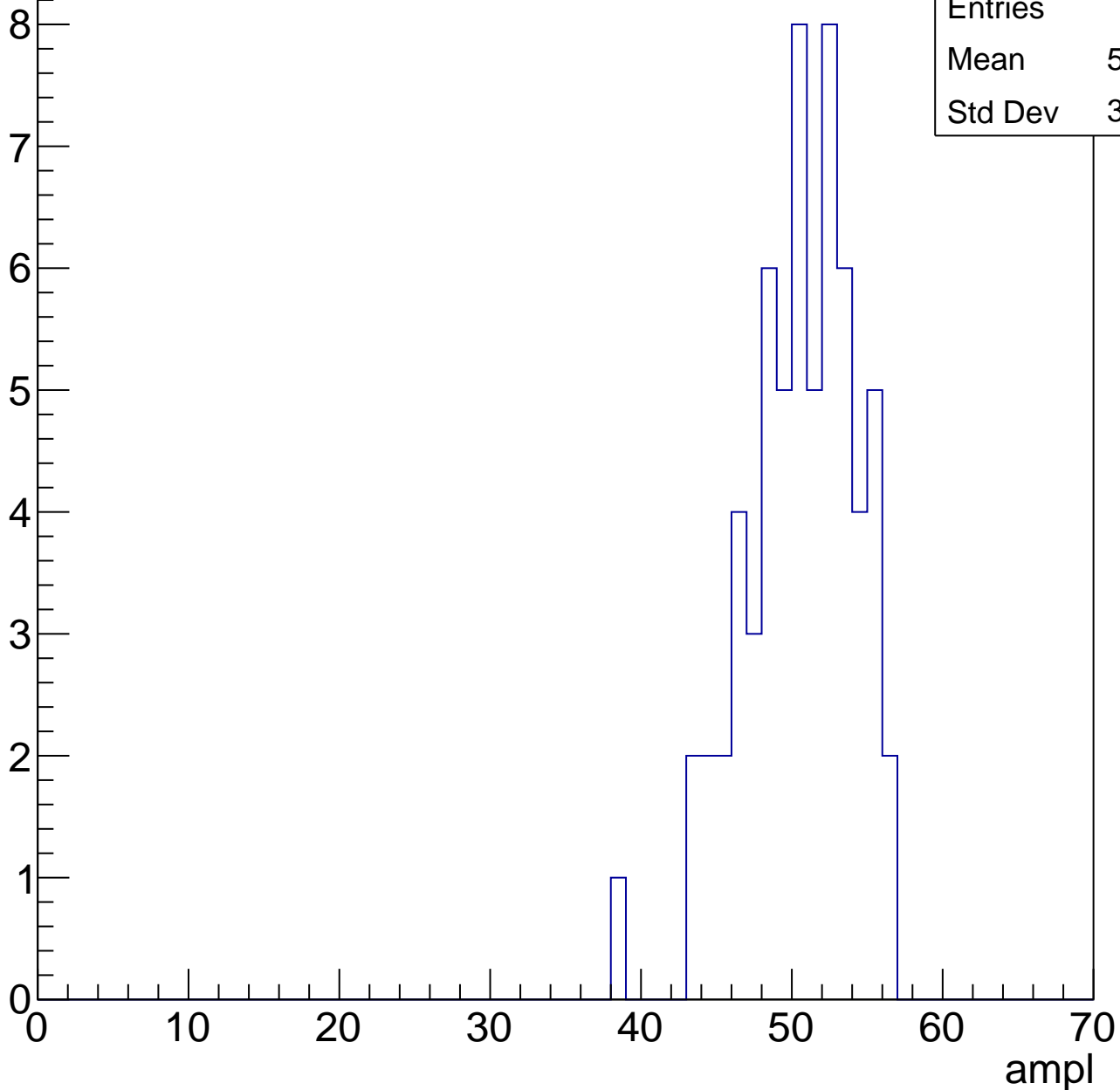


B1L102S, U10-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.03
Std Dev	3.643

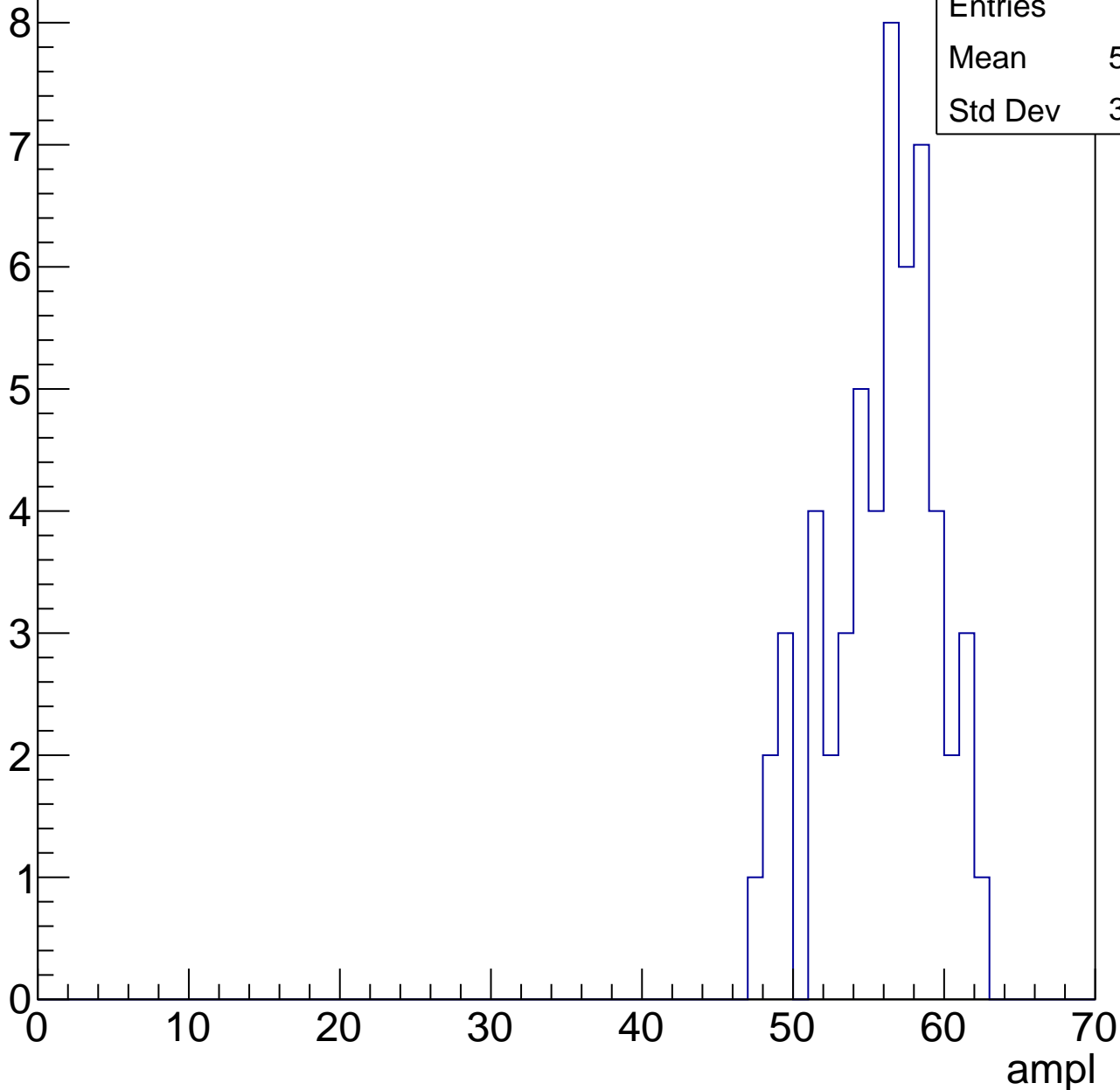


B1L102S, U10-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.35
Std Dev	3.639

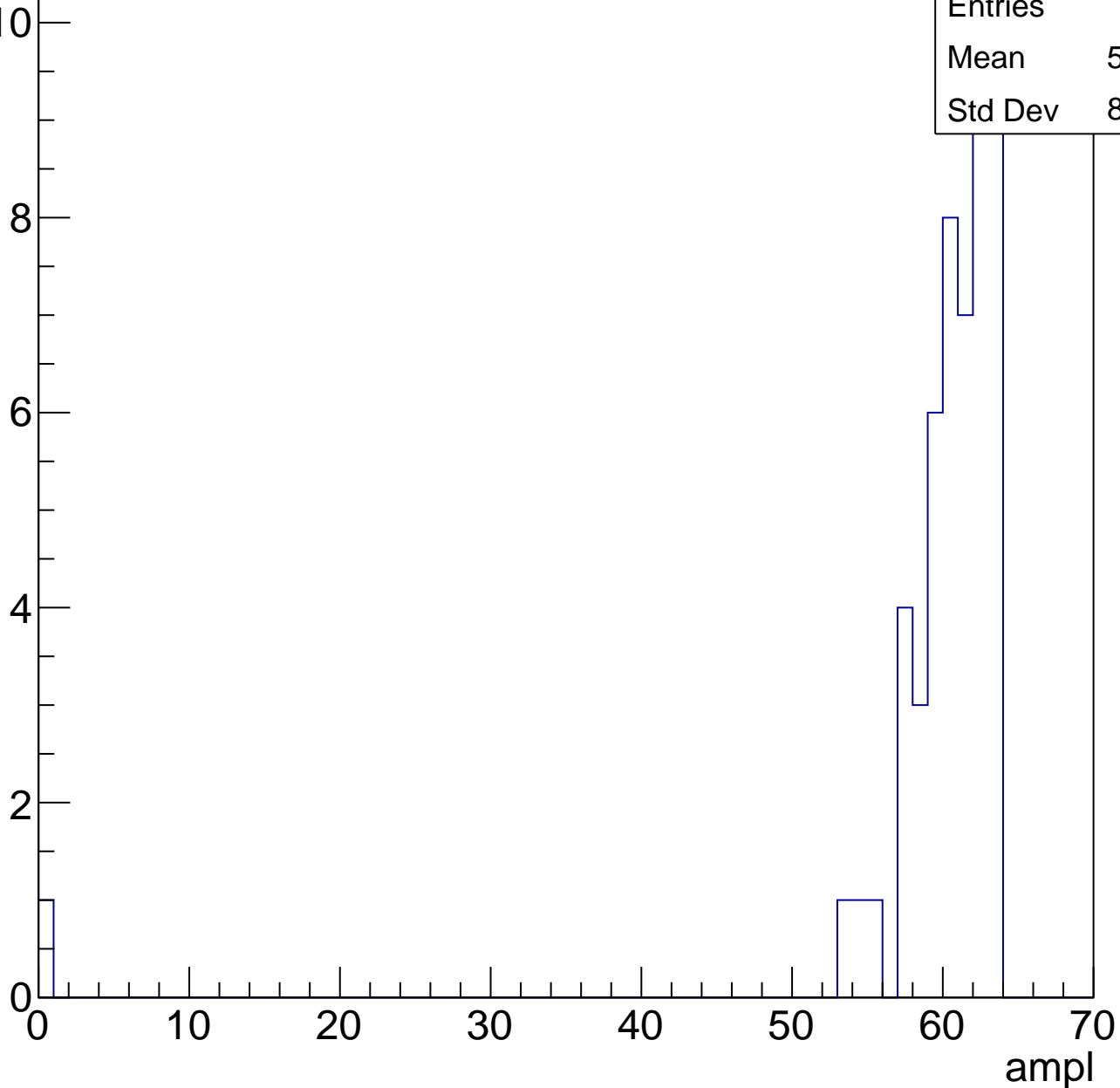


B1L102S, U10-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.08
Std Dev	8.693



B1L102S, U10-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

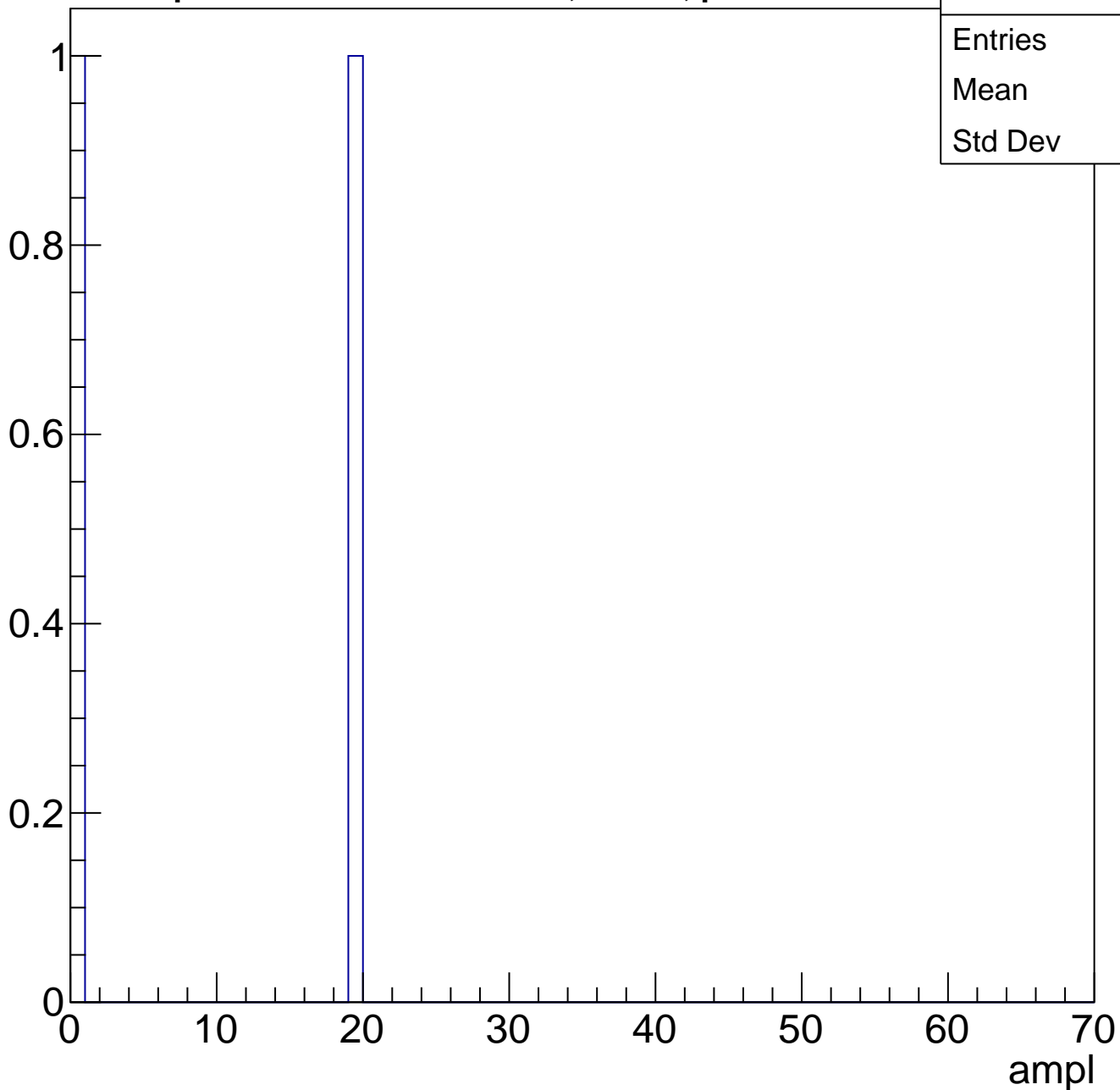
Std Dev

0

B1L102S, U10-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U10-ch102, adc0

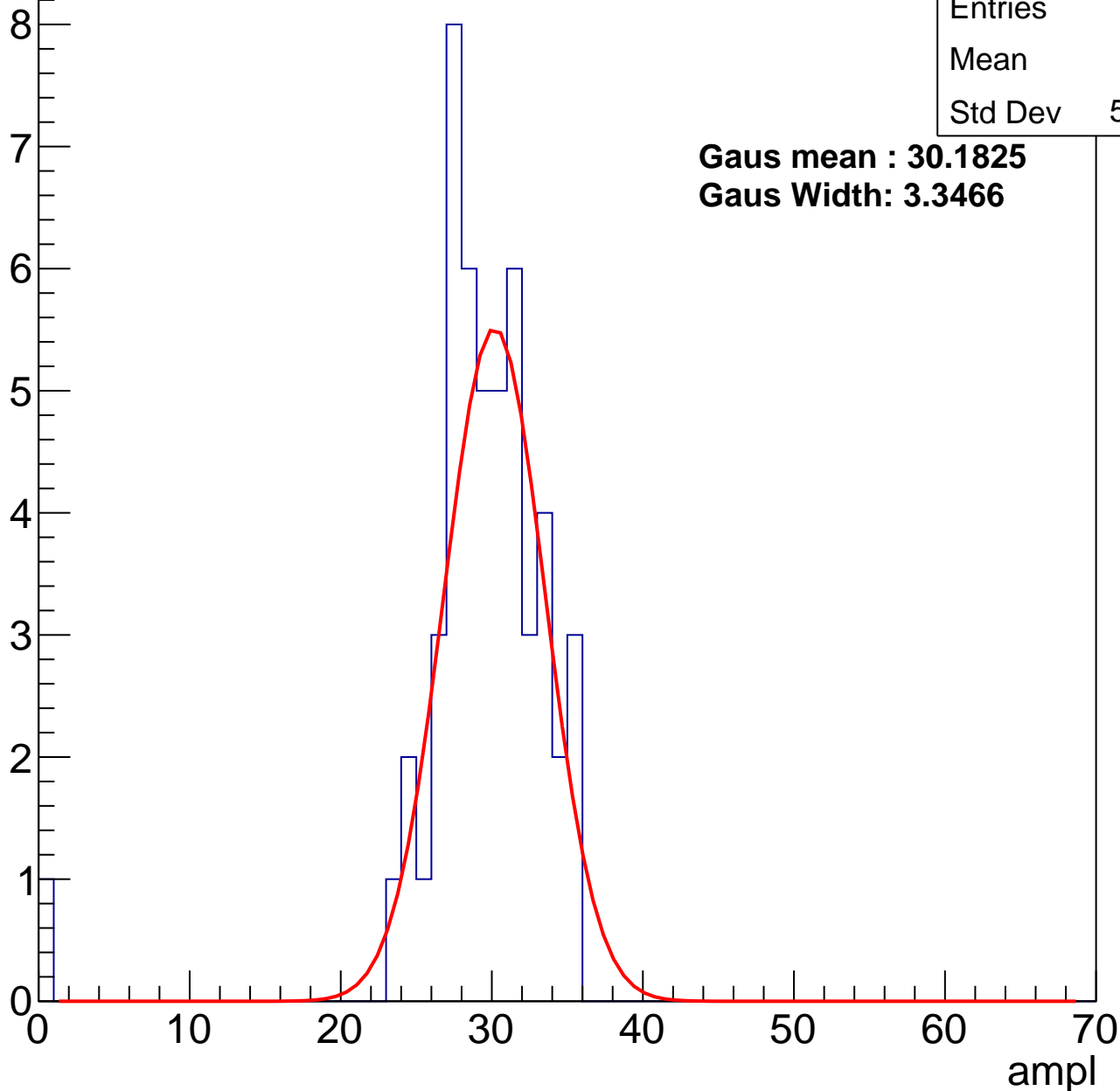
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	28.8
Std Dev	5.068

Gaus mean : 30.1825

Gaus Width: 3.3466



B1L102S, U10-ch102, adc1

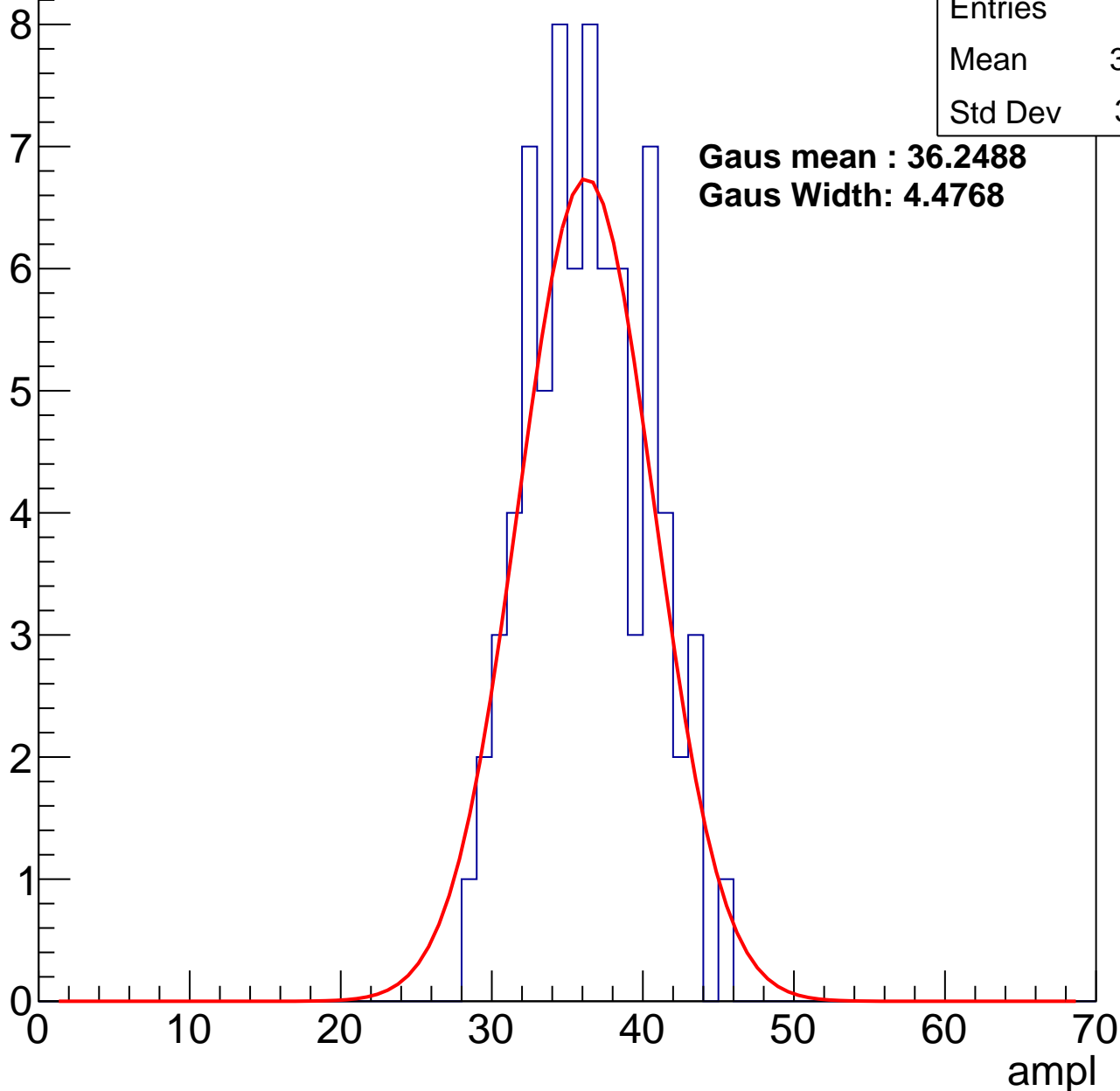
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.89
Std Dev	3.861

Gaus mean : 36.2488

Gaus Width: 4.4768



B1L102S, U10-ch102, adc2

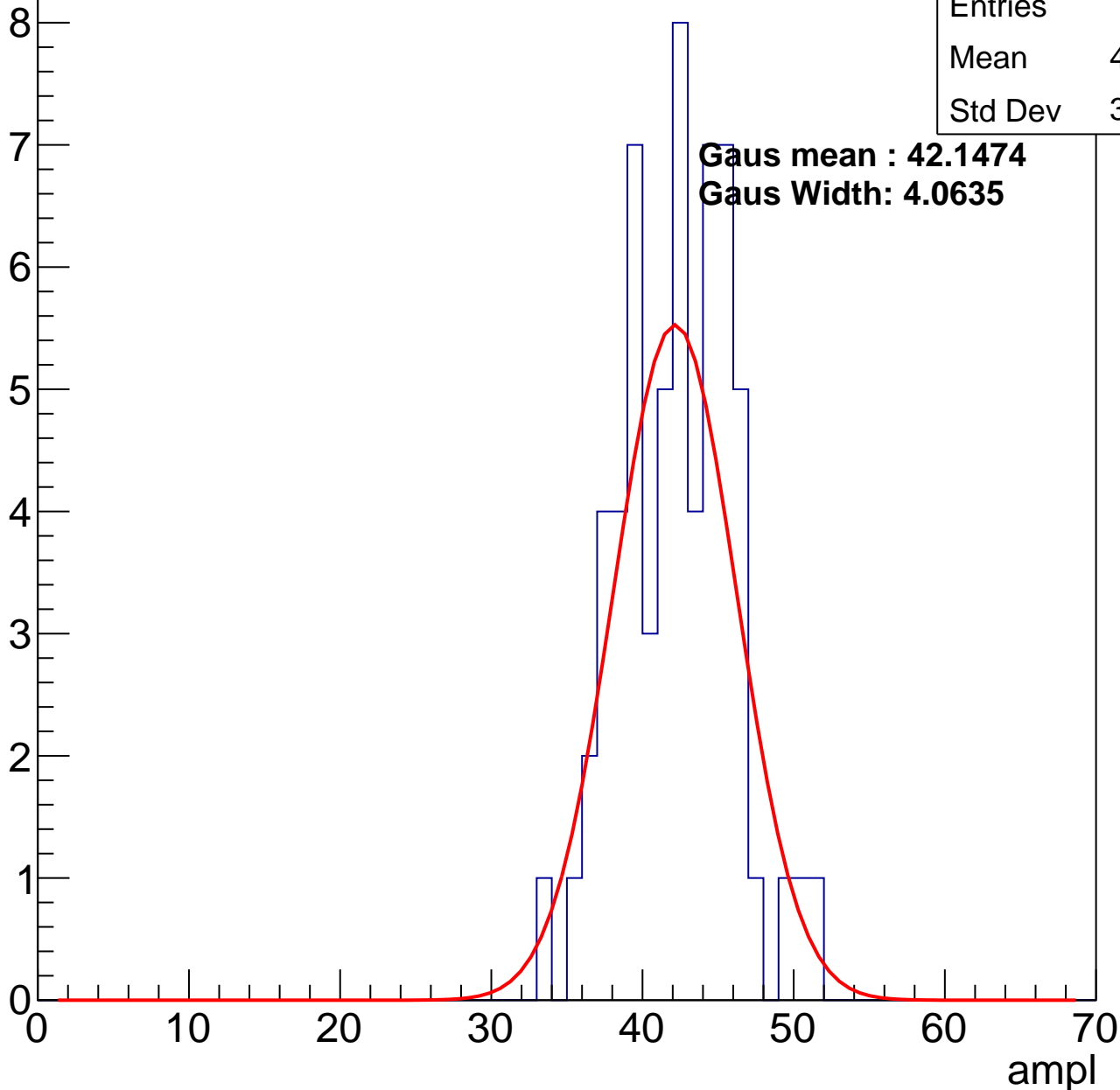
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.87
Std Dev	3.679

Gaus mean : 42.1474

Gaus Width: 4.0635

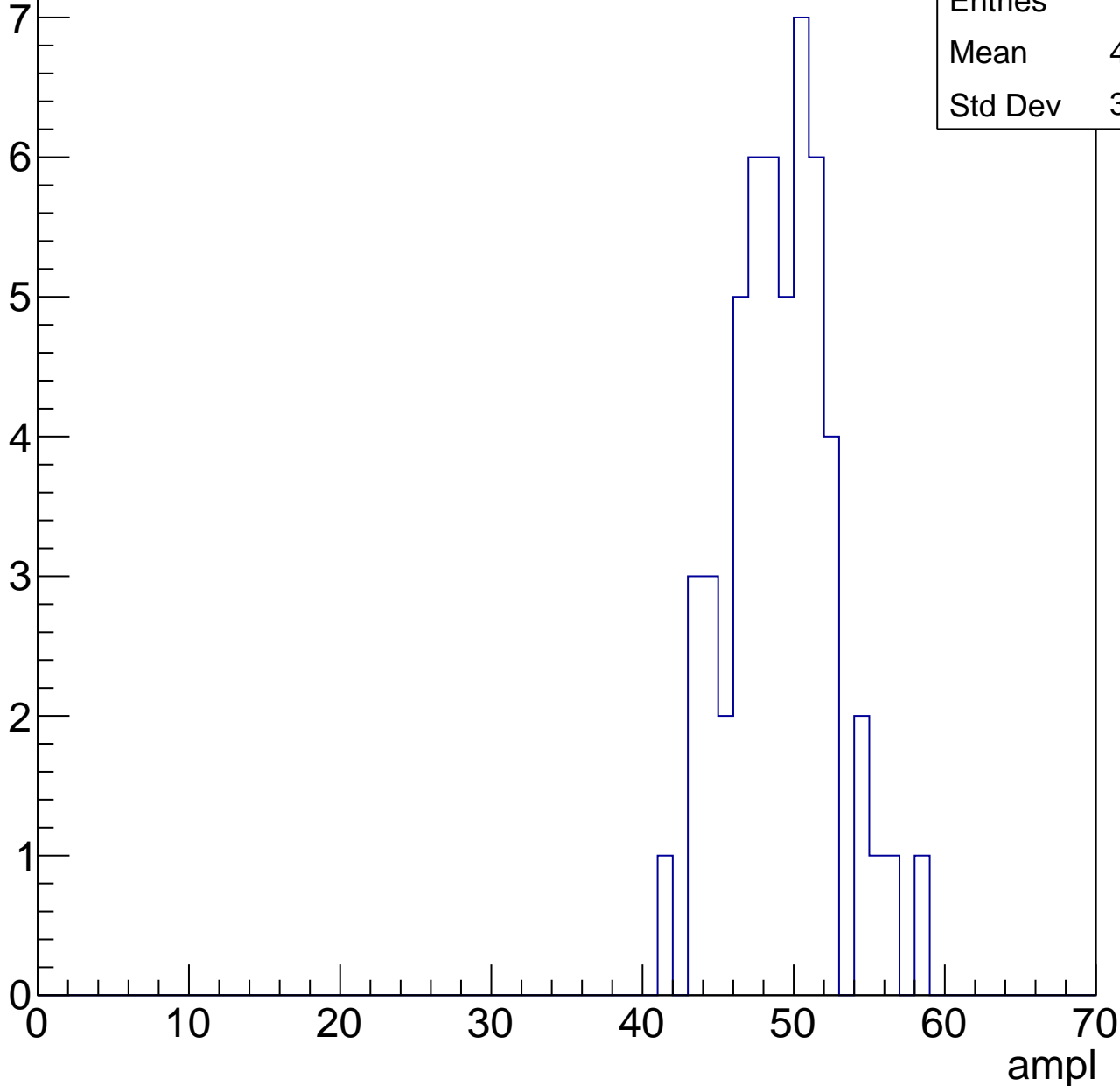


B1L102S, U10-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.64
Std Dev	3.448

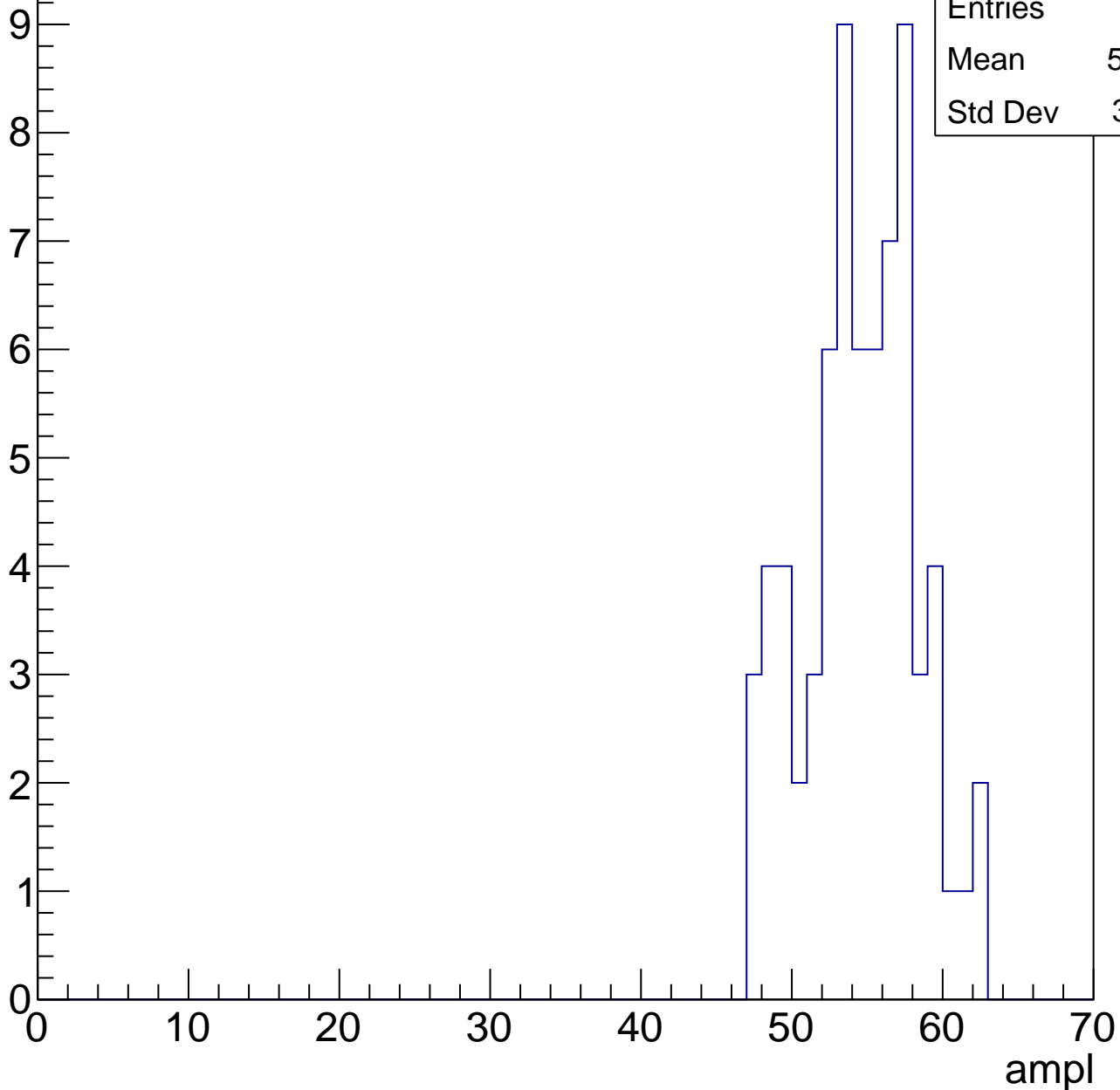


B1L102S, U10-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.07
Std Dev	3.701

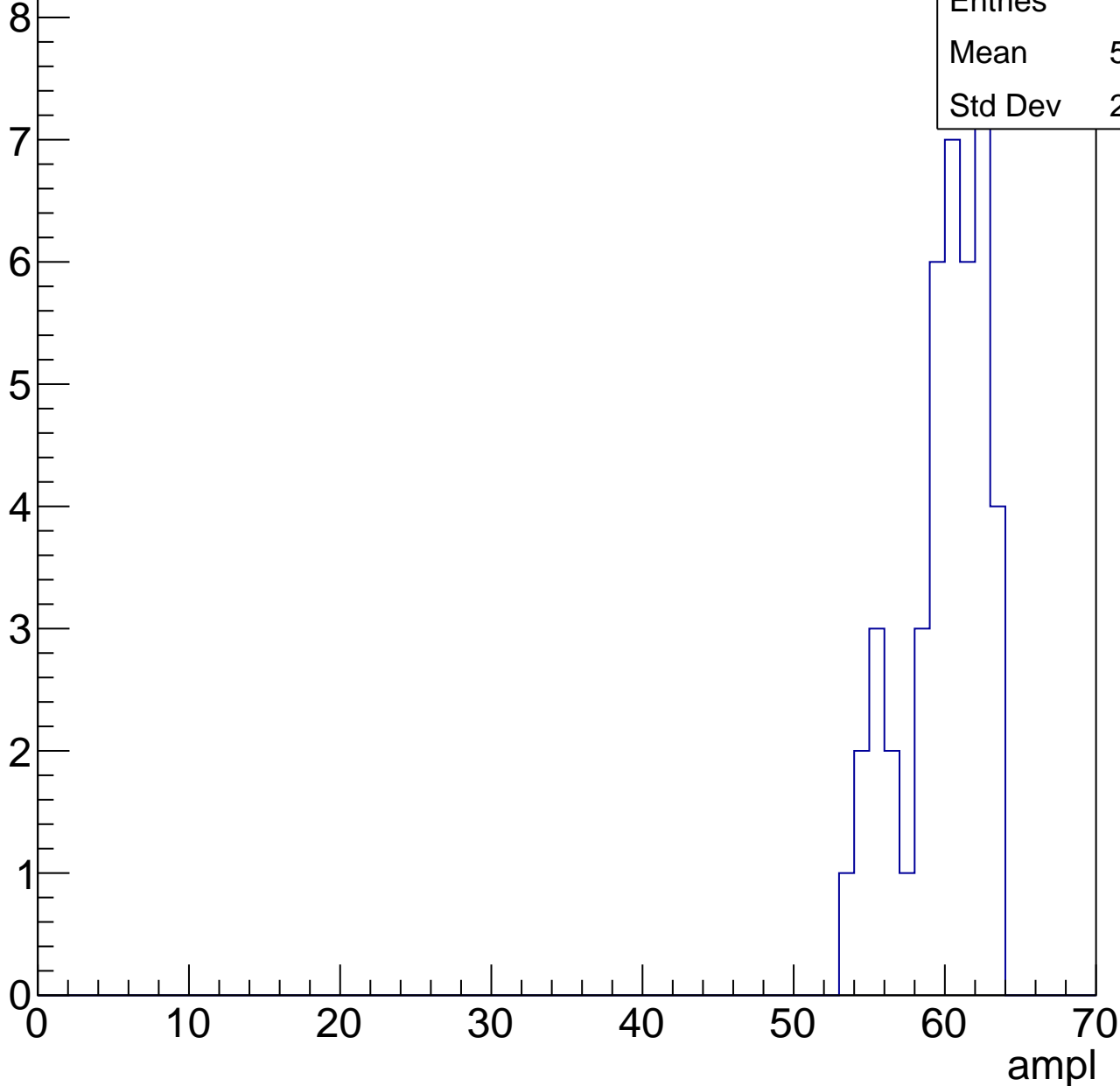


B1L102S, U10-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.47
Std Dev	2.714

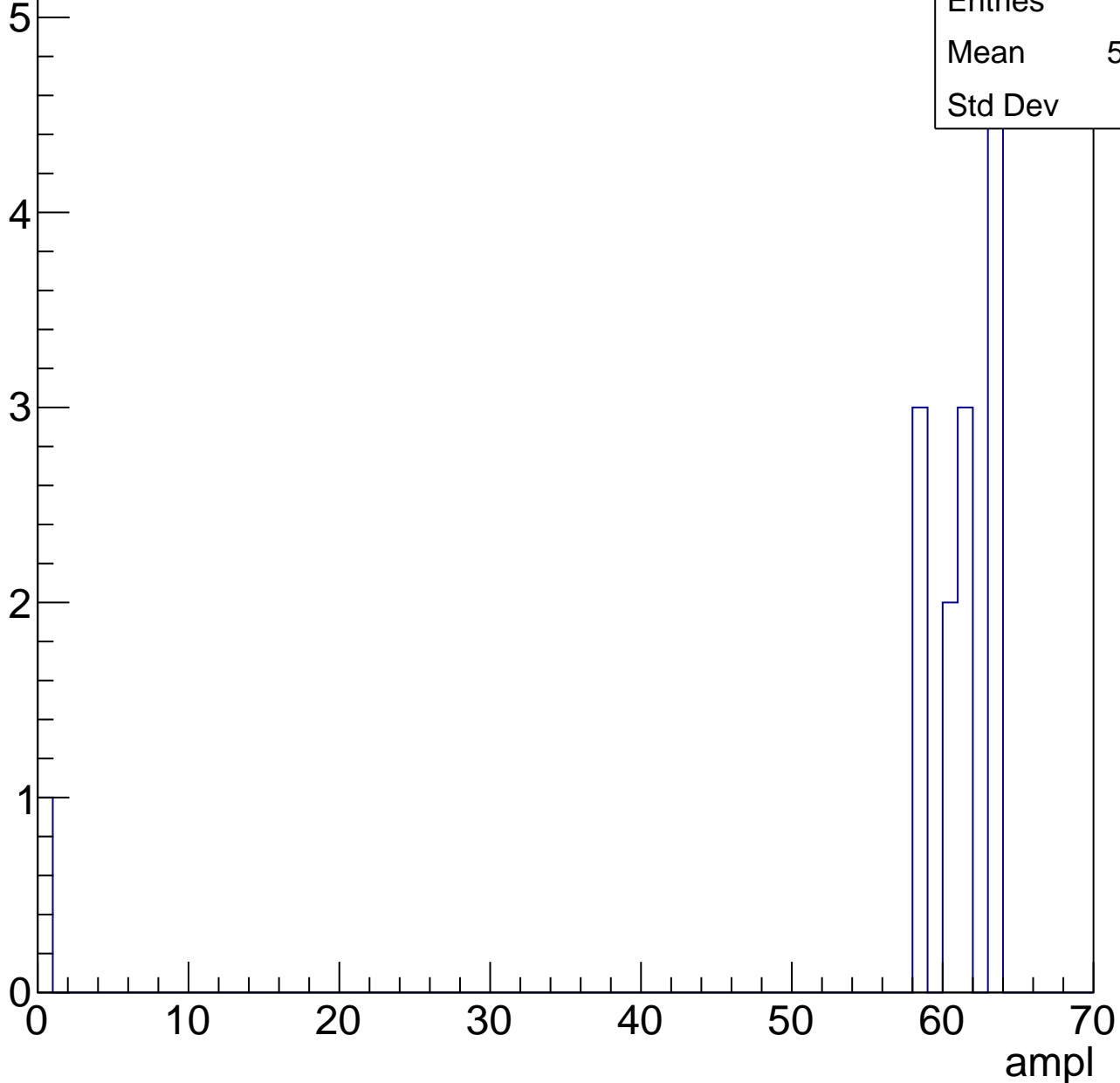


B1L102S, U10-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

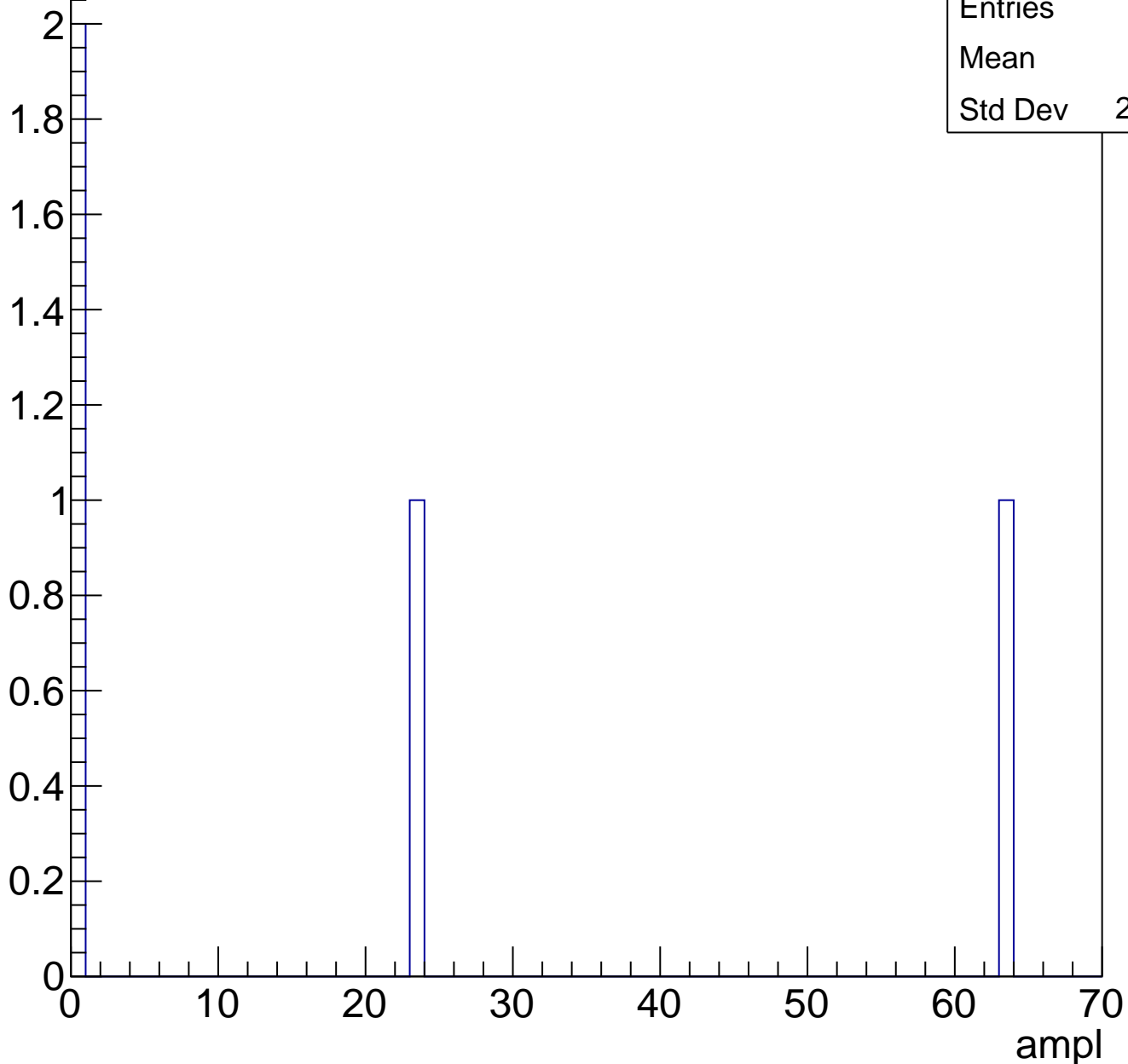
Entries	14
Mean	56.57
Std Dev	15.8



B1L102S, U10-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	21.5
Std Dev	25.73

B1L102S, U10-ch103, adc0

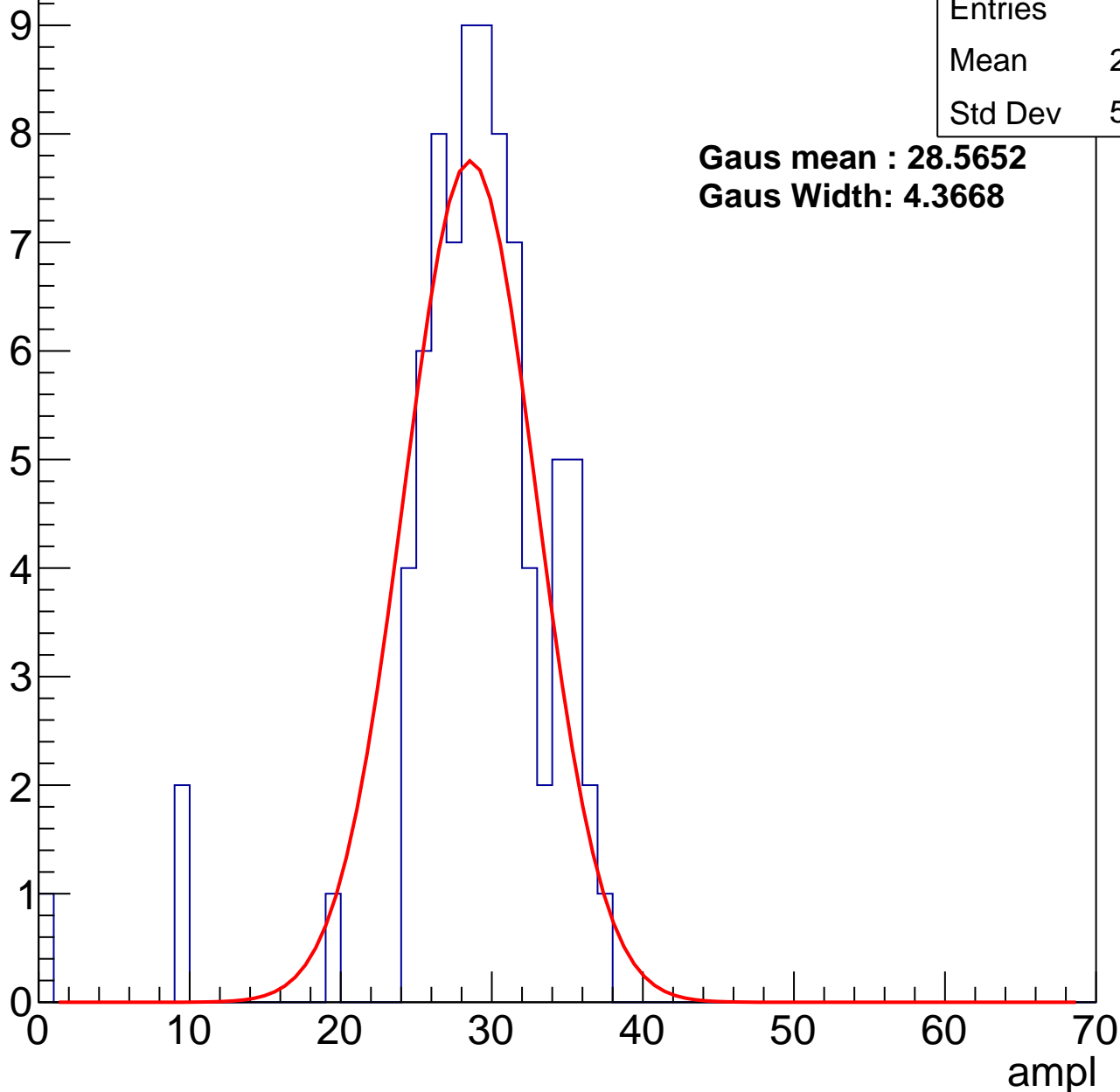
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.37
Std Dev	5.649

Gaus mean : 28.5652

Gaus Width: 4.3668



B1L102S, U10-ch103, adc1

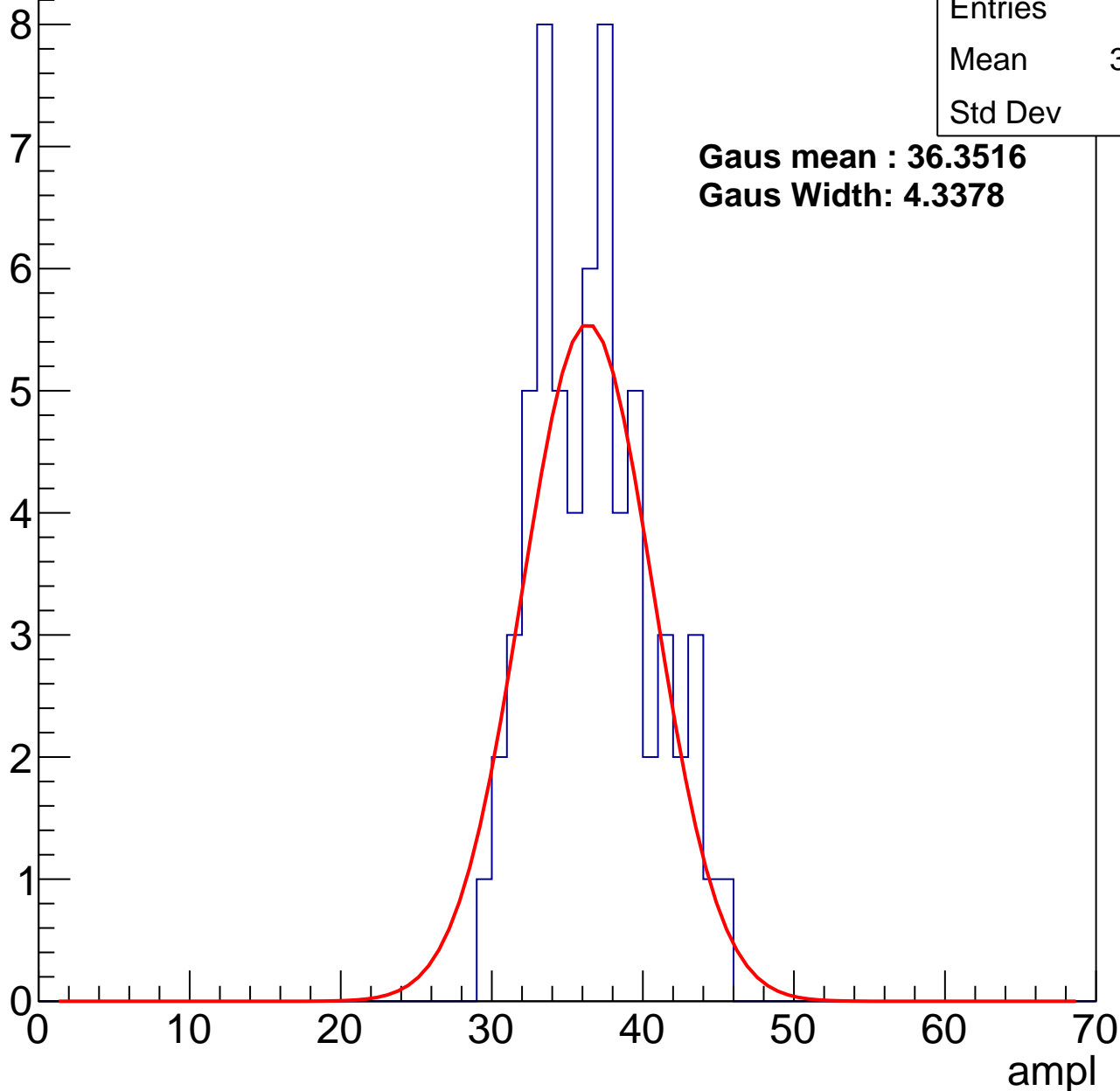
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.19
Std Dev	3.8

Gaus mean : 36.3516

Gaus Width: 4.3378



B1L102S, U10-ch103, adc2

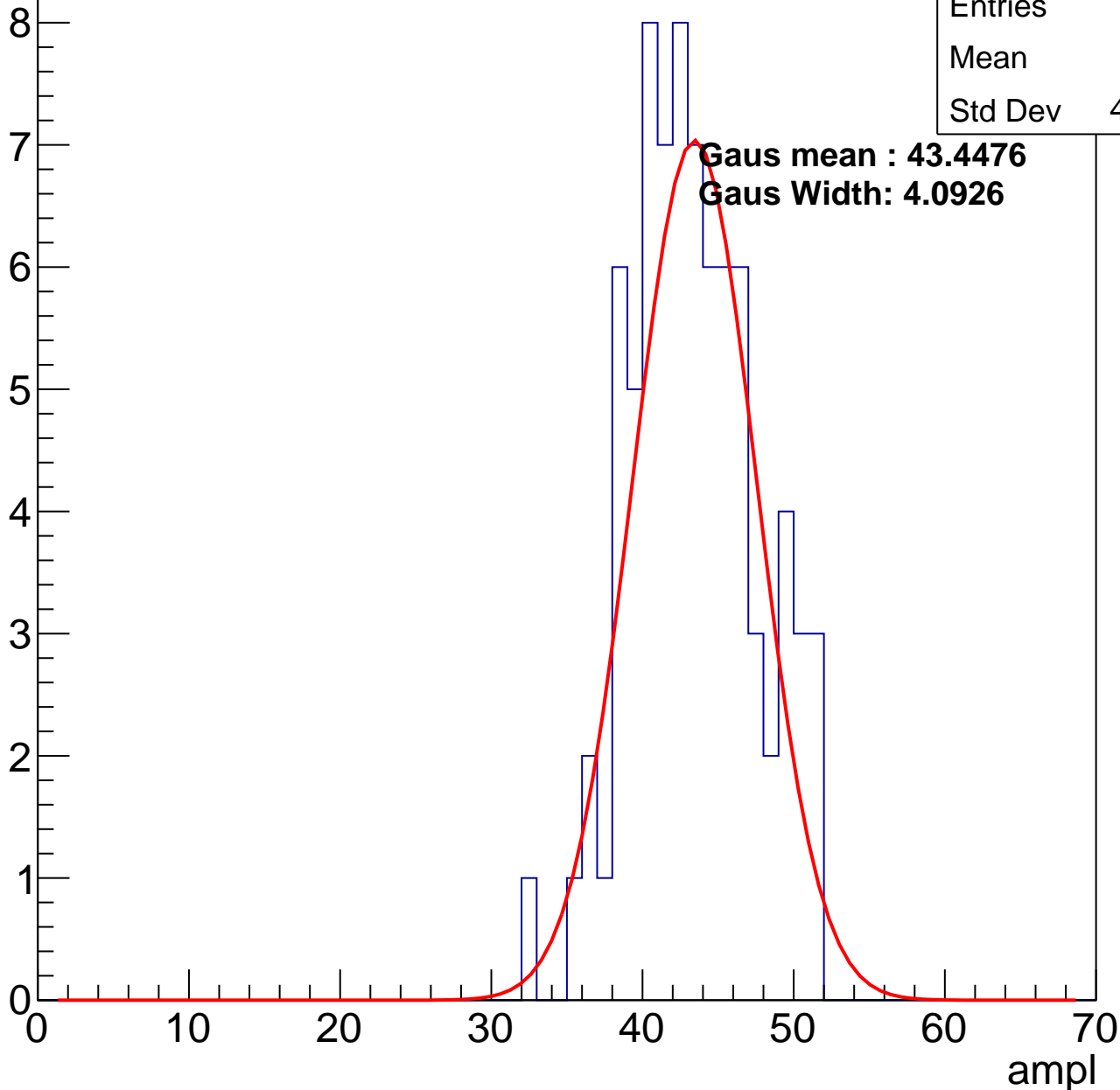
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	42.9
Std Dev	4.089

Gaus mean : 43.4476

Gaus Width: 4.0926

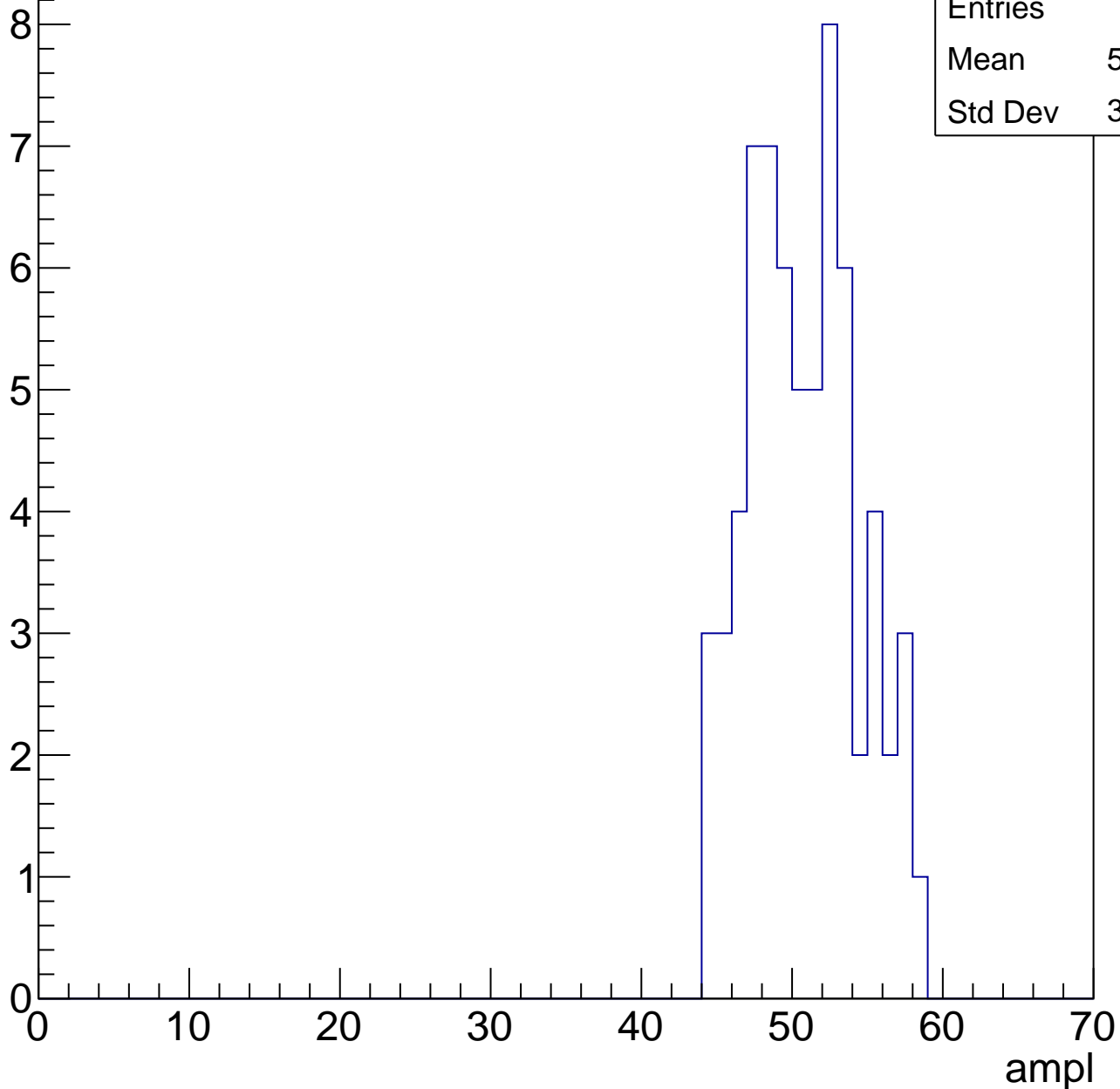


B1L102S, U10-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.27
Std Dev	3.578

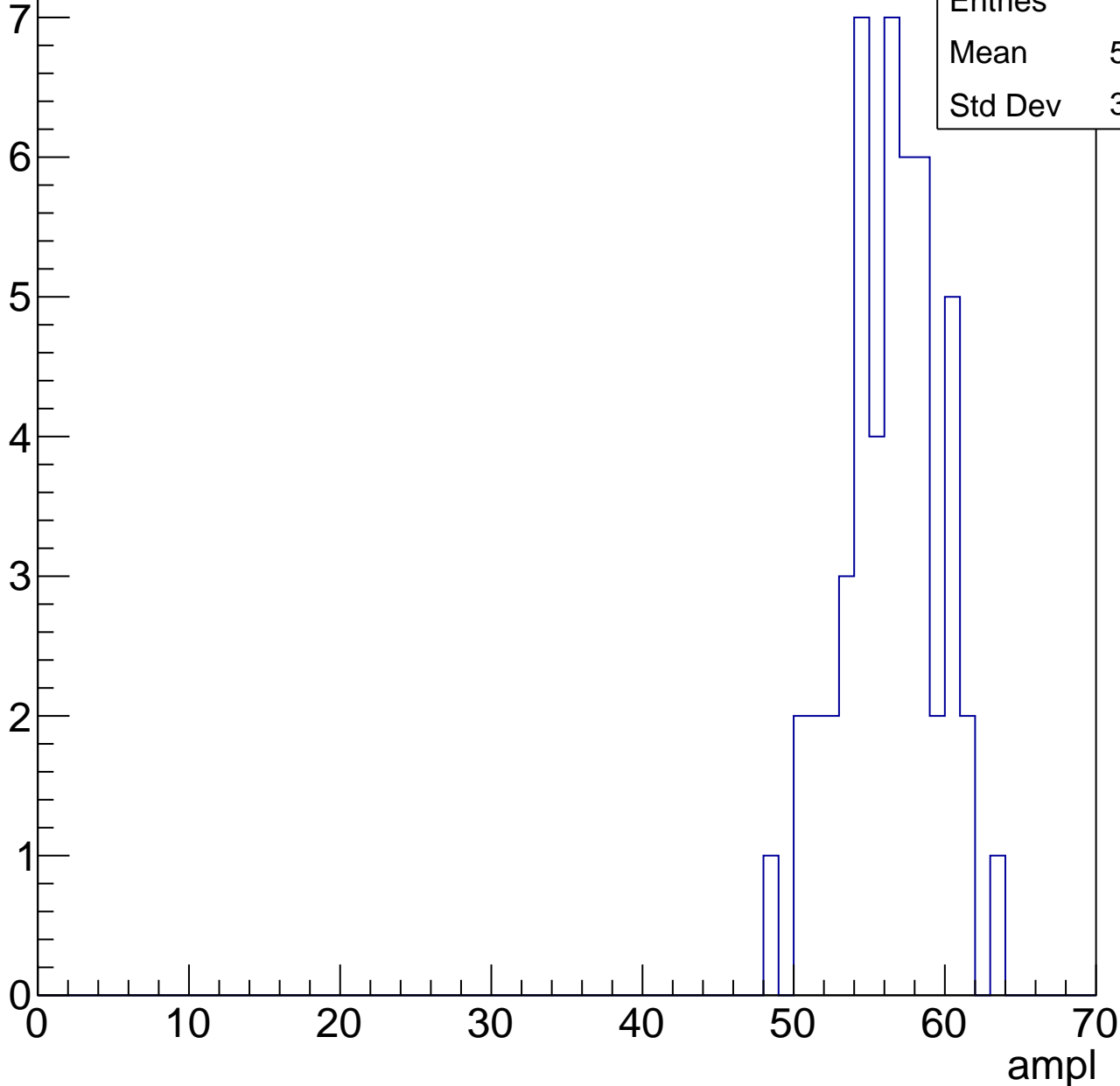


B1L102S, U10-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

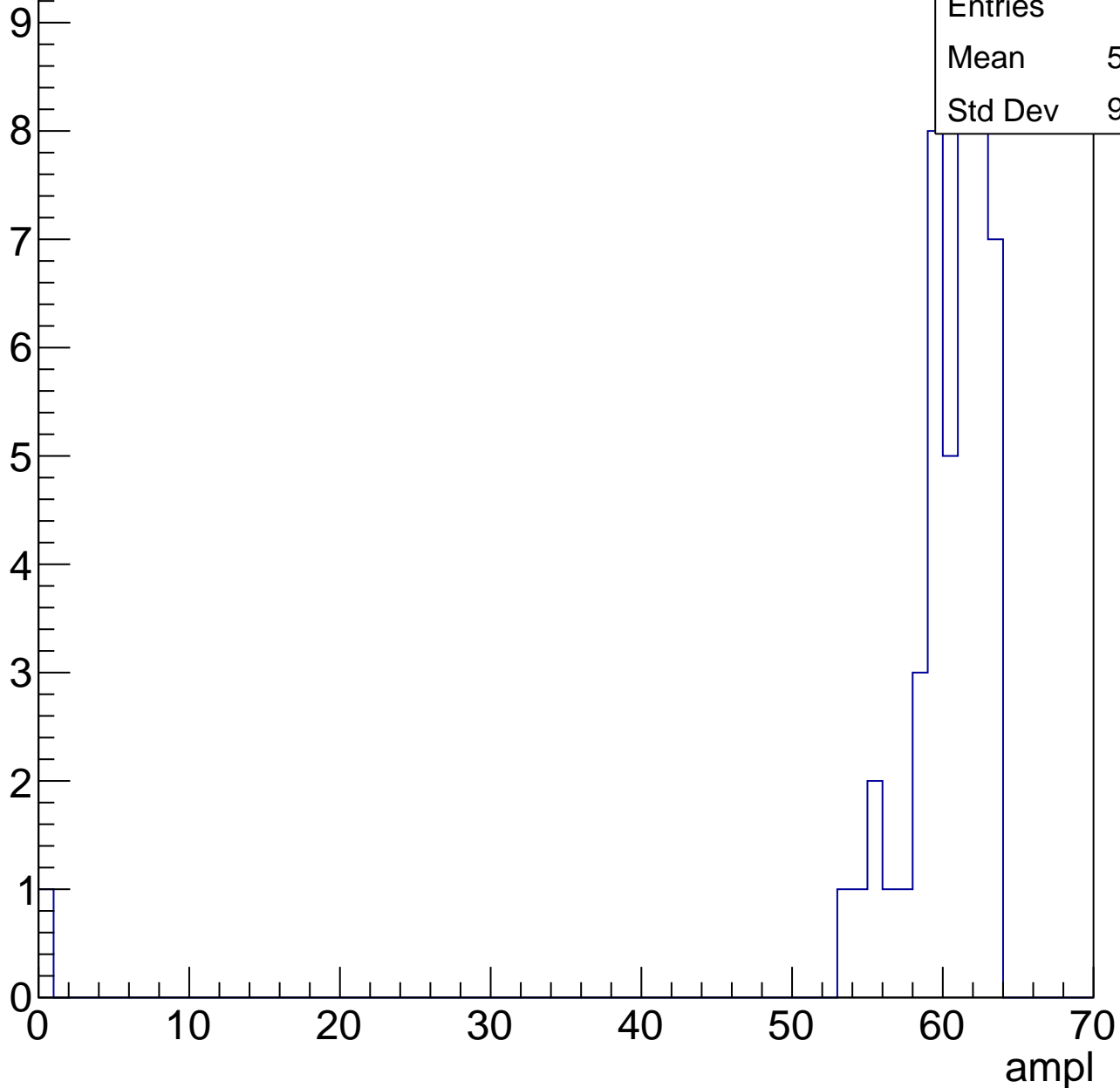
Entries	50
Mean	55.92
Std Dev	3.174



B1L102S, U10-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

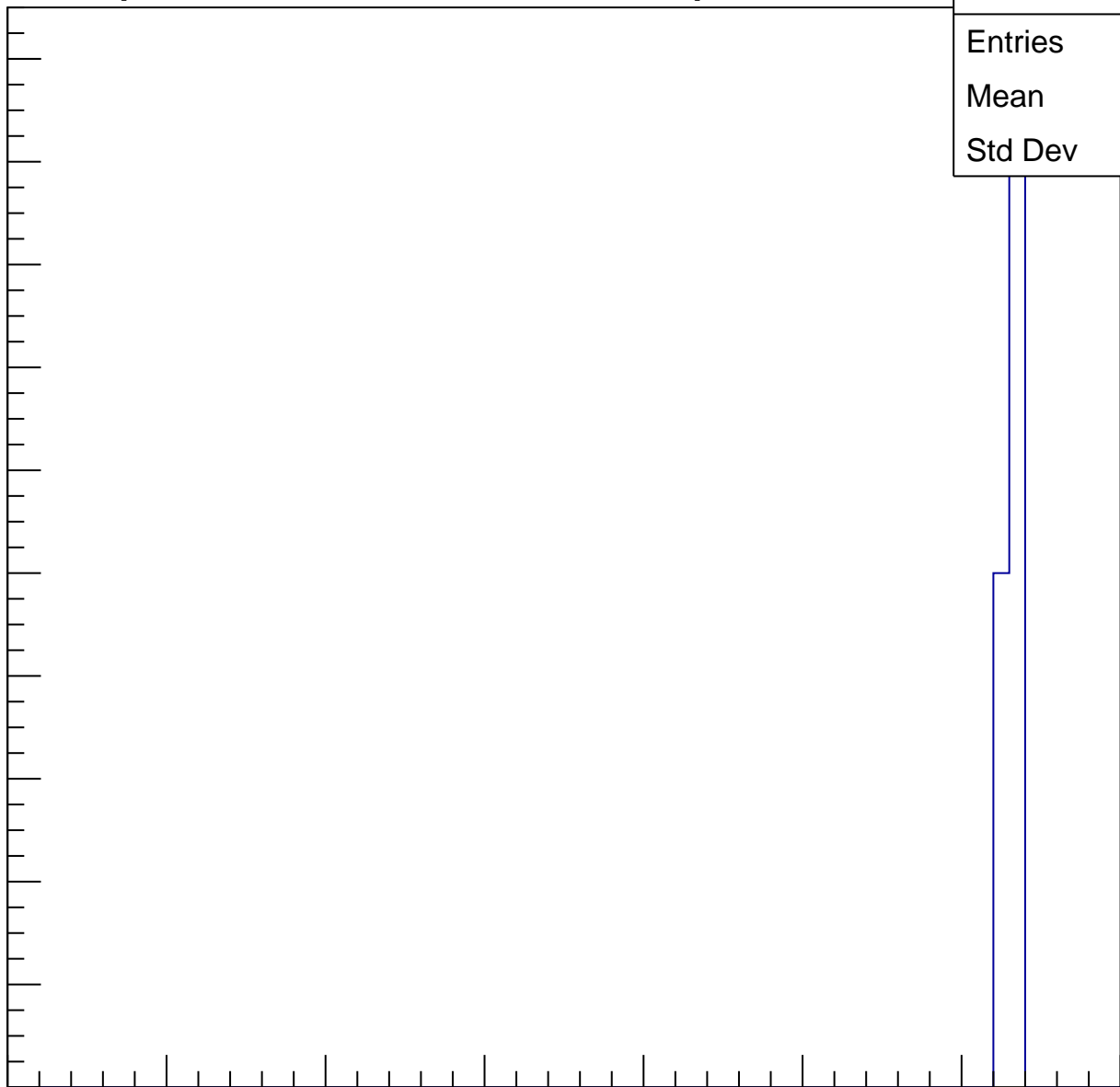
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch104, adc0

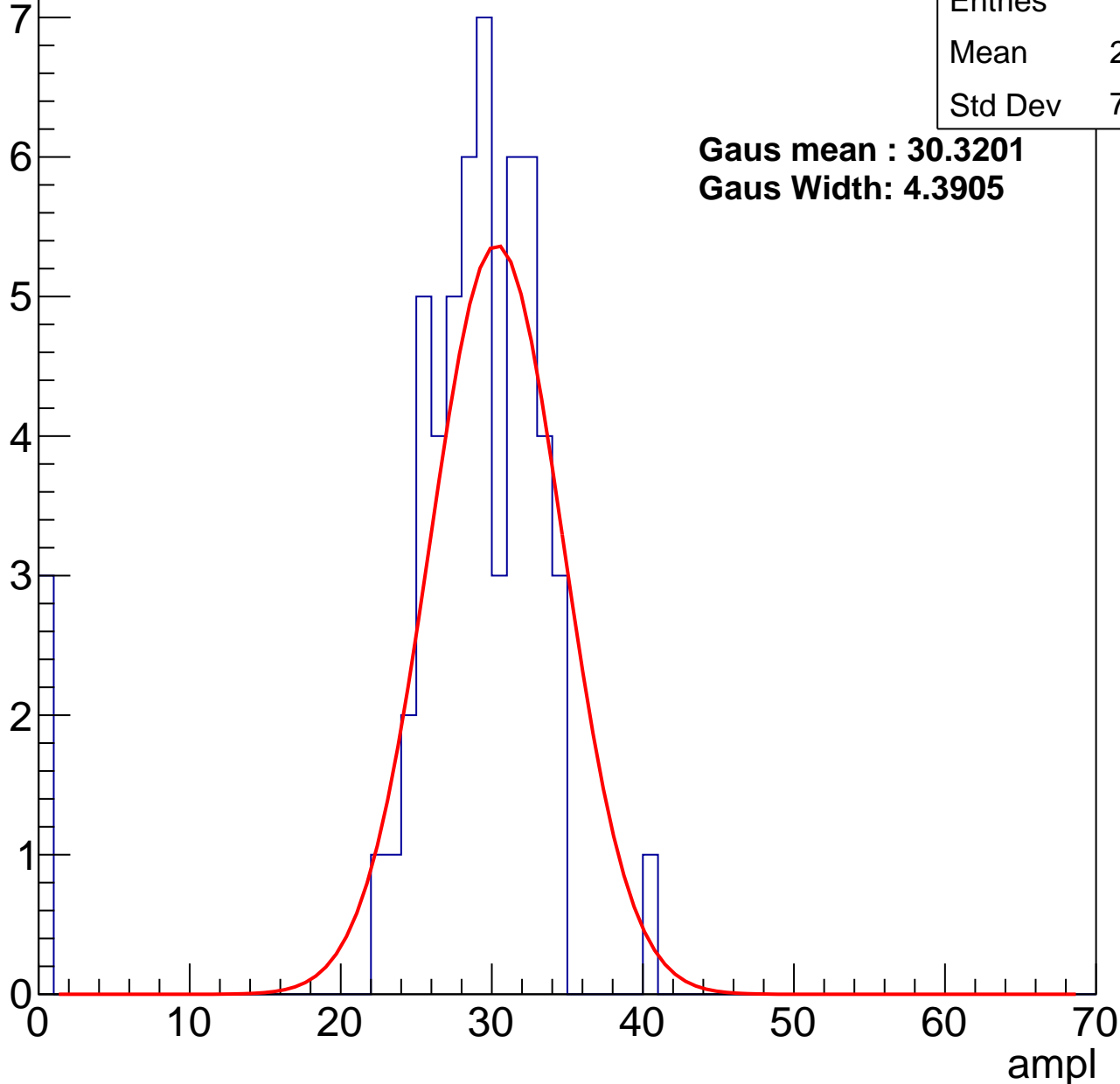
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.54
Std Dev	7.279

Gaus mean : 30.3201

Gaus Width: 4.3905



B1L102S, U10-ch104, adc1

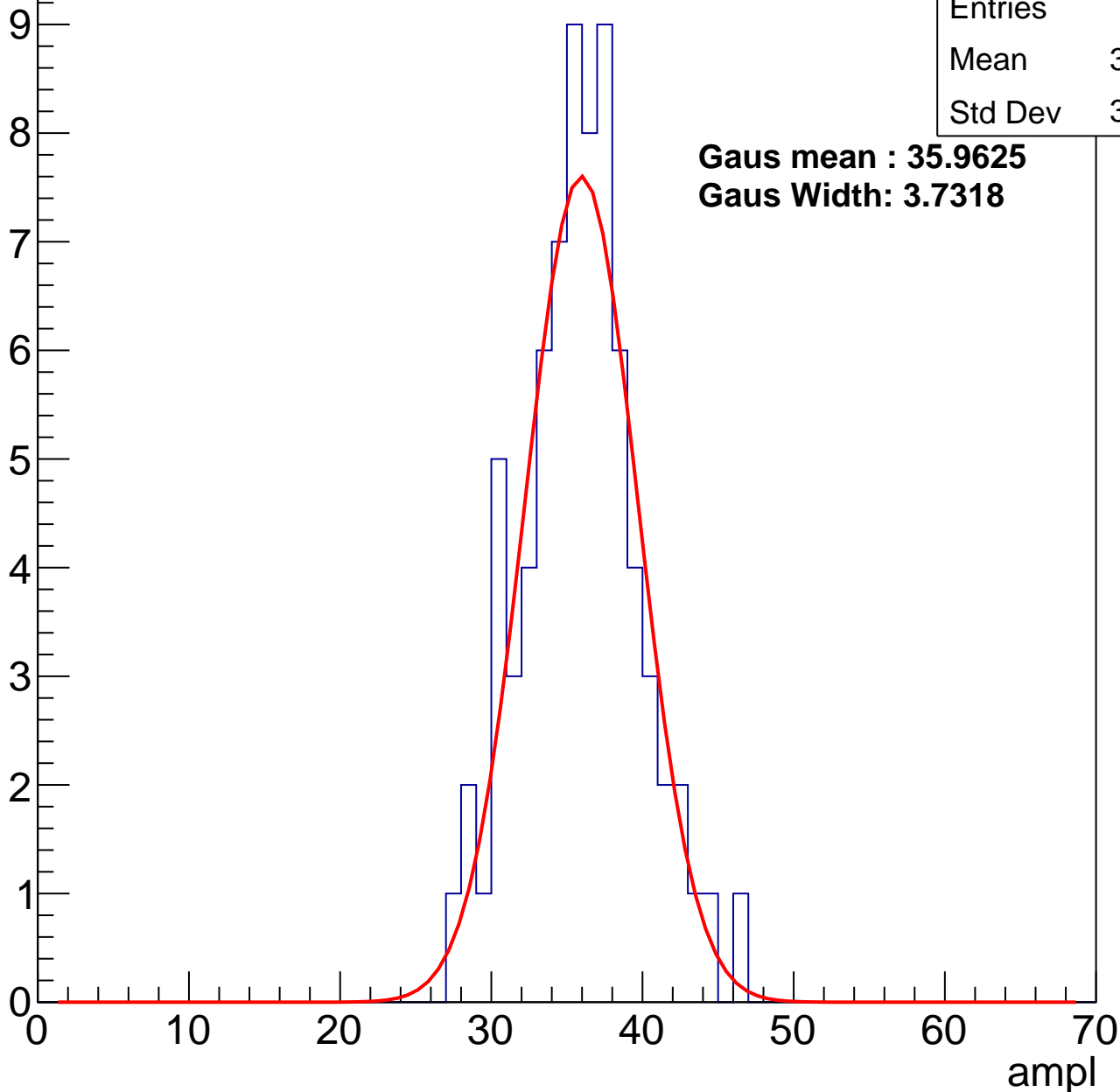
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.44
Std Dev	3.824

Gaus mean : 35.9625

Gaus Width: 3.7318



B1L102S, U10-ch104, adc2

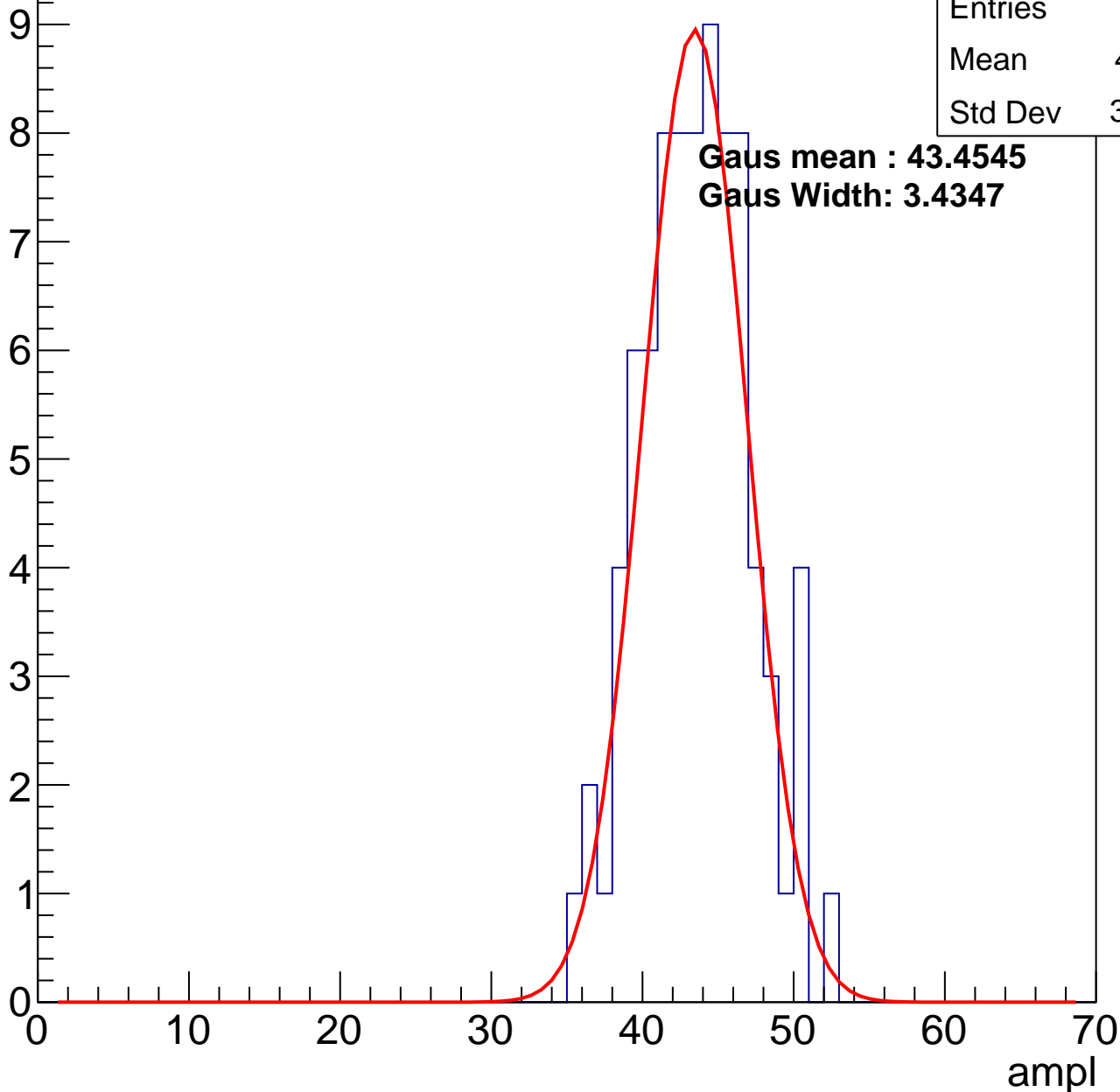
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	43.11
Std Dev	3.582

Gaus mean : 43.4545

Gaus Width: 3.4347

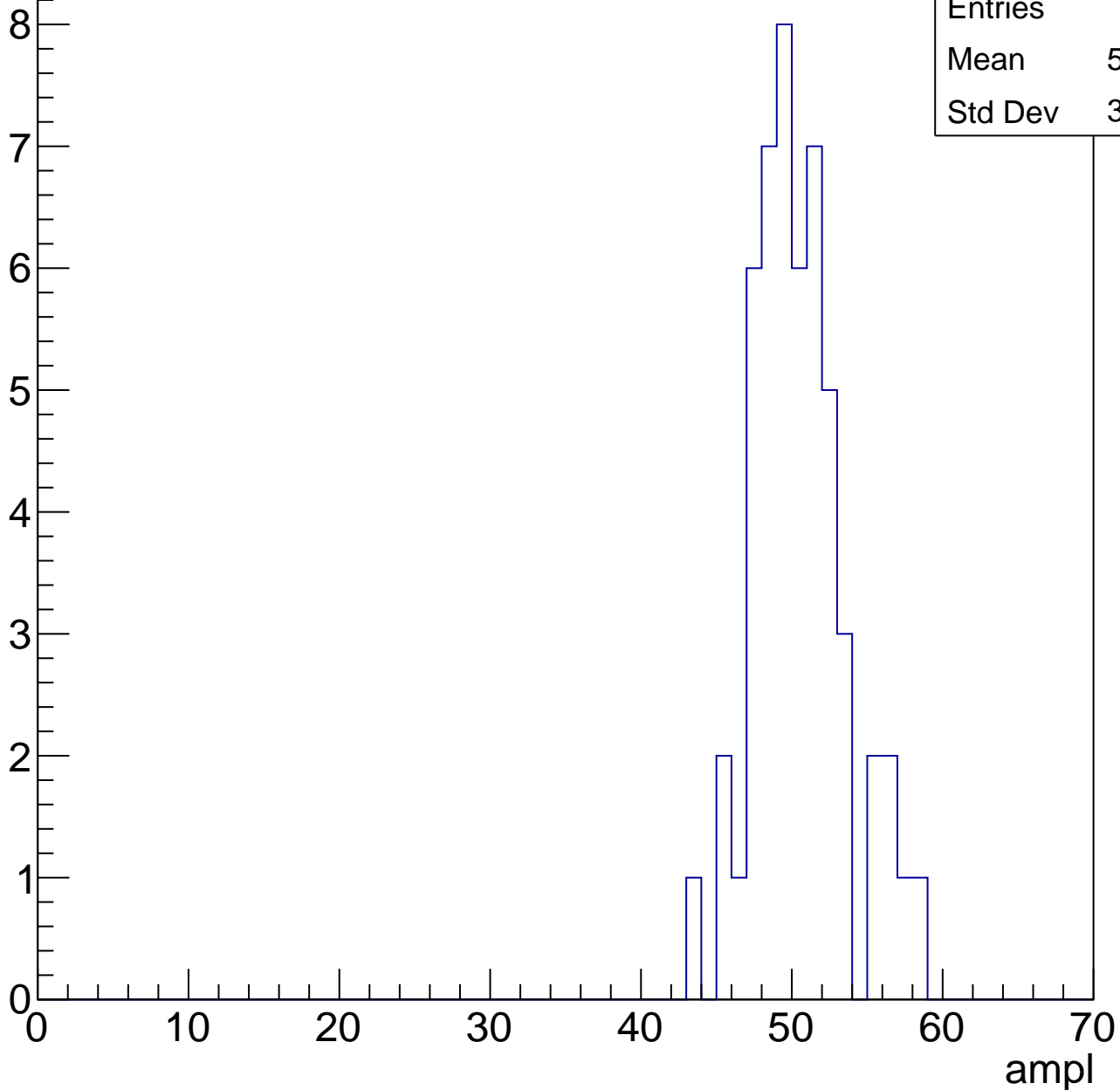


B1L102S, U10-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	50.04
Std Dev	3.082

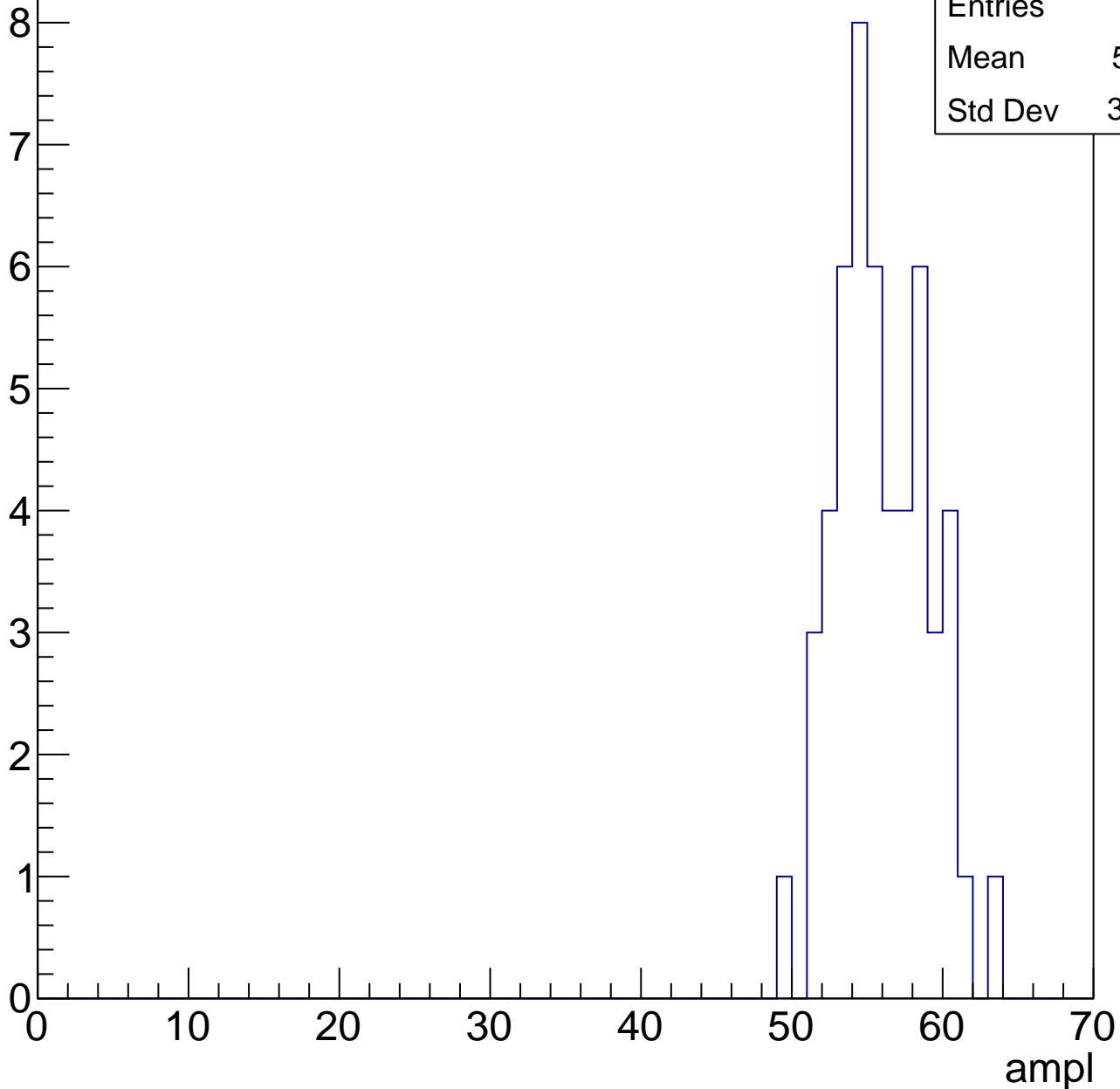


B1L102S, U10-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.51
Std Dev	3.006

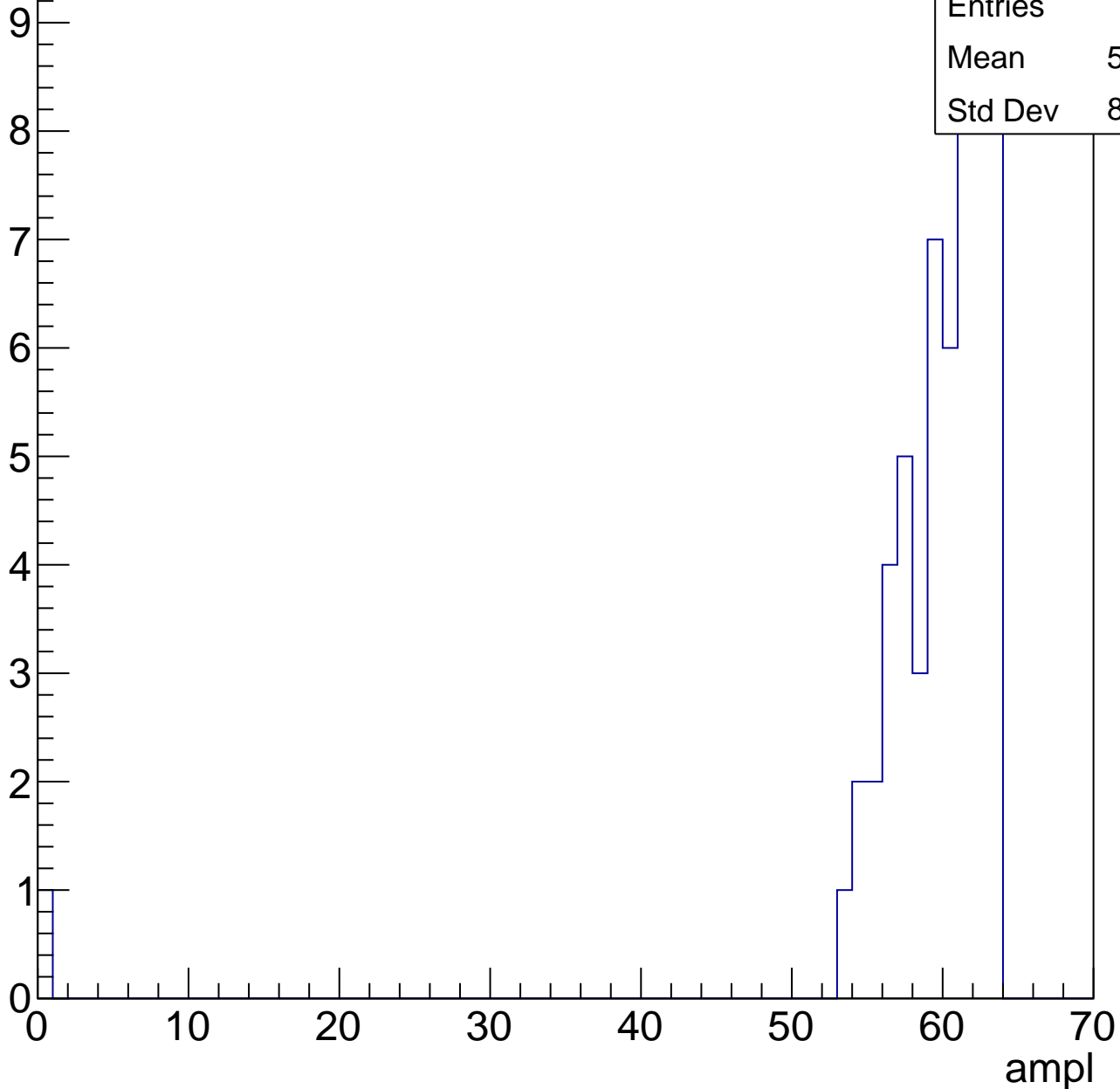


B1L102S, U10-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.64
Std Dev	8.218



B1L102S, U10-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

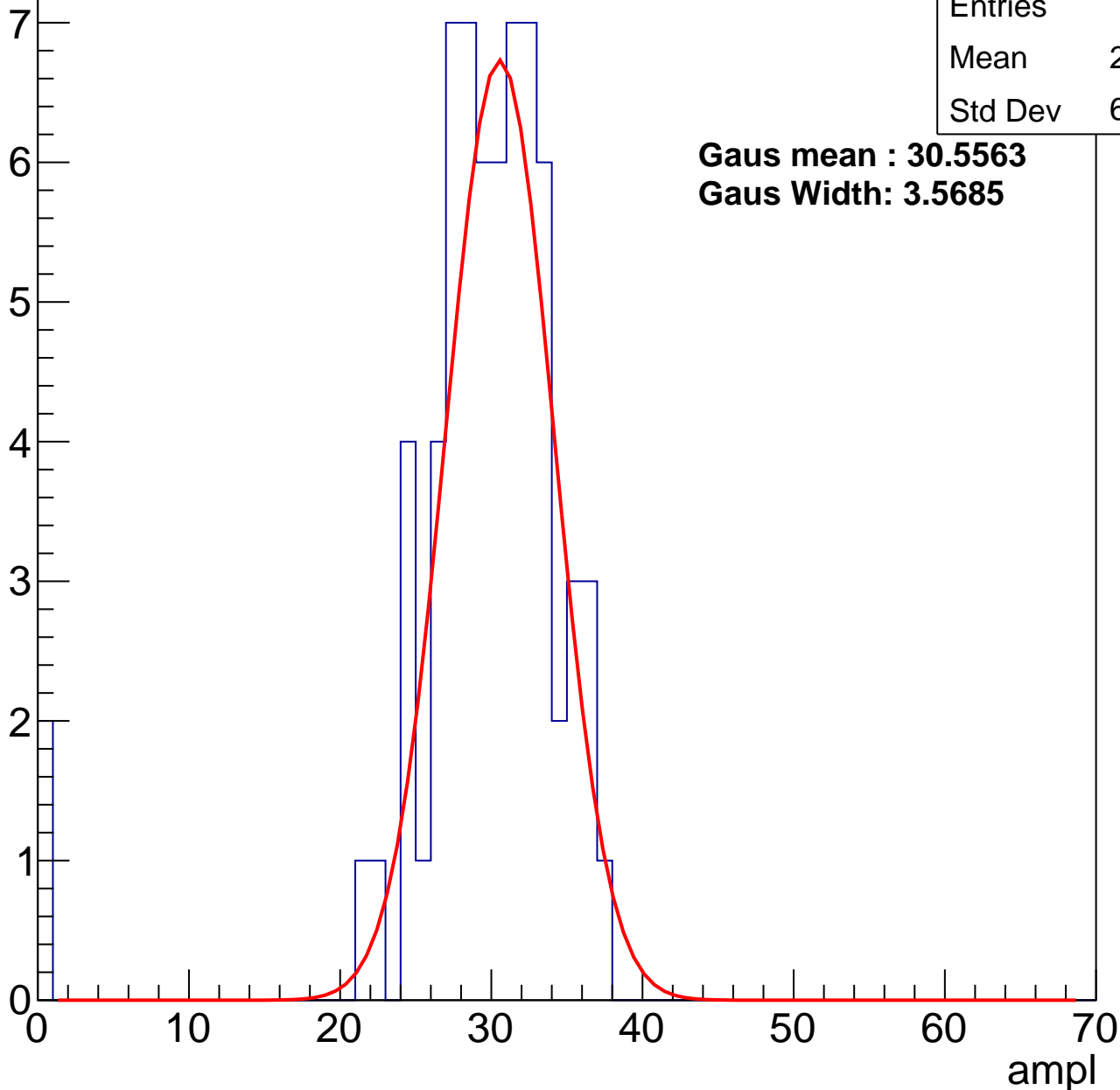
B1L102S, U10-ch105, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.88
Std Dev	6.115

Gaus mean : 30.5563
Gaus Width: 3.5685



B1L102S, U10-ch105, adc1

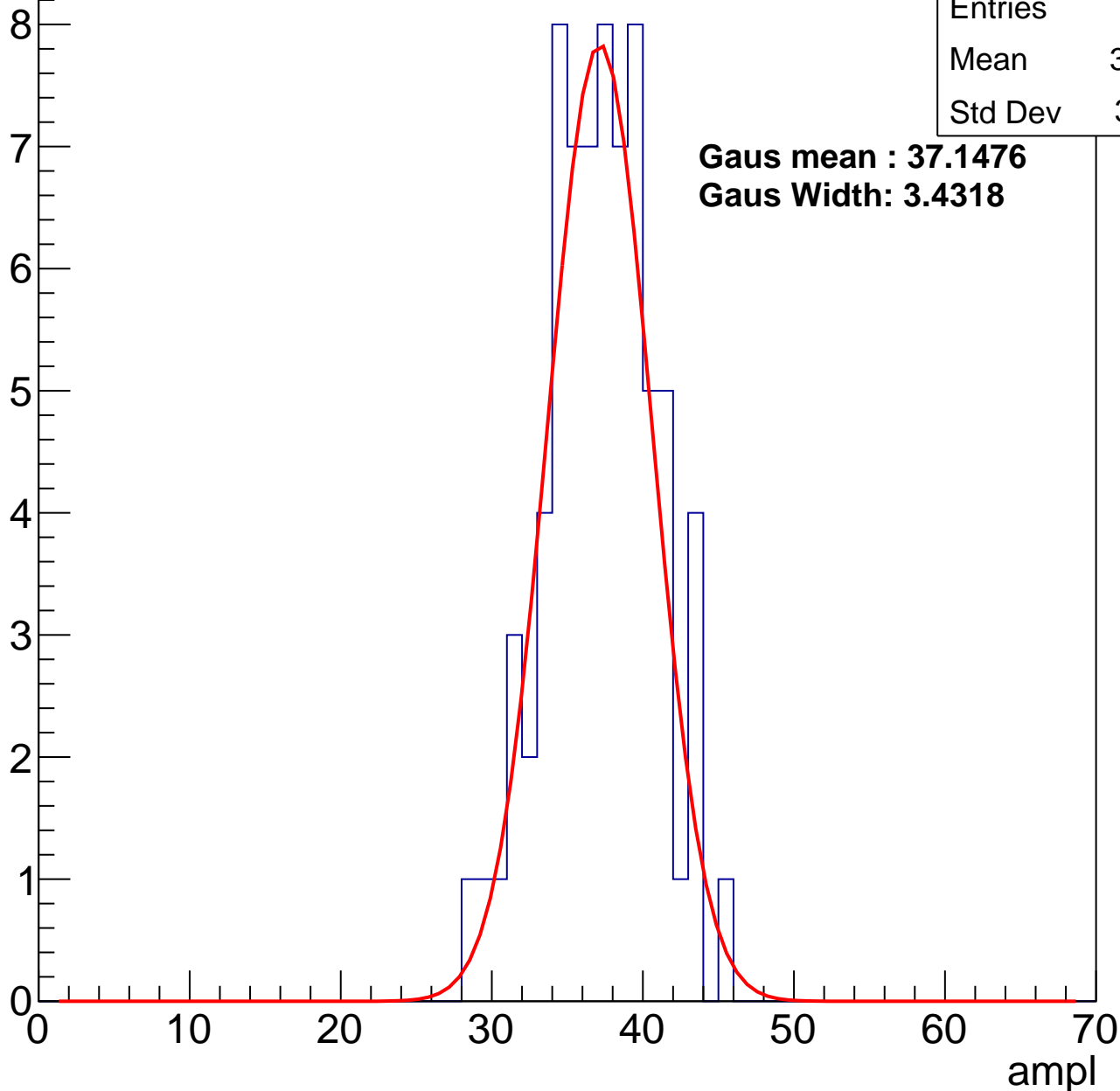
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.75
Std Dev	3.541

Gaus mean : 37.1476

Gaus Width: 3.4318



B1L102S, U10-ch105, adc2

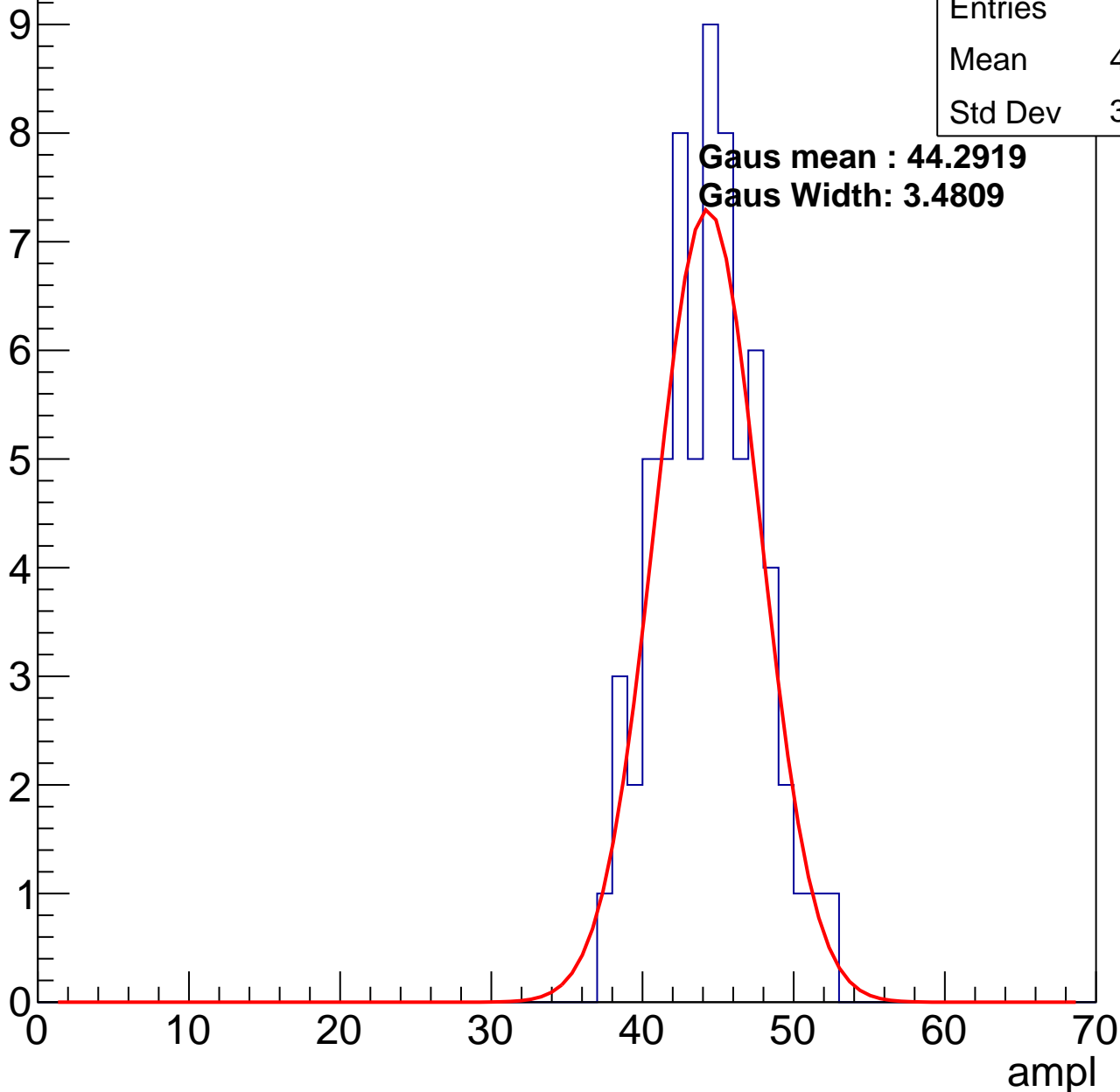
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.88
Std Dev	3.287

Gaus mean : 44.2919

Gaus Width: 3.4809

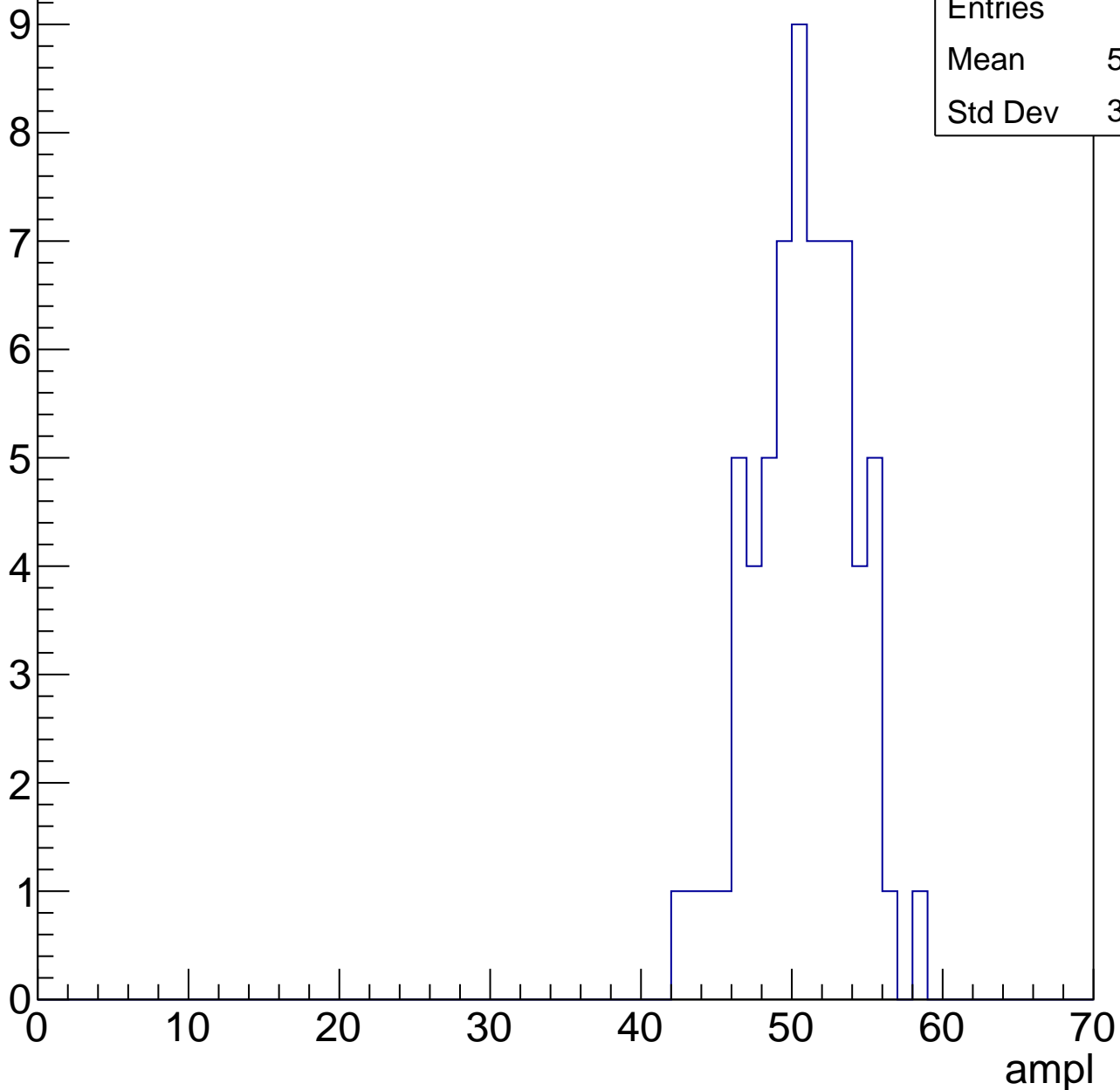


B1L102S, U10-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.33
Std Dev	3.249

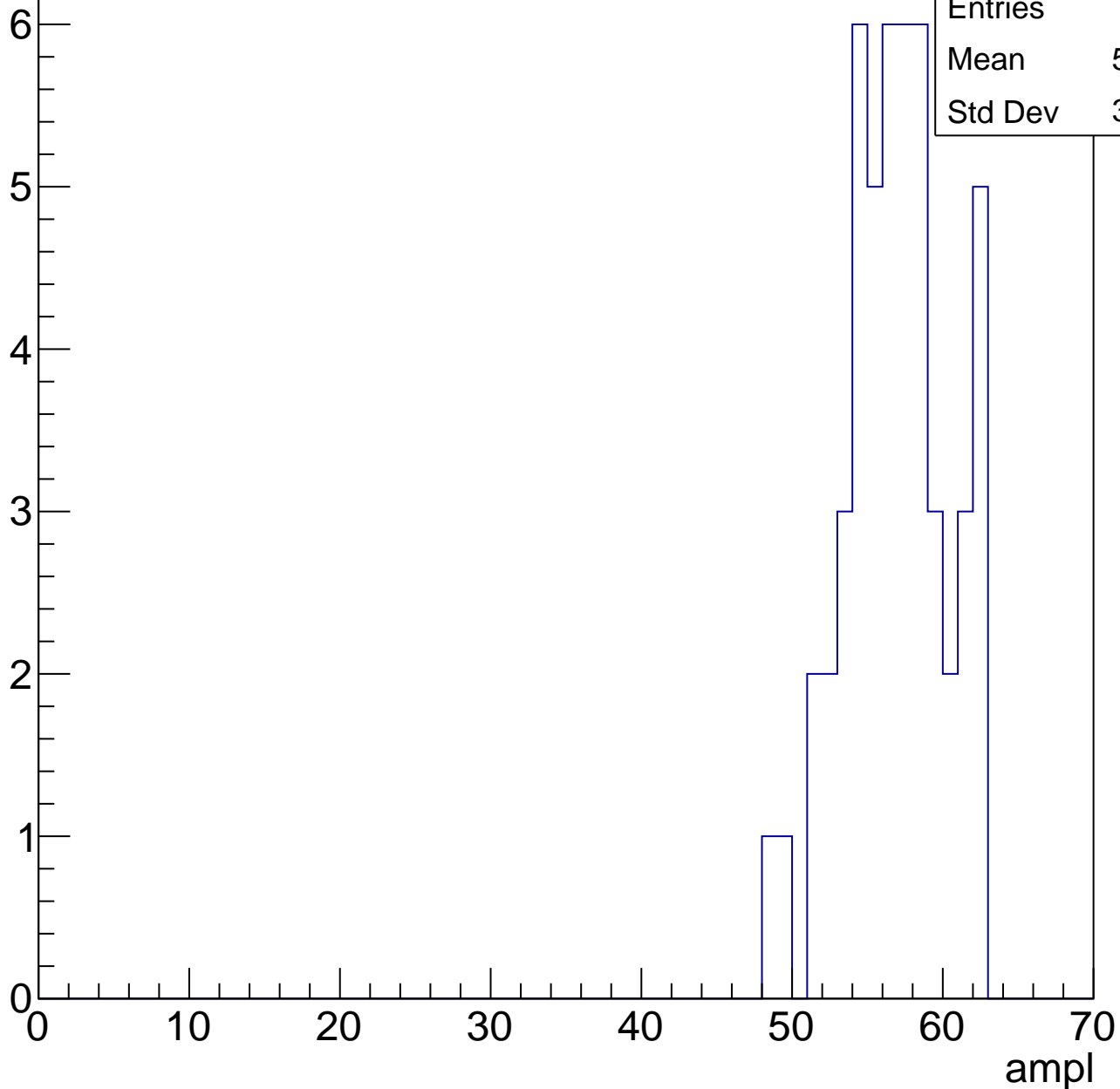


B1L102S, U10-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.41
Std Dev	3.391



B1L102S, U10-ch105, adc5

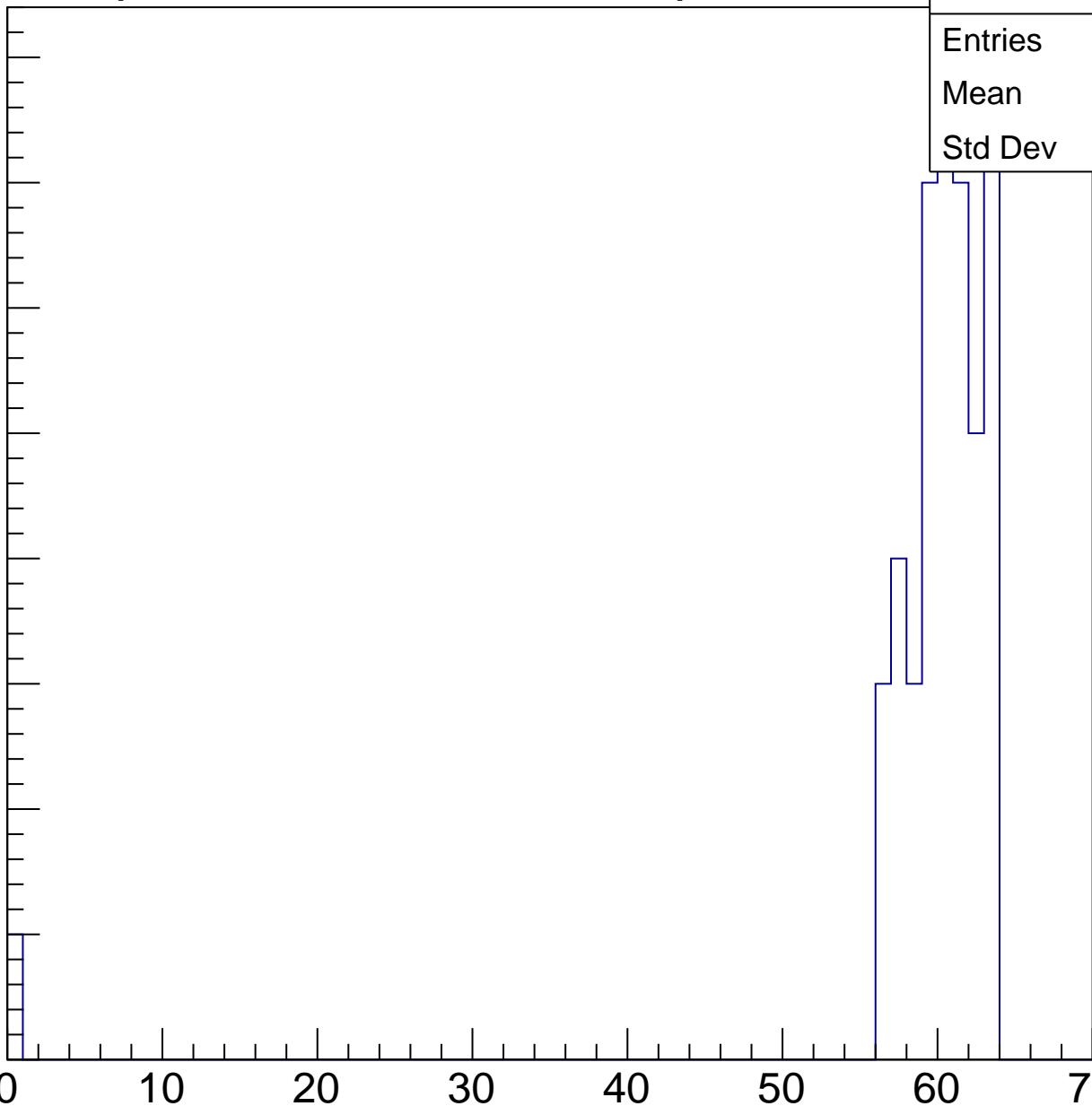
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.78
Std Dev	9.009

ampl



B1L102S, U10-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U10-ch106, adc0

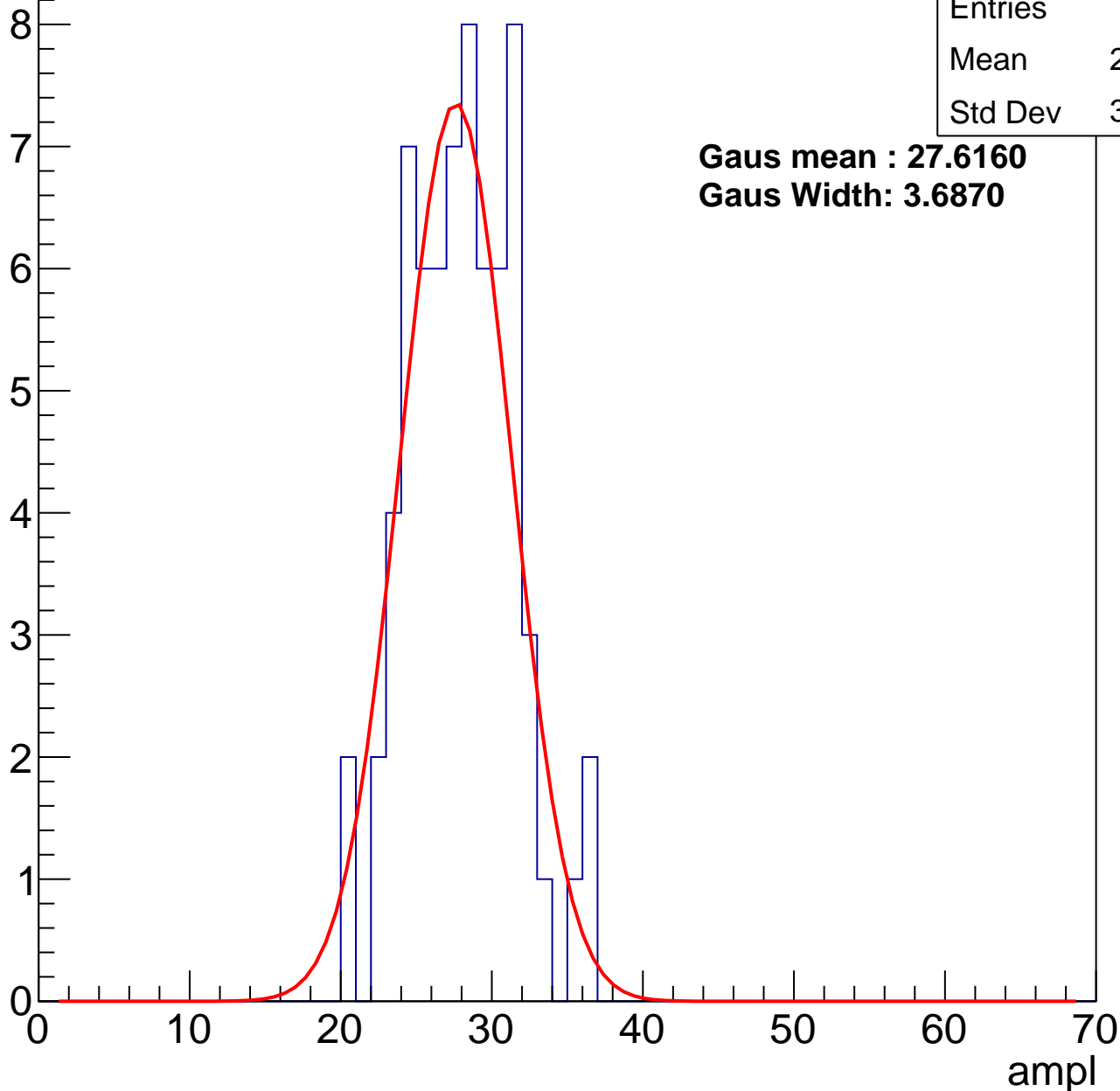
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.55
Std Dev	3.479

Gaus mean : 27.6160

Gaus Width: 3.6870



B1L102S, U10-ch106, adc1

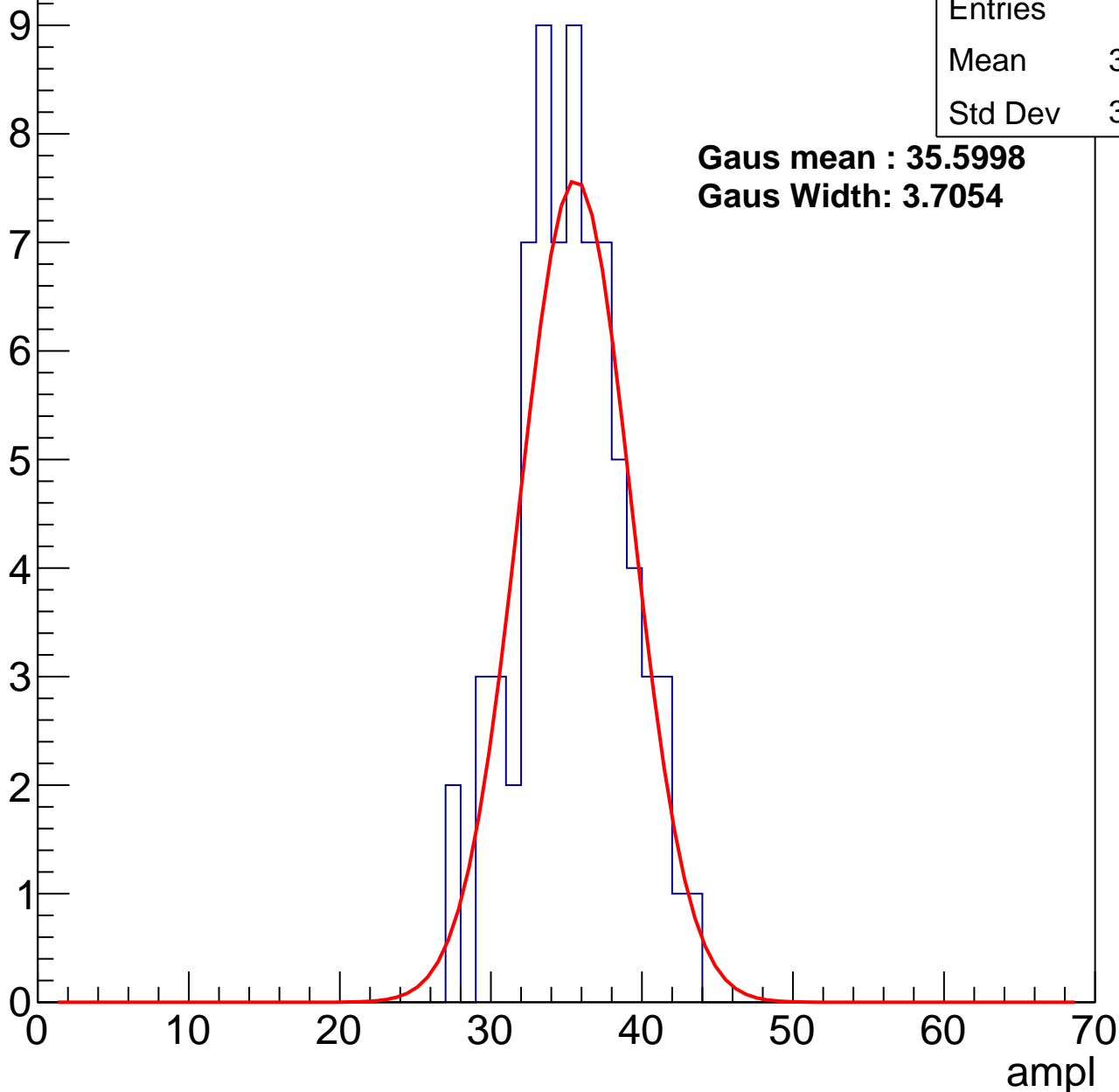
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.96
Std Dev	3.489

Gaus mean : 35.5998

Gaus Width: 3.7054



B1L102S, U10-ch106, adc2

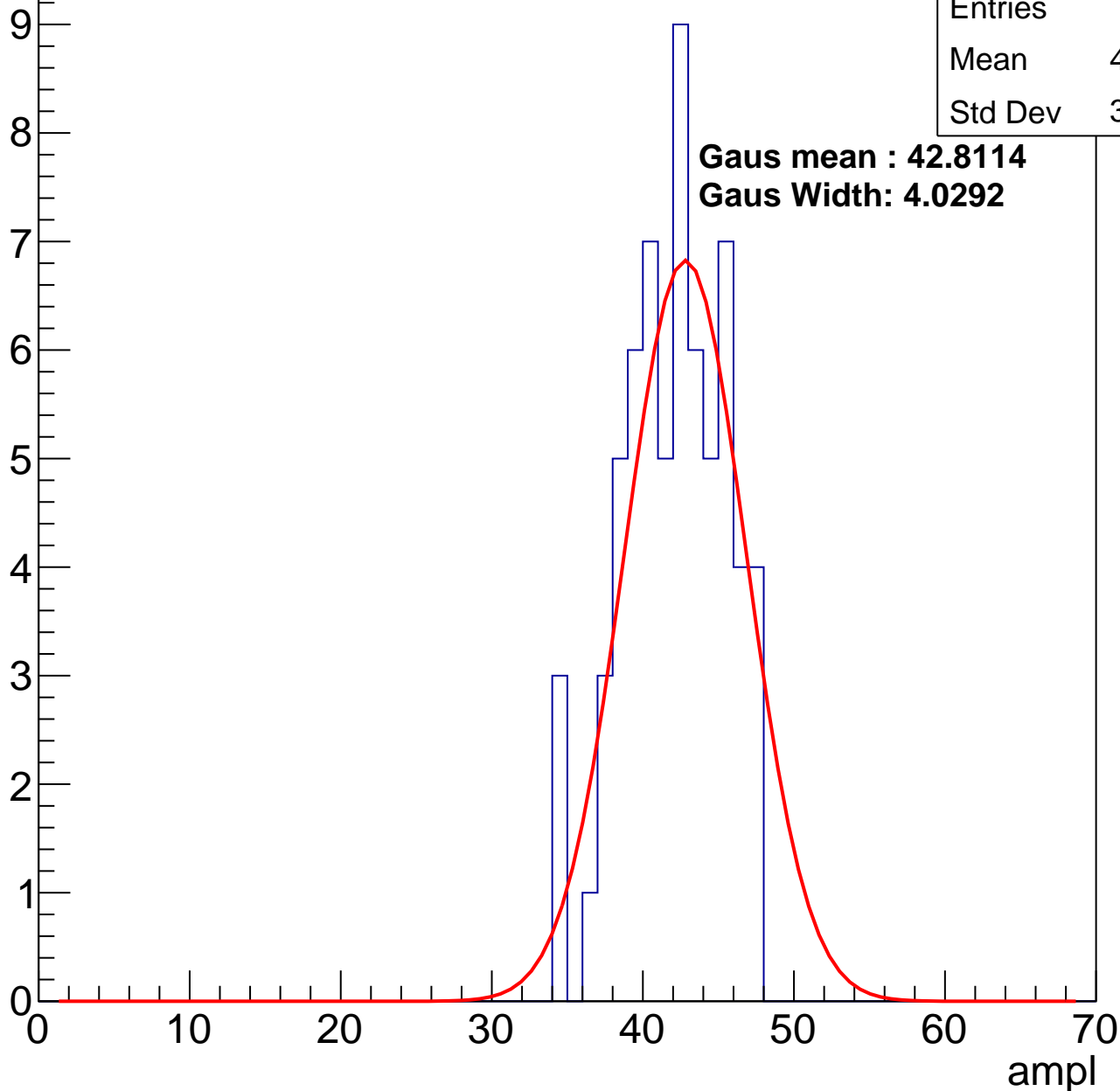
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.55
Std Dev	3.305

Gaus mean : 42.8114

Gaus Width: 4.0292

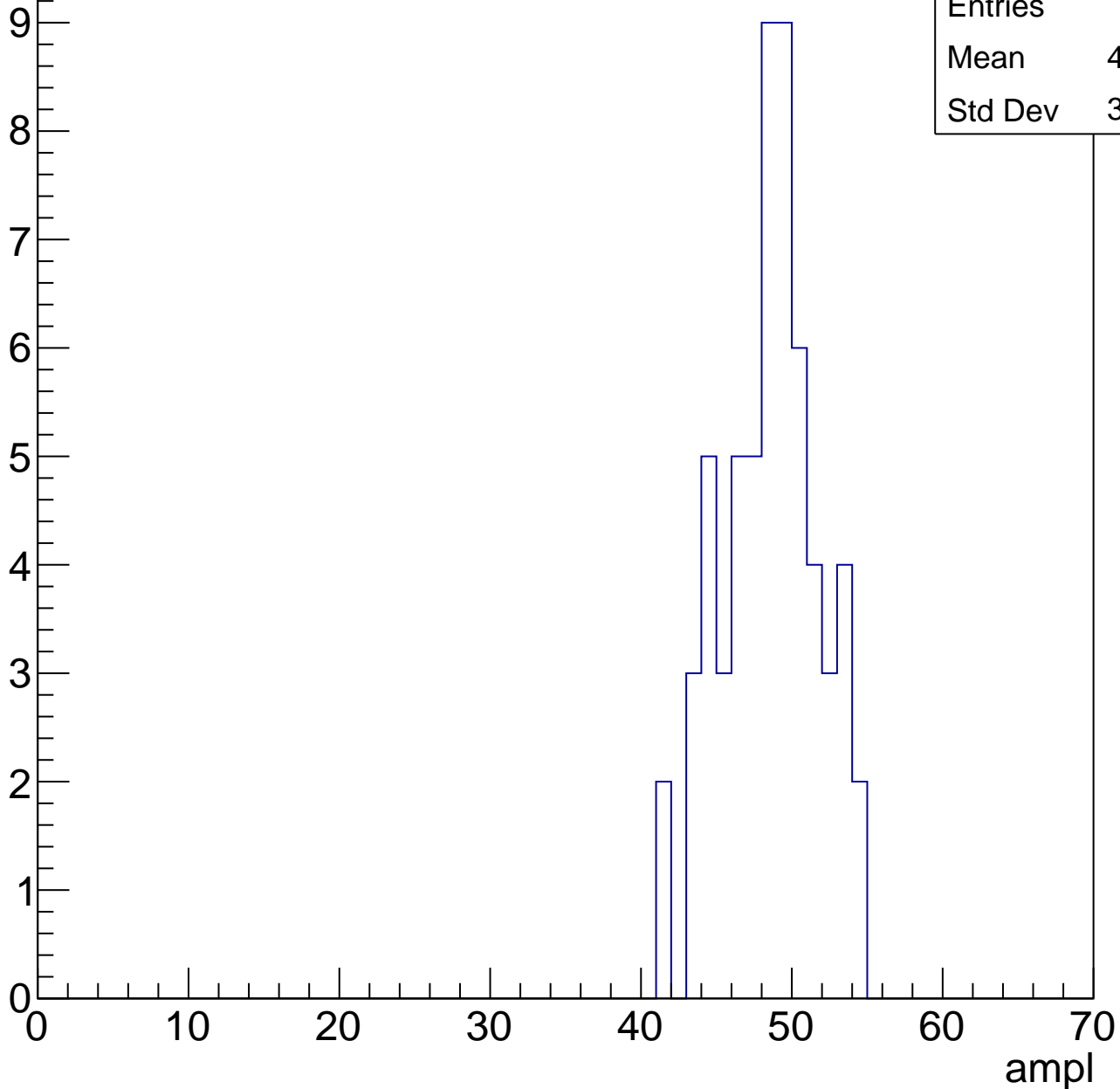


B1L102S, U10-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.07
Std Dev	3.146

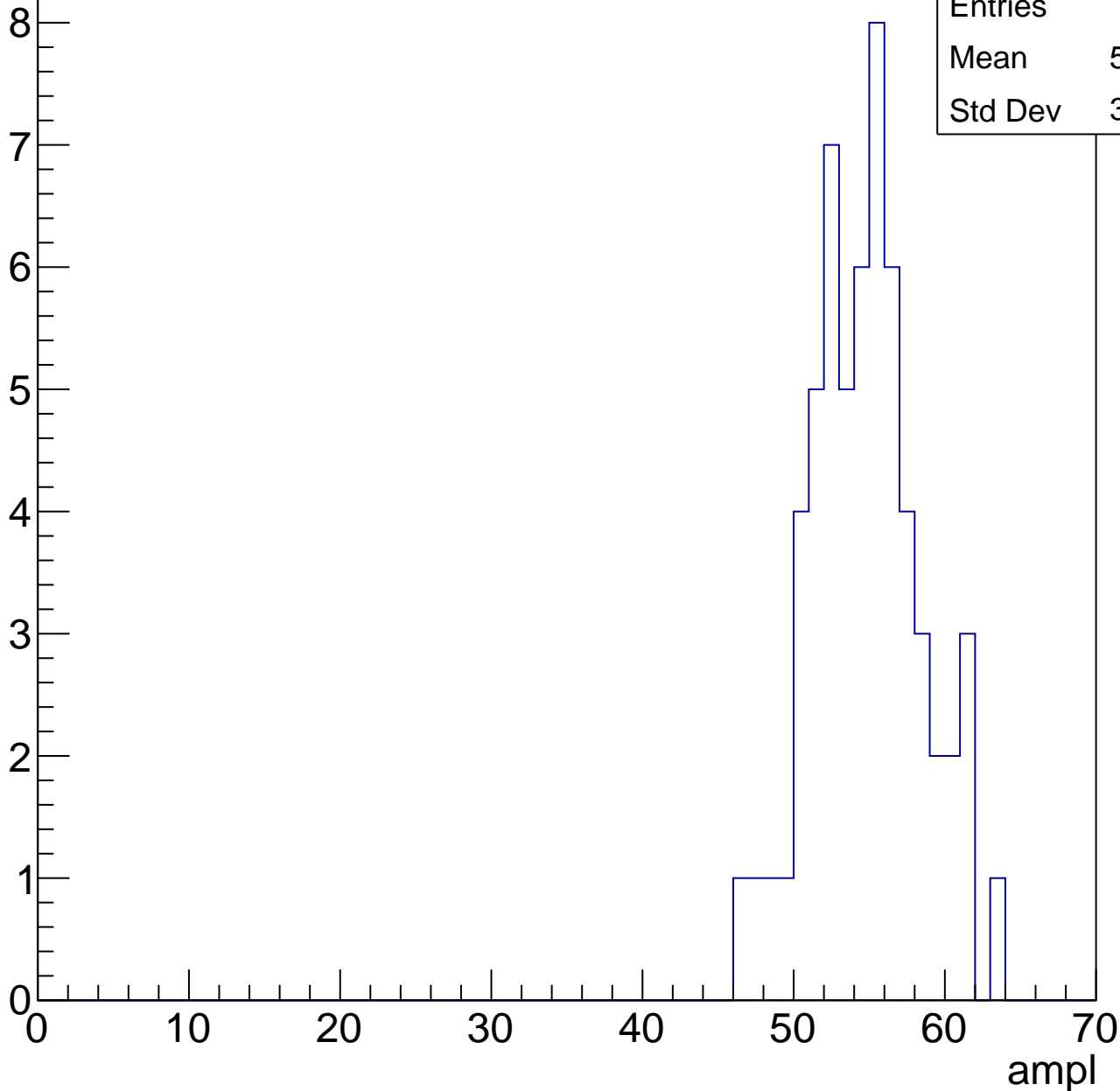


B1L102S, U10-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.33
Std Dev	3.595

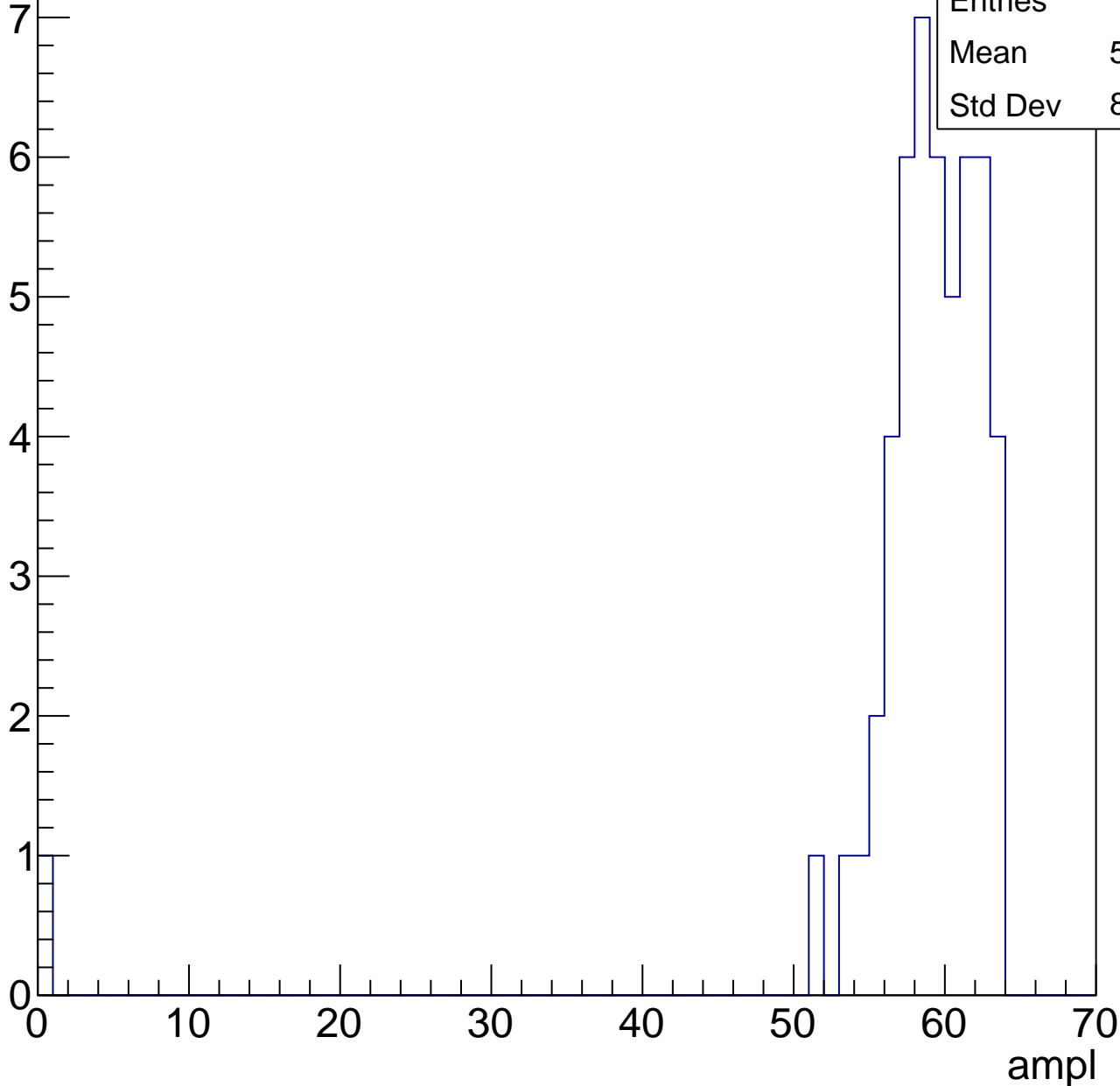


B1L102S, U10-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

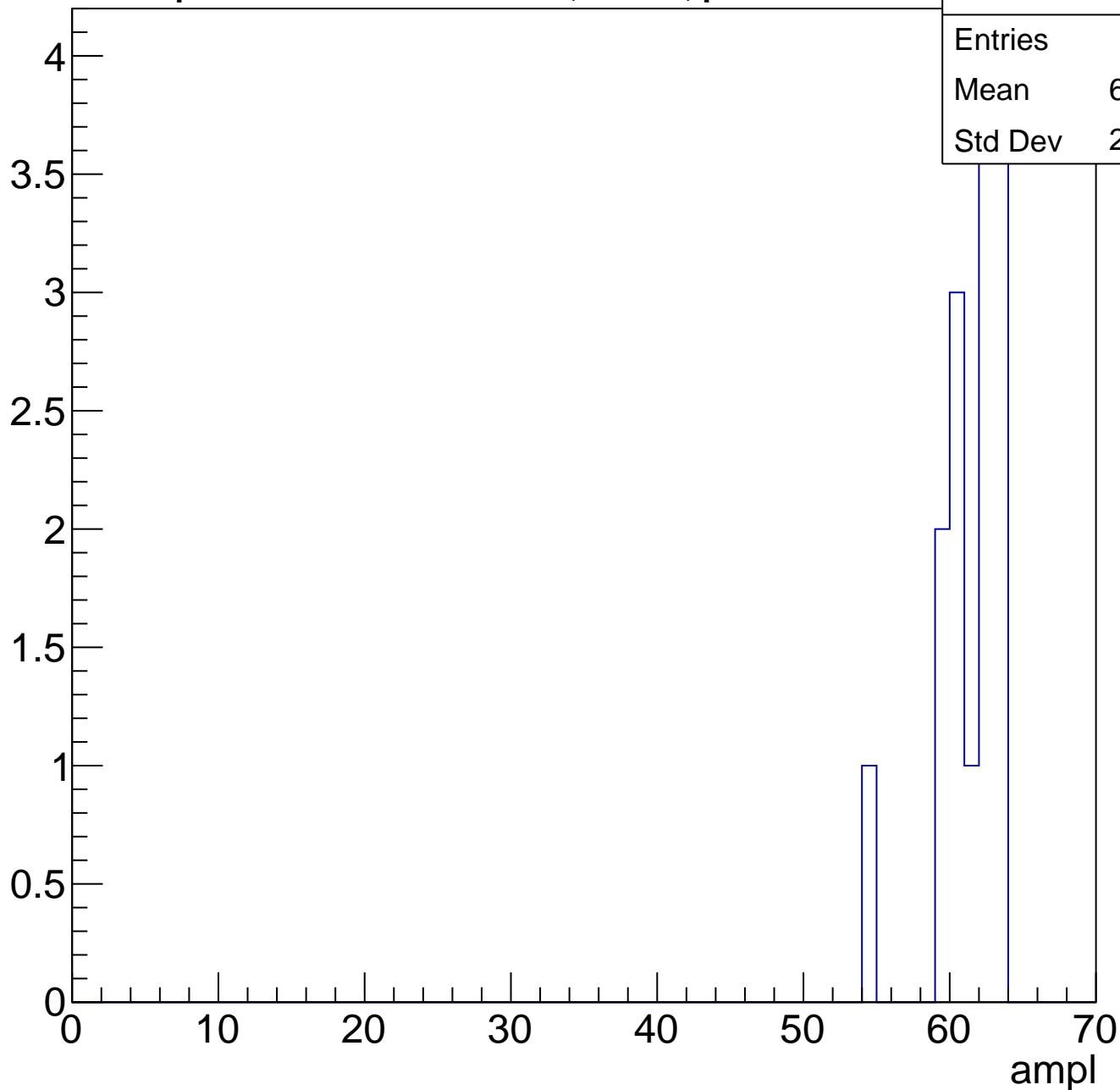
Entries	50
Mean	57.68
Std Dev	8.677



B1L102S, U10-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

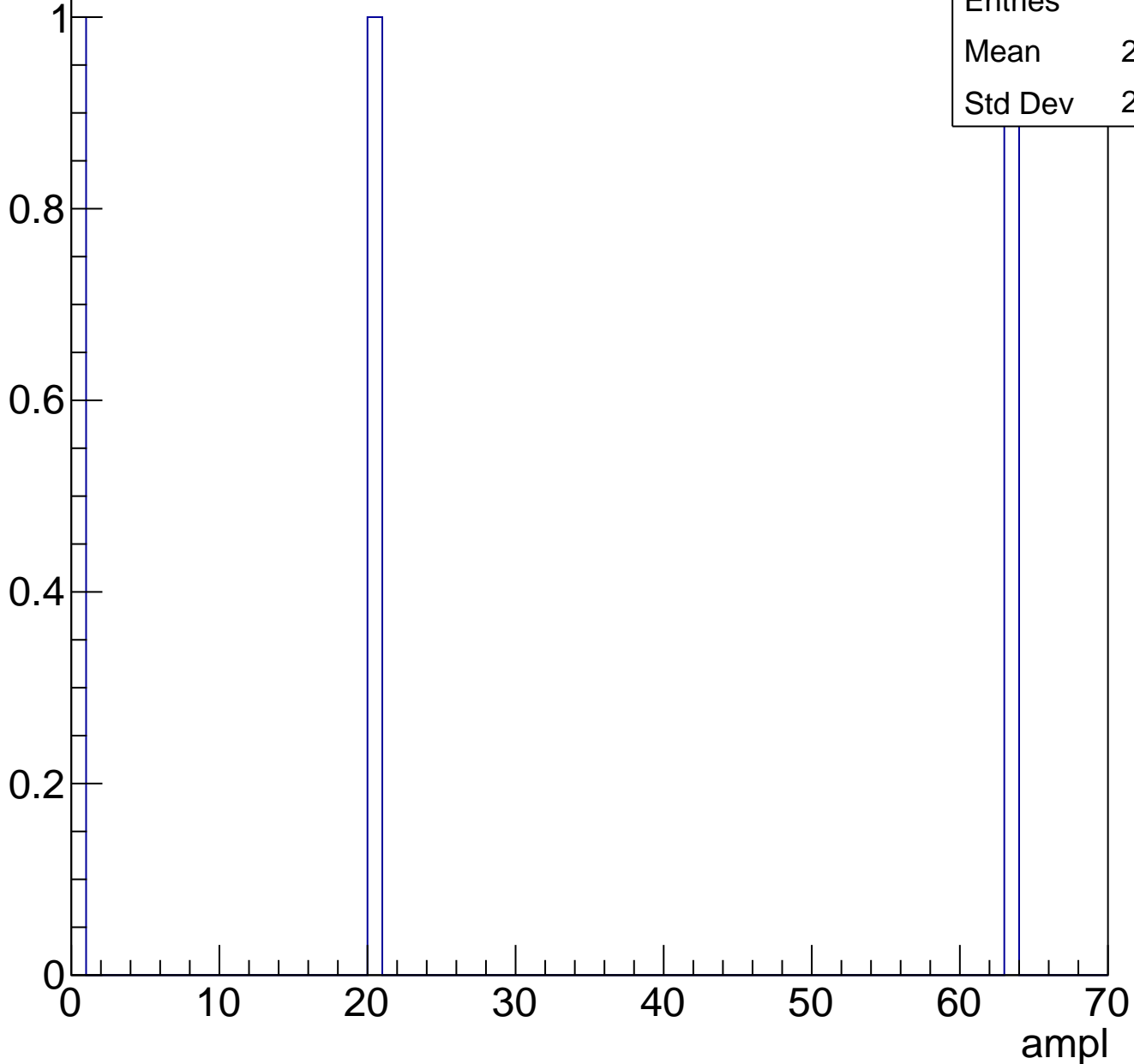
Entry



B1L102S, U10-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch107, adc0

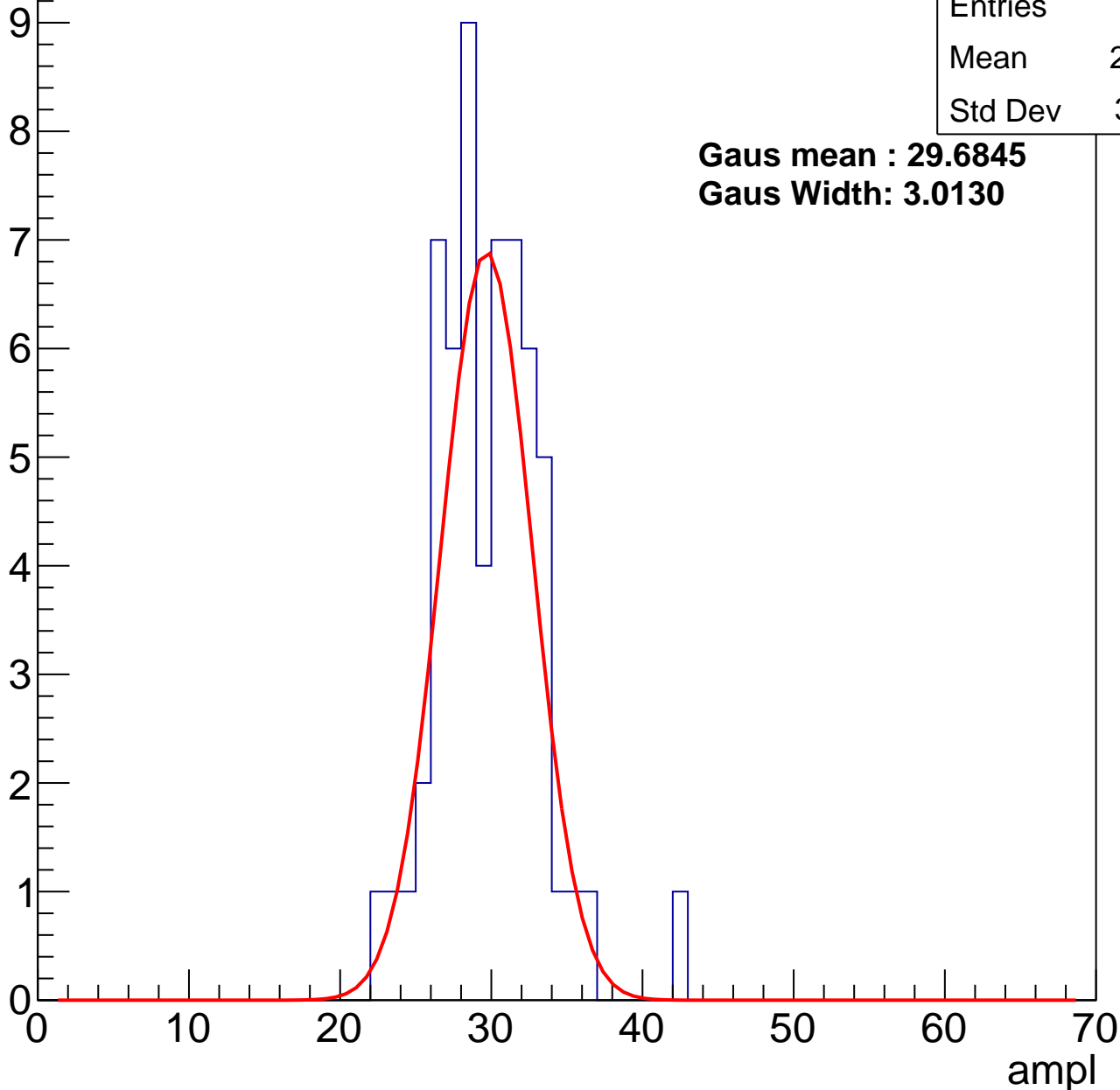
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.37
Std Dev	3.361

Gaus mean : 29.6845

Gaus Width: 3.0130



B1L102S, U10-ch107, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	36.02
Std Dev	3.764

Gaus mean : 36.7632

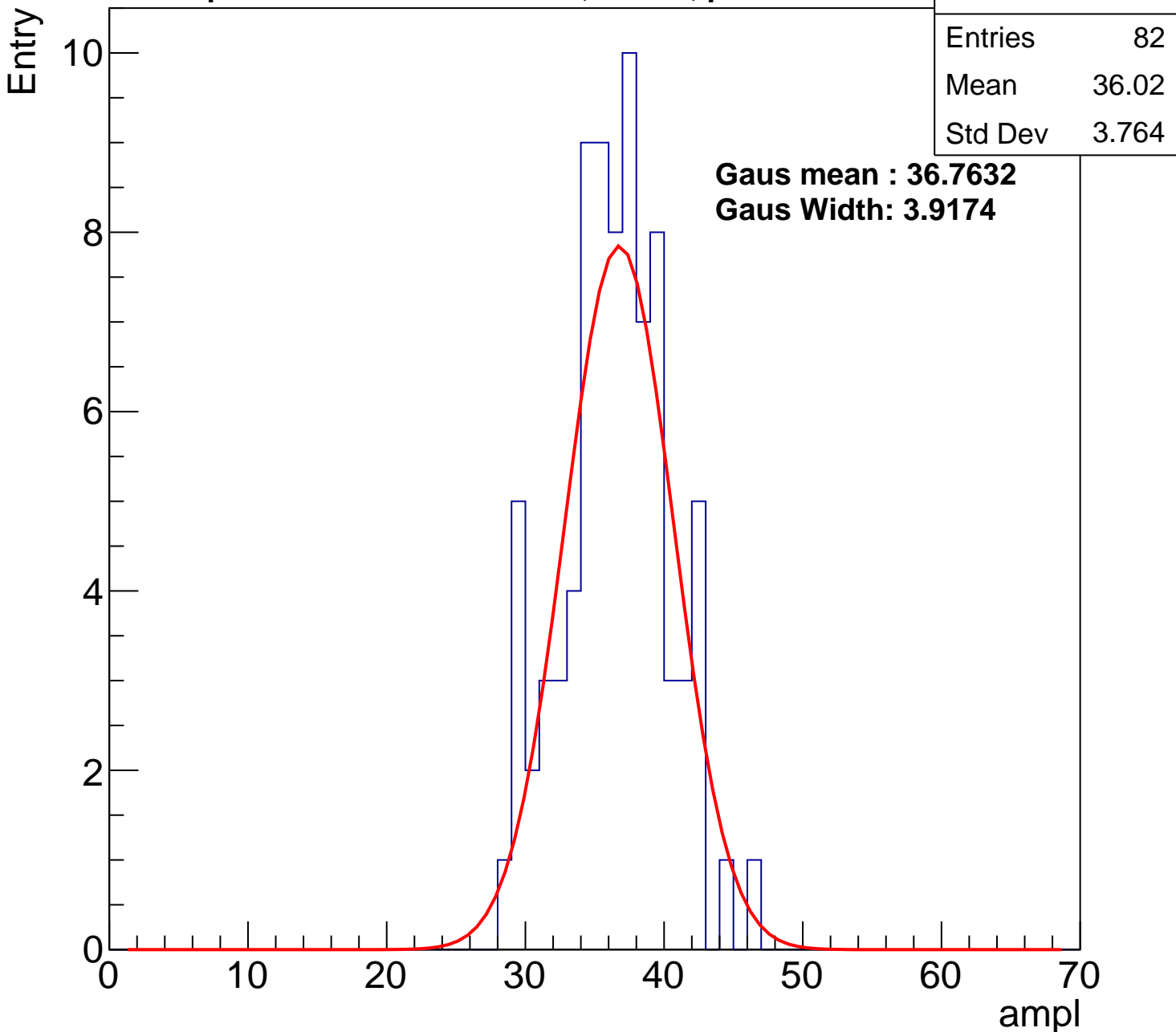
Gaus Width: 3.9174

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch107, adc2

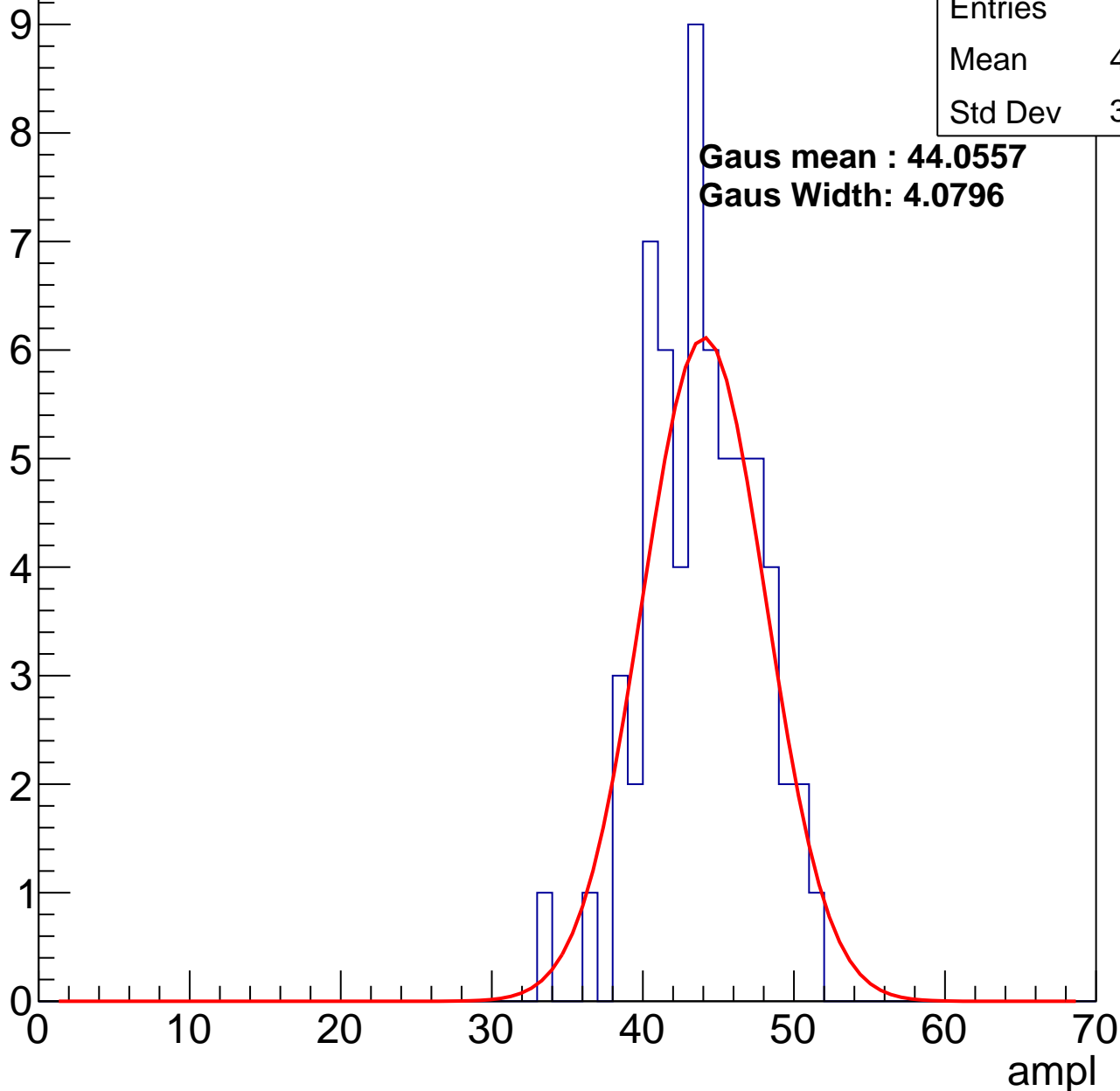
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.44
Std Dev	3.607

Gaus mean : 44.0557

Gaus Width: 4.0796

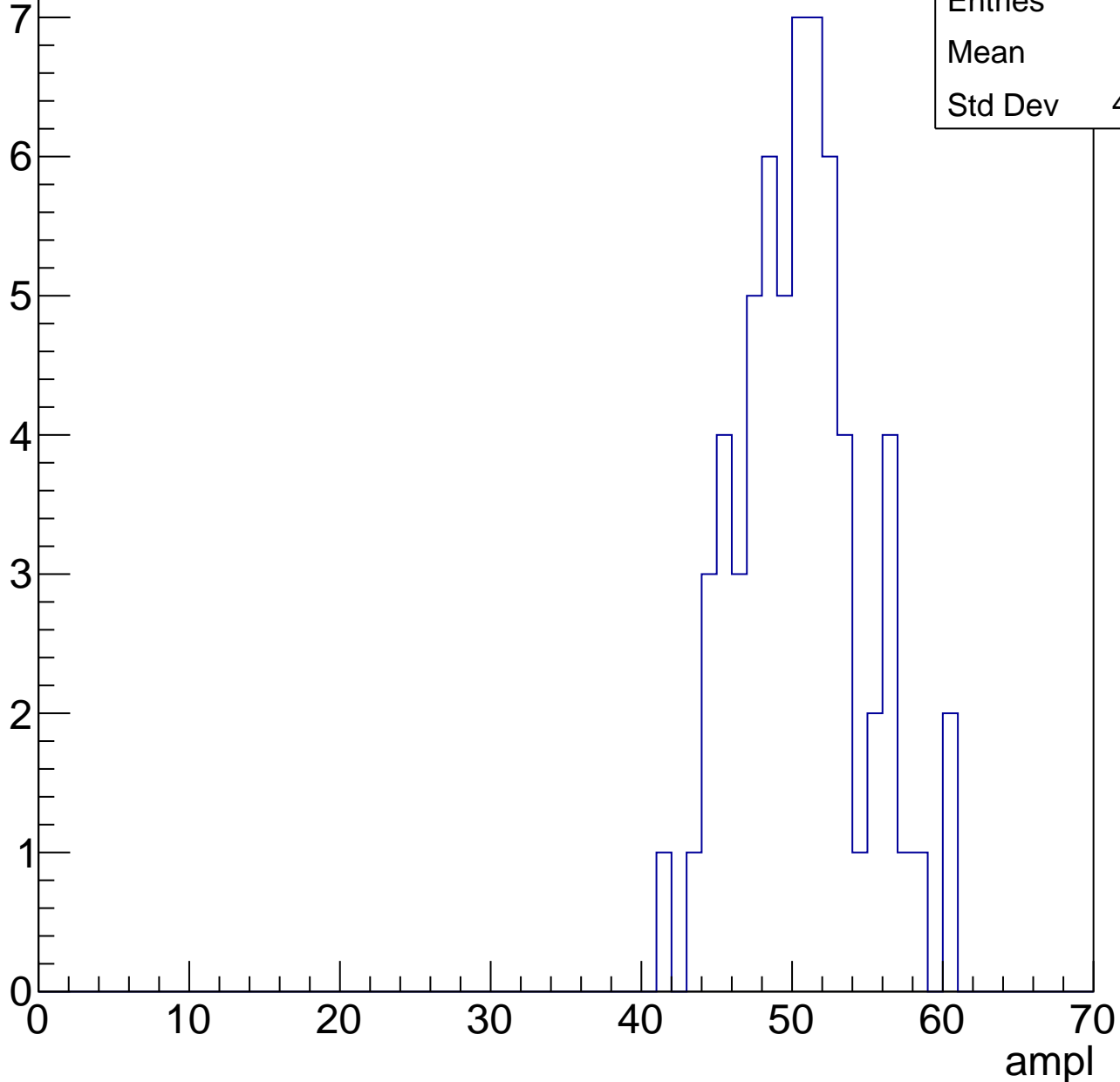


B1L102S, U10-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.1
Std Dev	4.081

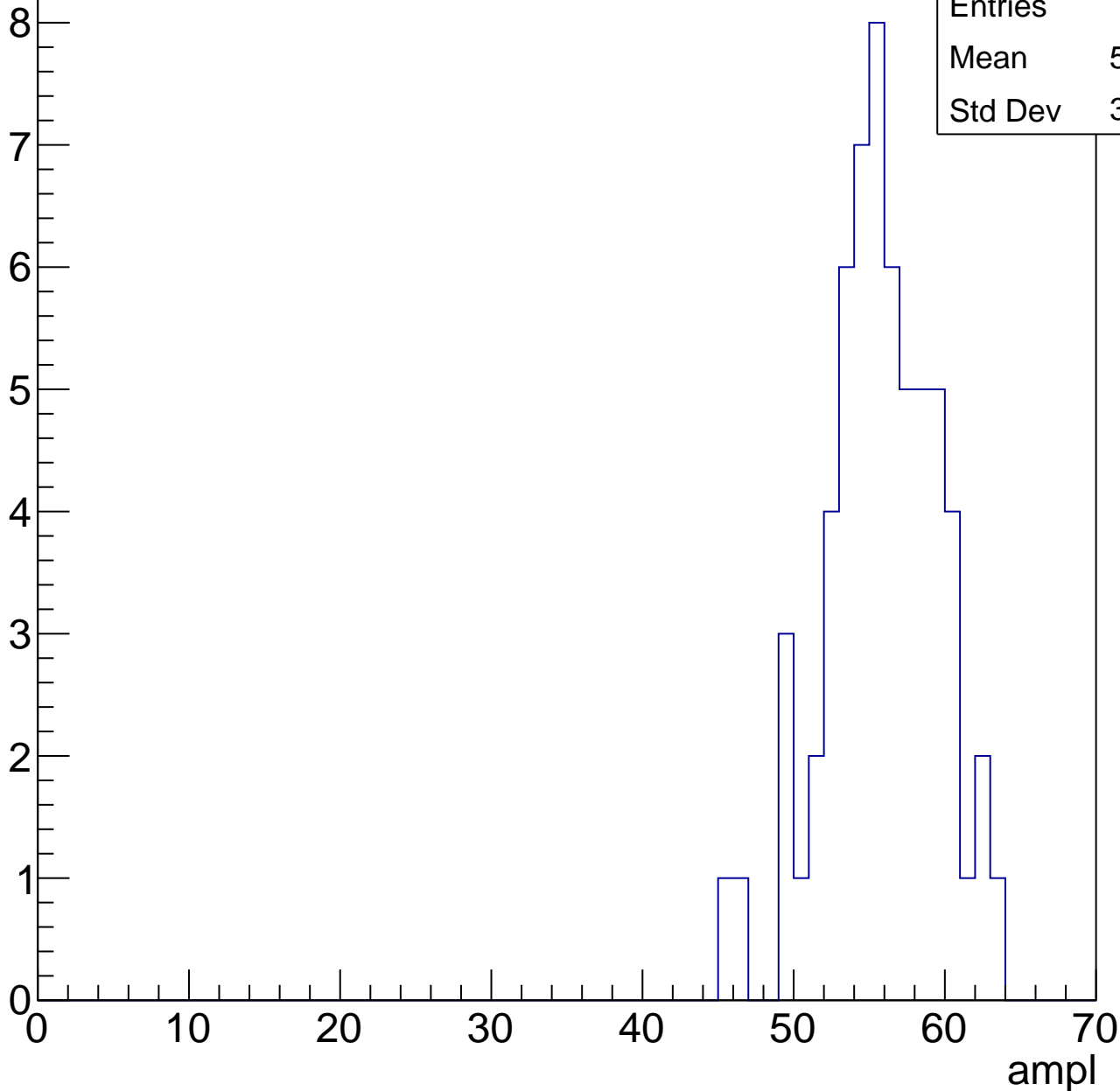


B1L102S, U10-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.29
Std Dev	3.717

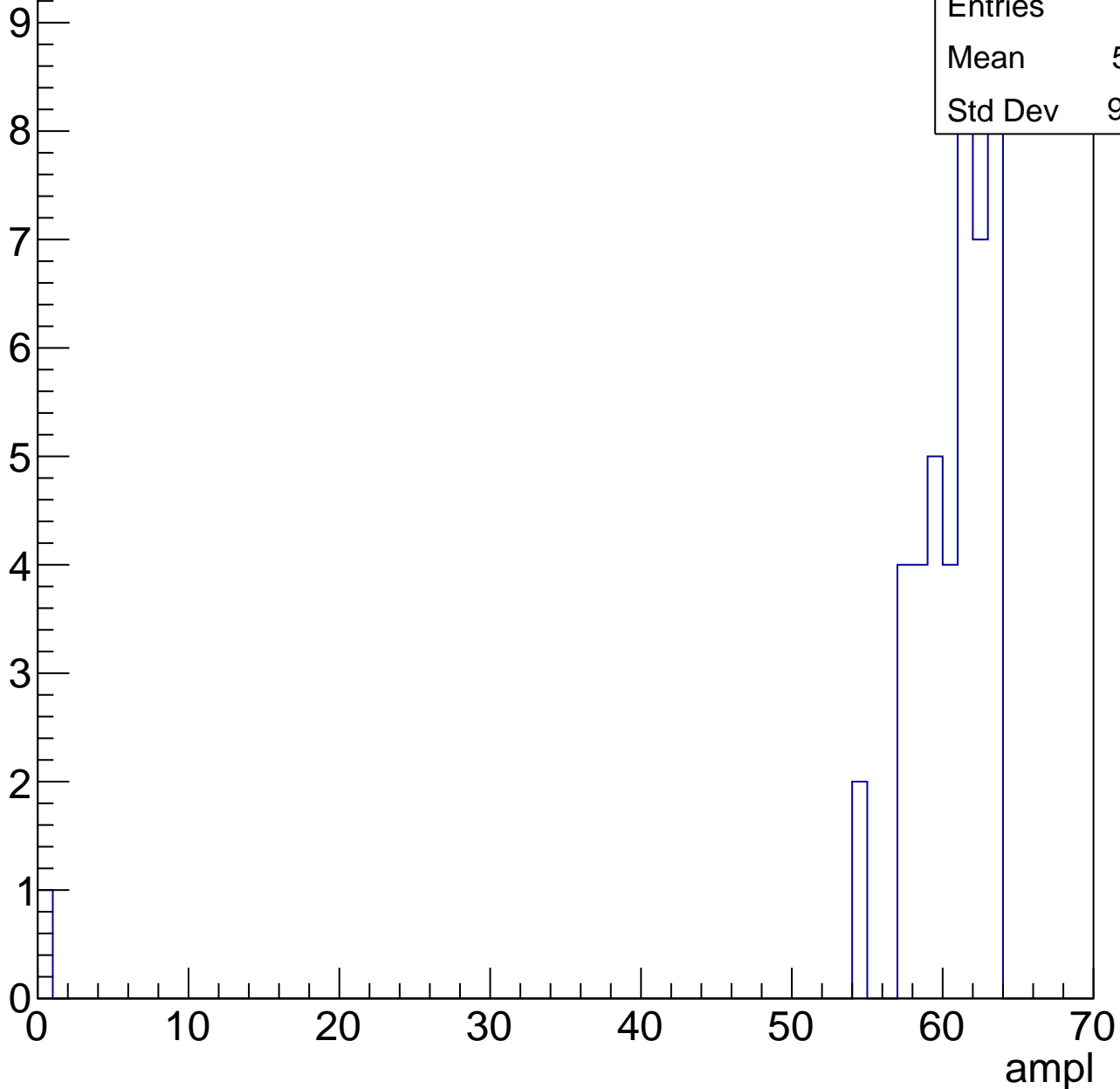


B1L102S, U10-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.91
Std Dev	9.285



B1L102S, U10-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	61
Std Dev	0

B1L102S, U10-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch108, adc0

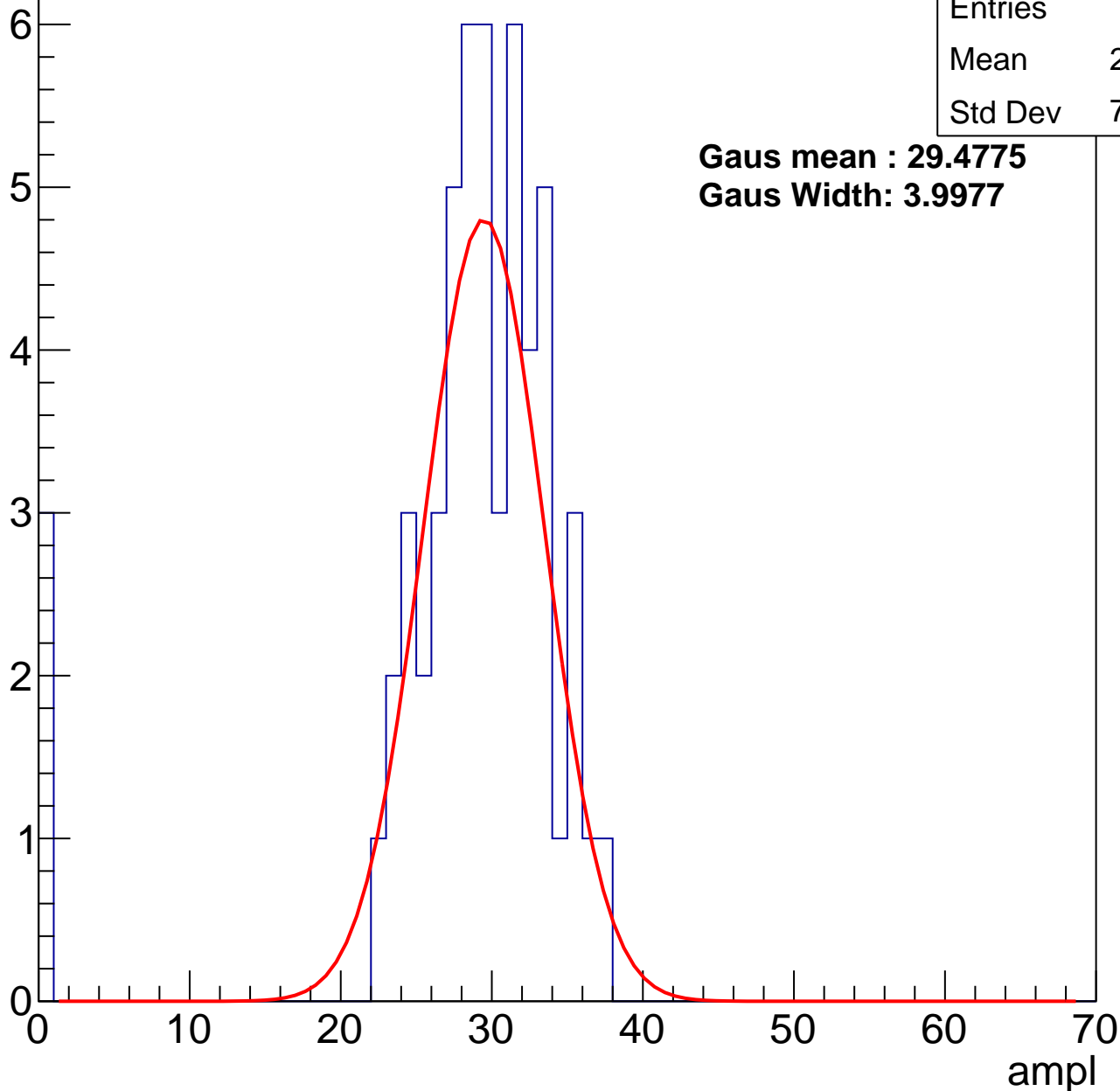
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	27.75
Std Dev	7.515

Gaus mean : 29.4775

Gaus Width: 3.9977



B1L102S, U10-ch108, adc1

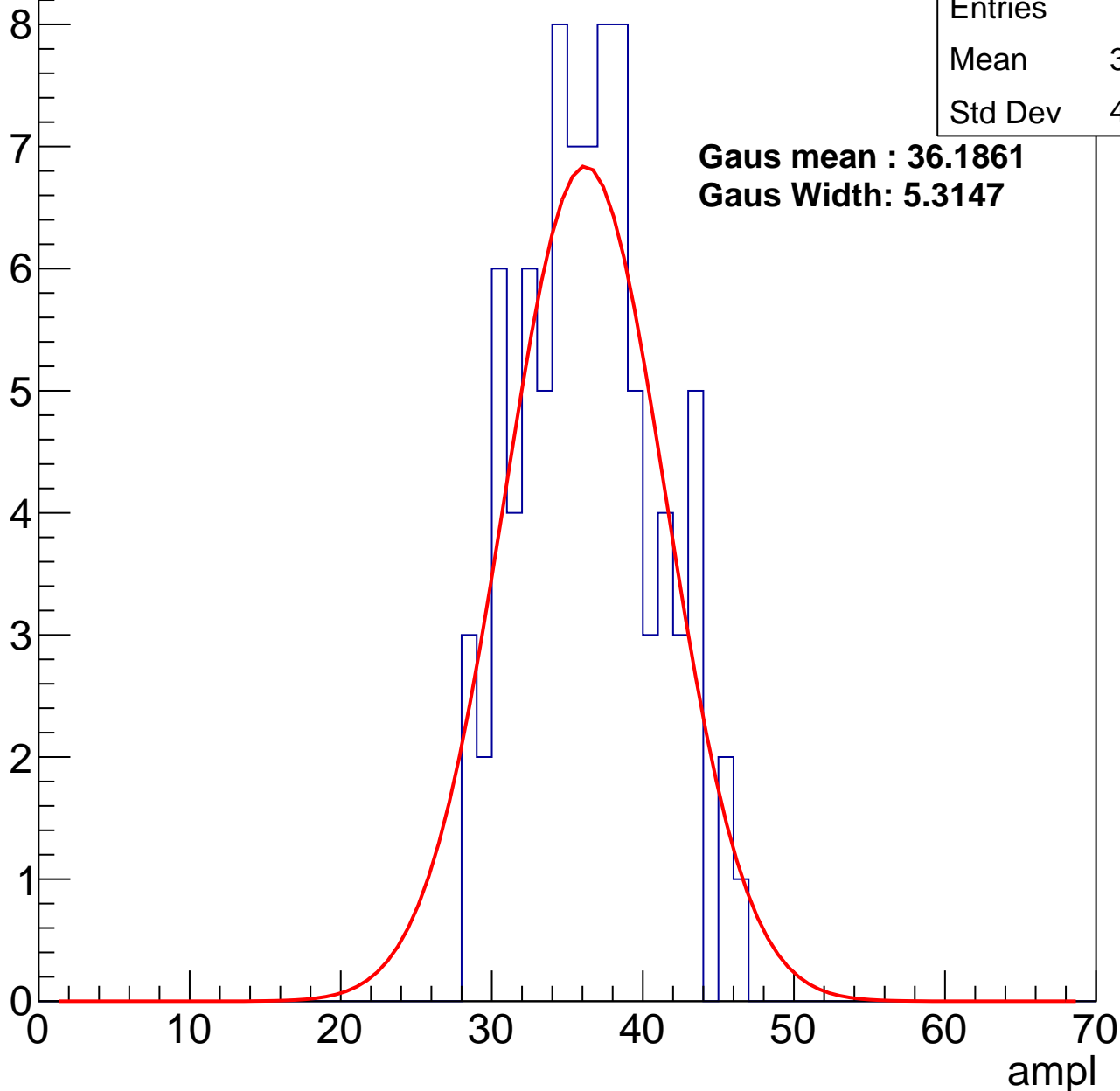
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.95
Std Dev	4.334

Gaus mean : 36.1861

Gaus Width: 5.3147



B1L102S, U10-ch108, adc2

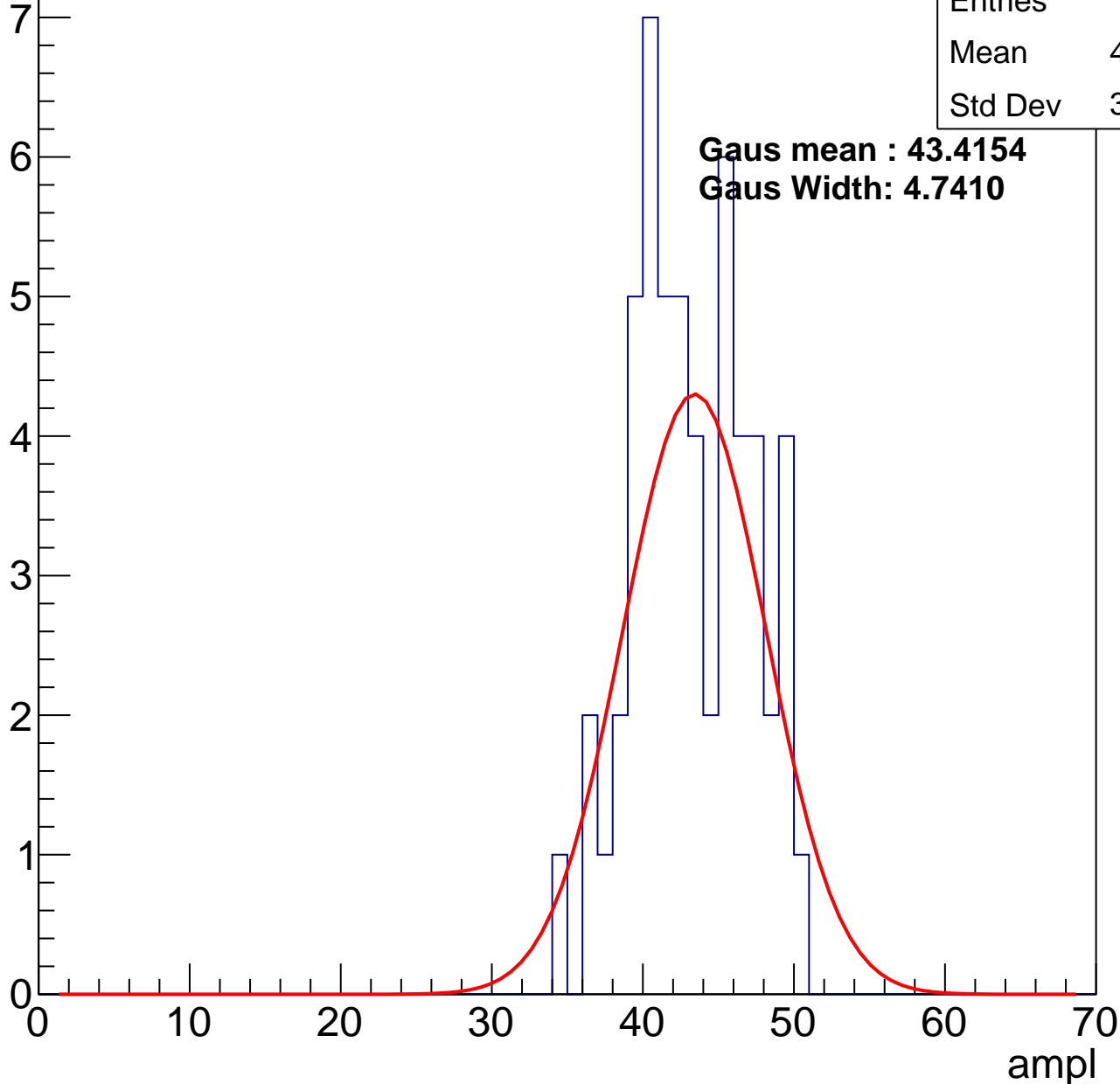
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.78
Std Dev	3.812

Gaus mean : 43.4154

Gaus Width: 4.7410

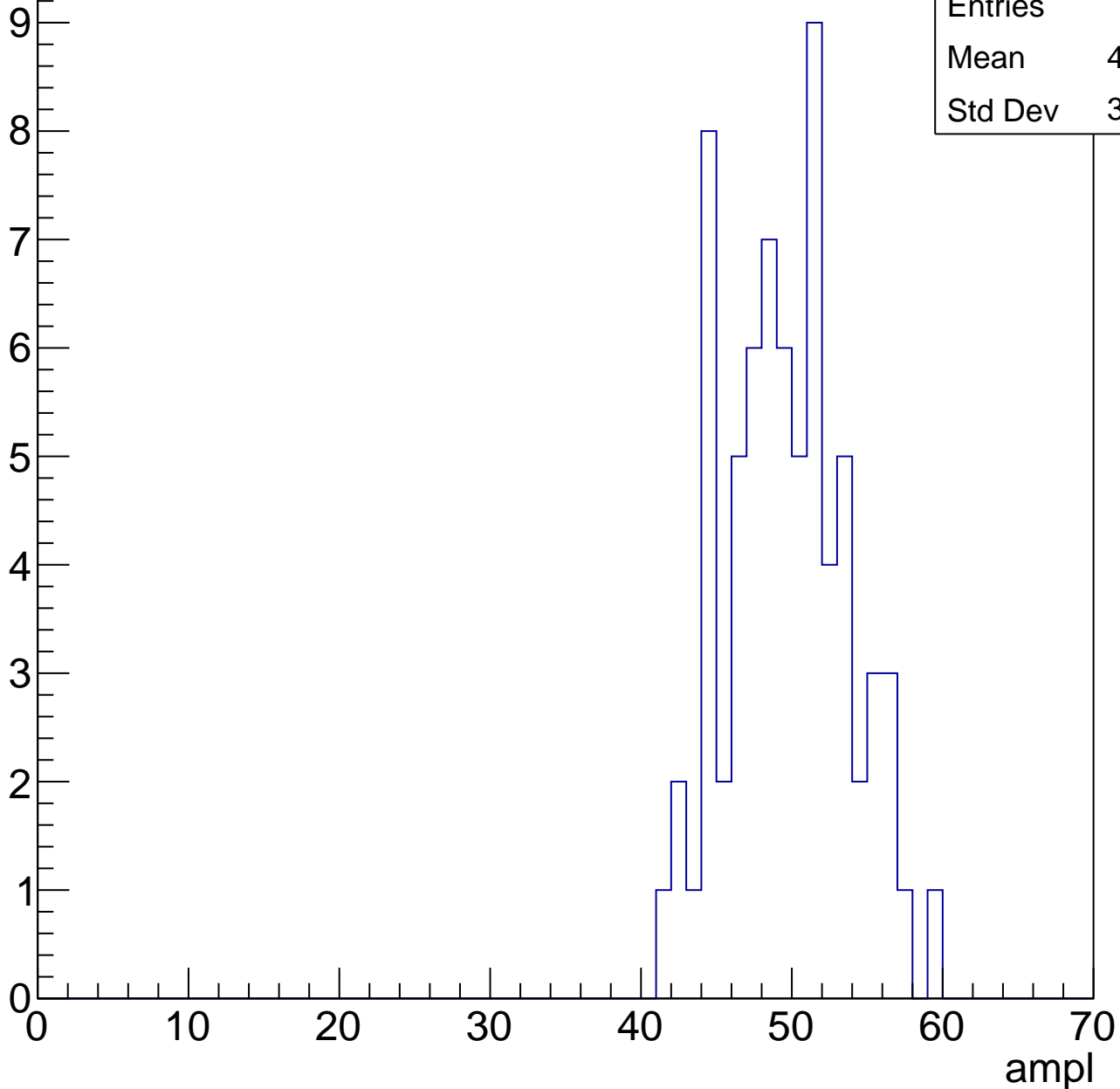


B1L102S, U10-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.17
Std Dev	3.989

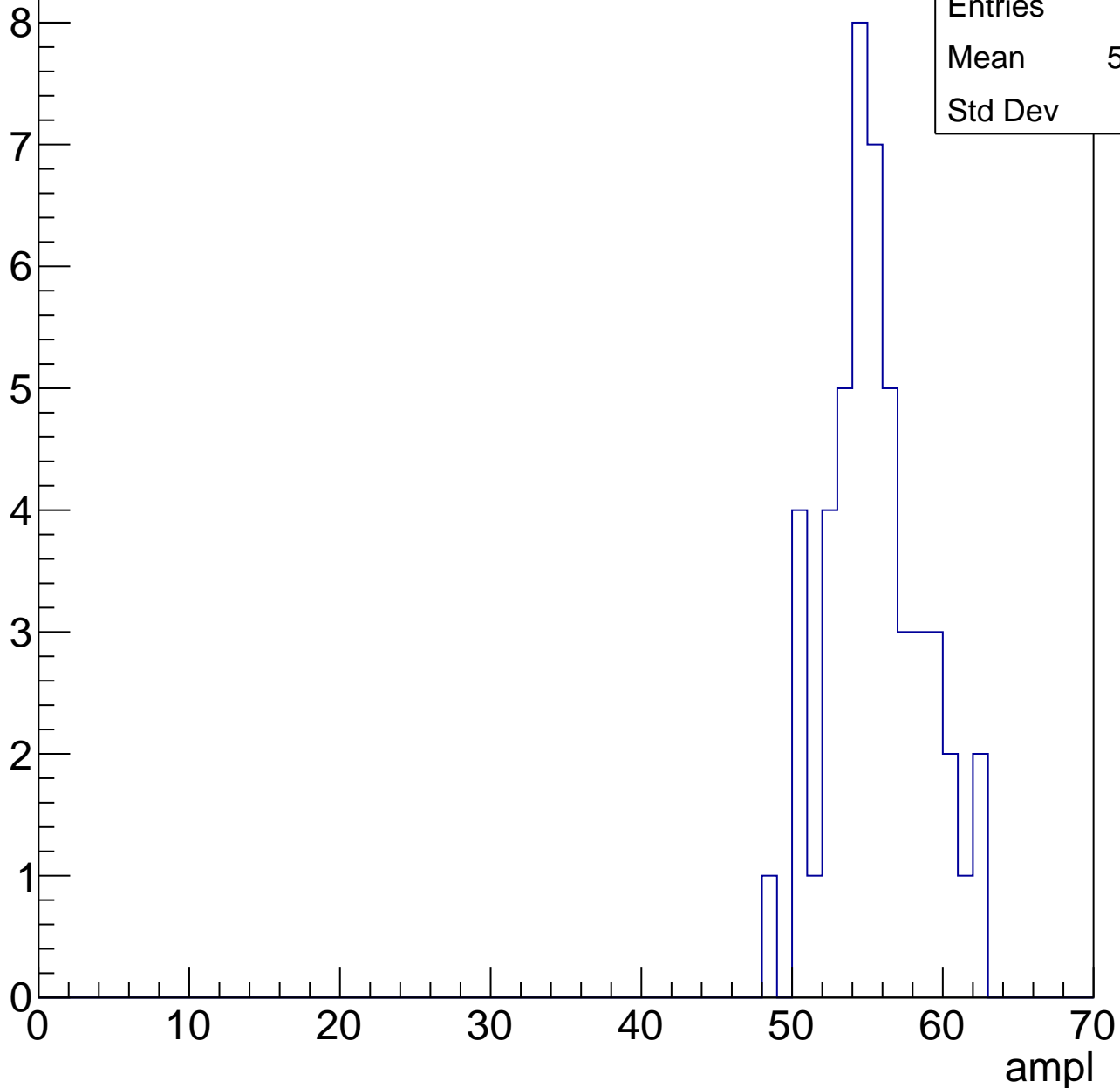


B1L102S, U10-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.02
Std Dev	3.21

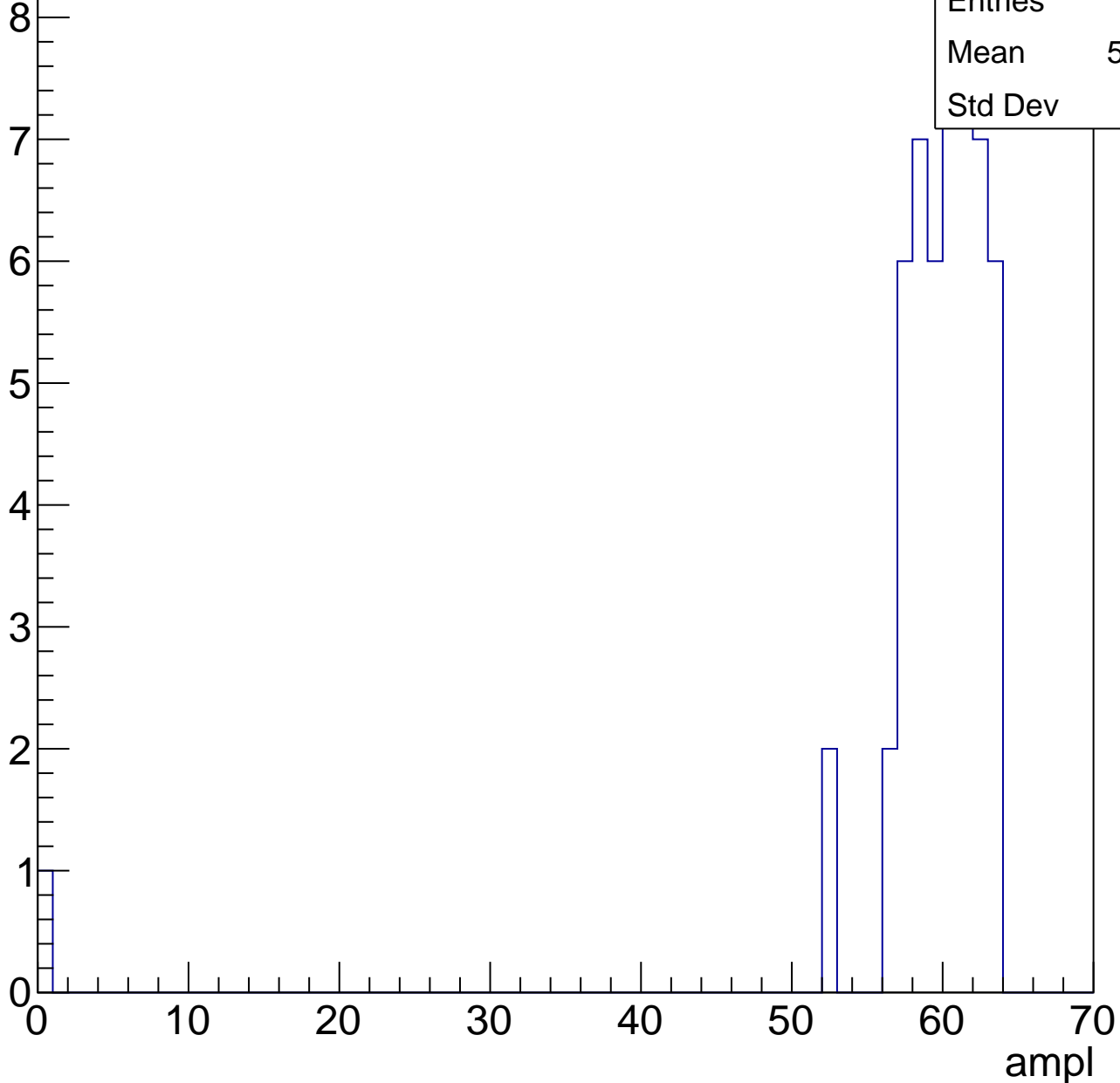


B1L102S, U10-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.45
Std Dev	8.48



B1L102S, U10-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

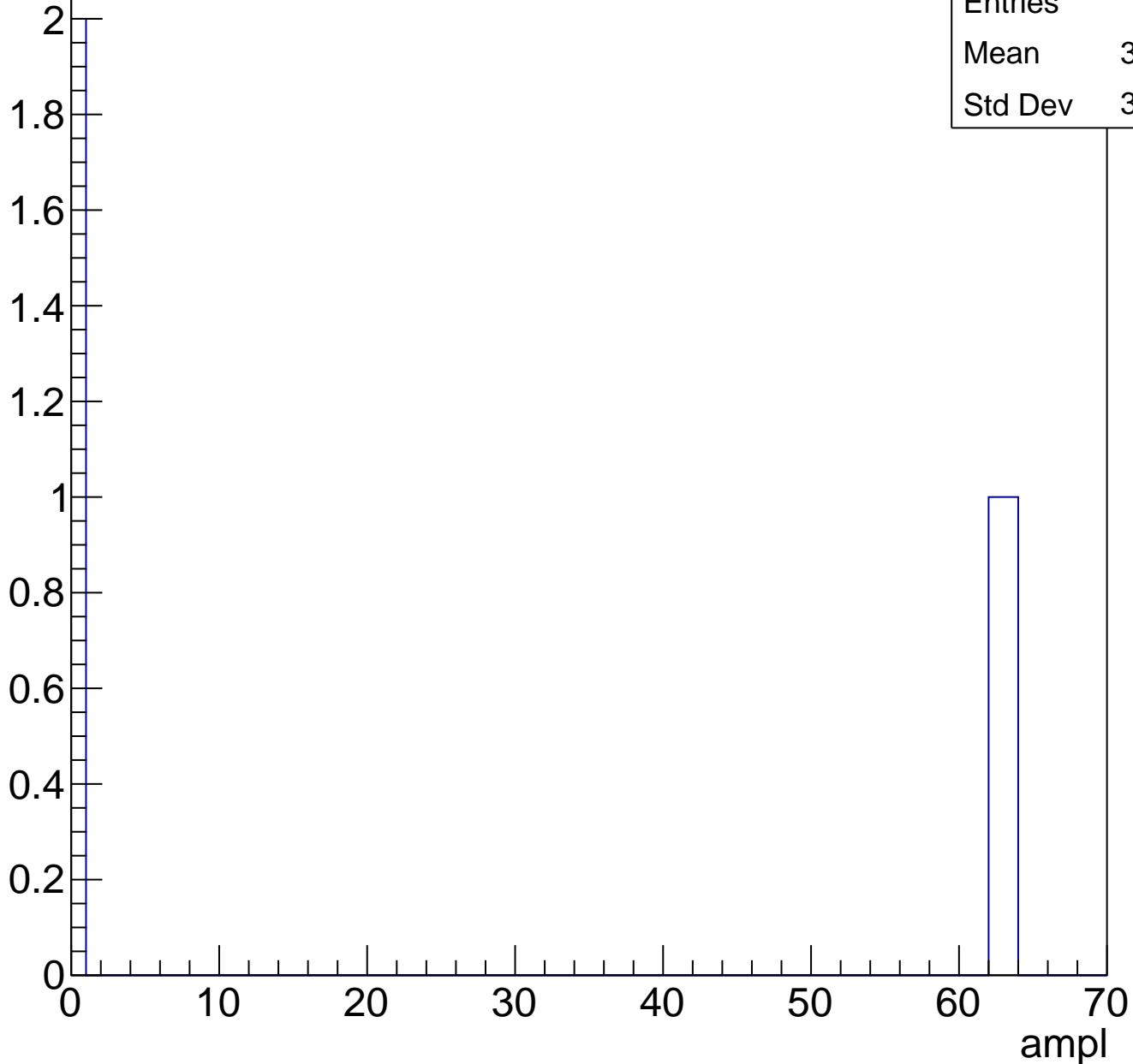
Entry



B1L102S, U10-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L102S, U10-ch109, adc0

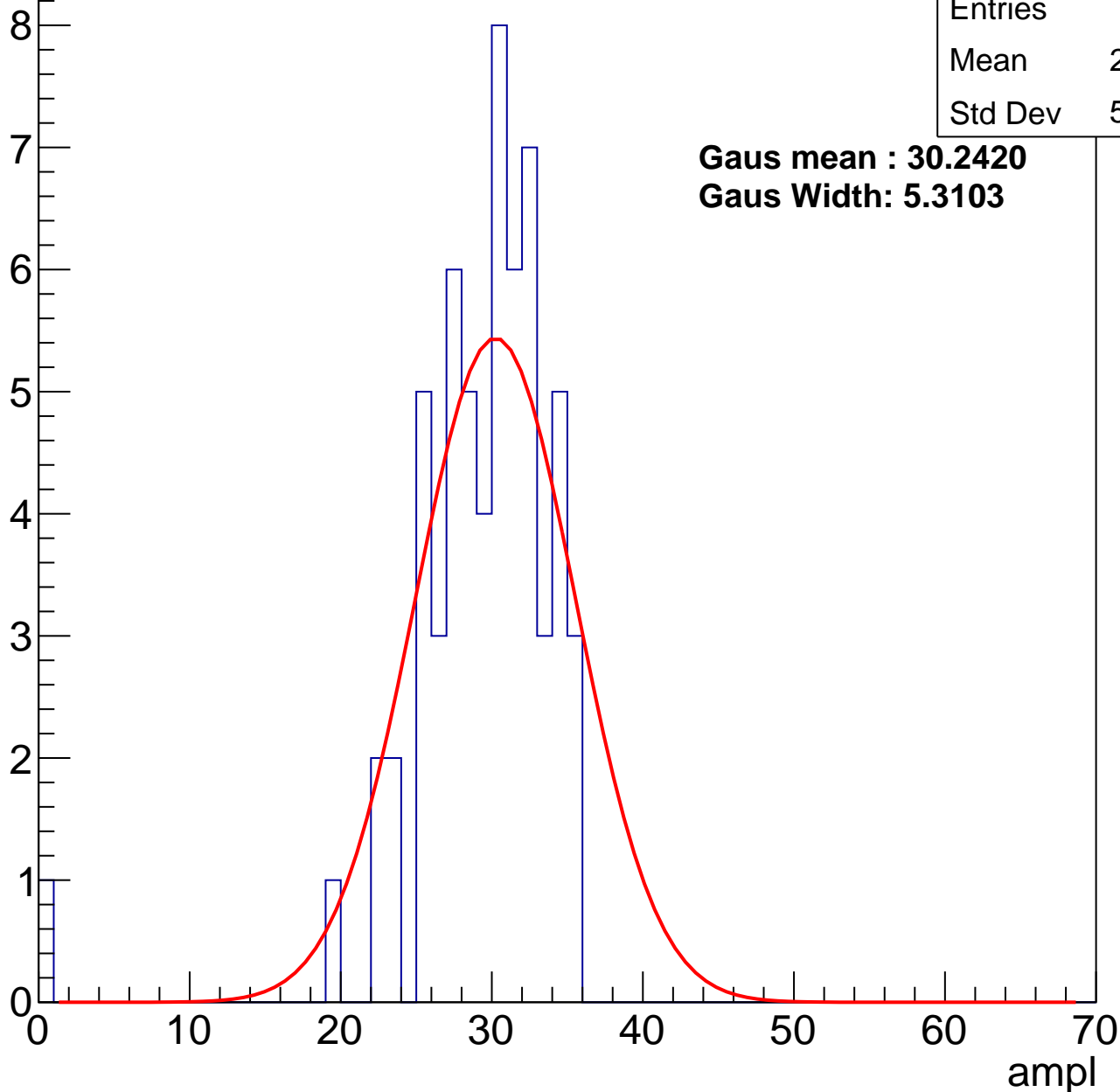
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.75
Std Dev	5.152

Gaus mean : 30.2420

Gaus Width: 5.3103



B1L102S, U10-ch109, adc1

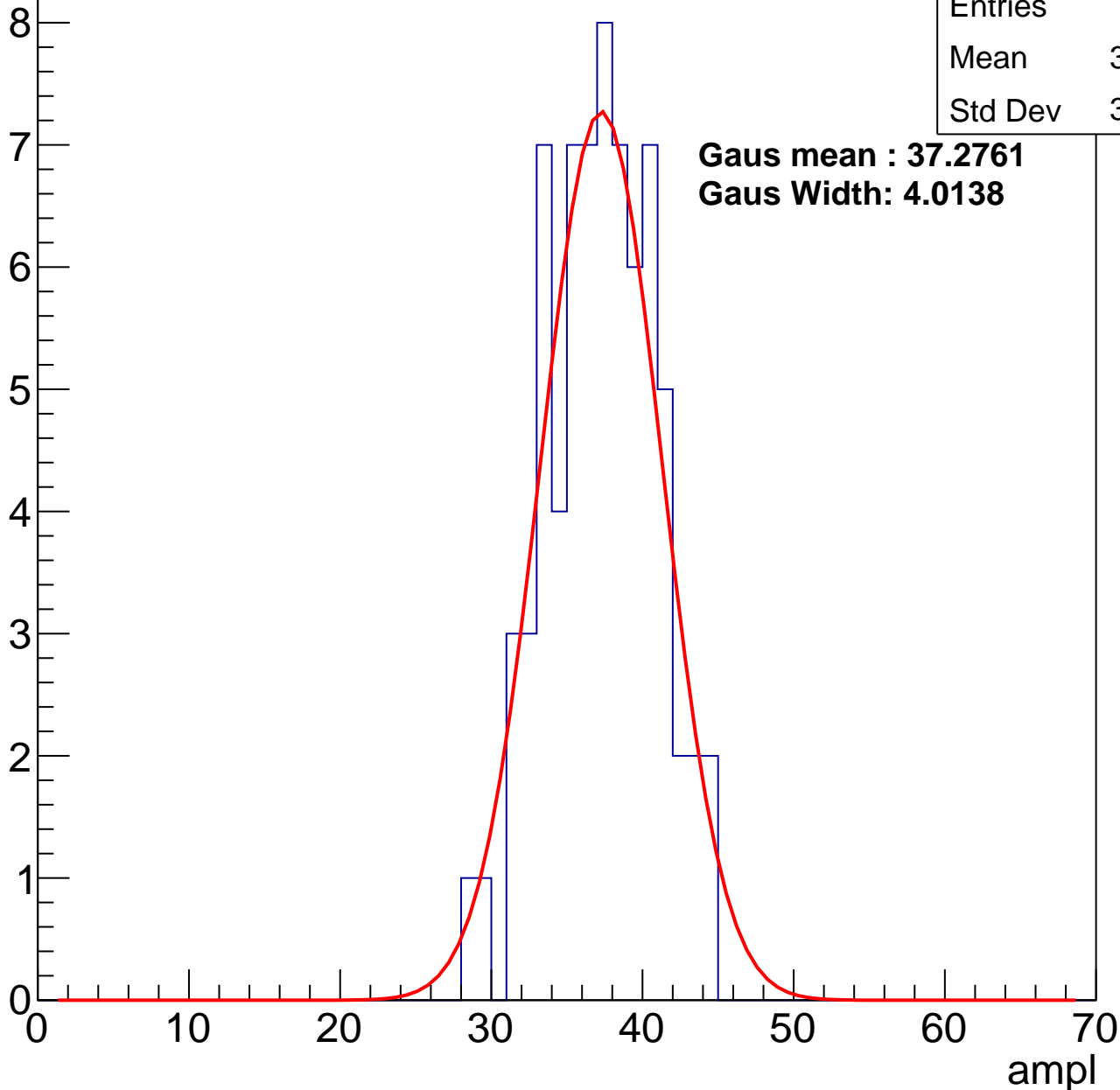
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.79
Std Dev	3.539

Gaus mean : 37.2761

Gaus Width: 4.0138



B1L102S, U10-ch109, adc2

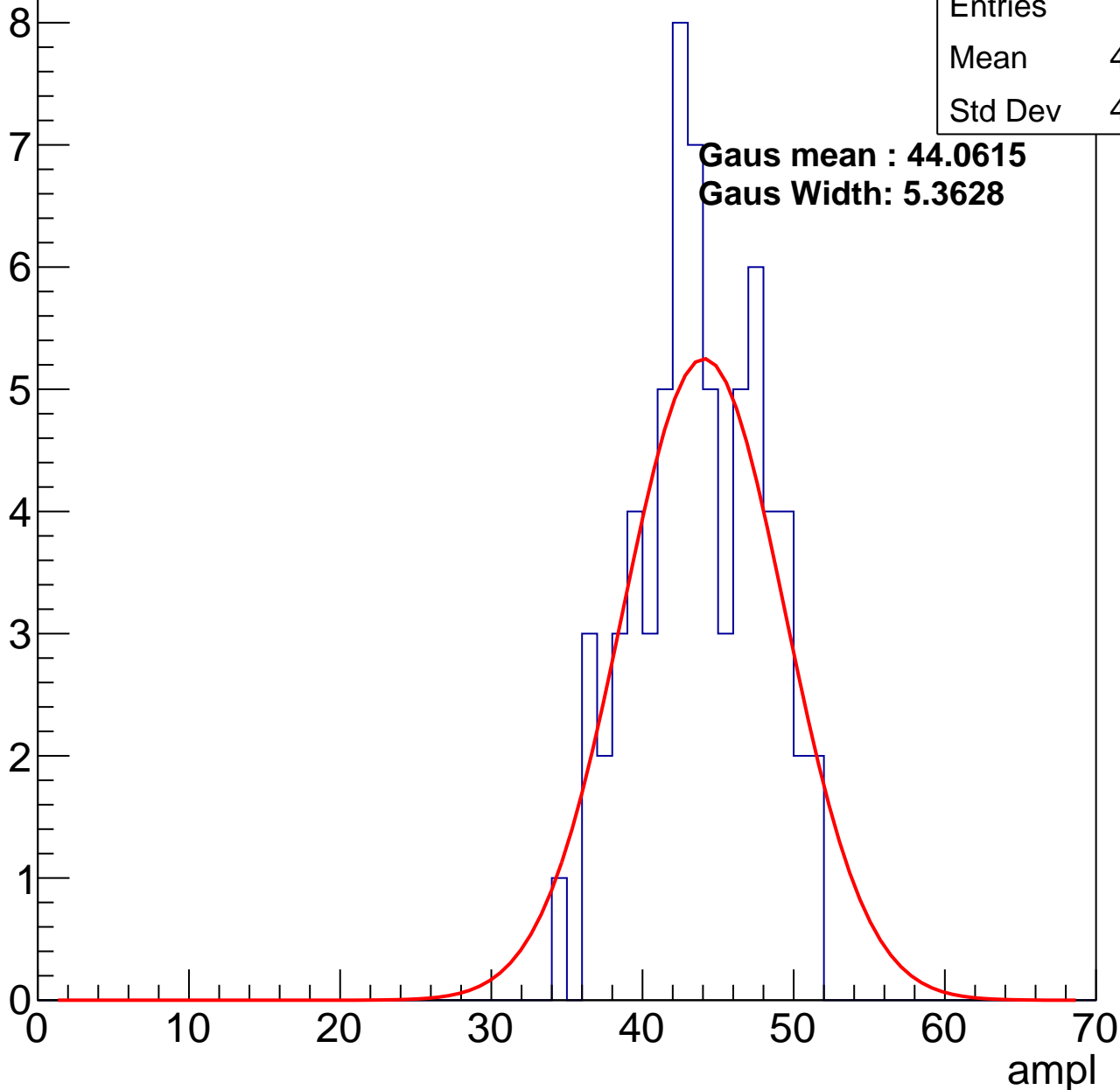
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.36
Std Dev	4.077

Gaus mean : 44.0615

Gaus Width: 5.3628

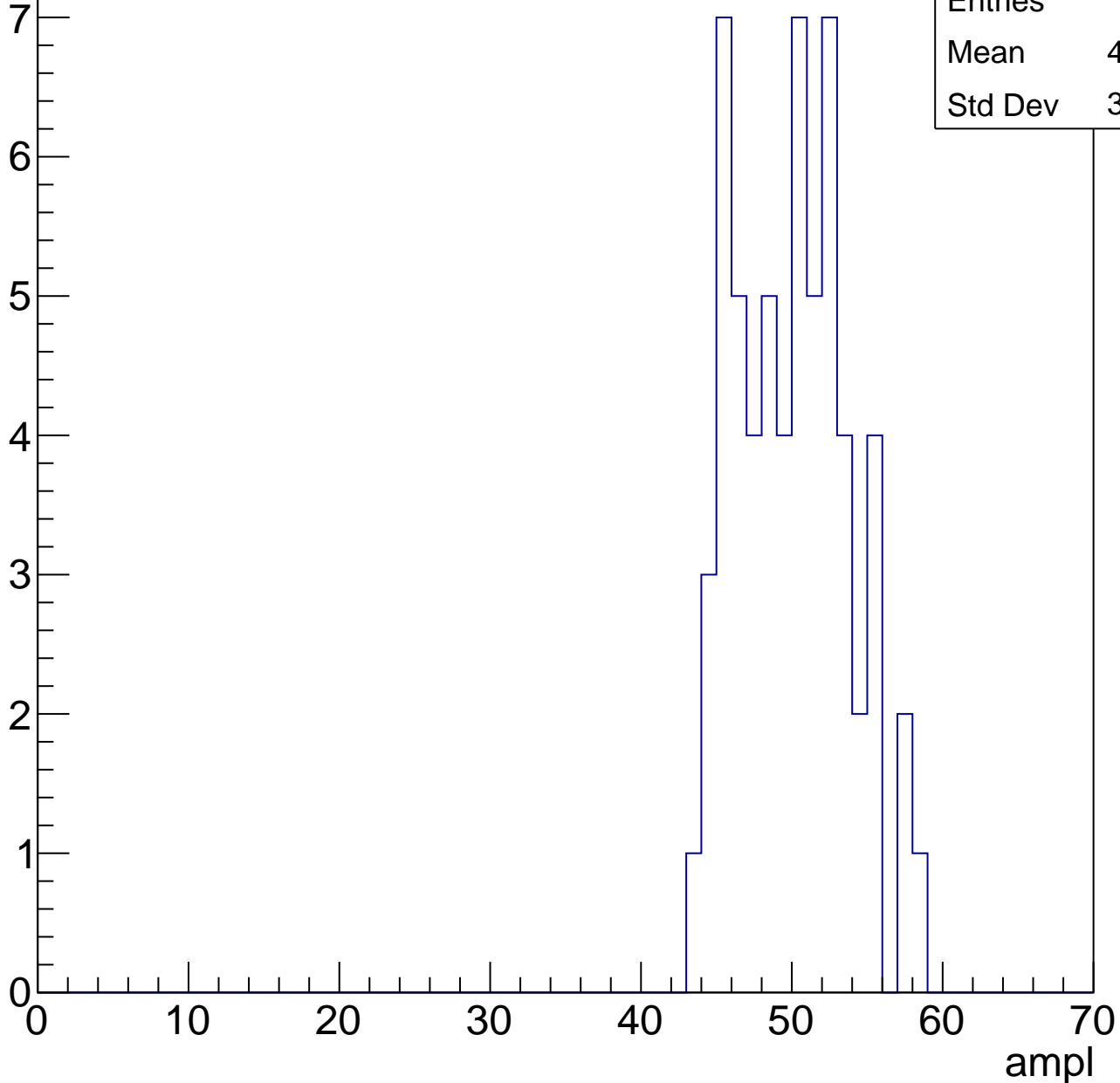


B1L102S, U10-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.59
Std Dev	3.677

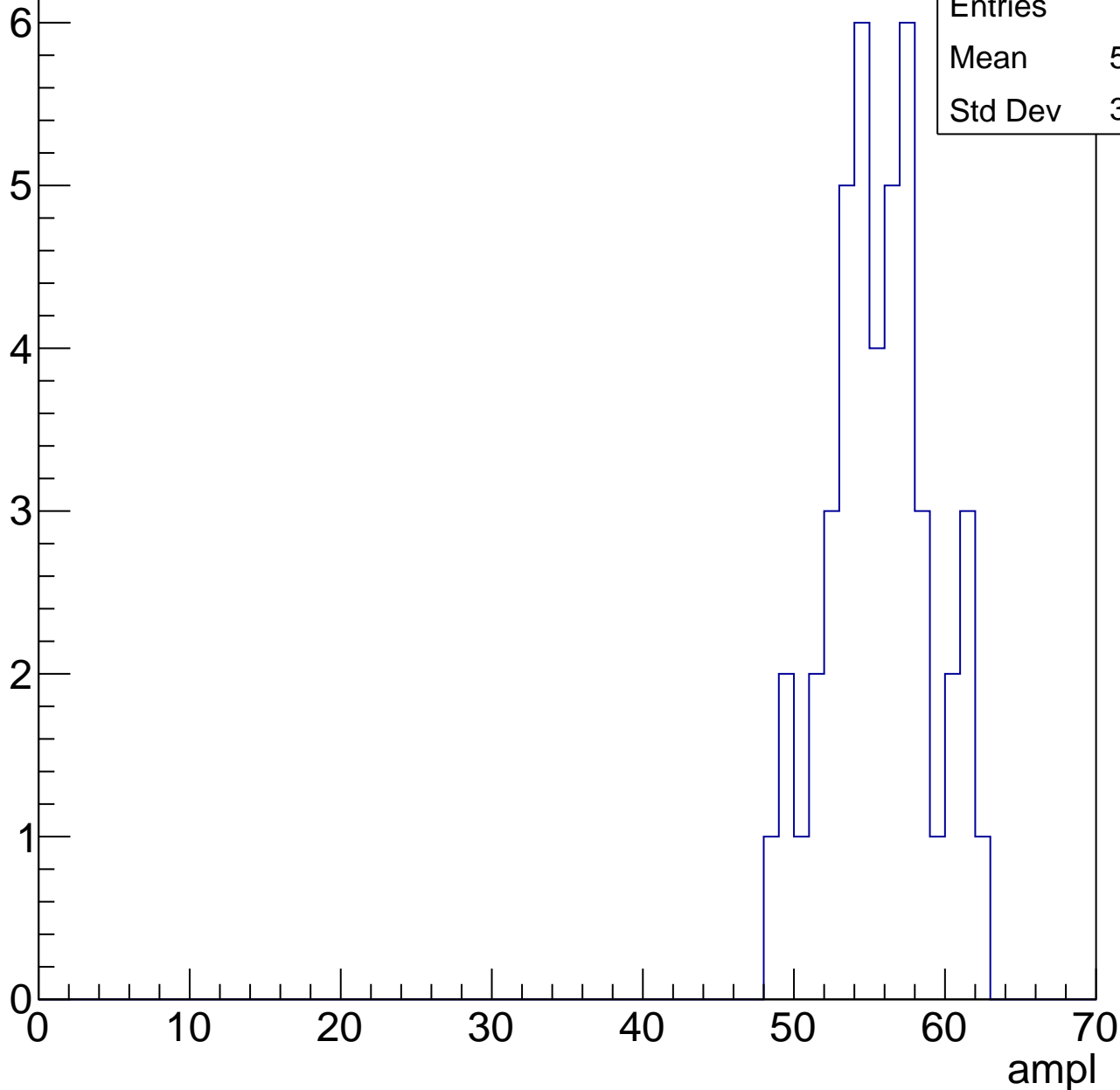


B1L102S, U10-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	55.18
Std Dev	3.362

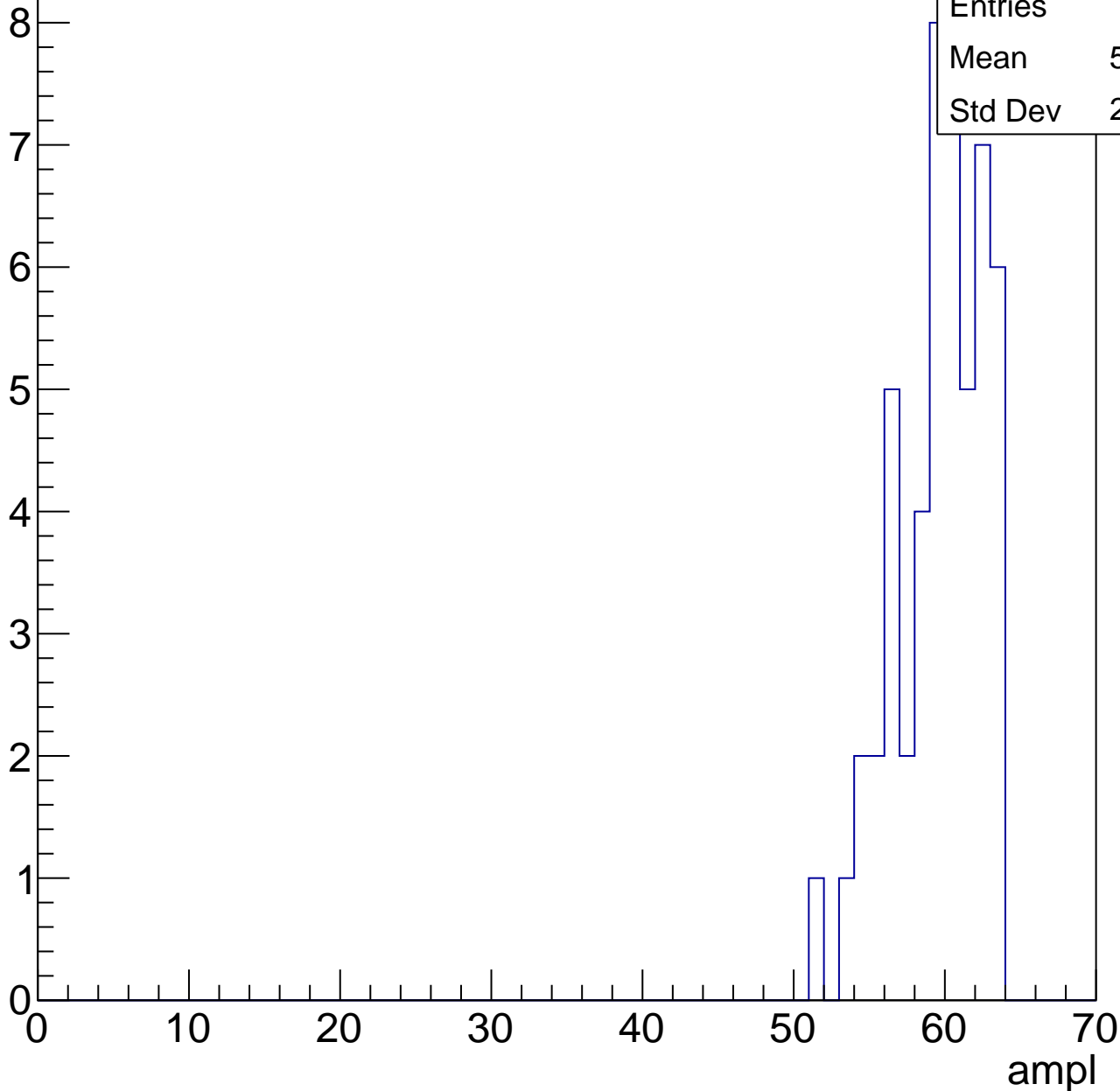


B1L102S, U10-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

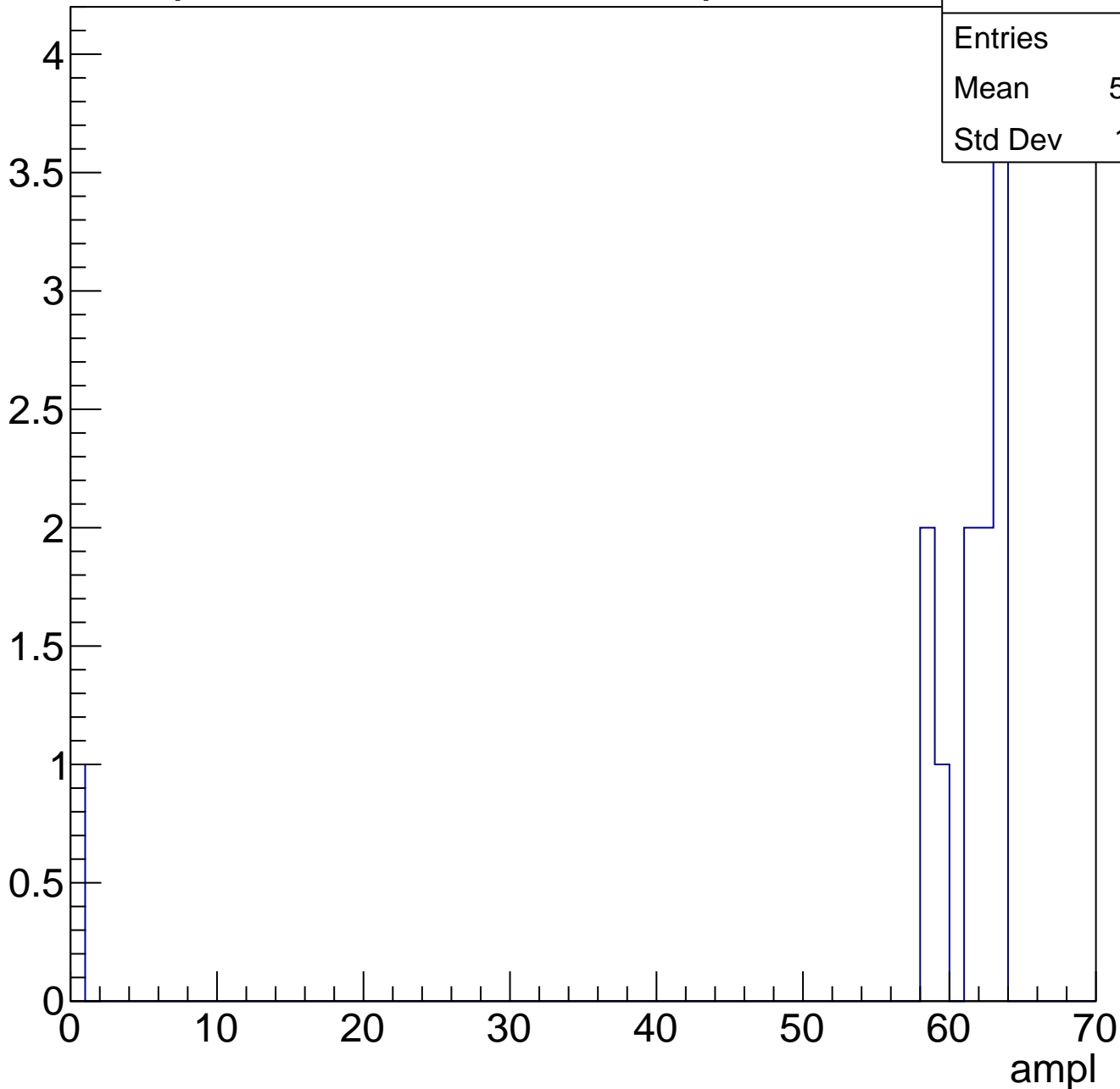
Entries	51
Mean	59.16
Std Dev	2.886



B1L102S, U10-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch110, adc0

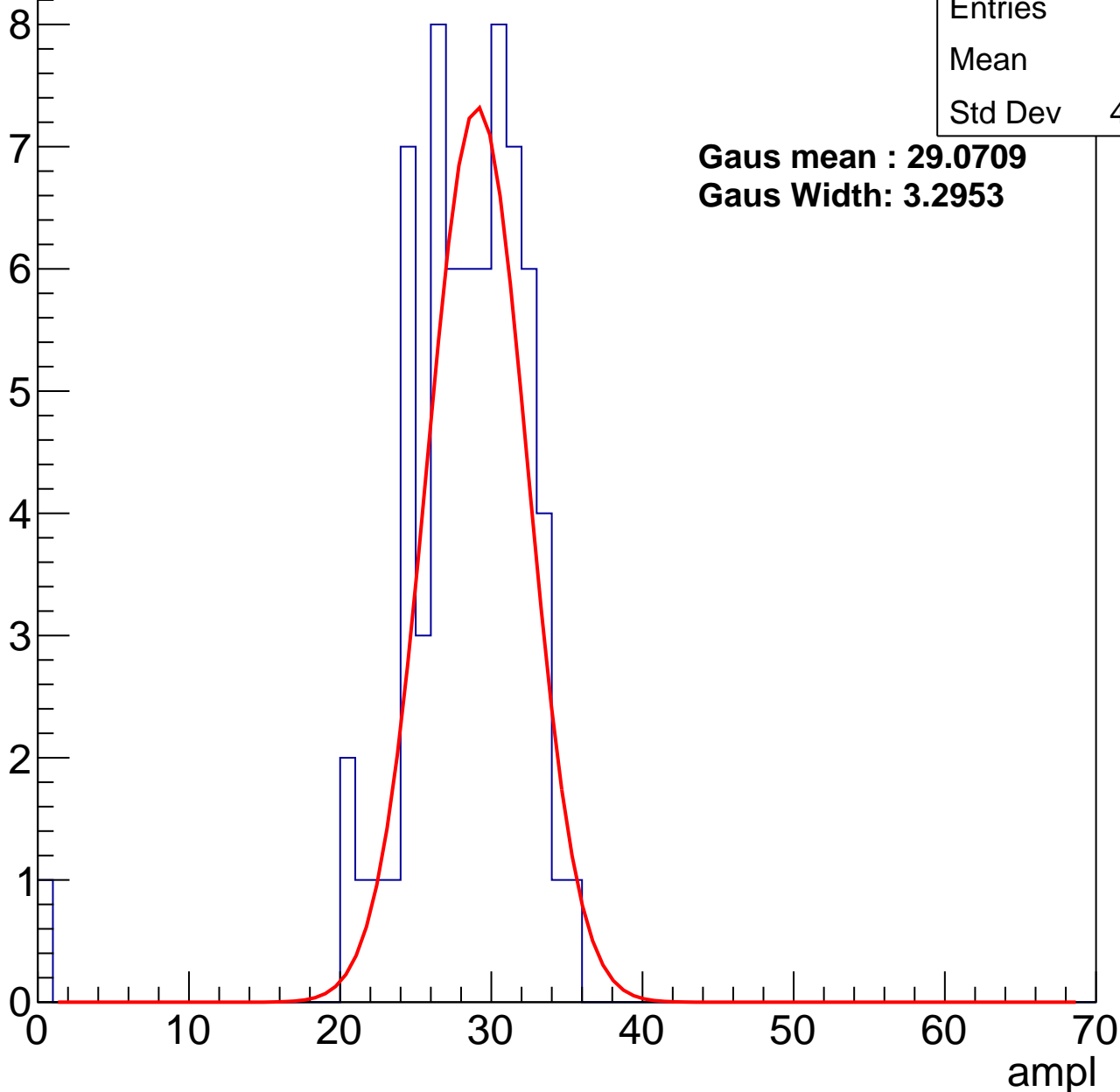
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.7
Std Dev	4.777

Gaus mean : 29.0709

Gaus Width: 3.2953



B1L102S, U10-ch110, adc1

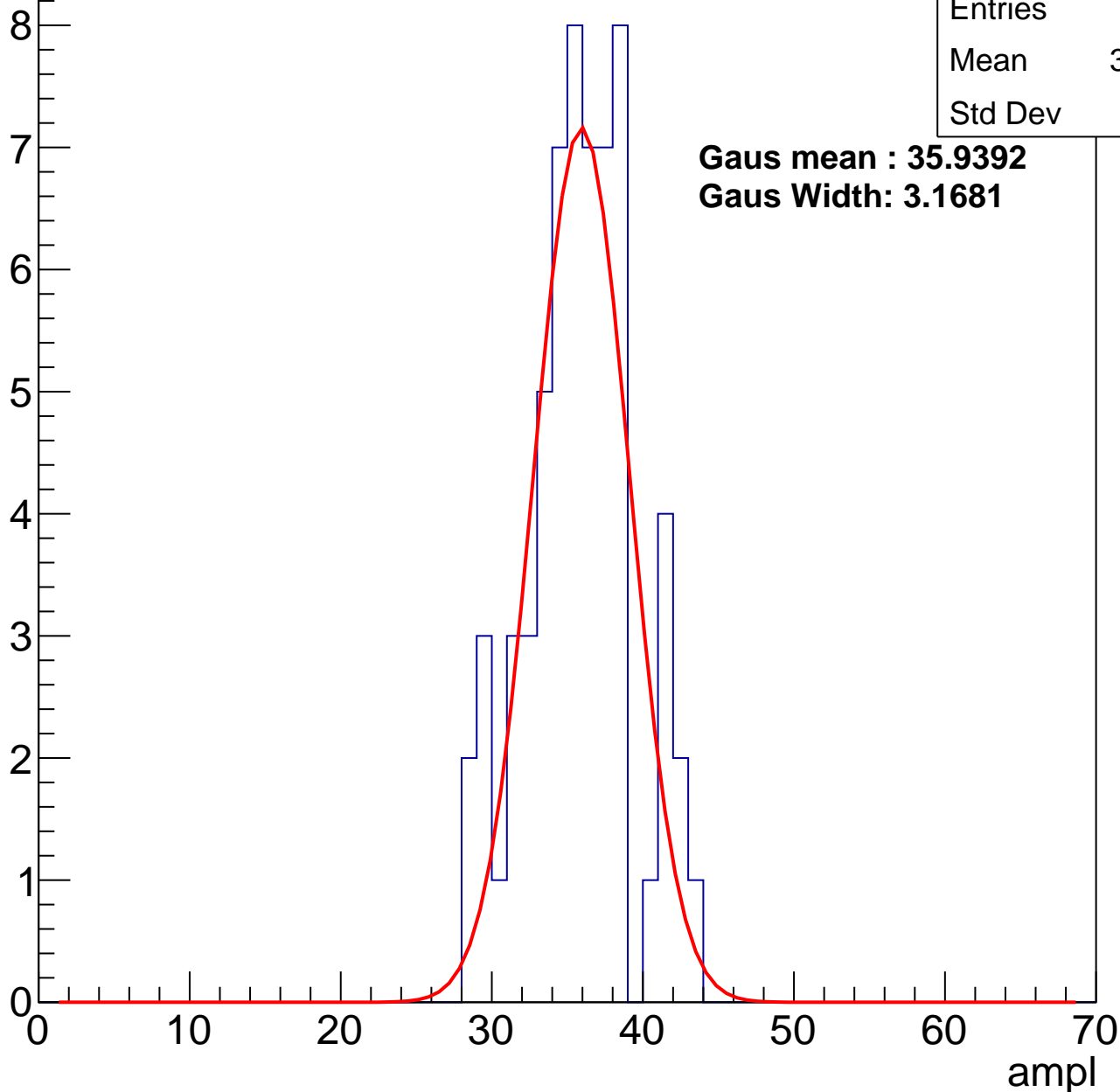
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.34
Std Dev	3.51

Gaus mean : 35.9392

Gaus Width: 3.1681



B1L102S, U10-ch110, adc2

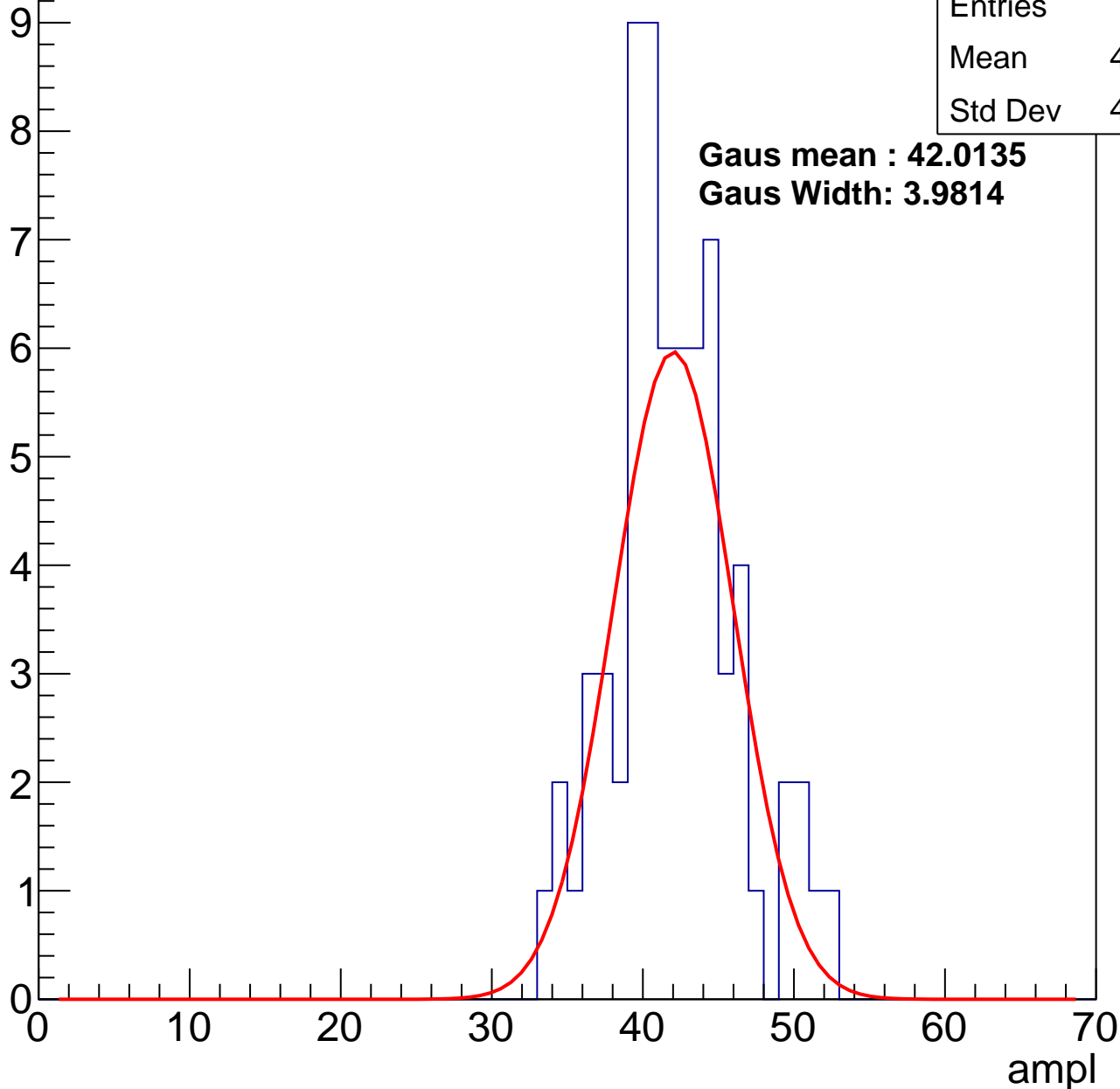
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.64
Std Dev	4.072

Gaus mean : 42.0135

Gaus Width: 3.9814

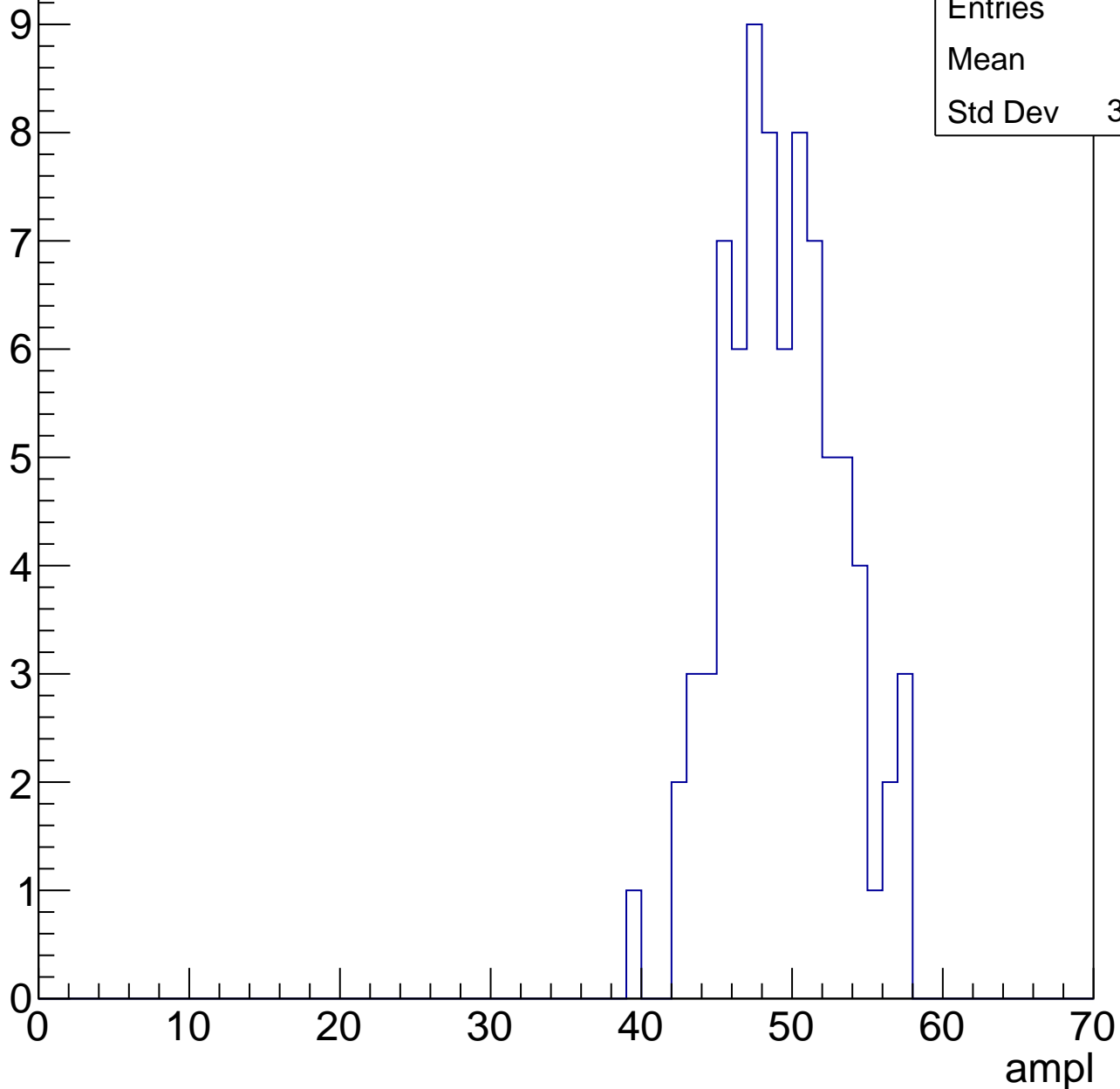


B1L102S, U10-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	48.9
Std Dev	3.836

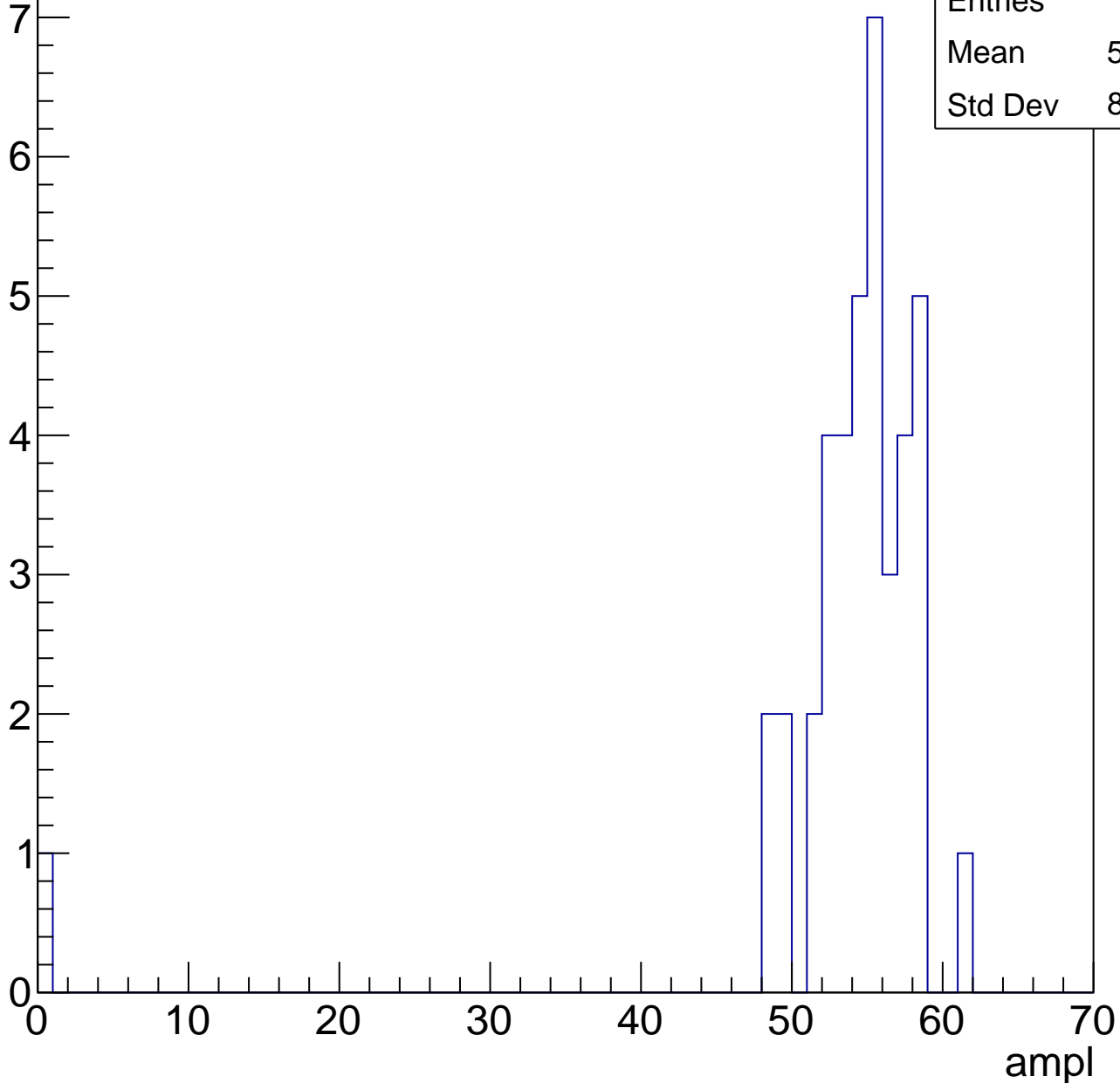


B1L102S, U10-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	52.95
Std Dev	8.966

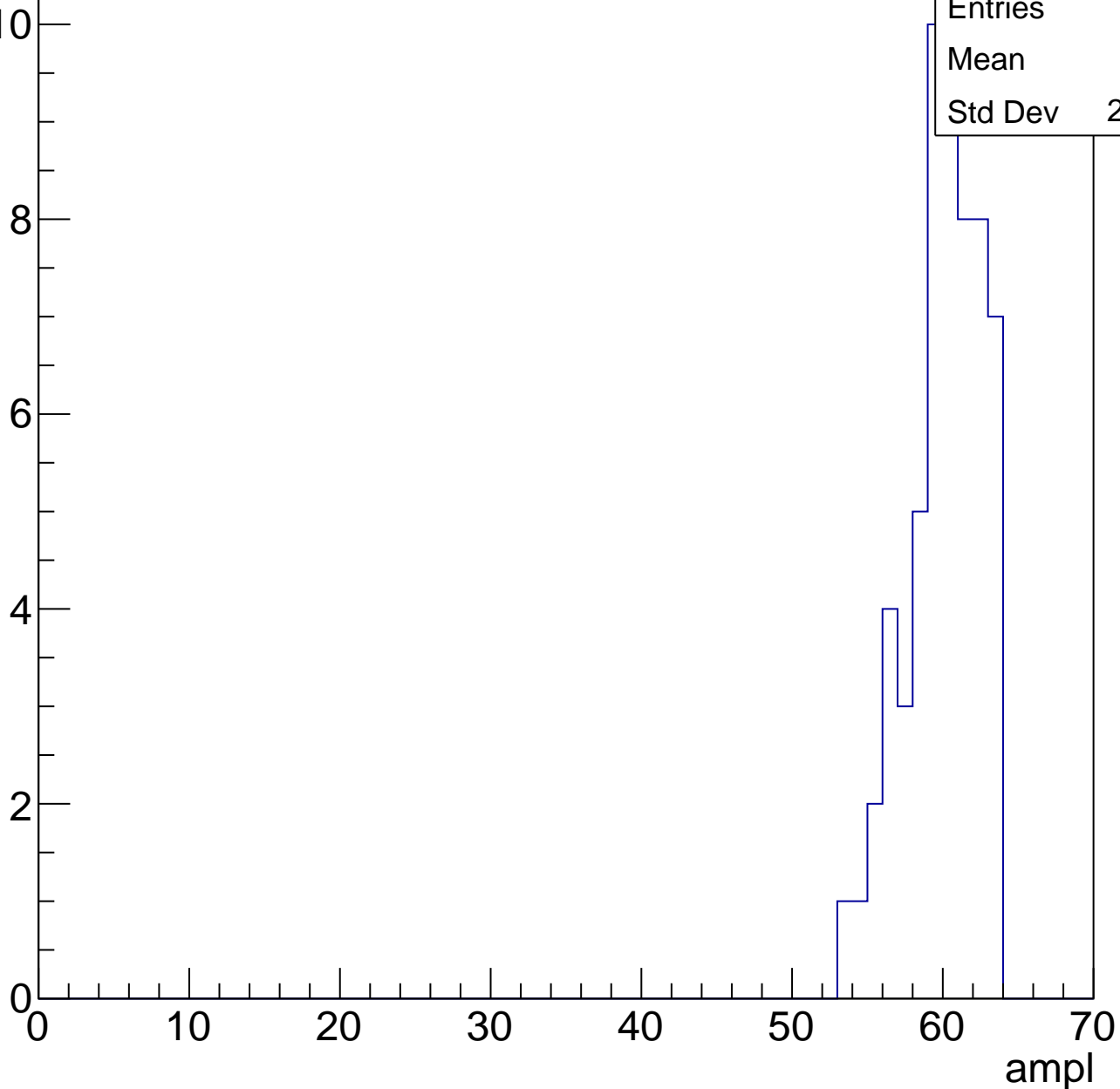


B1L102S, U10-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	59.6
Std Dev	2.456



B1L102S, U10-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

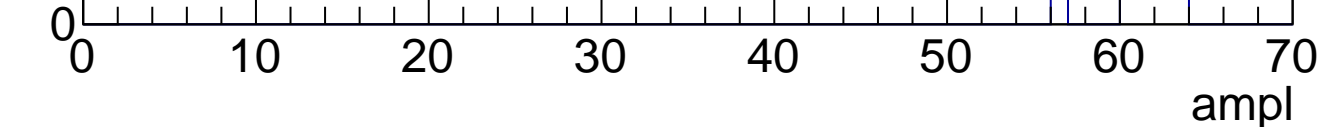
8

Mean

61.25

Std Dev

2.222



B1L102S, U10-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U10-ch111, adc0

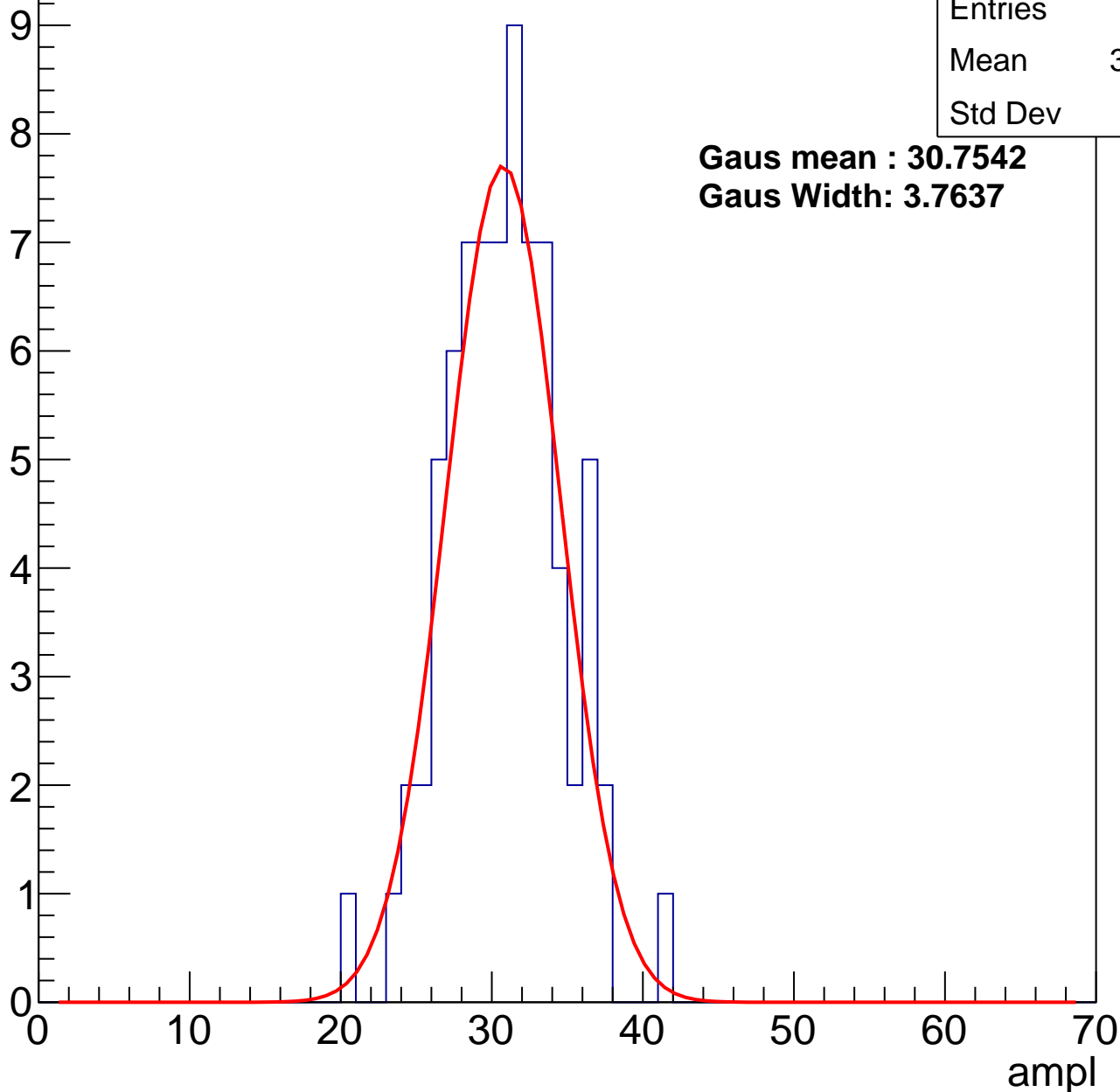
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	30.36
Std Dev	3.73

Gaus mean : 30.7542

Gaus Width: 3.7637



B1L102S, U10-ch111, adc1

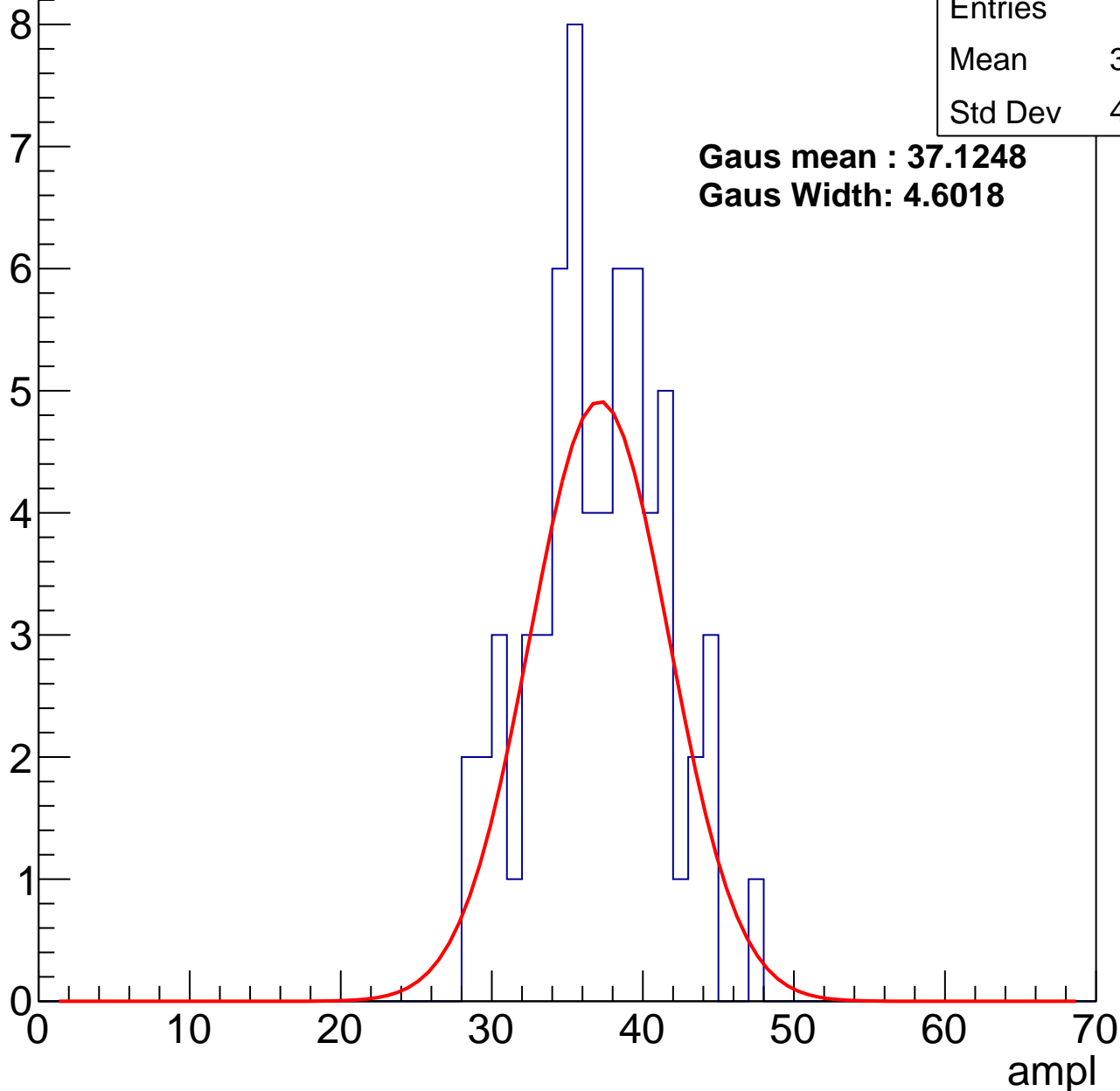
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.56
Std Dev	4.264

Gaus mean : 37.1248

Gaus Width: 4.6018



B1L102S, U10-ch111, adc2

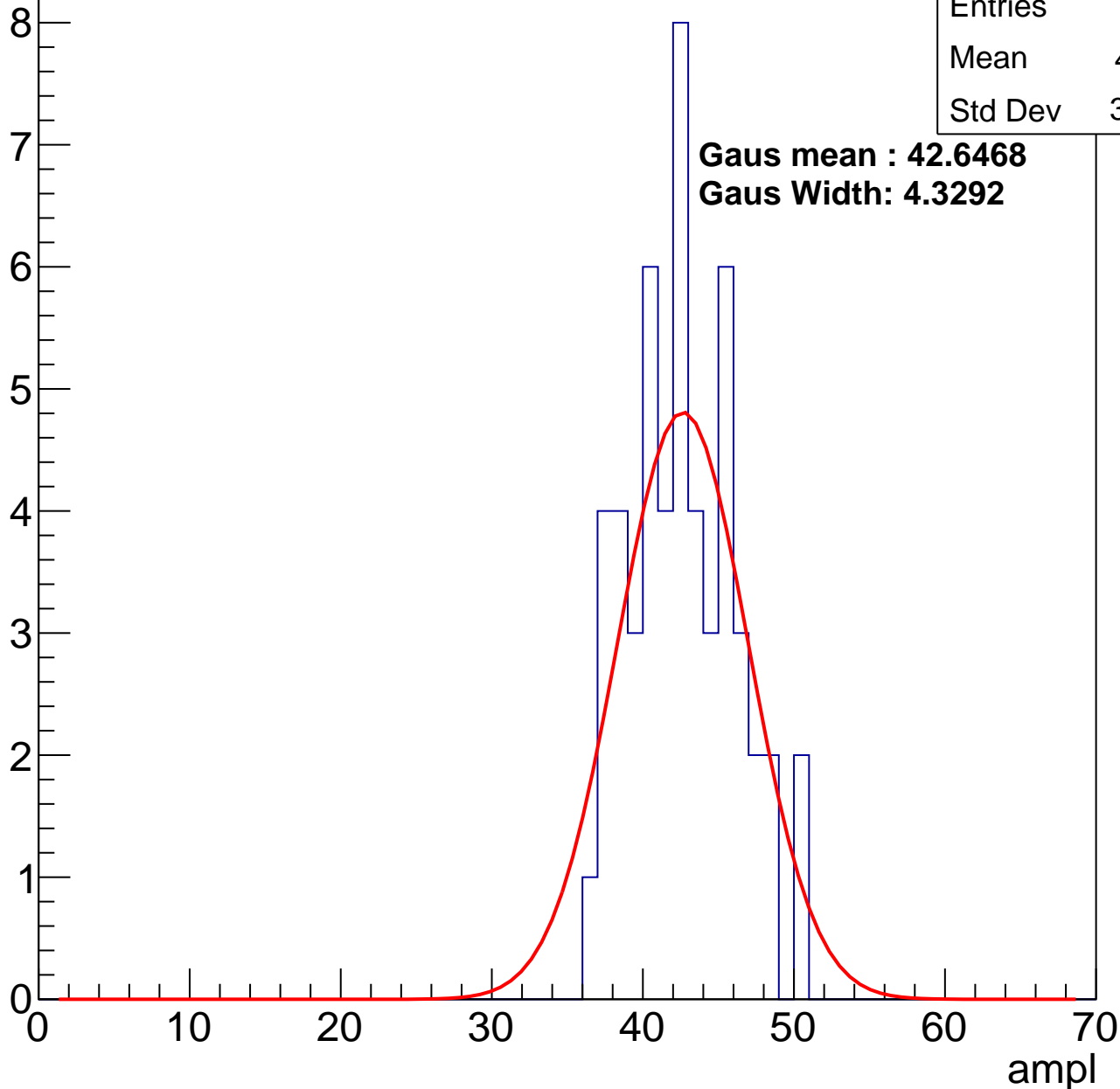
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.21
Std Dev	3.455

Gaus mean : 42.6468

Gaus Width: 4.3292

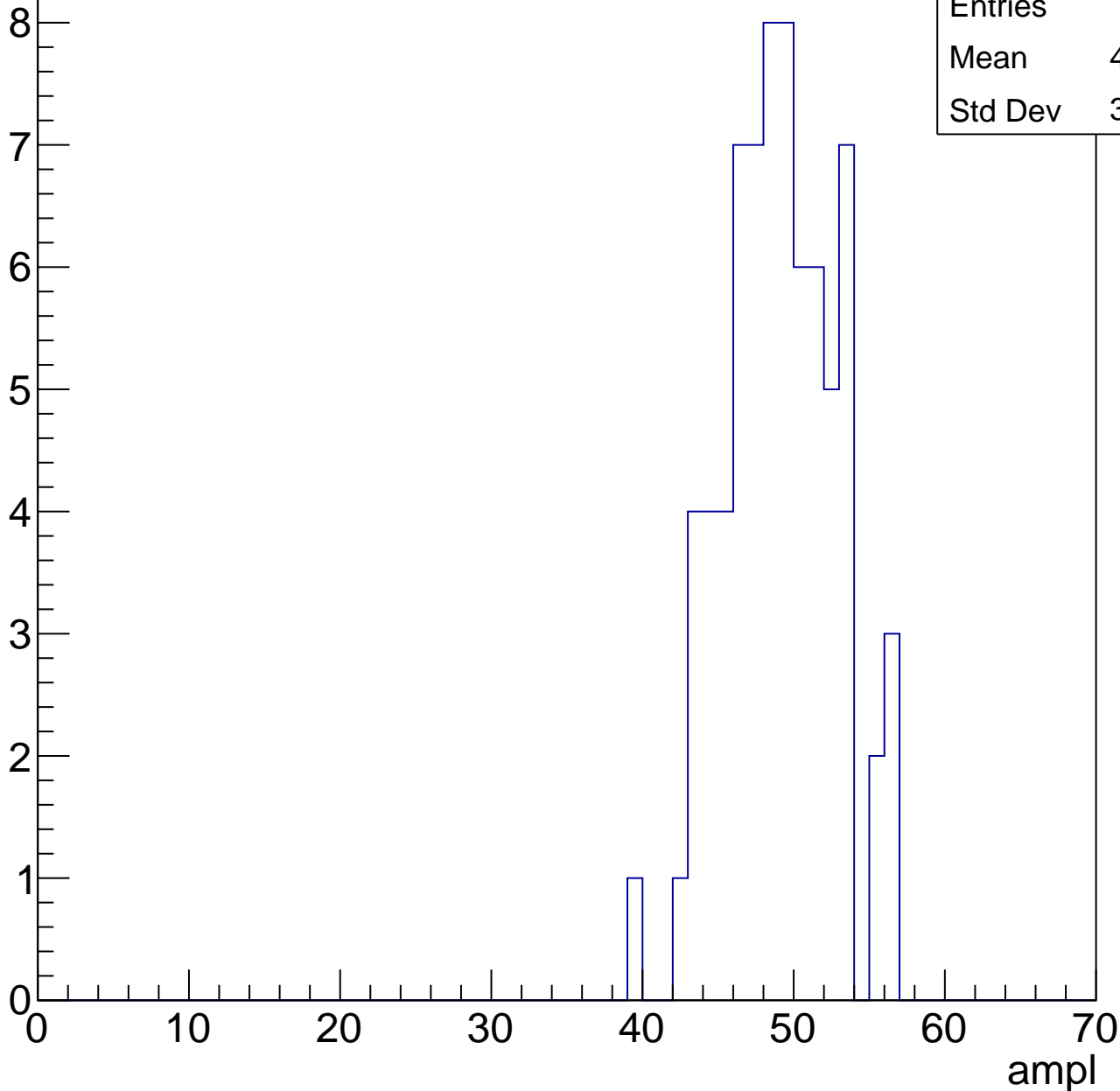


B1L102S, U10-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.64
Std Dev	3.613

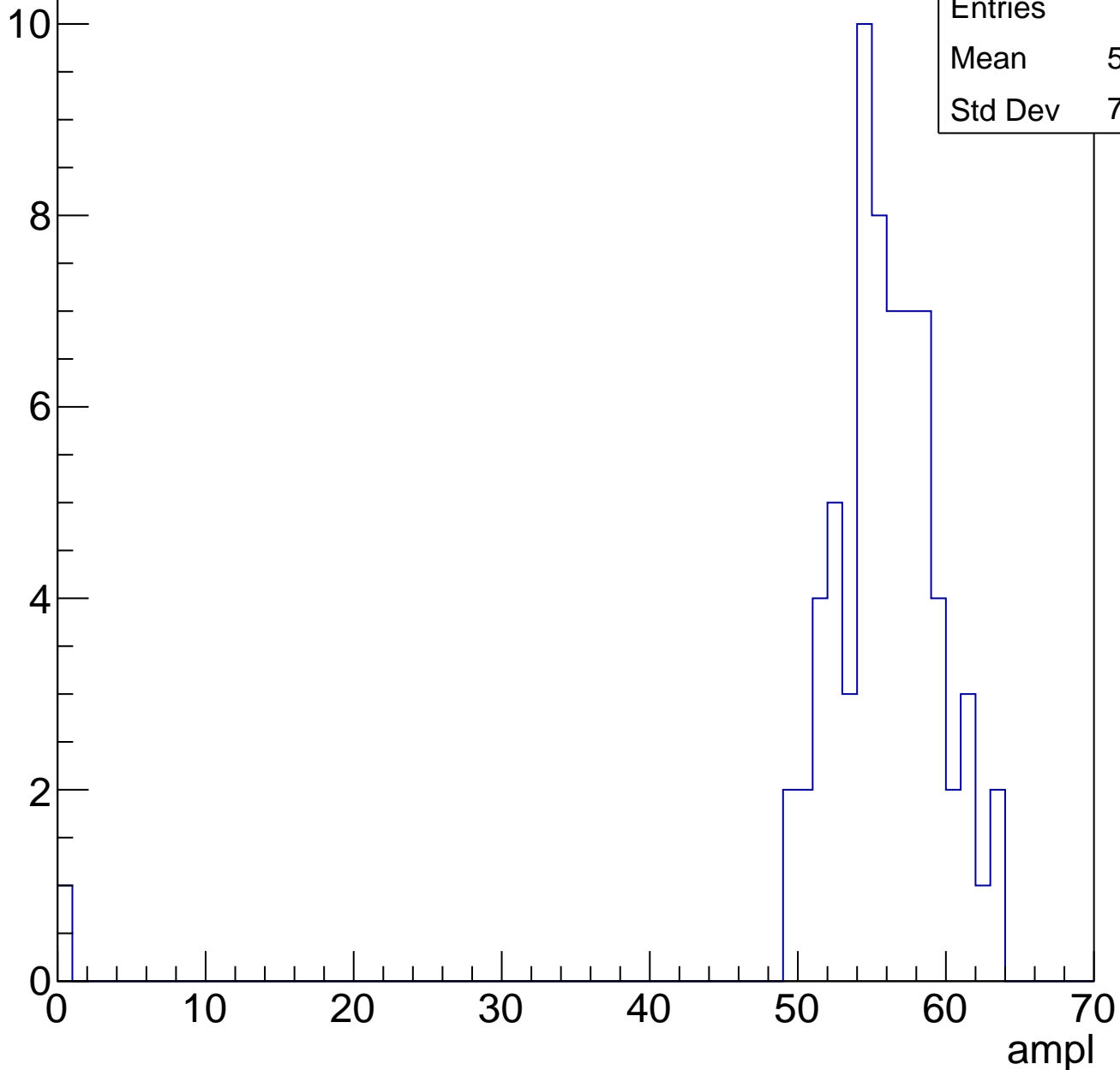


B1L102S, U10-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	54.78
Std Dev	7.448

Entry

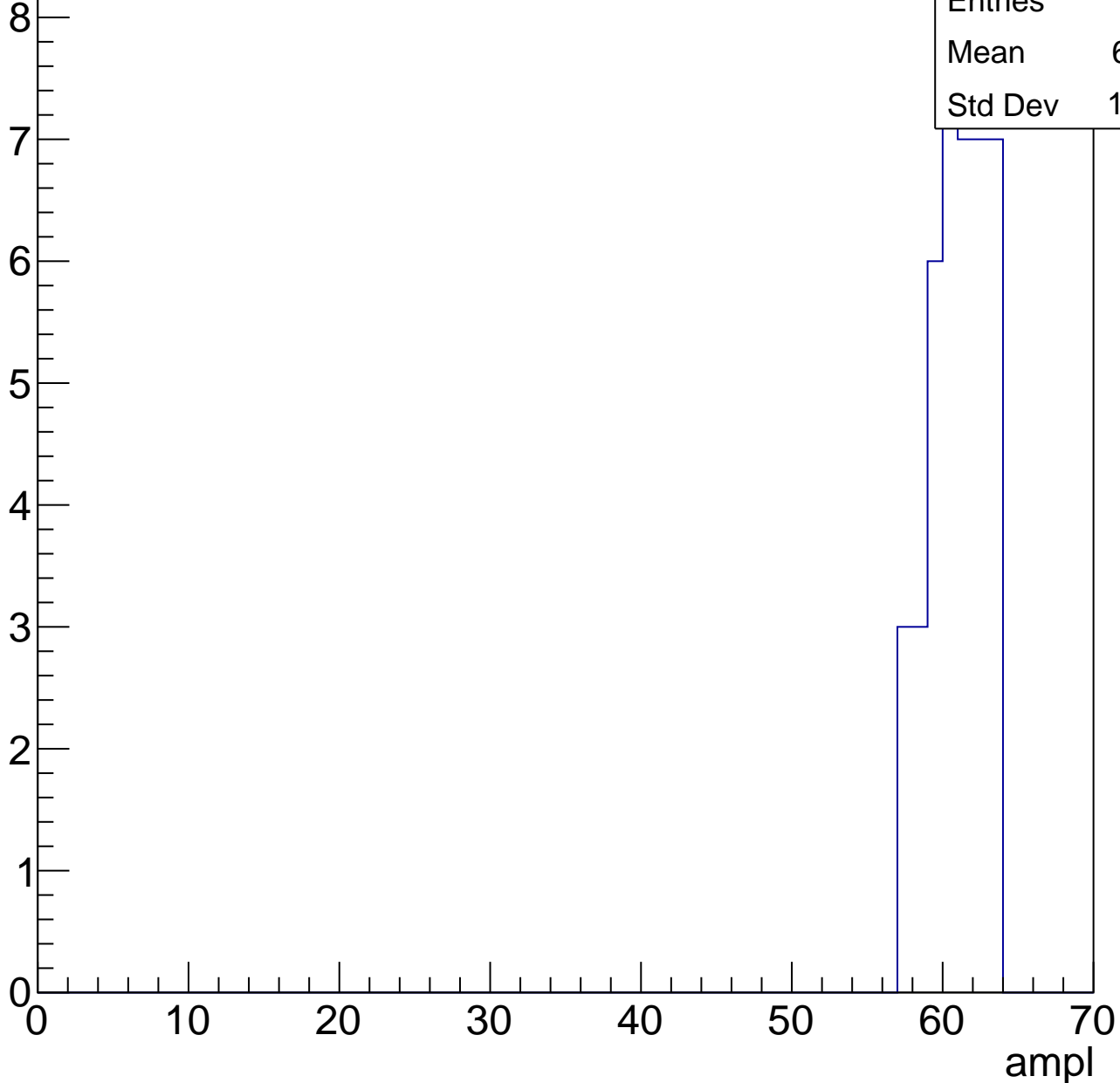


B1L102S, U10-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	60.51
Std Dev	1.796



B1L102S, U10-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L102S, U10-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch112, adc0

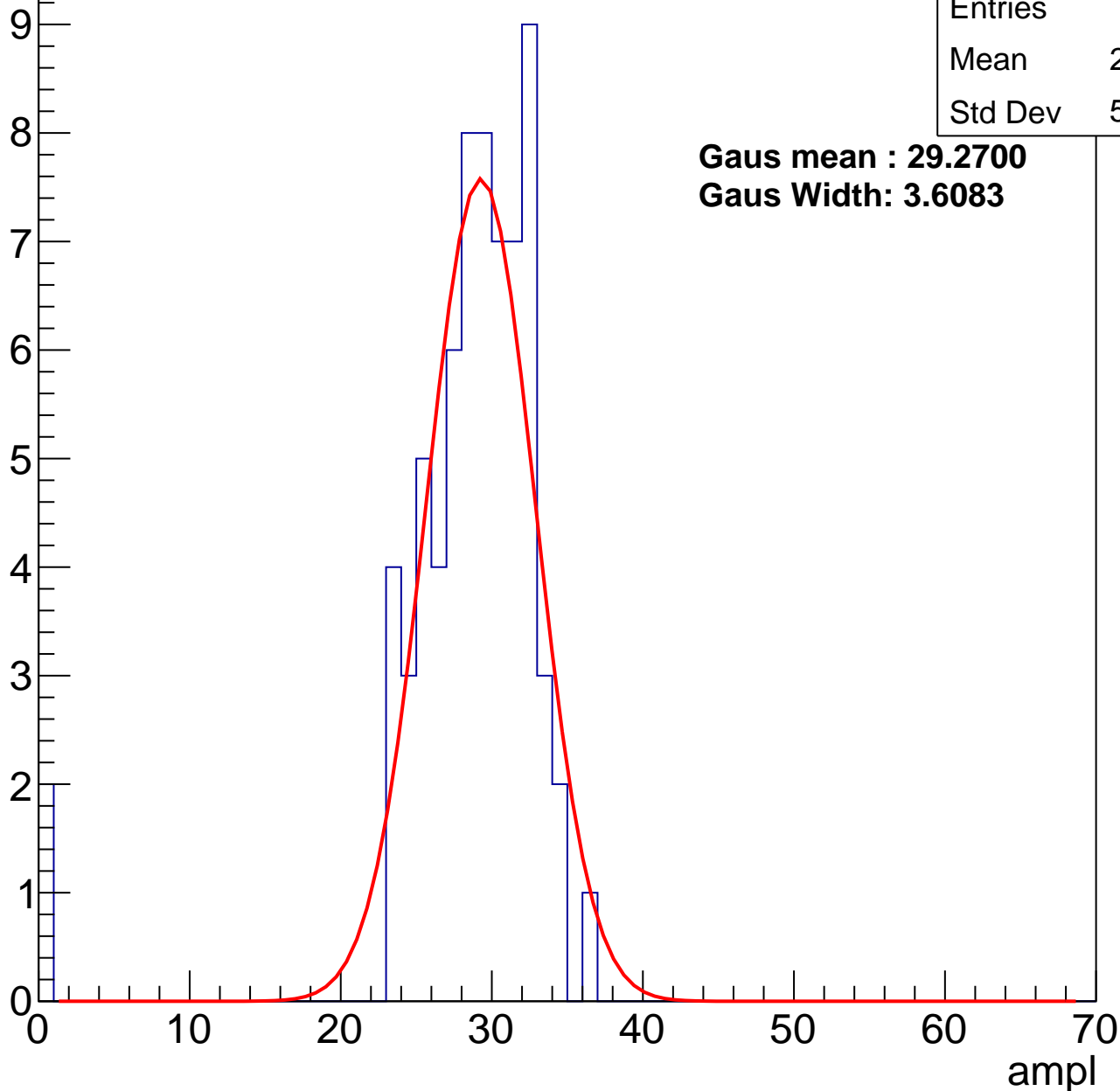
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.96
Std Dev	5.694

Gaus mean : 29.2700

Gaus Width: 3.6083



B1L102S, U10-ch112, adc1

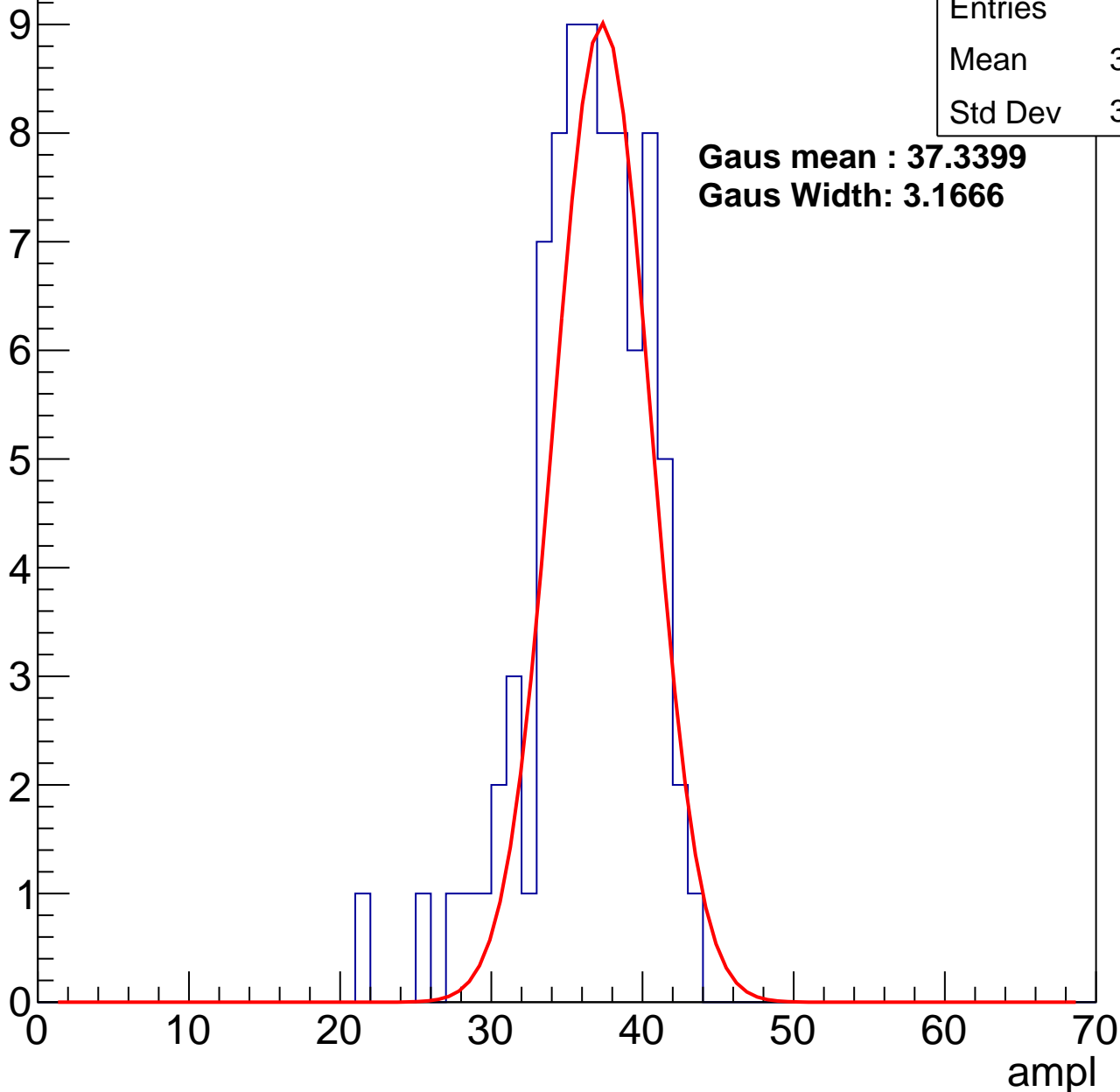
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.89
Std Dev	3.942

Gaus mean : 37.3399

Gaus Width: 3.1666



B1L102S, U10-ch112, adc2

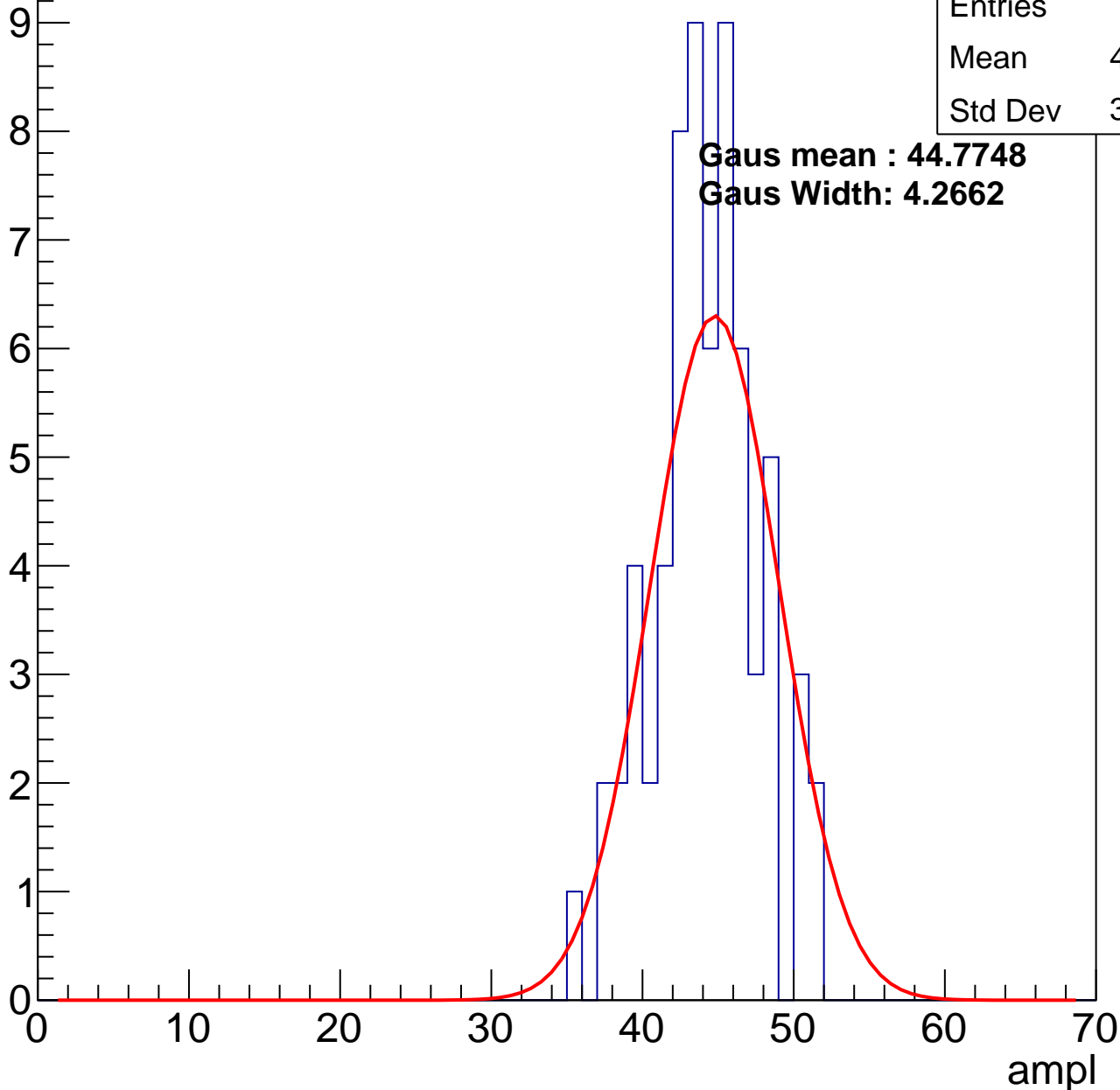
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.73
Std Dev	3.484

Gaus mean : 44.7748

Gaus Width: 4.2662

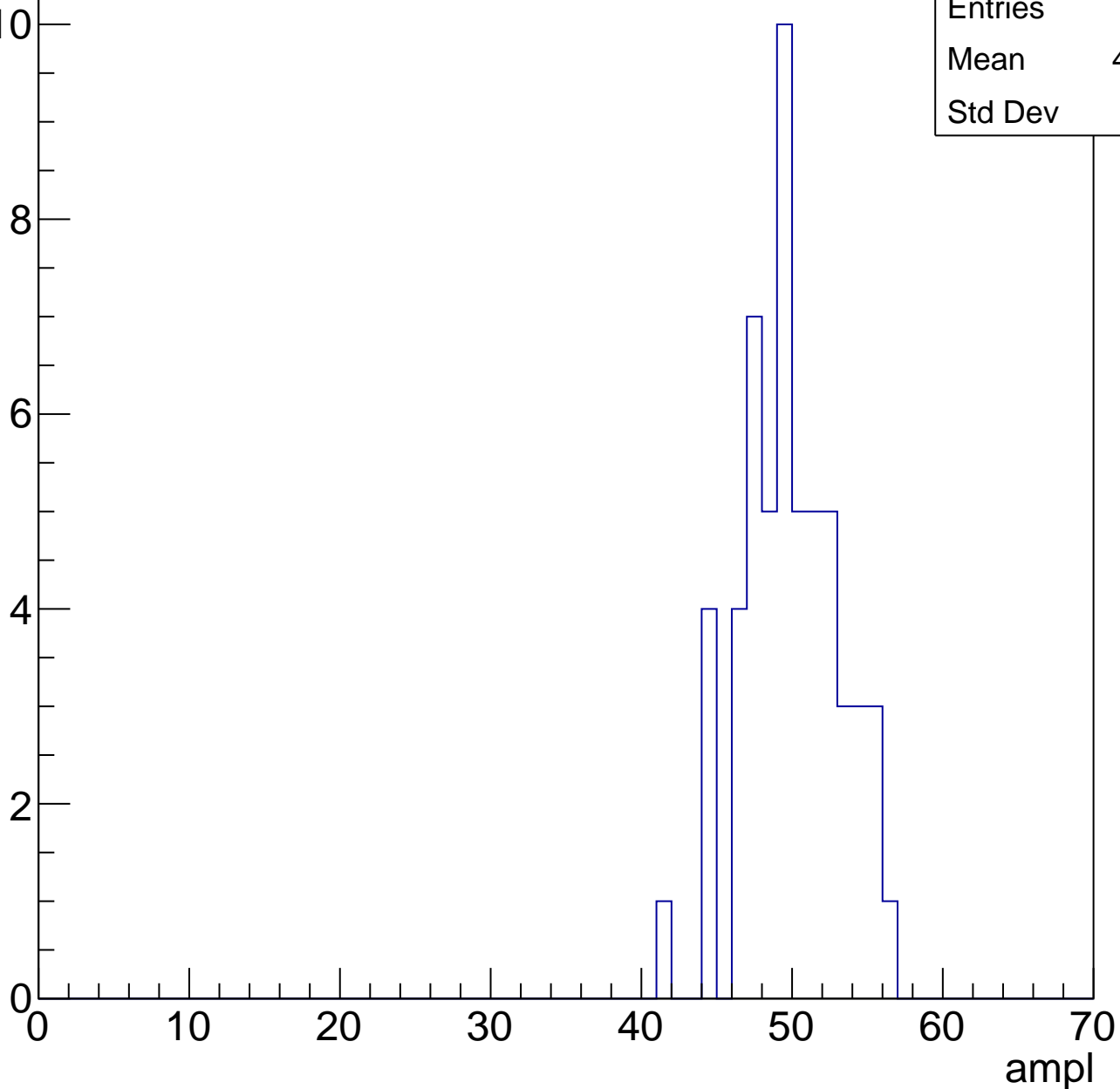


B1L102S, U10-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.41
Std Dev	3.2

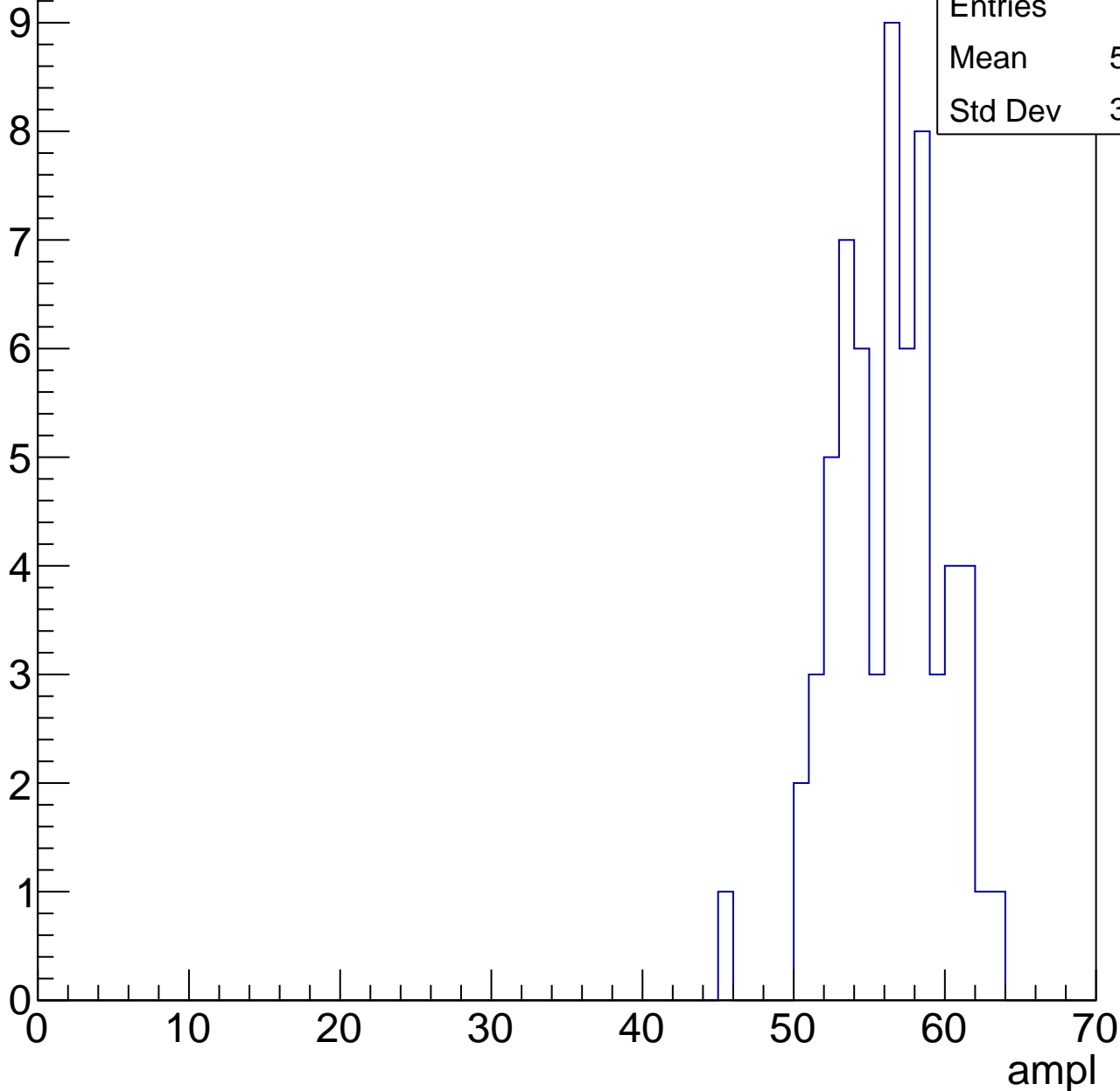


B1L102S, U10-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.78
Std Dev	3.448

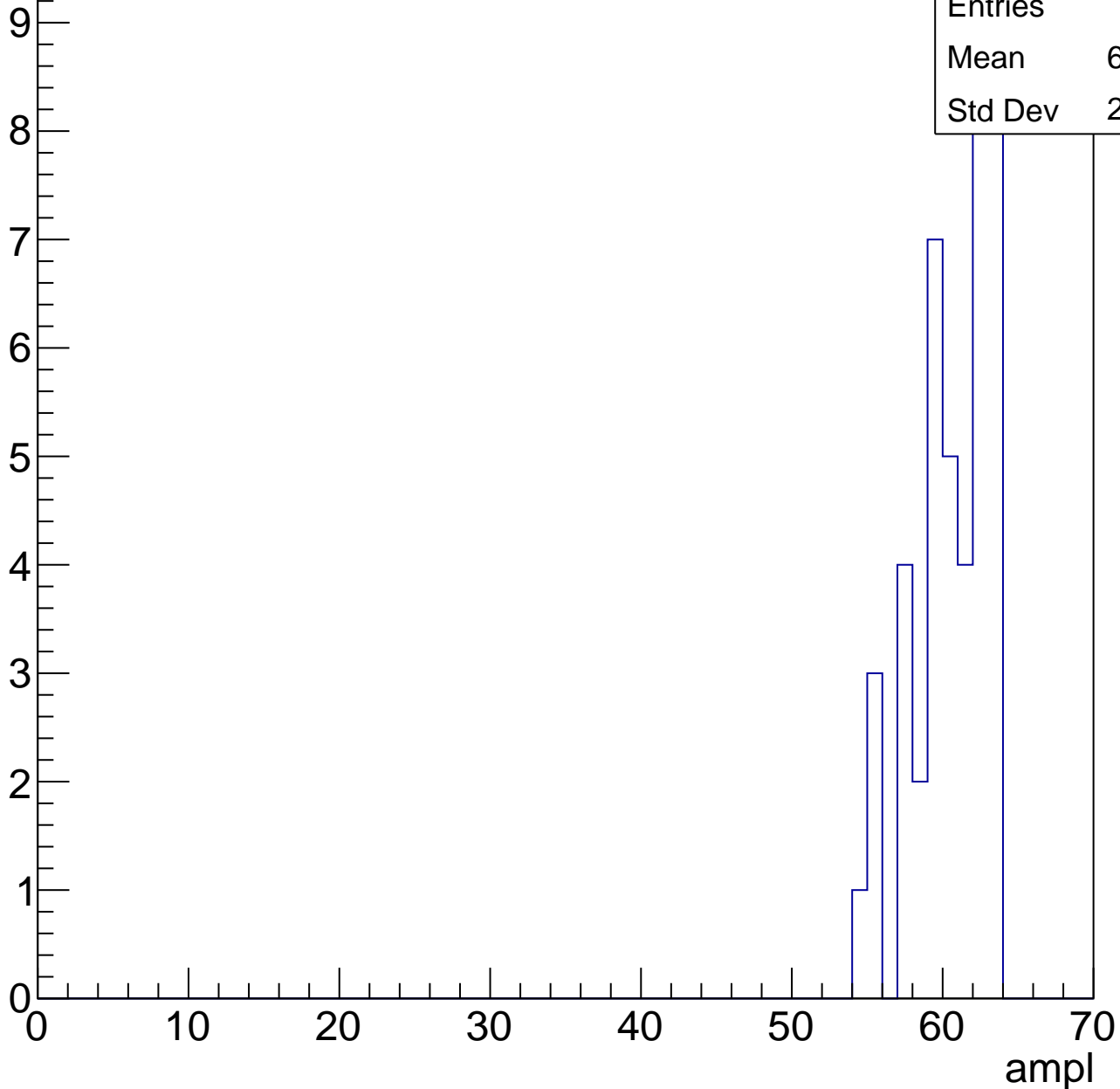


B1L102S, U10-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	60.05
Std Dev	2.524



B1L102S, U10-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

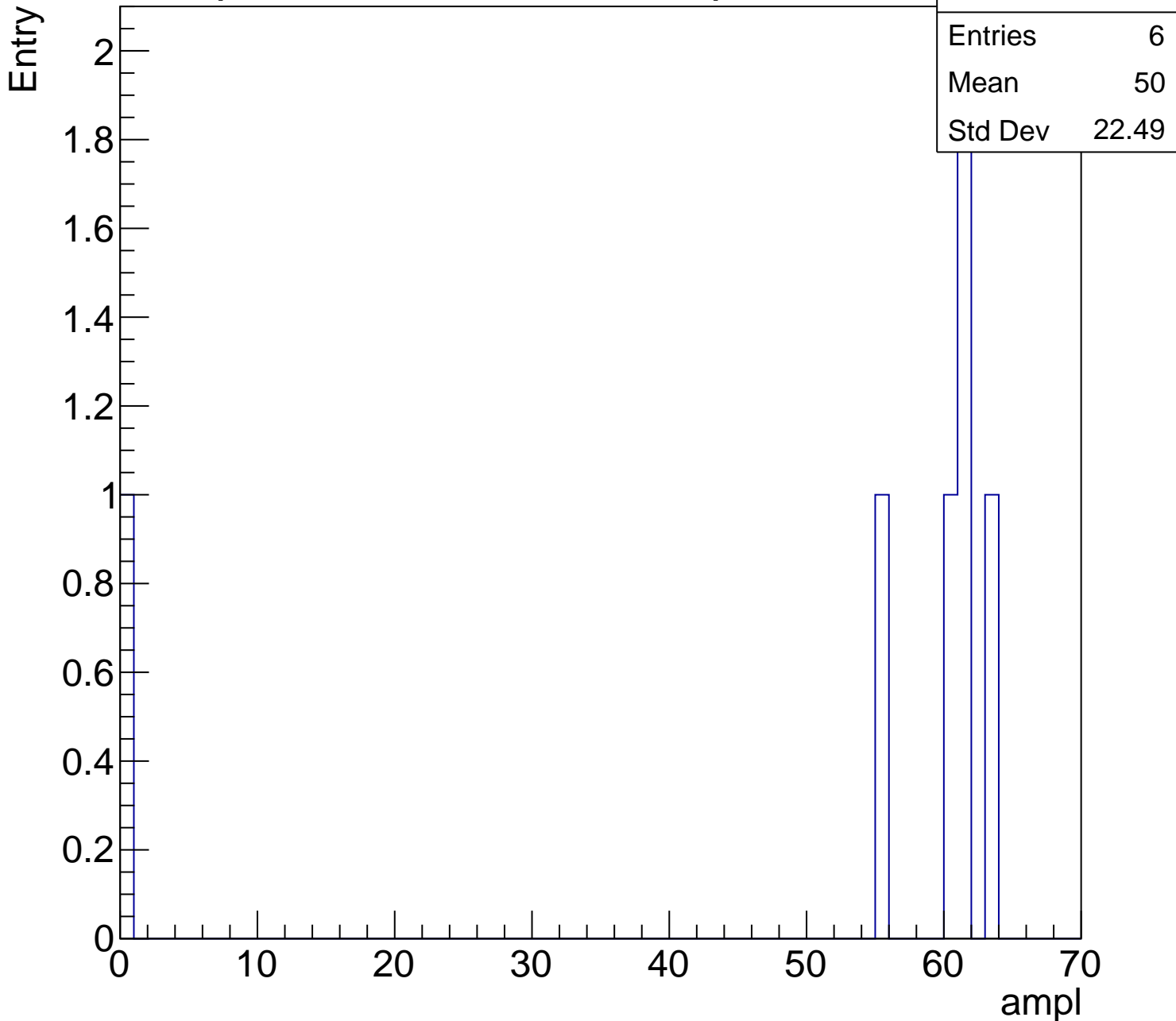
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50
Std Dev	22.49

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch113, adc0

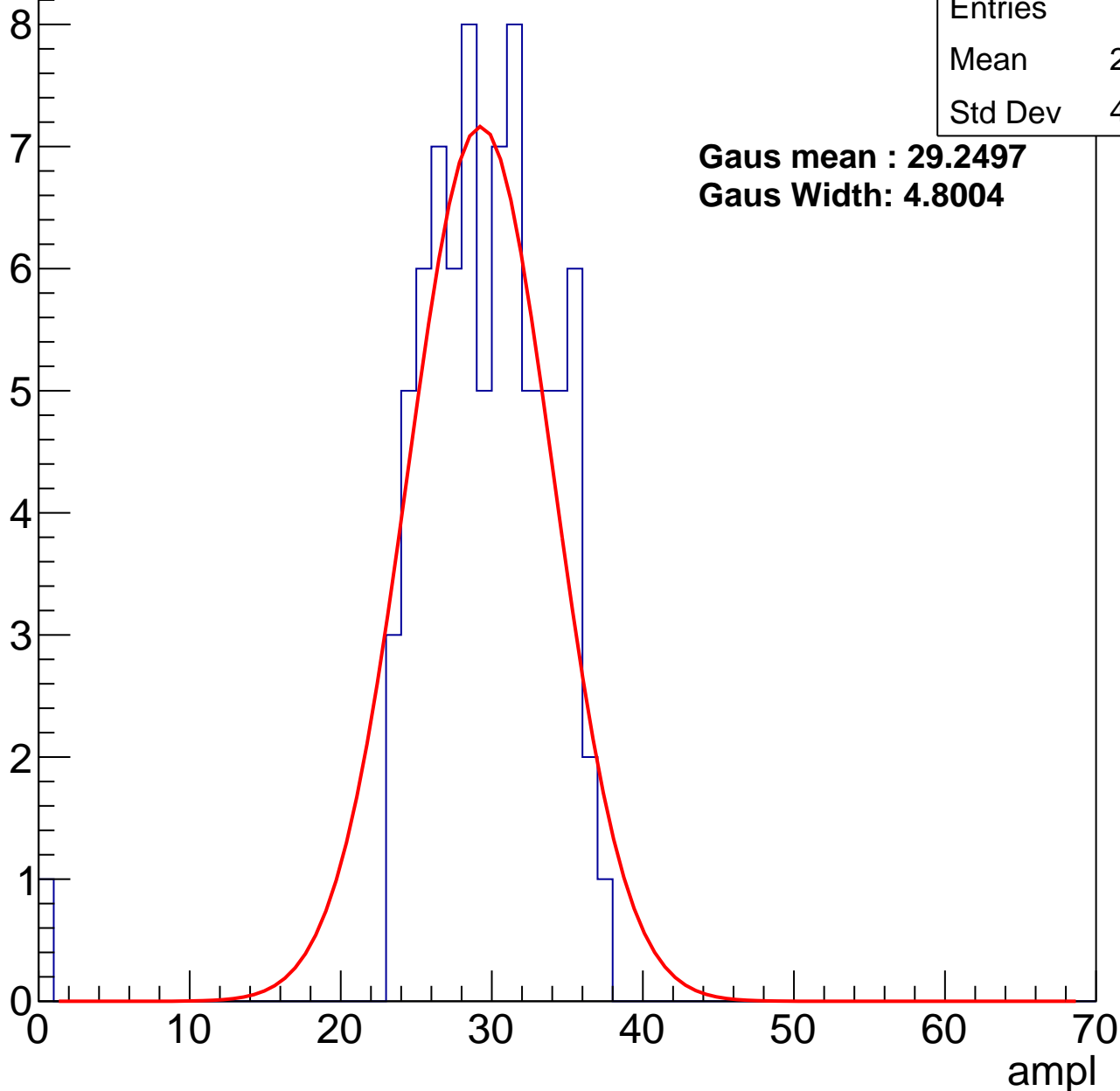
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.05
Std Dev	4.914

Gaus mean : 29.2497

Gaus Width: 4.8004



B1L102S, U10-ch113, adc1

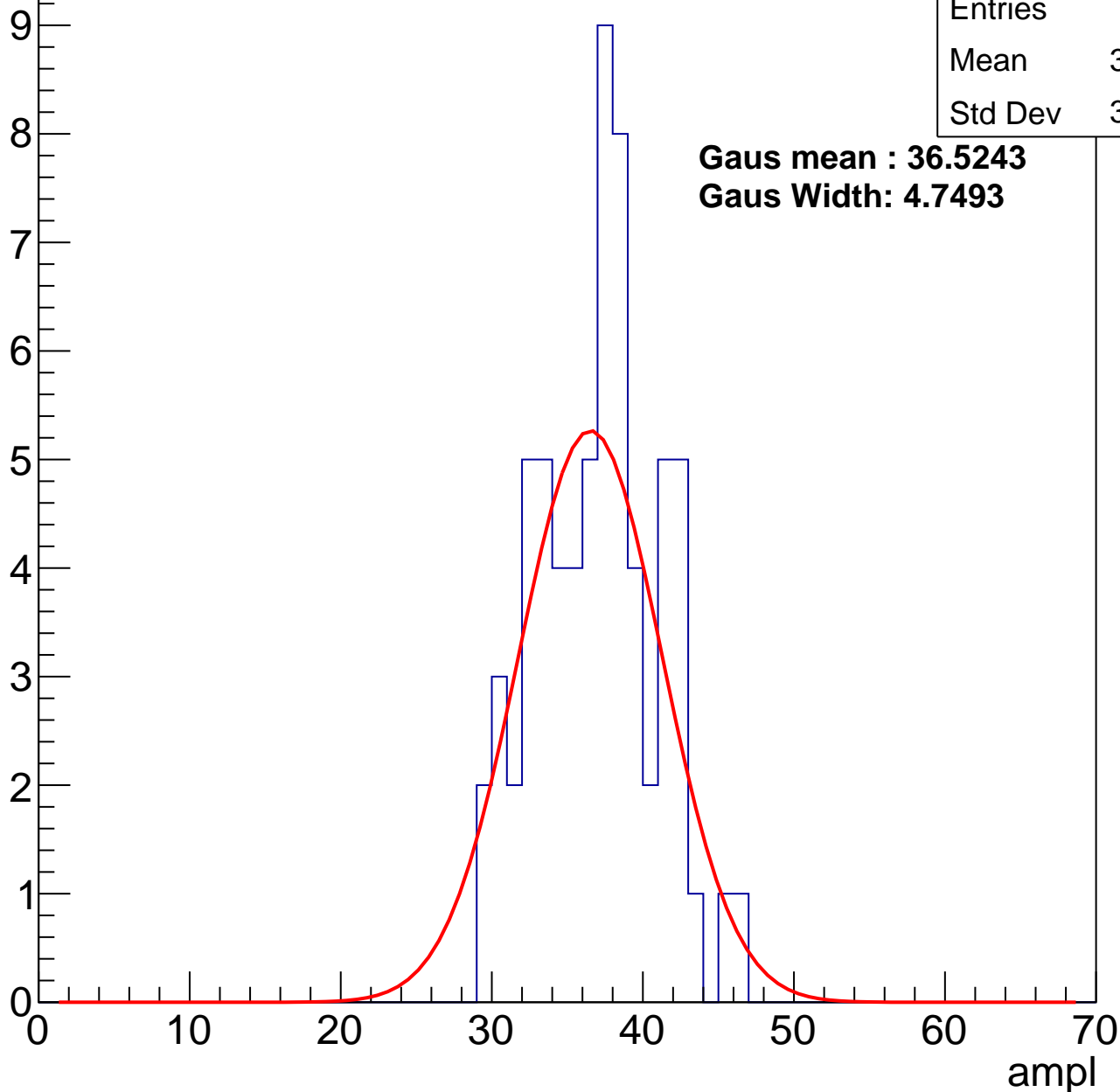
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.56
Std Dev	3.939

Gaus mean : 36.5243

Gaus Width: 4.7493



B1L102S, U10-ch113, adc2

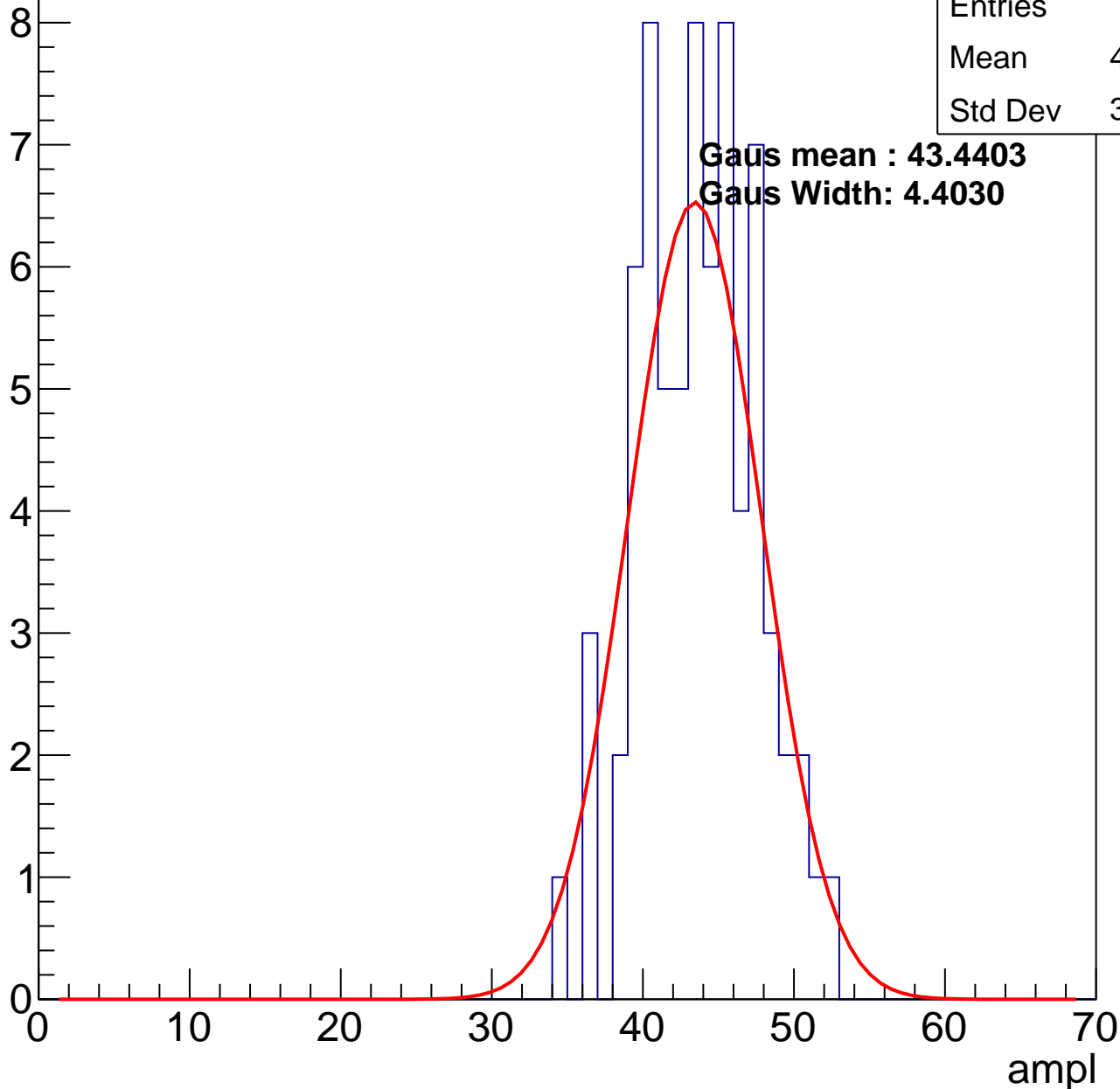
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.24
Std Dev	3.806

Gaus mean : 43.4403

Gaus Width: 4.4030

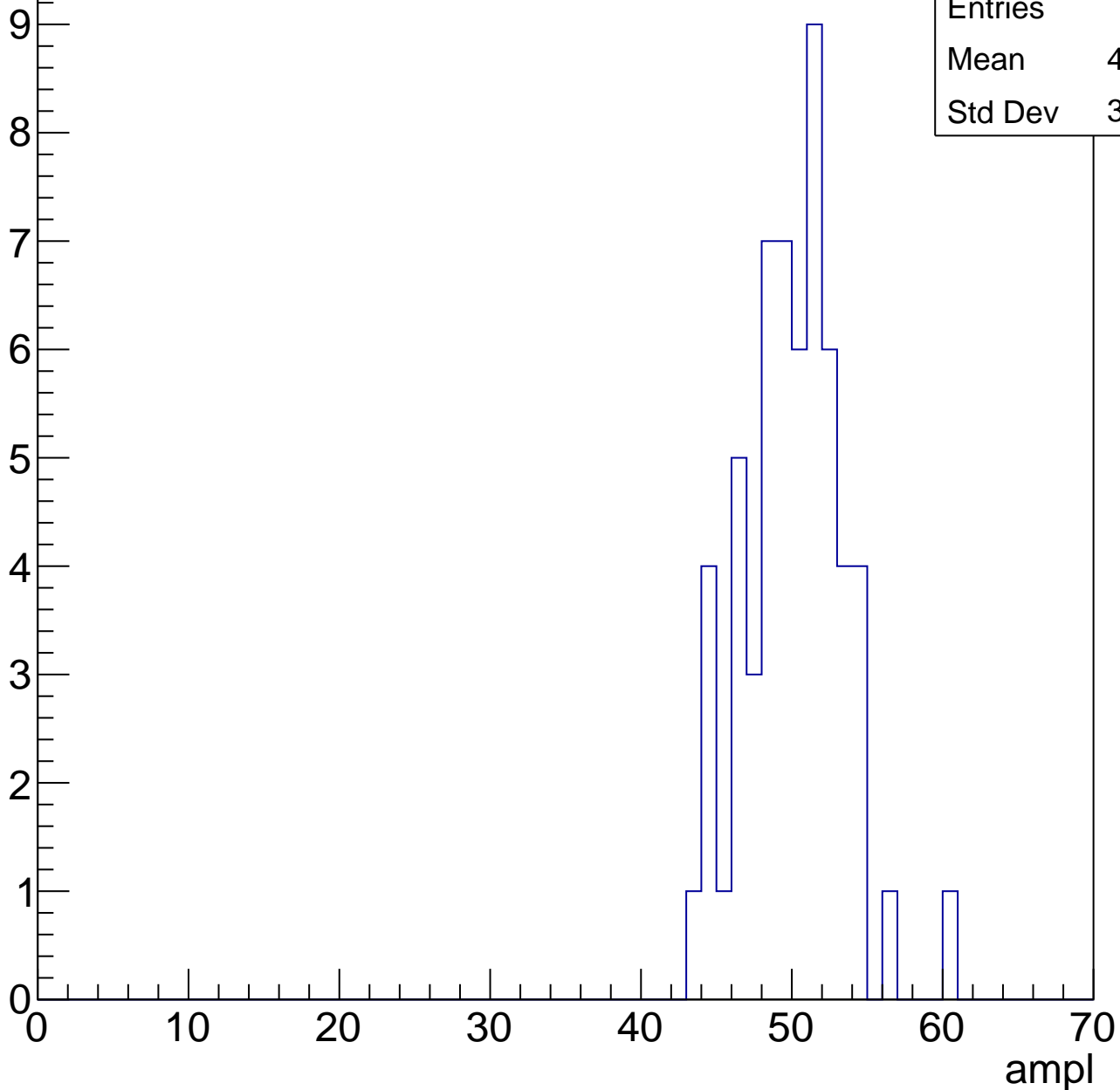


B1L102S, U10-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.64
Std Dev	3.246

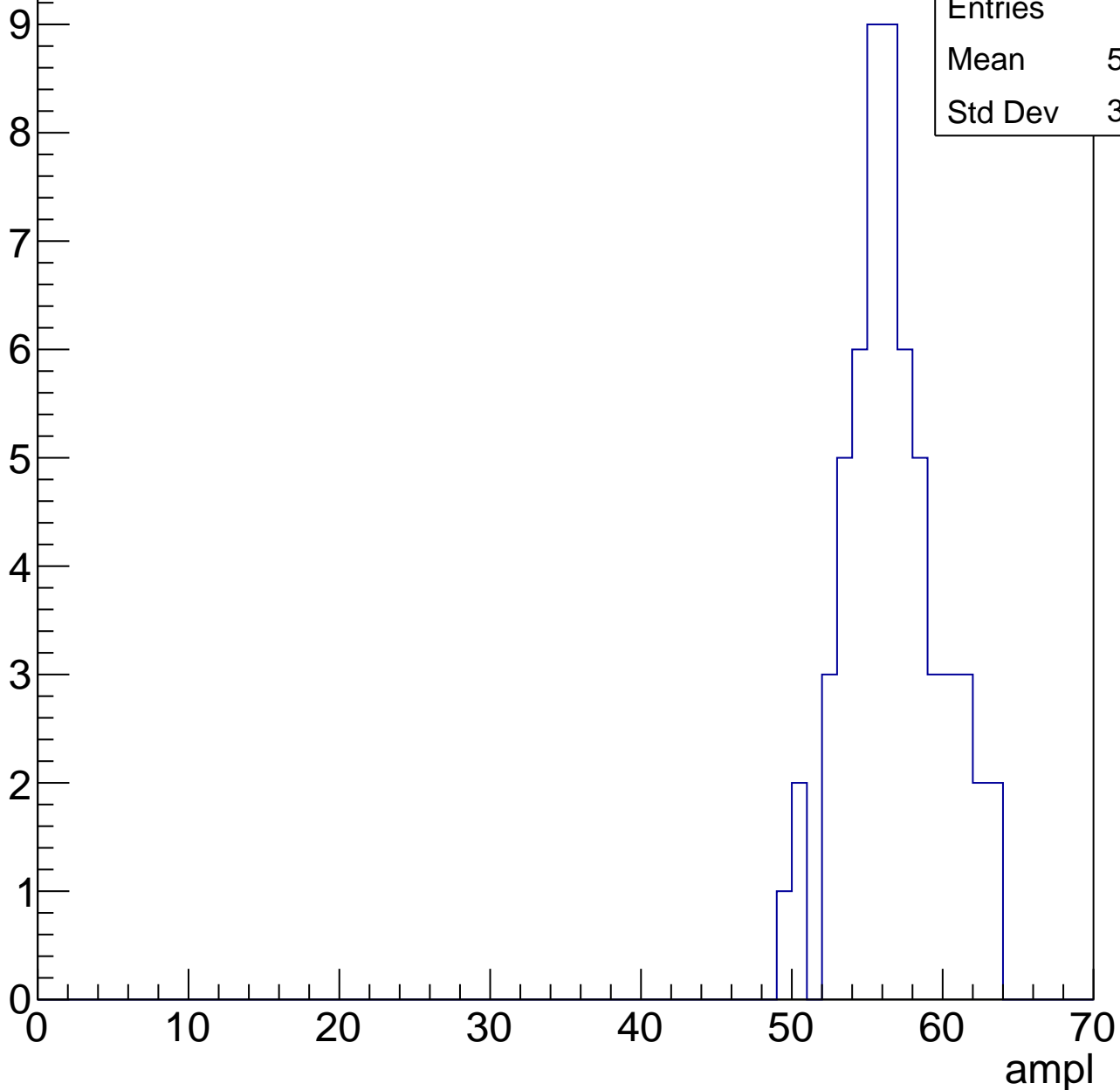


B1L102S, U10-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.19
Std Dev	3.165

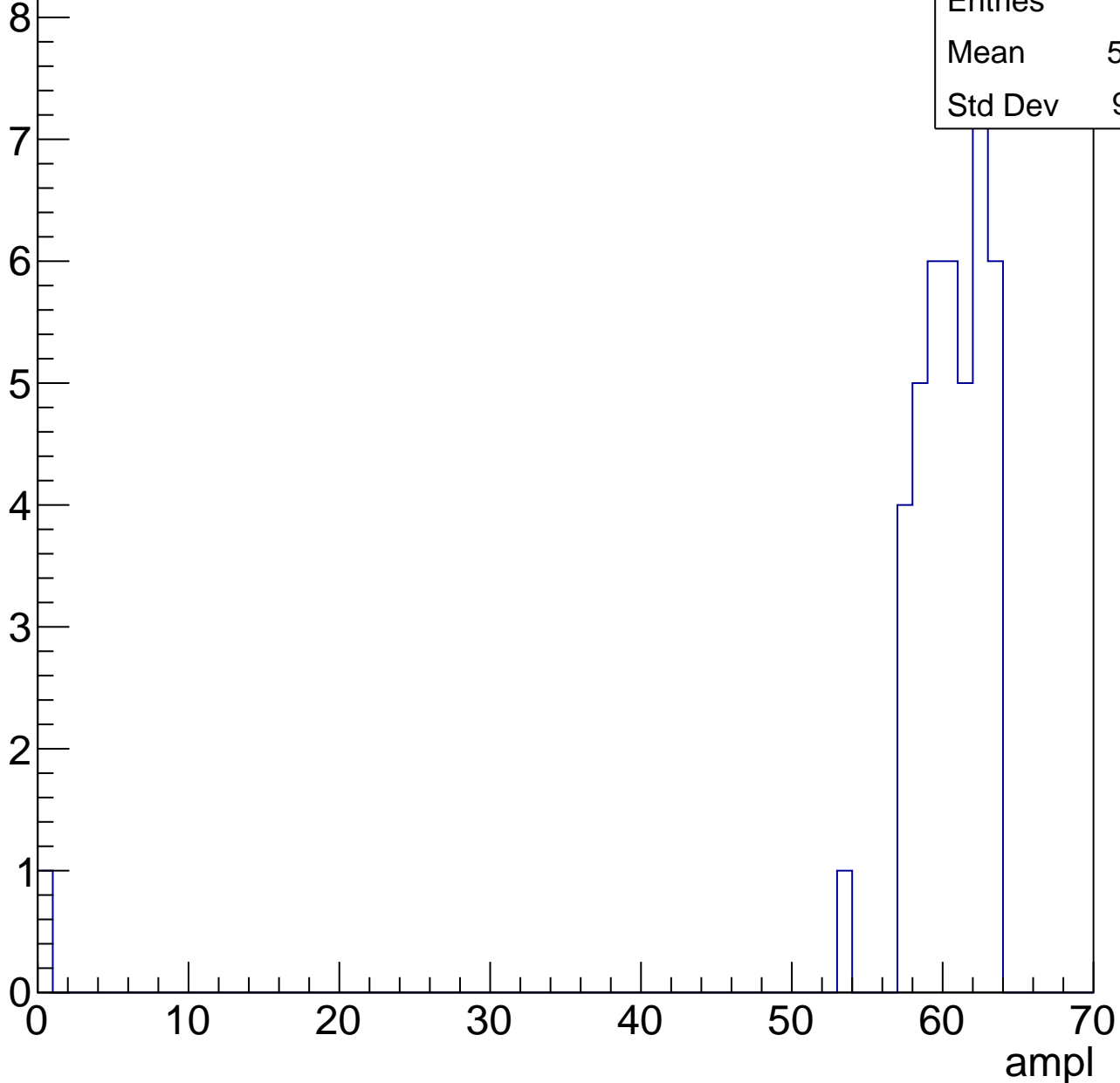


B1L102S, U10-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.67
Std Dev	9.421



B1L102S, U10-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

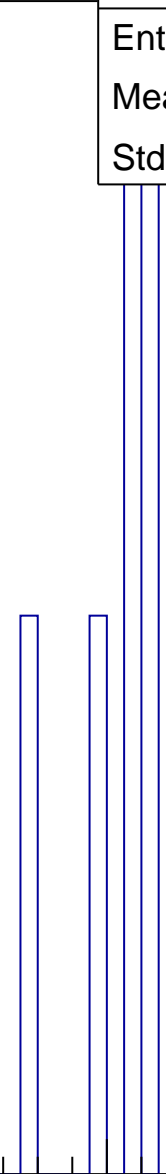
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.33
Std Dev	2.749

0 10 20 30 40 50 60 70

ampl



B1L102S, U10-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch114, adc0

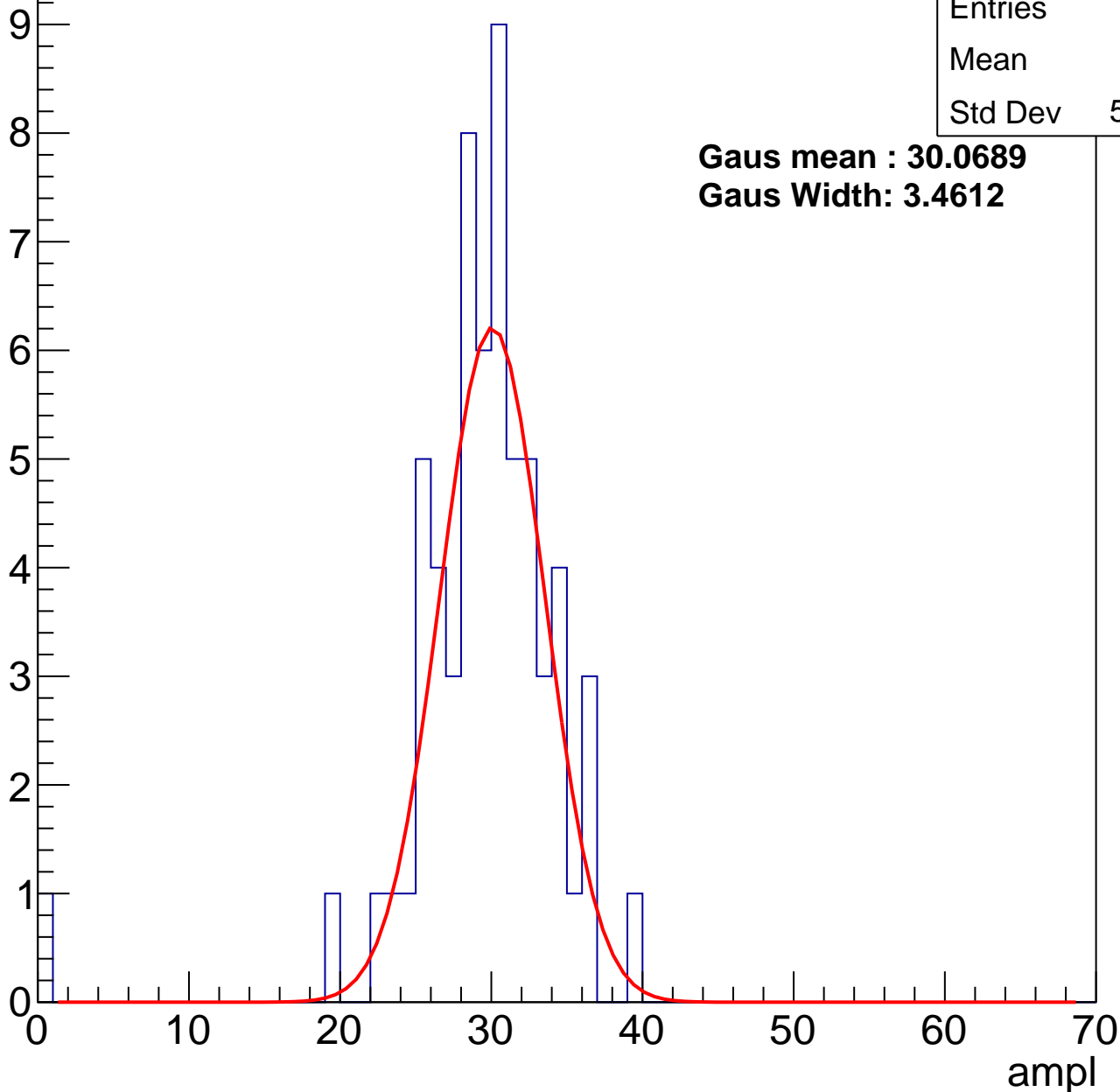
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29
Std Dev	5.224

Gaus mean : 30.0689

Gaus Width: 3.4612



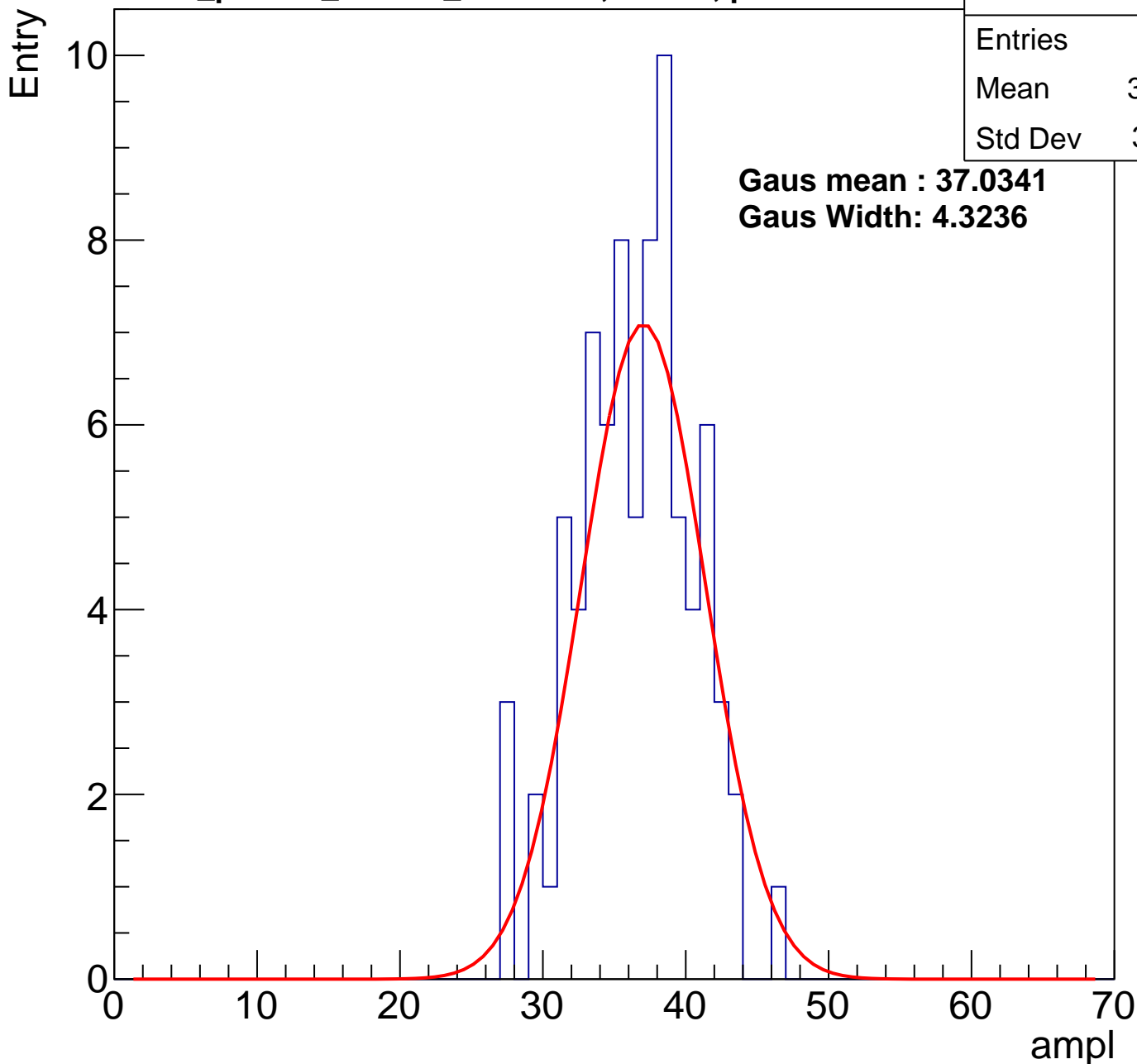
B1L102S, U10-ch114, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	36.02
Std Dev	3.991

Gaus mean : 37.0341

Gaus Width: 4.3236



B1L102S, U10-ch114, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	43.63
Std Dev	3.56

Gaus mean : 43.7660

Gaus Width: 3.8367

Entry

10

8

6

4

2

0

0

10

20

30

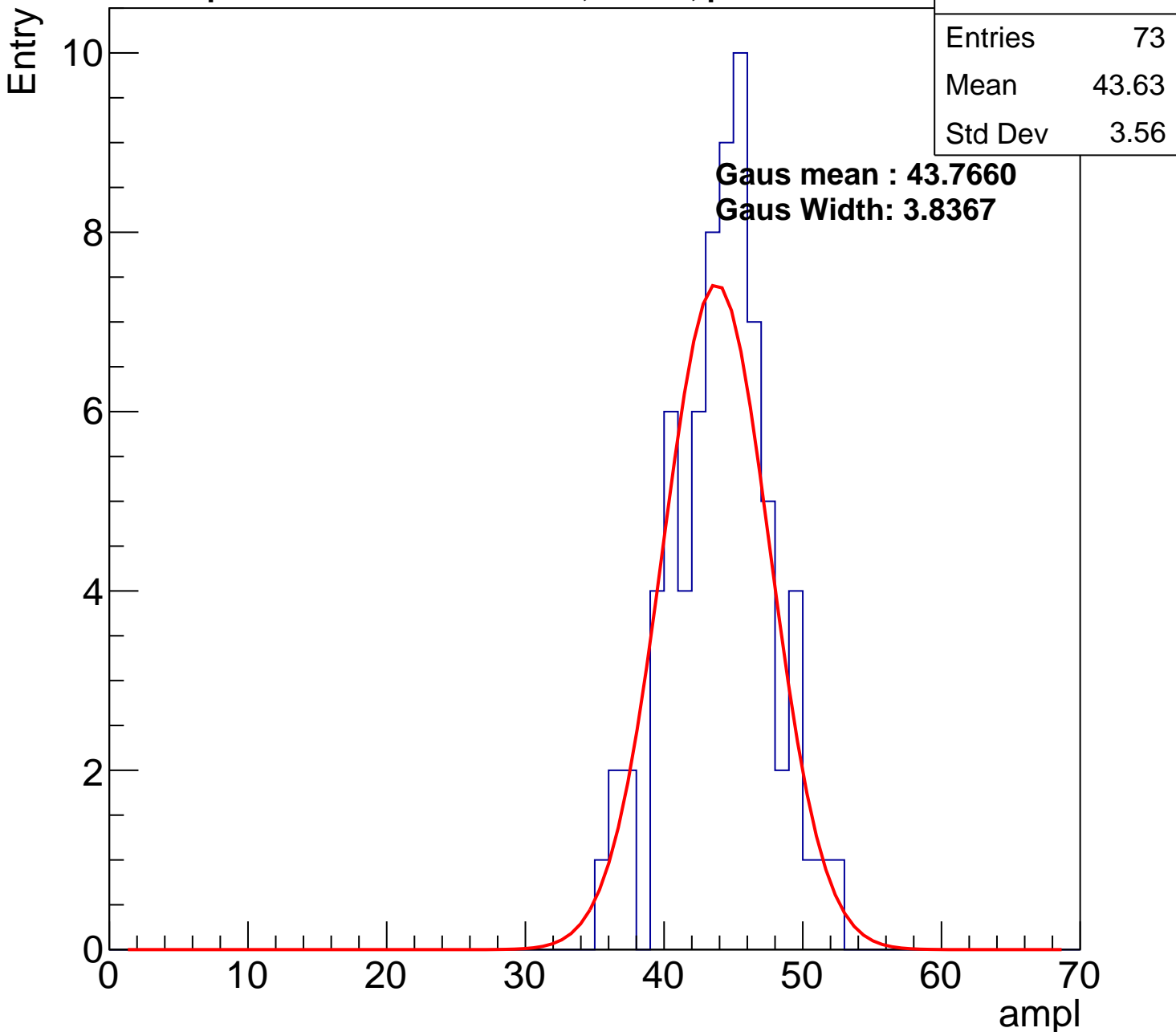
40

50

60

70

ampl

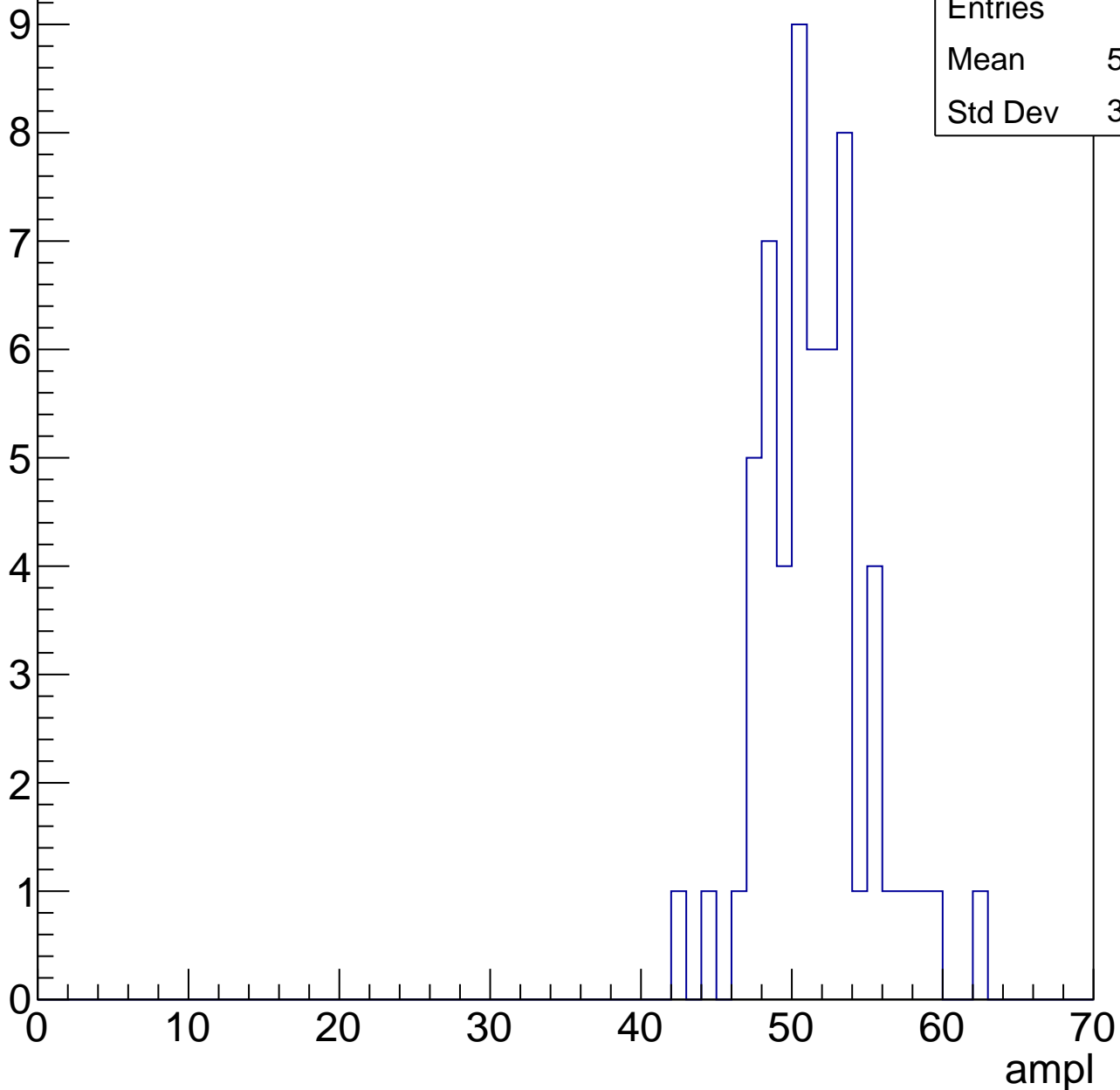


B1L102S, U10-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.98
Std Dev	3.545

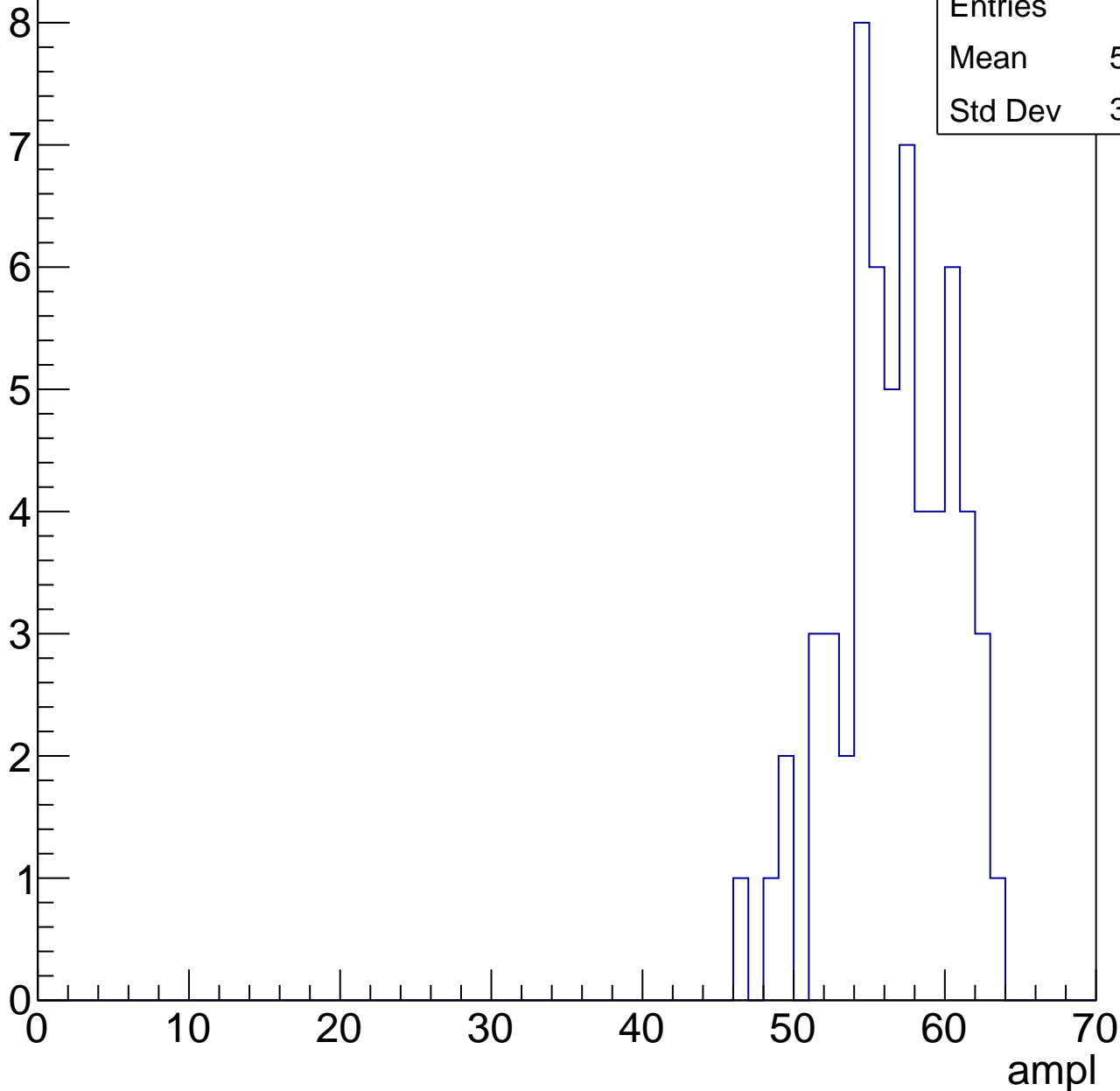


B1L102S, U10-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.15
Std Dev	3.768

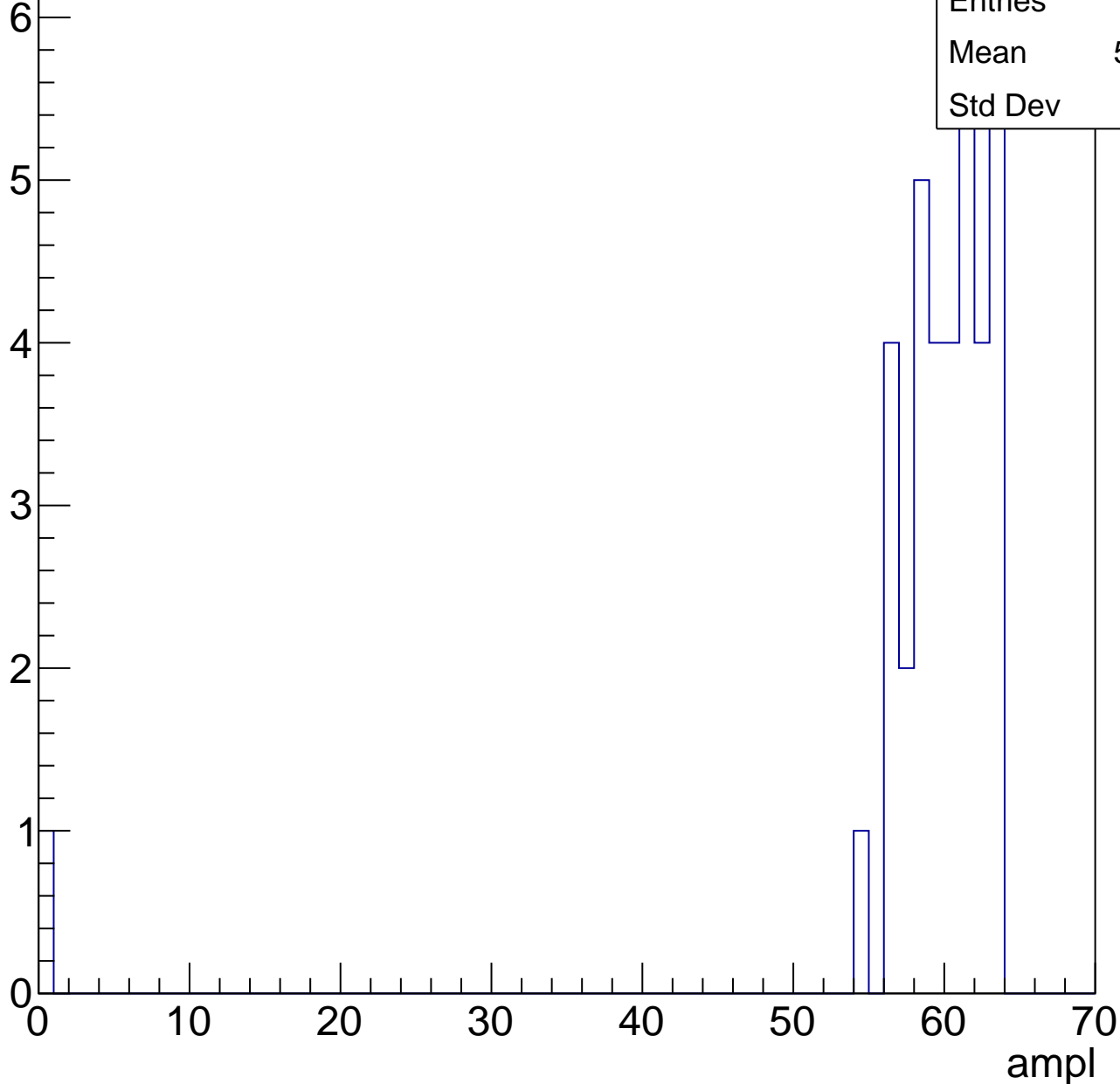


B1L102S, U10-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

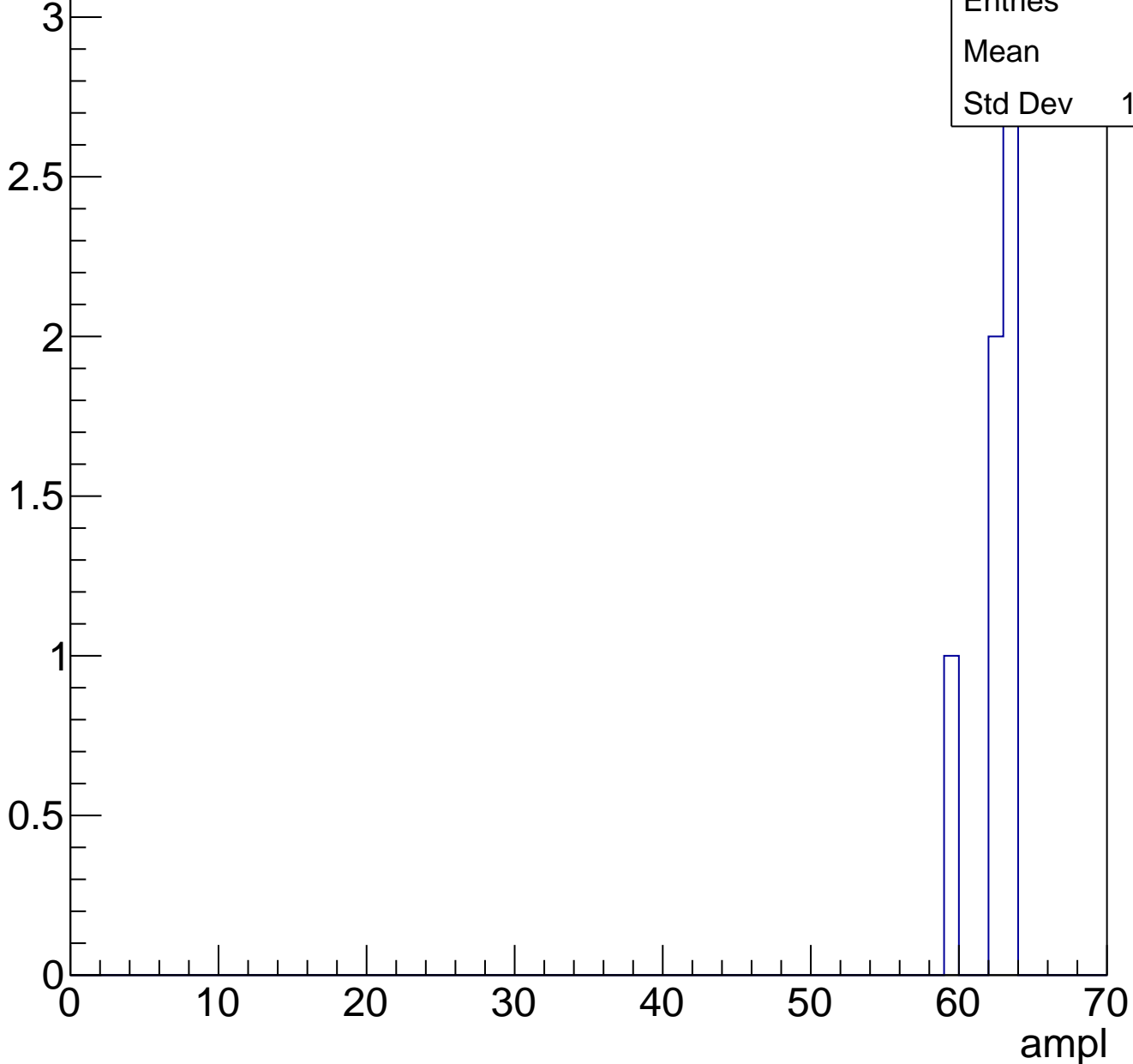
Entries	37
Mean	58.11
Std Dev	9.98



B1L102S, U10-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch115, adc0

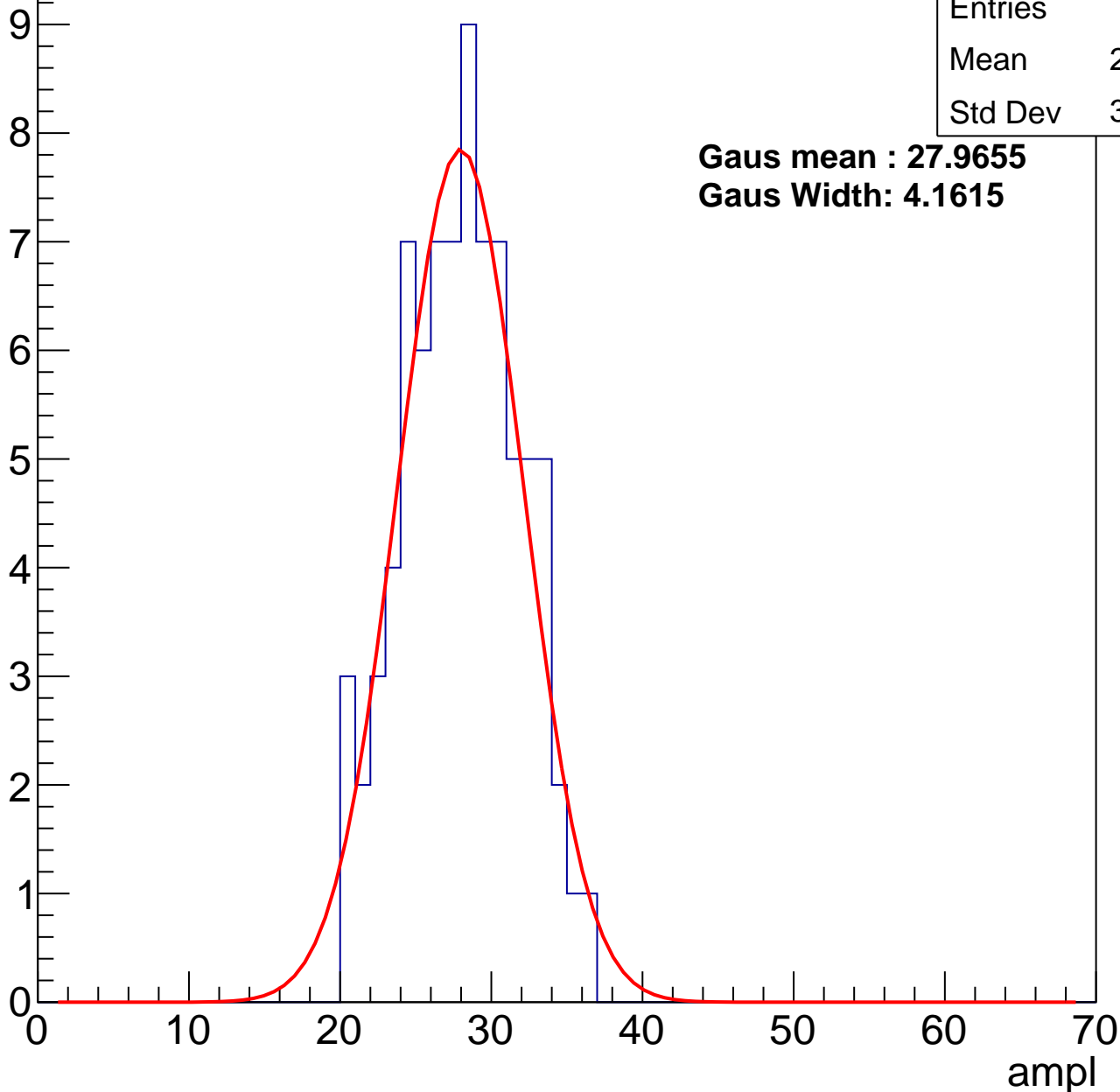
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.57
Std Dev	3.774

Gaus mean : 27.9655

Gaus Width: 4.1615



B1L102S, U10-ch115, adc1

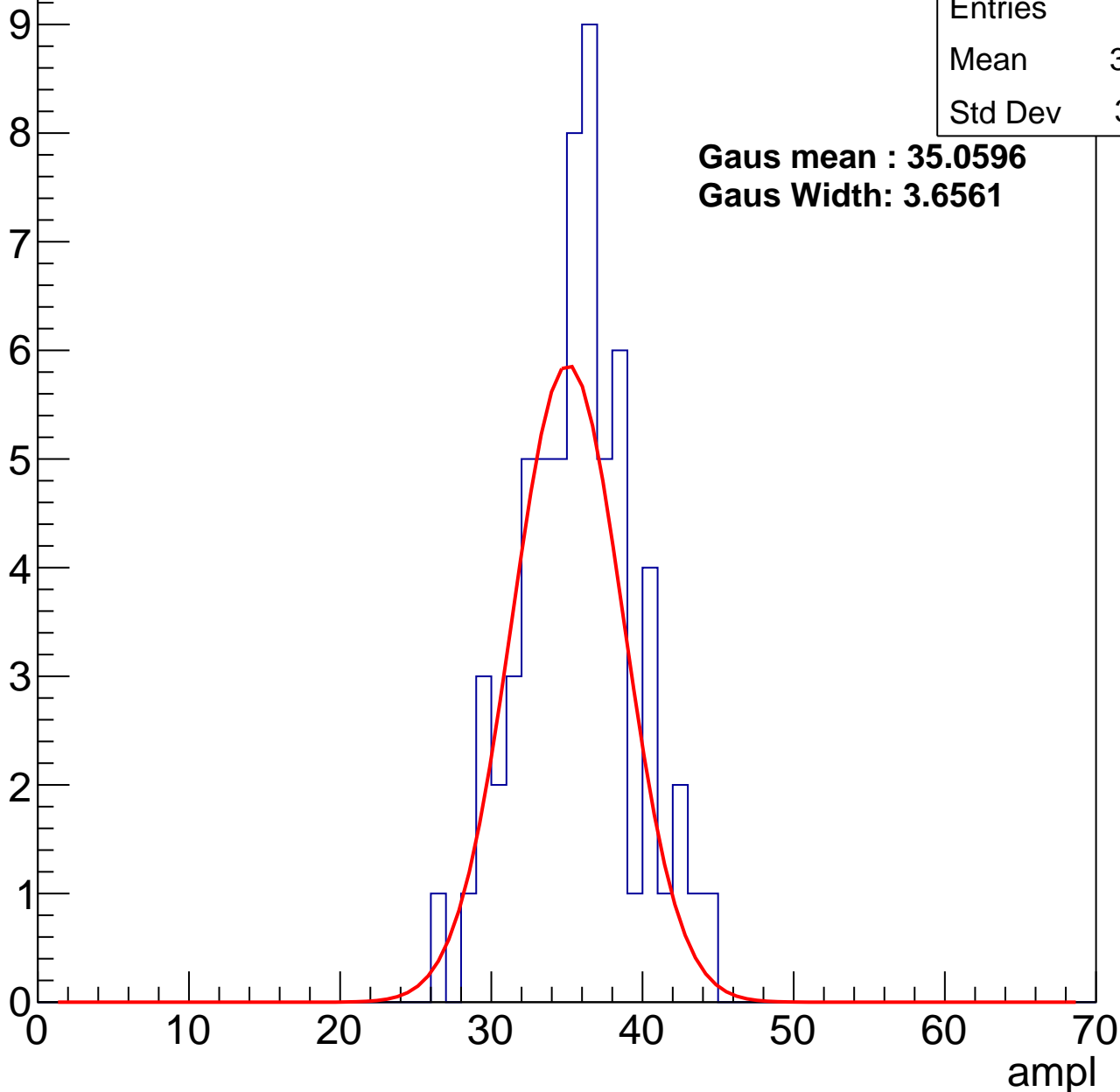
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.19
Std Dev	3.741

Gaus mean : 35.0596

Gaus Width: 3.6561



B1L102S, U10-ch115, adc2

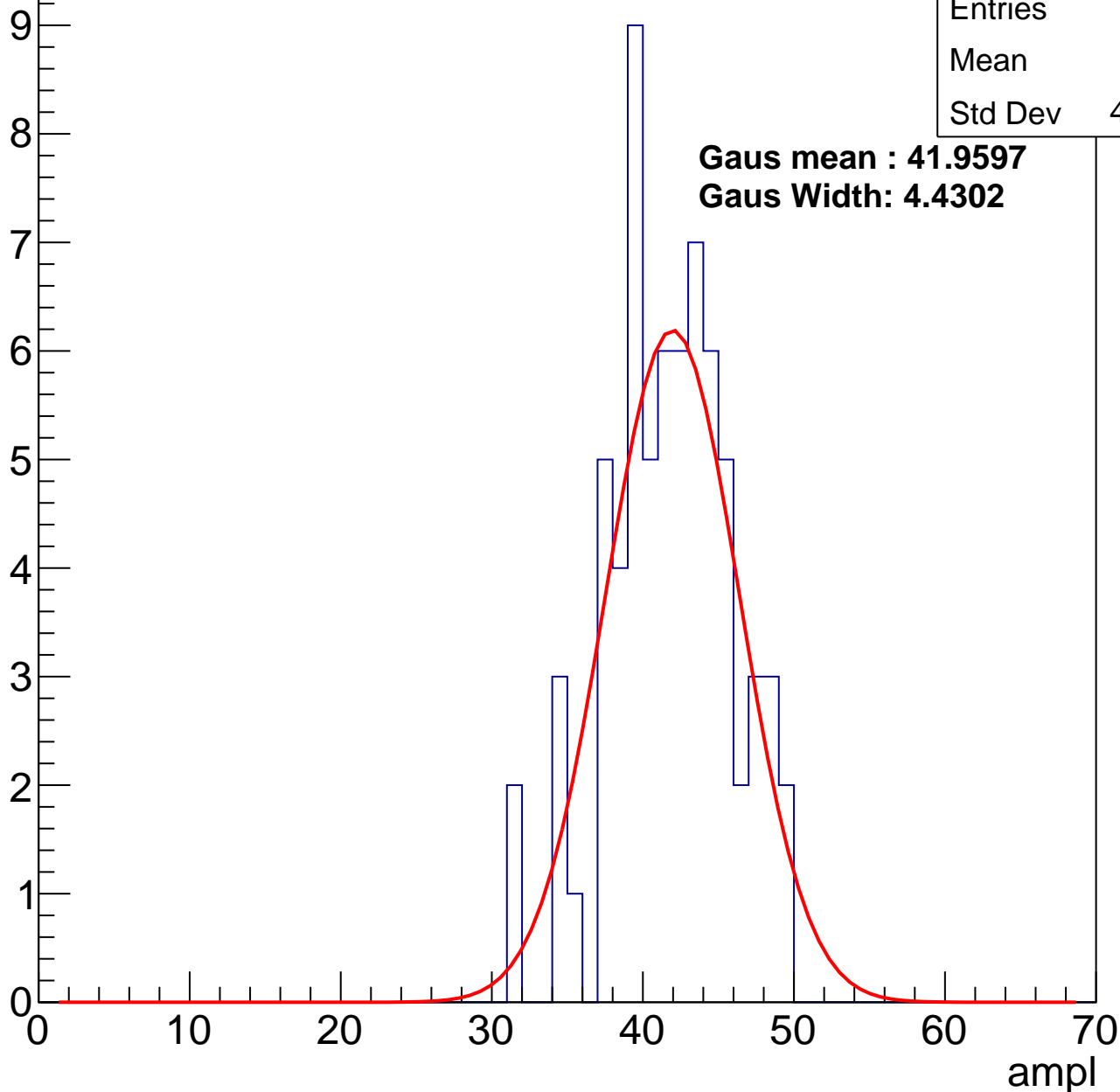
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.3
Std Dev	4.055

Gaus mean : 41.9597

Gaus Width: 4.4302

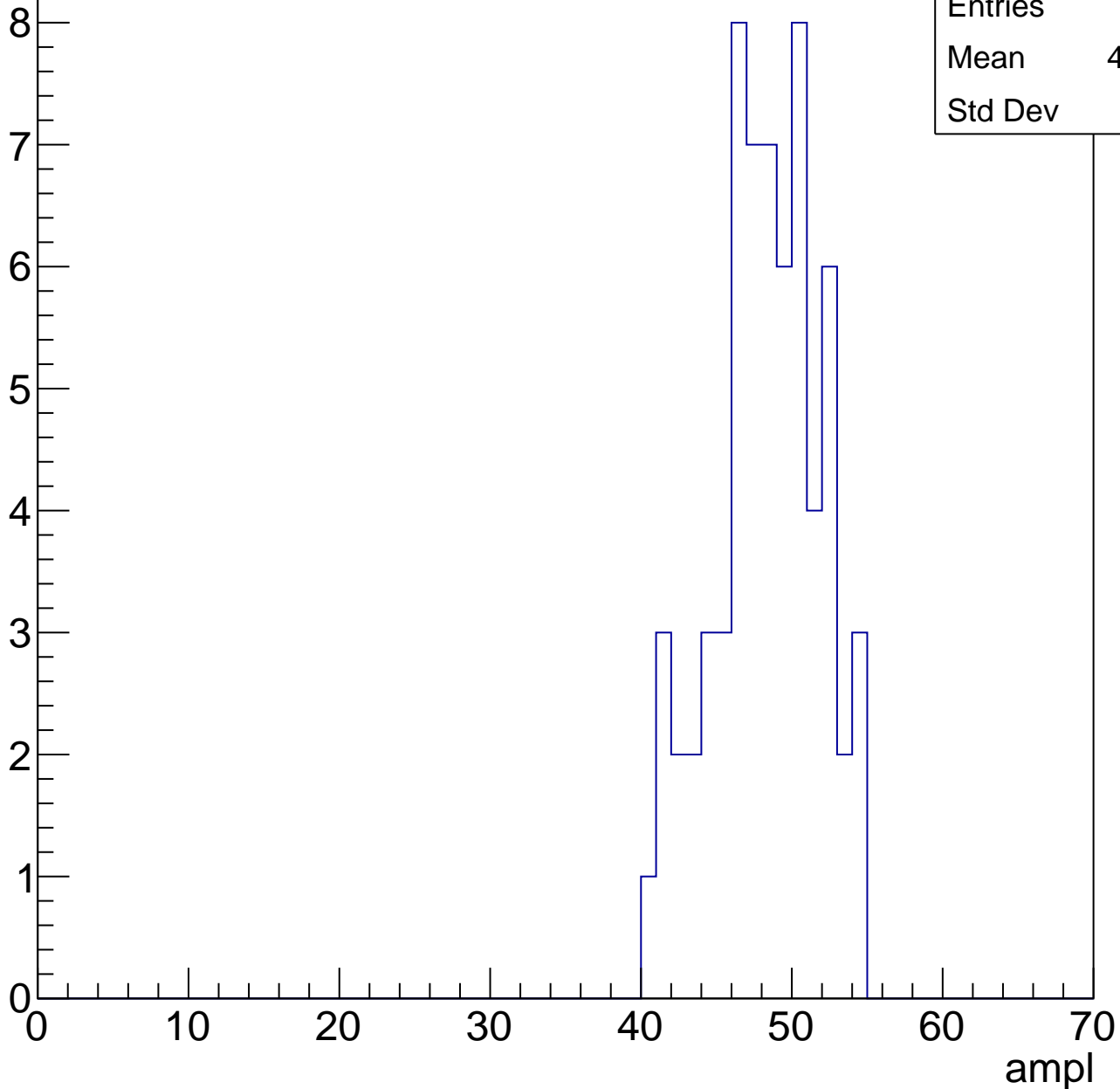


B1L102S, U10-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.86
Std Dev	3.45

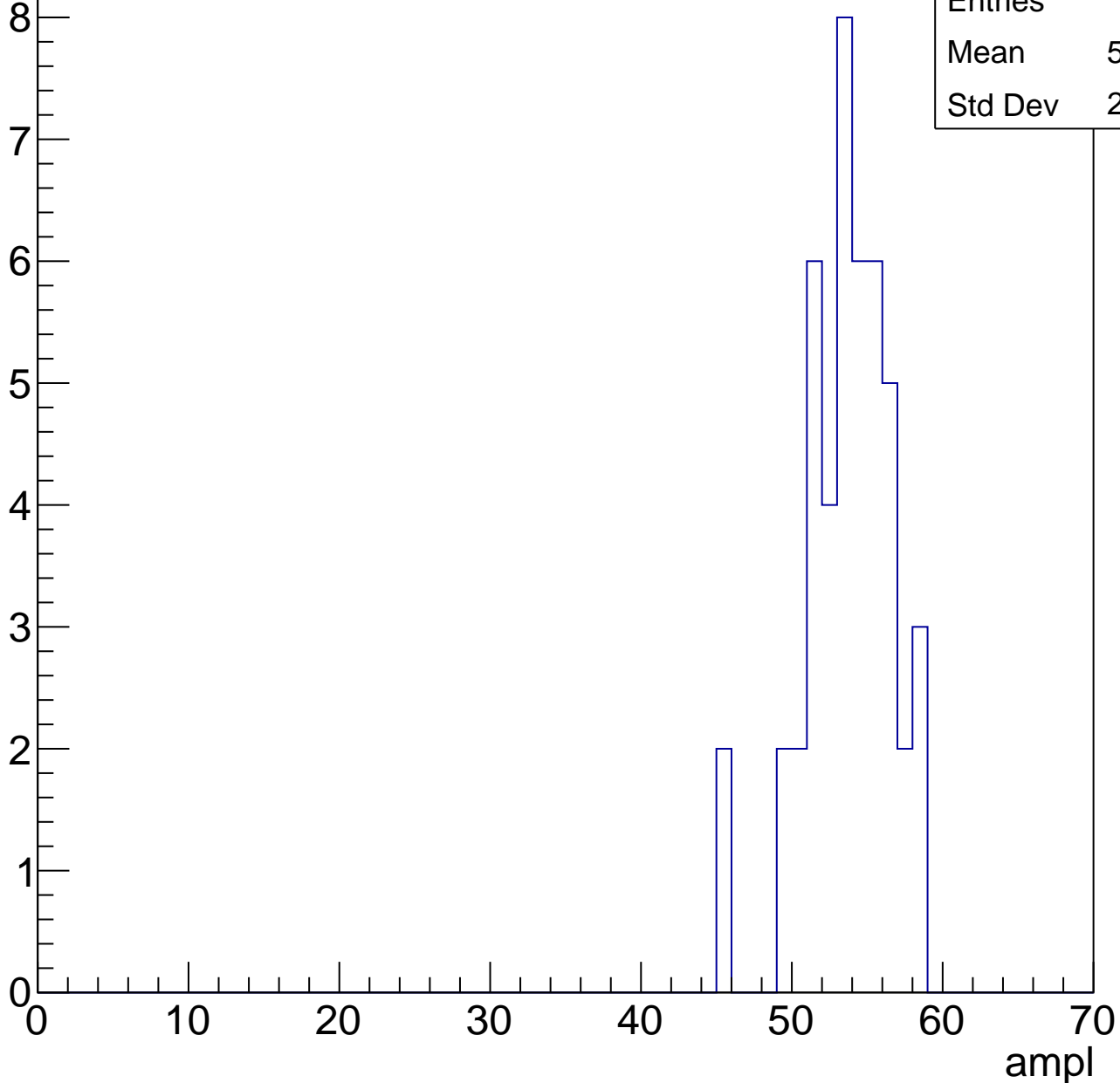


B1L102S, U10-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.22
Std Dev	2.896

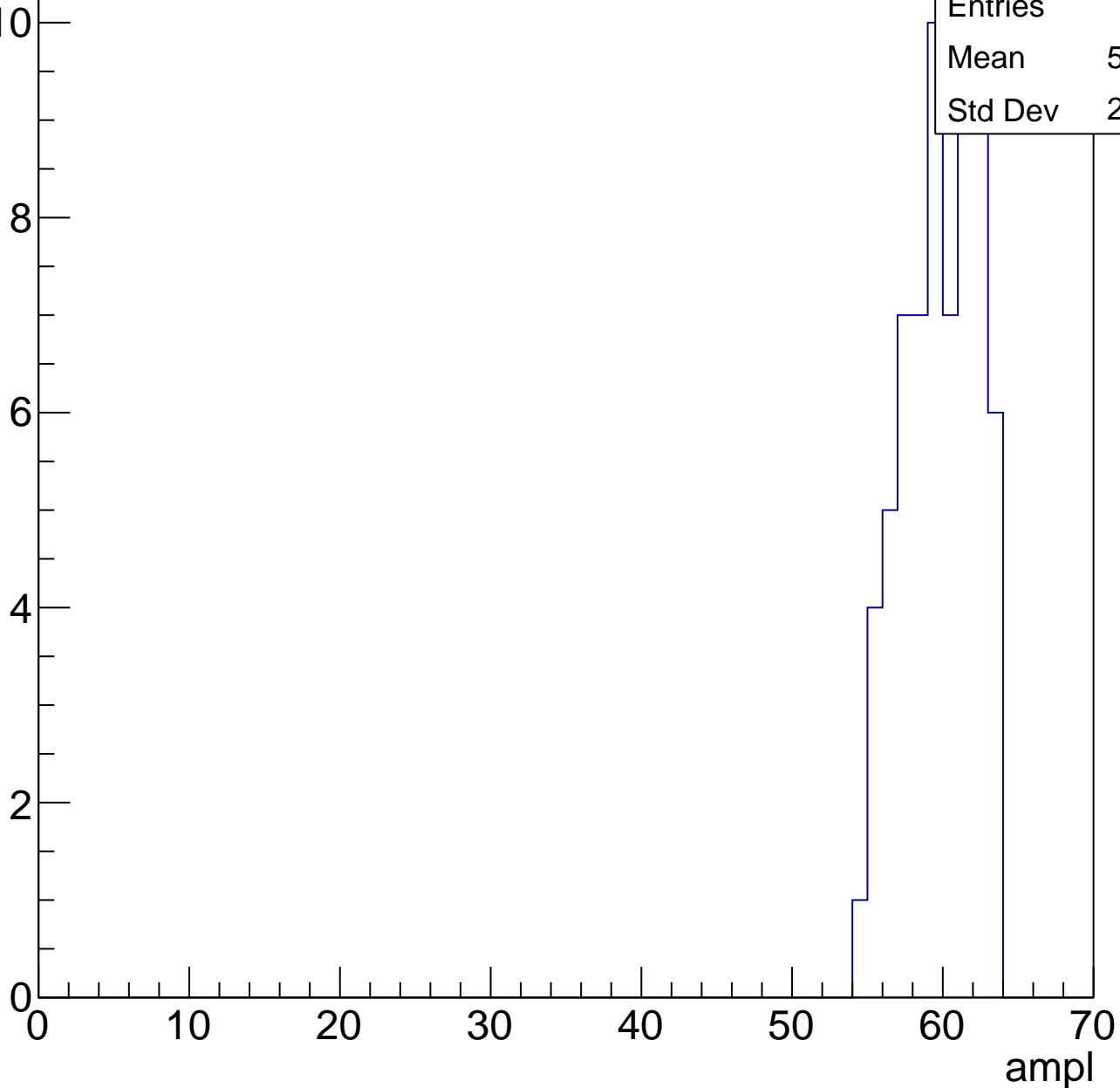


B1L102S, U10-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

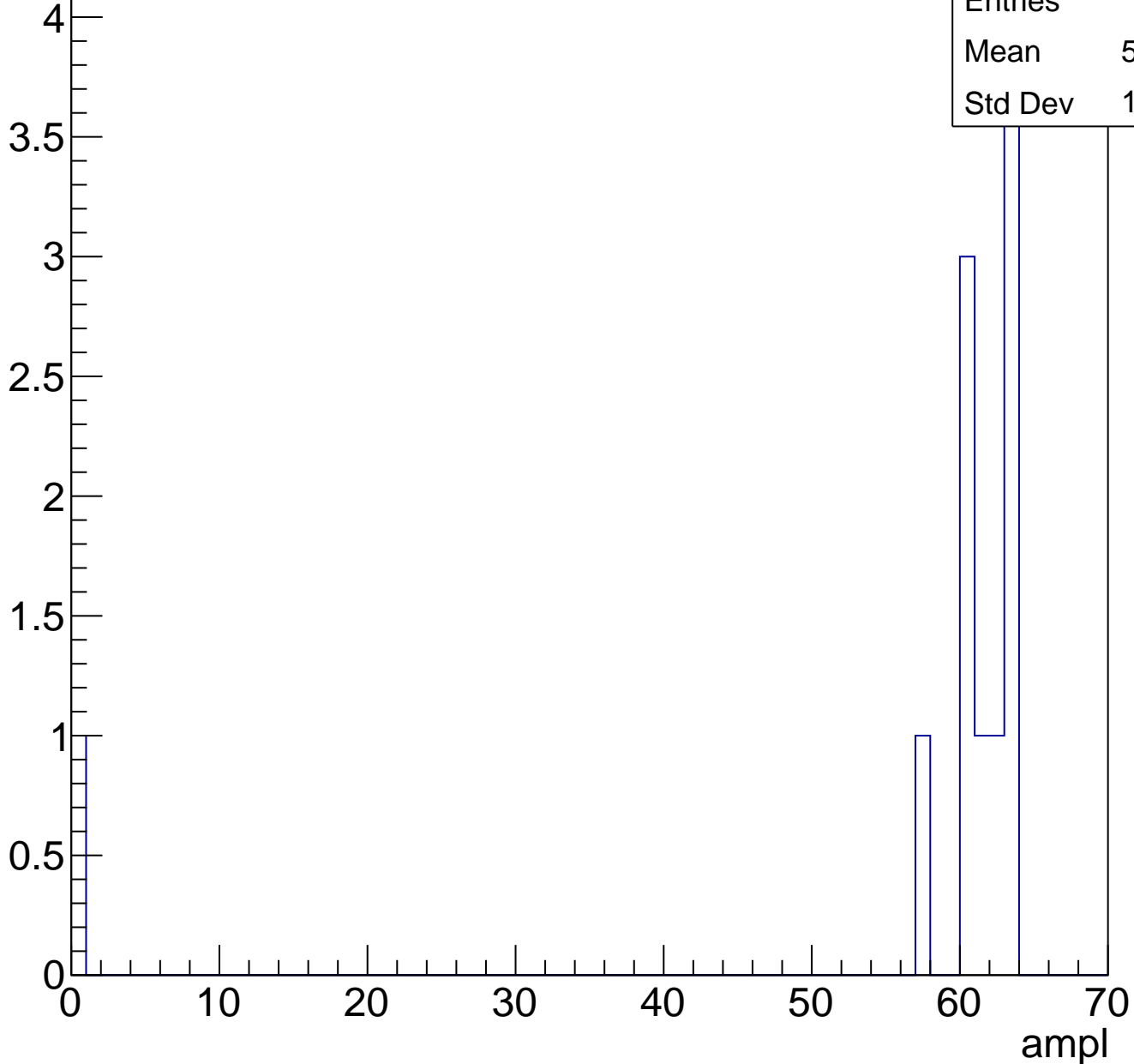
Entries	65
Mean	59.29
Std Dev	2.429



B1L102S, U10-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

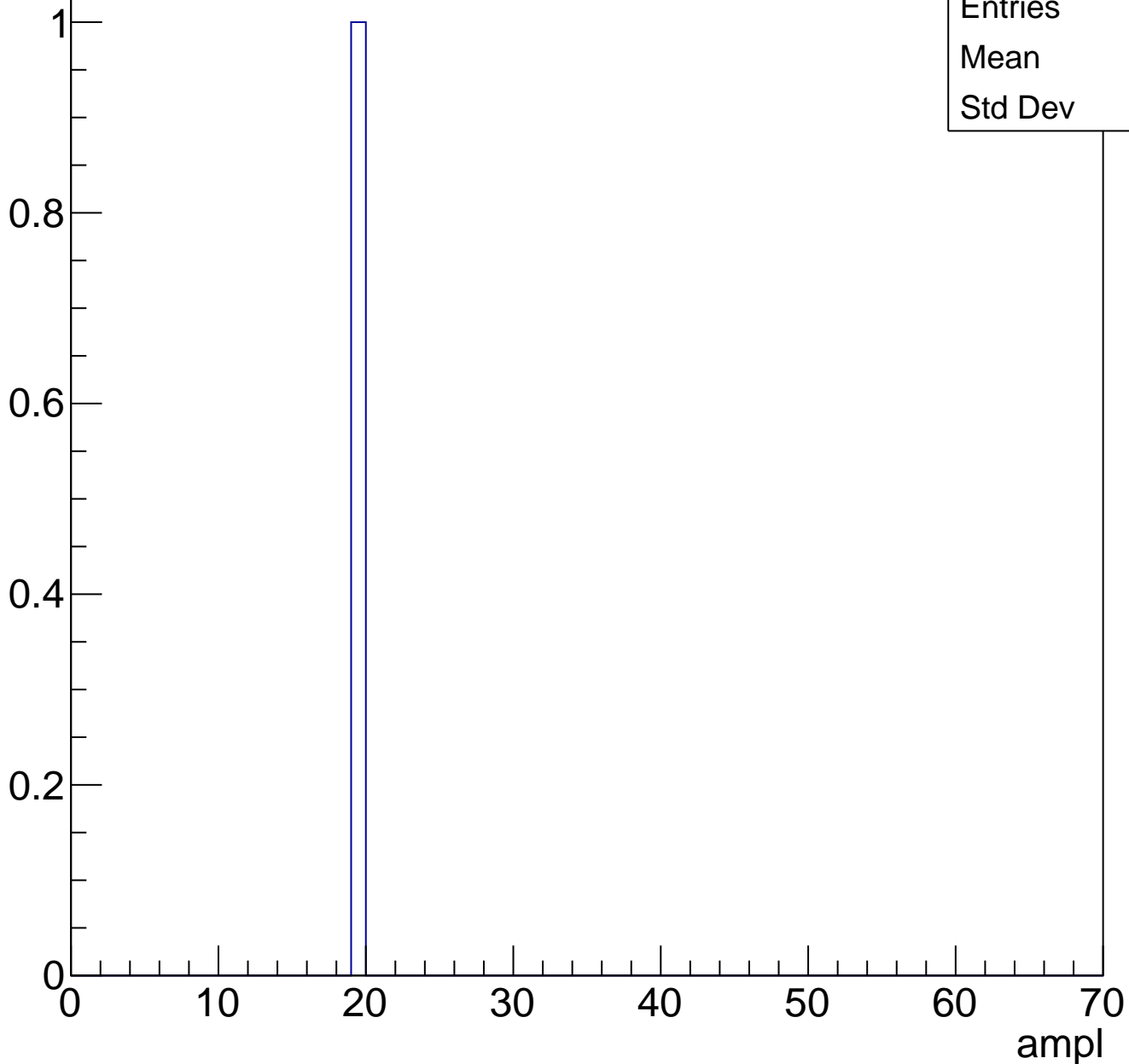
Entry



B1L102S, U10-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch116, adc0

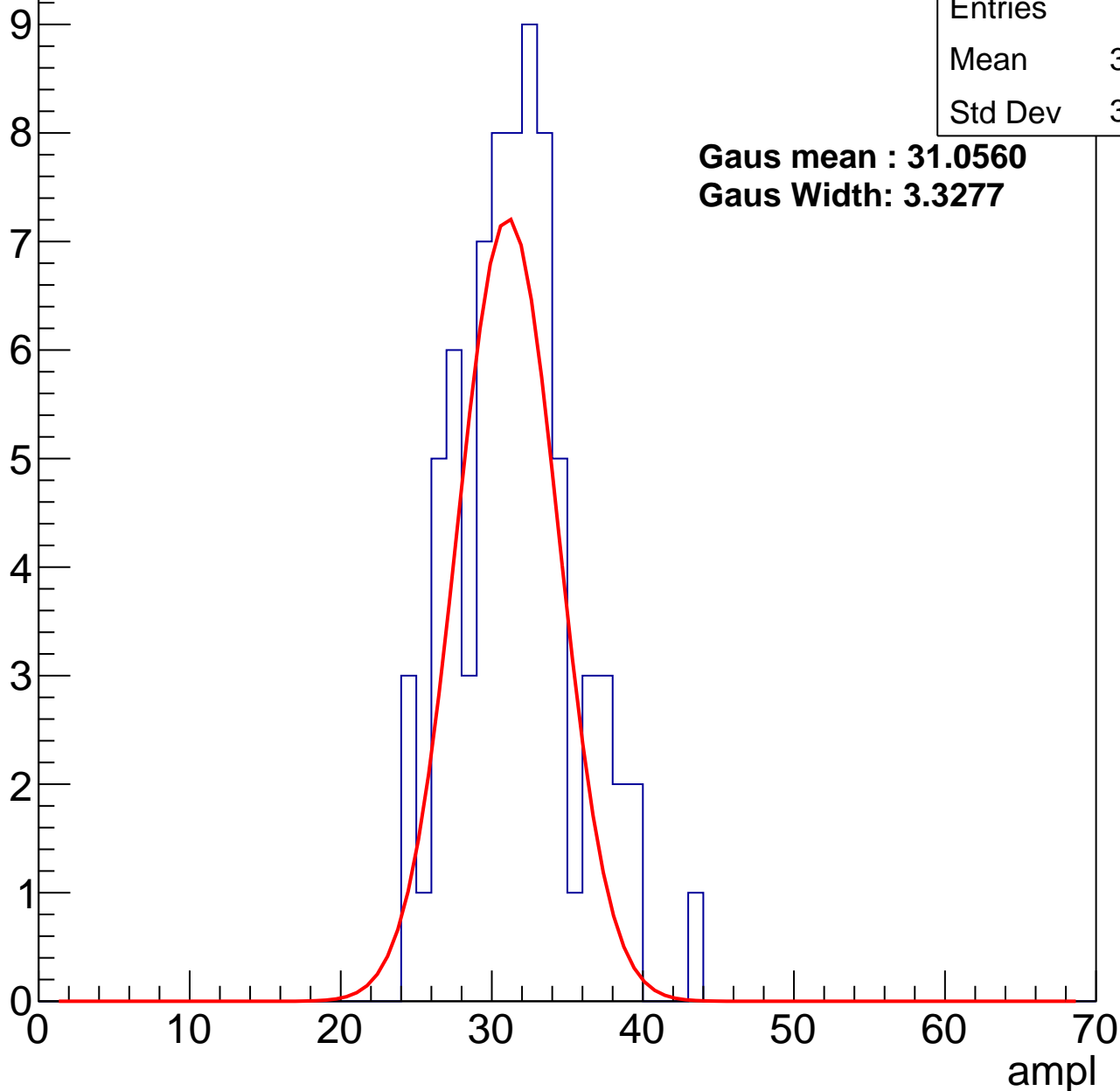
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	31.16
Std Dev	3.865

Gaus mean : 31.0560

Gaus Width: 3.3277



B1L102S, U10-ch116, adc1

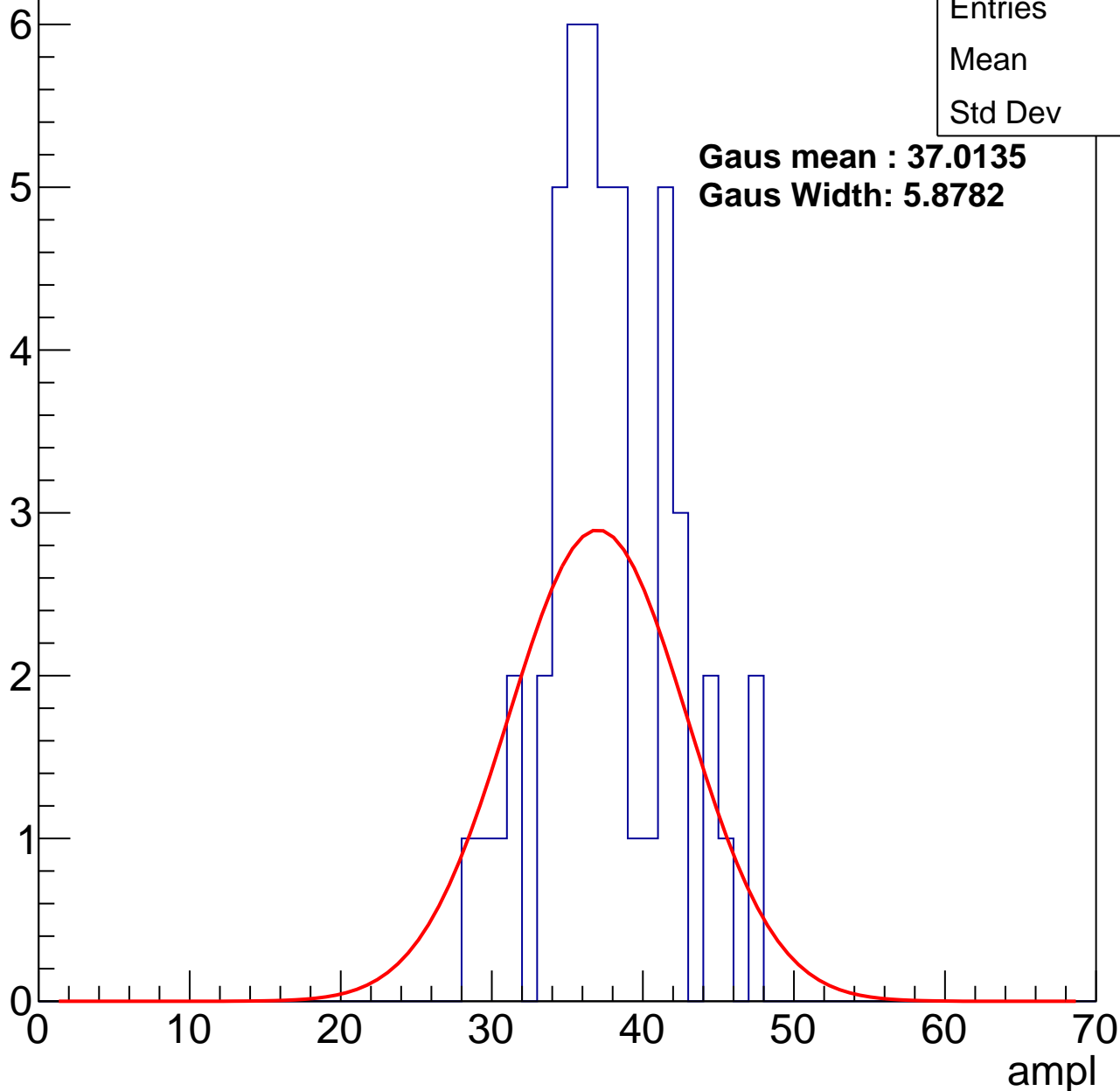
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	37.2
Std Dev	4.3

Gaus mean : 37.0135

Gaus Width: 5.8782



B1L102S, U10-ch116, adc2

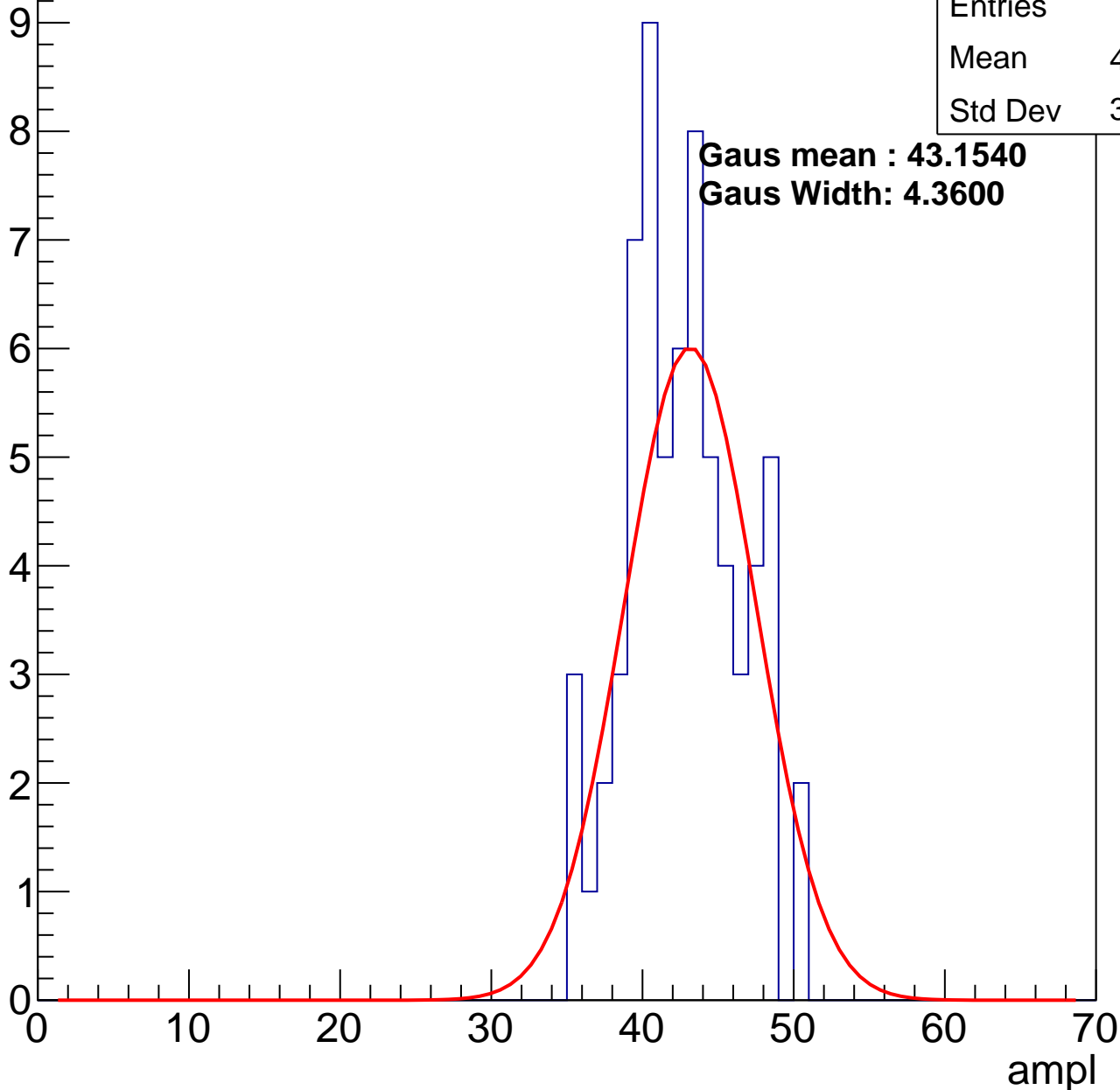
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.22
Std Dev	3.668

Gaus mean : 43.1540

Gaus Width: 4.3600

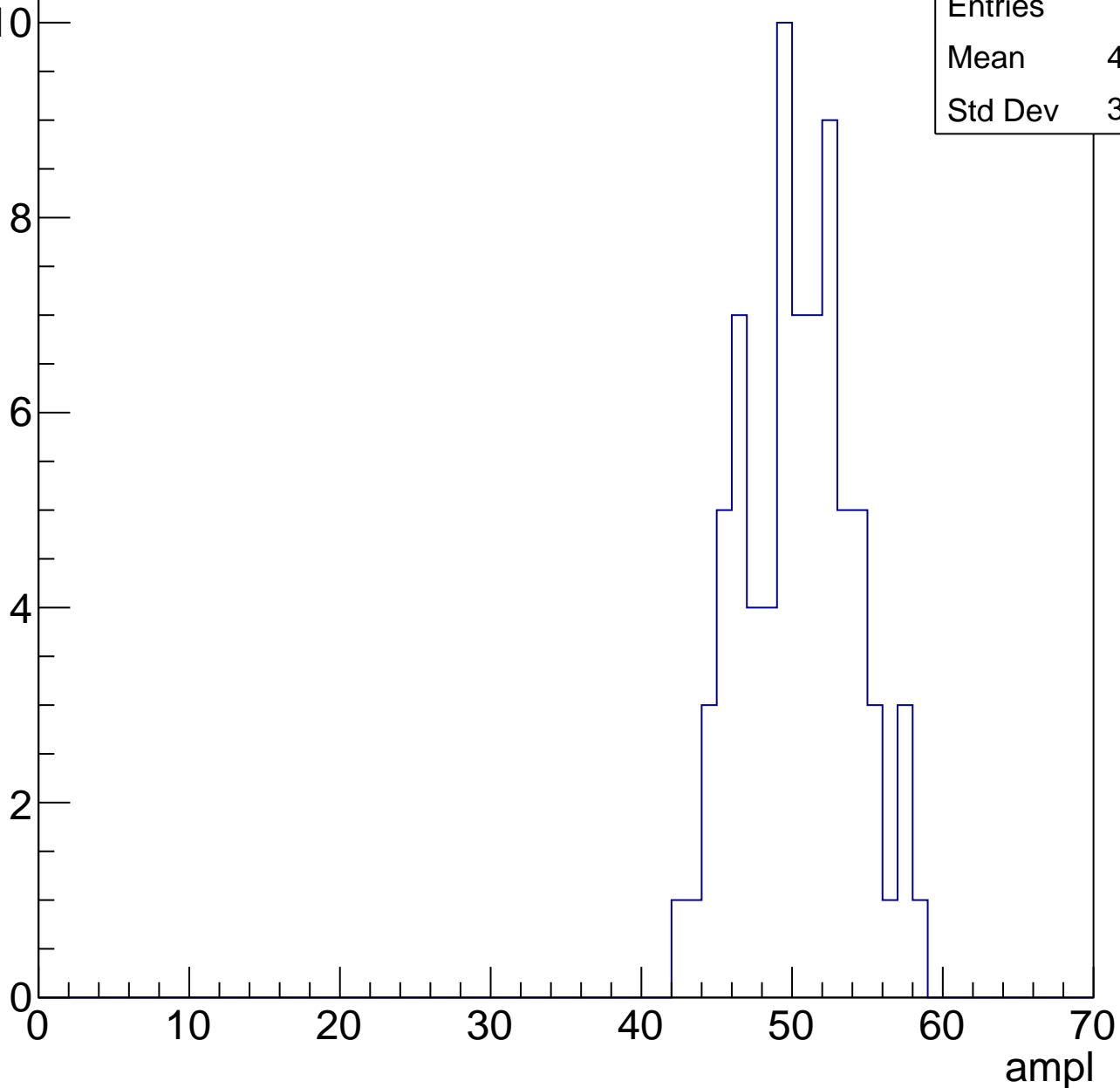


B1L102S, U10-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	49.92
Std Dev	3.645

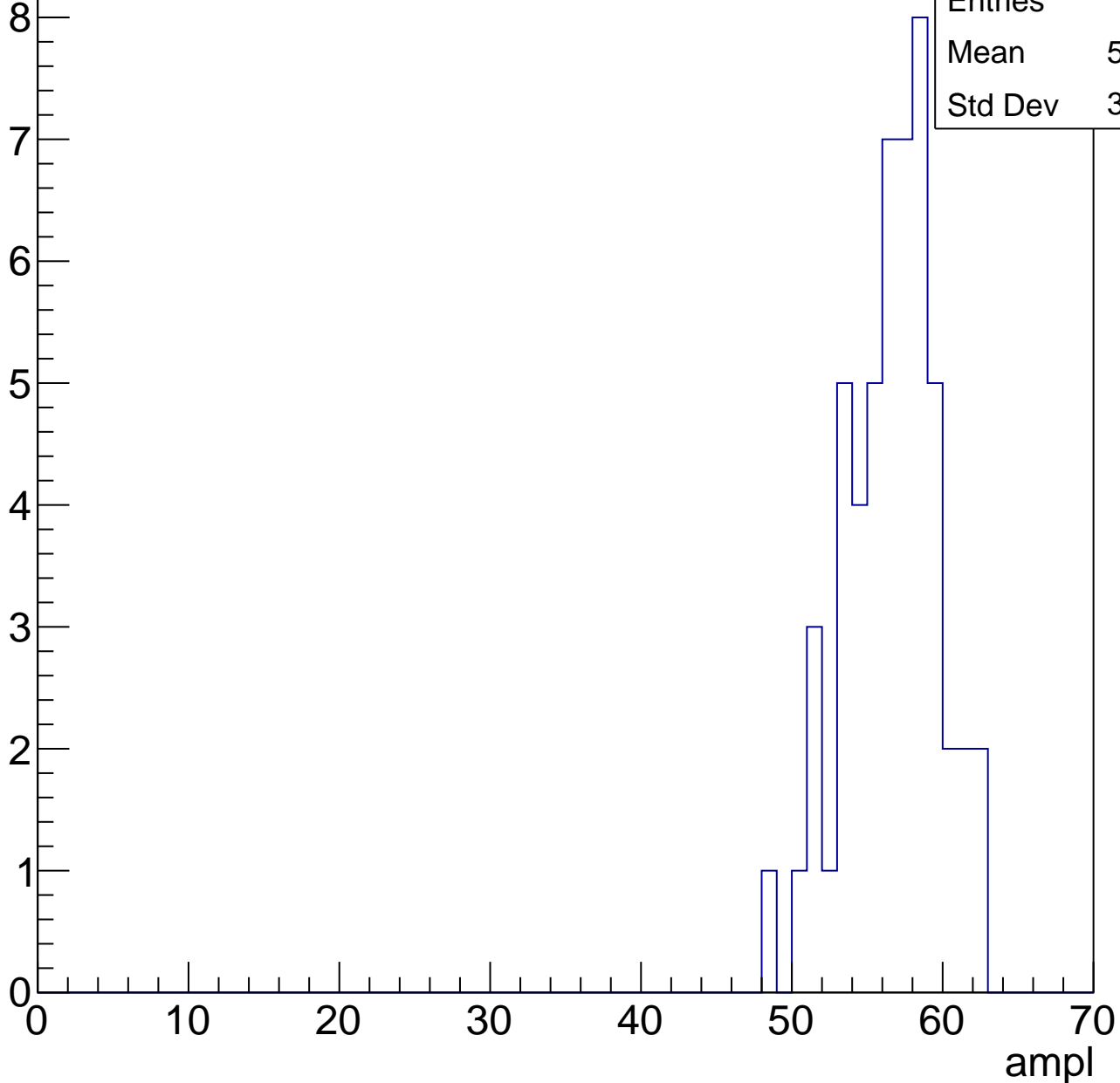


B1L102S, U10-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.13
Std Dev	3.053



B1L102S, U10-ch116, adc5

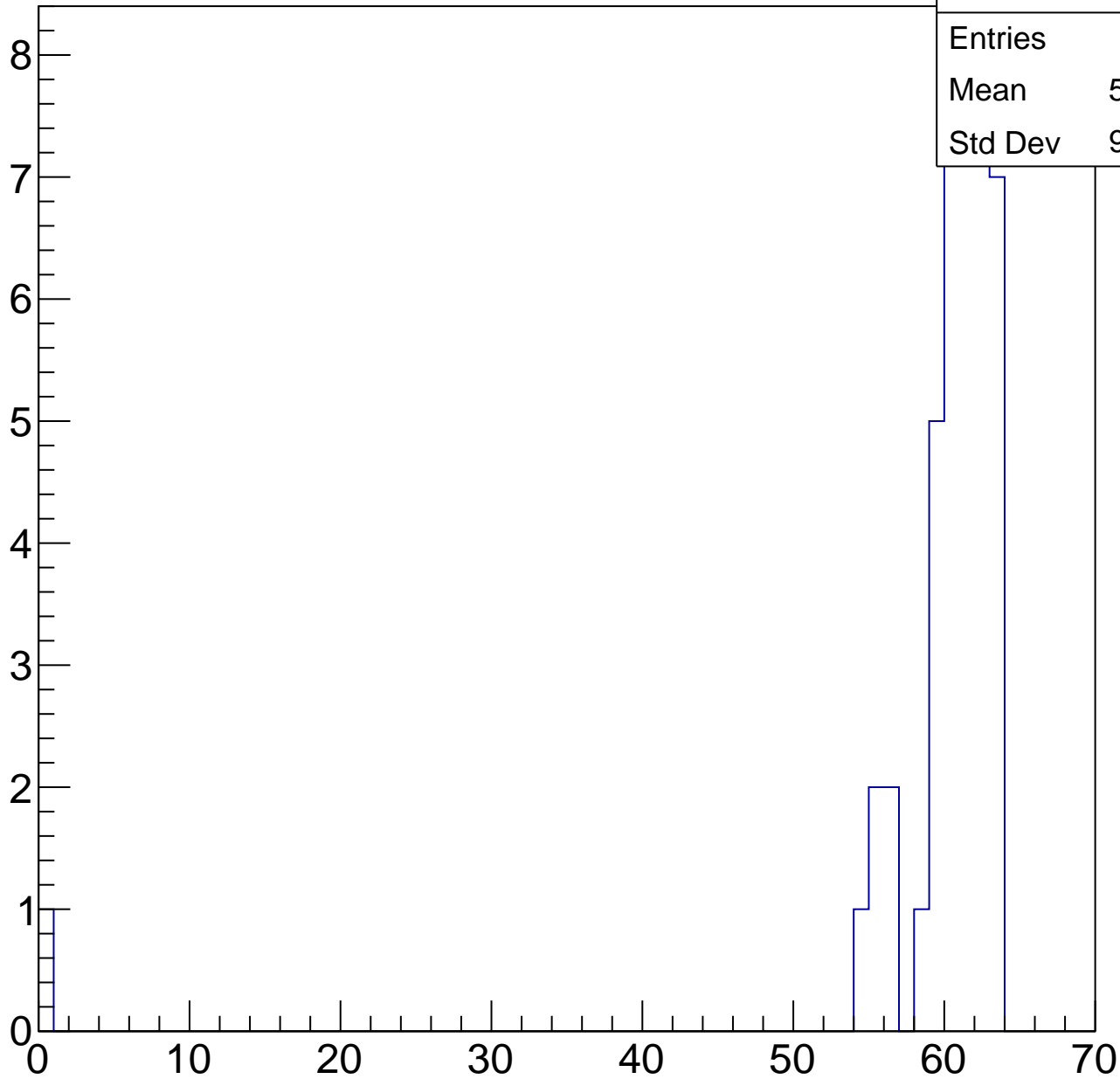
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.93
Std Dev	9.377

ampl



B1L102S, U10-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	50
Std Dev	18.38

0 10 20 30 40 50 60 70

ampl

24

63

B1L102S, U10-ch117, adc0

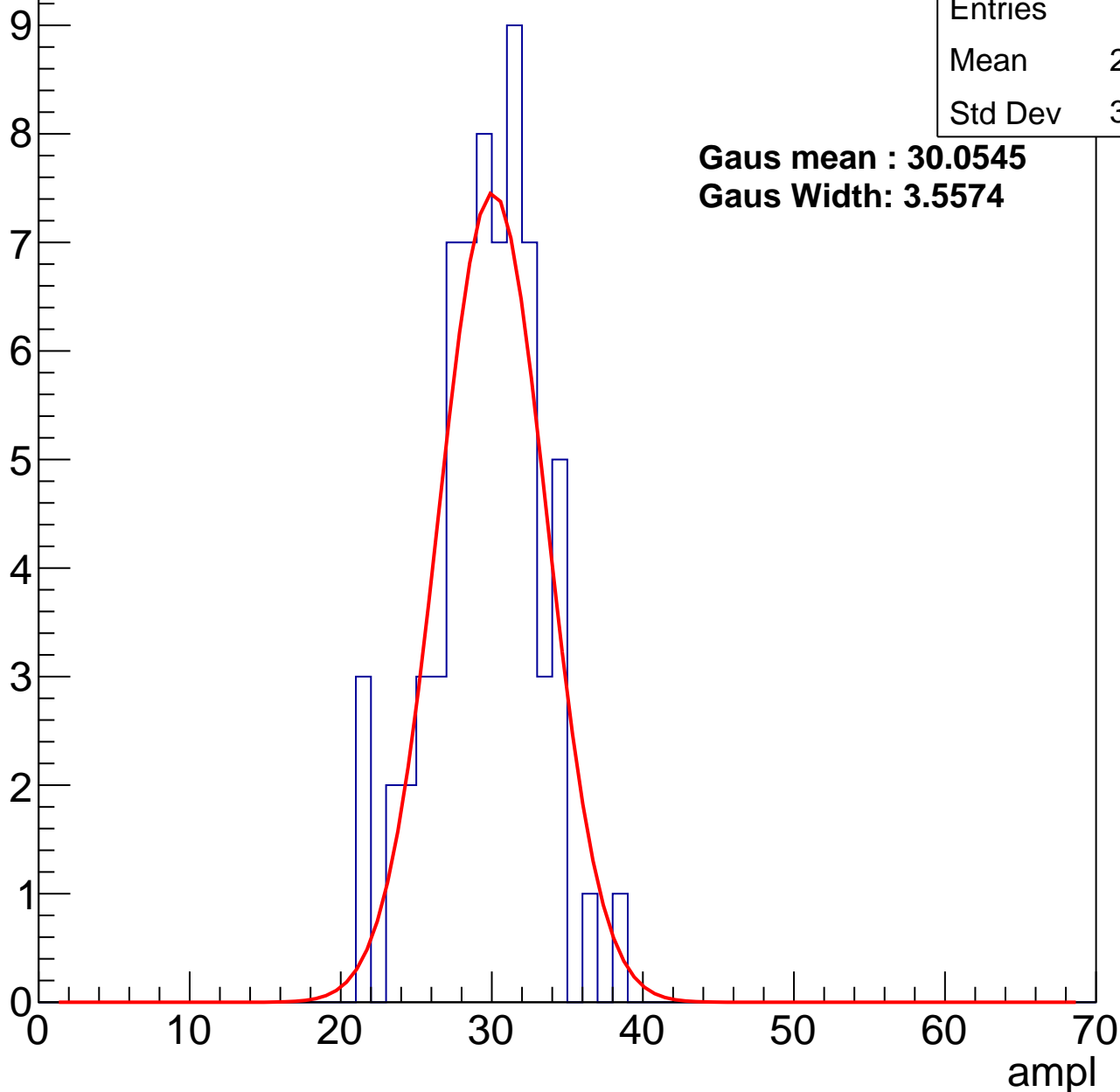
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.16
Std Dev	3.496

Gaus mean : 30.0545

Gaus Width: 3.5574



B1L102S, U10-ch117, adc1

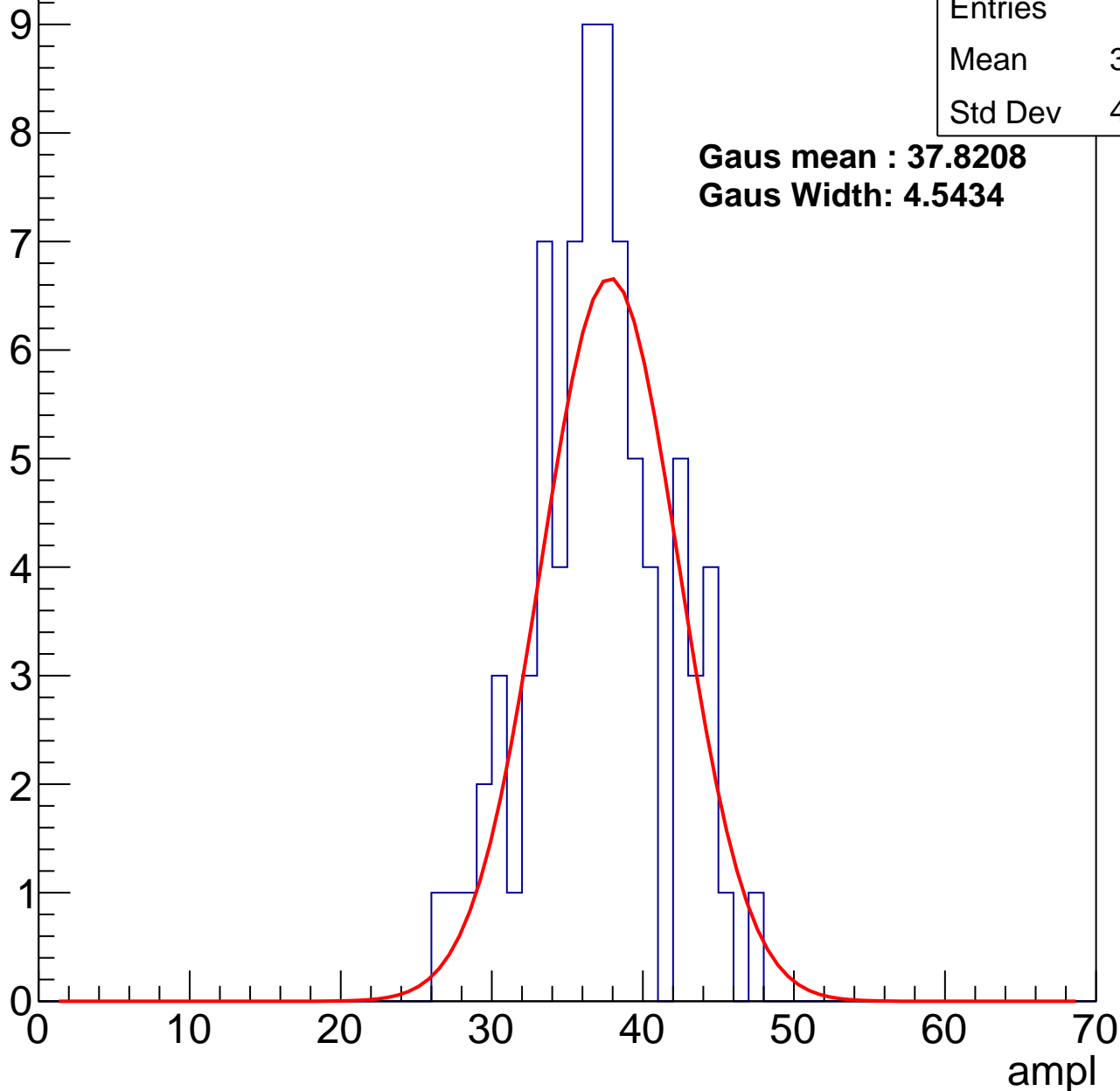
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.58
Std Dev	4.407

Gaus mean : 37.8208

Gaus Width: 4.5434



B1L102S, U10-ch117, adc2

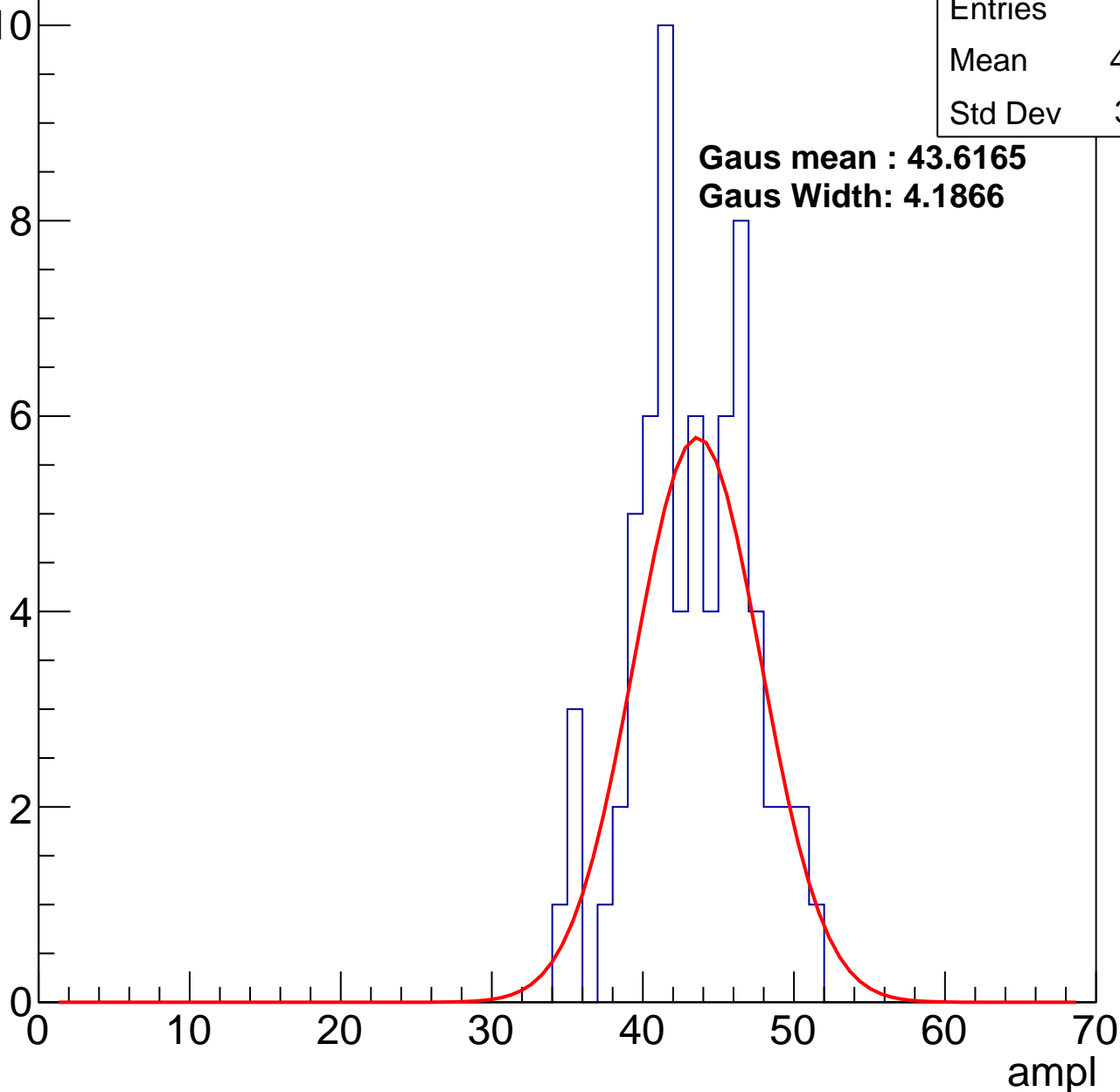
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.84
Std Dev	3.831

Gaus mean : 43.6165

Gaus Width: 4.1866

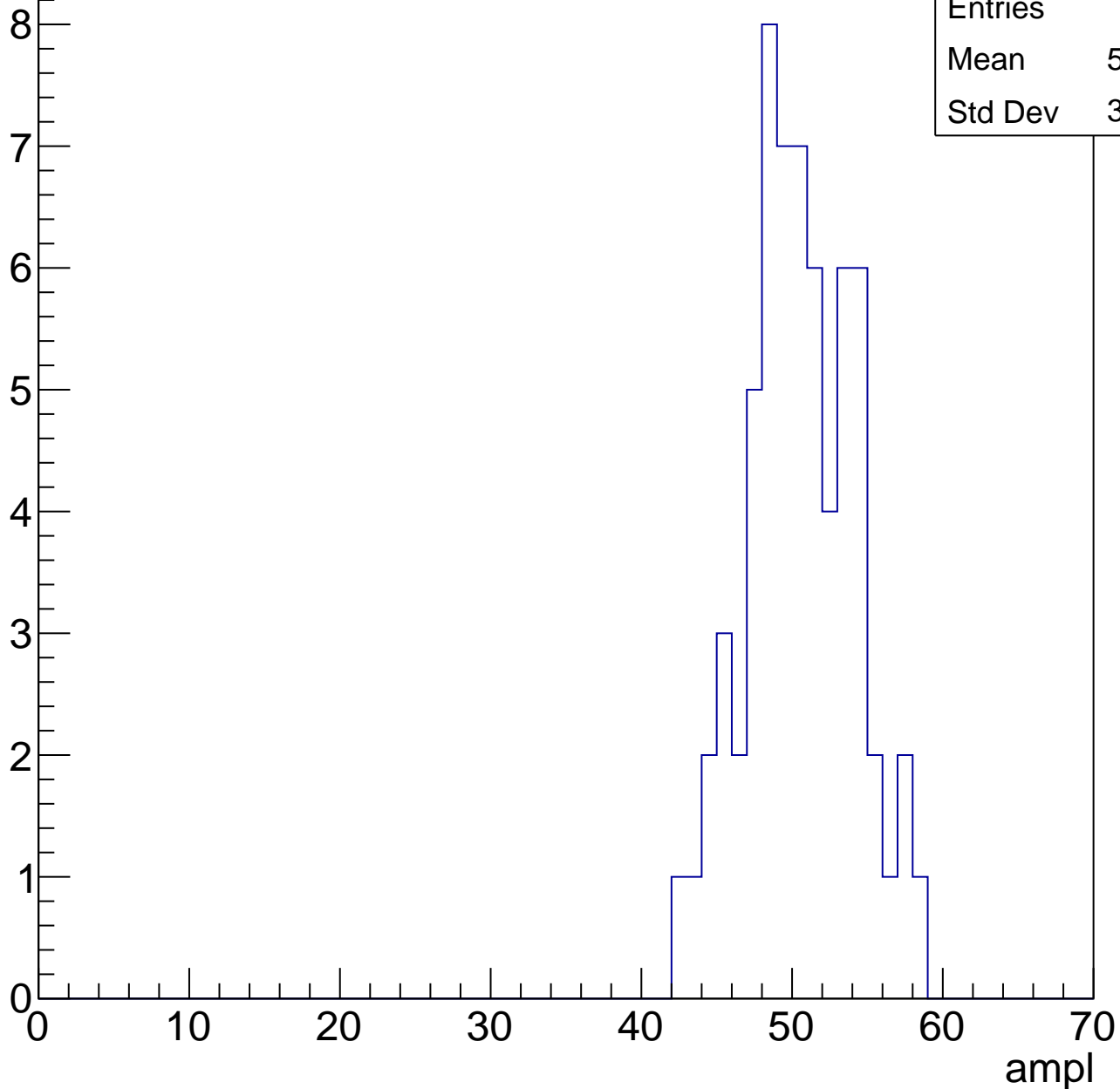


B1L102S, U10-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.09
Std Dev	3.525

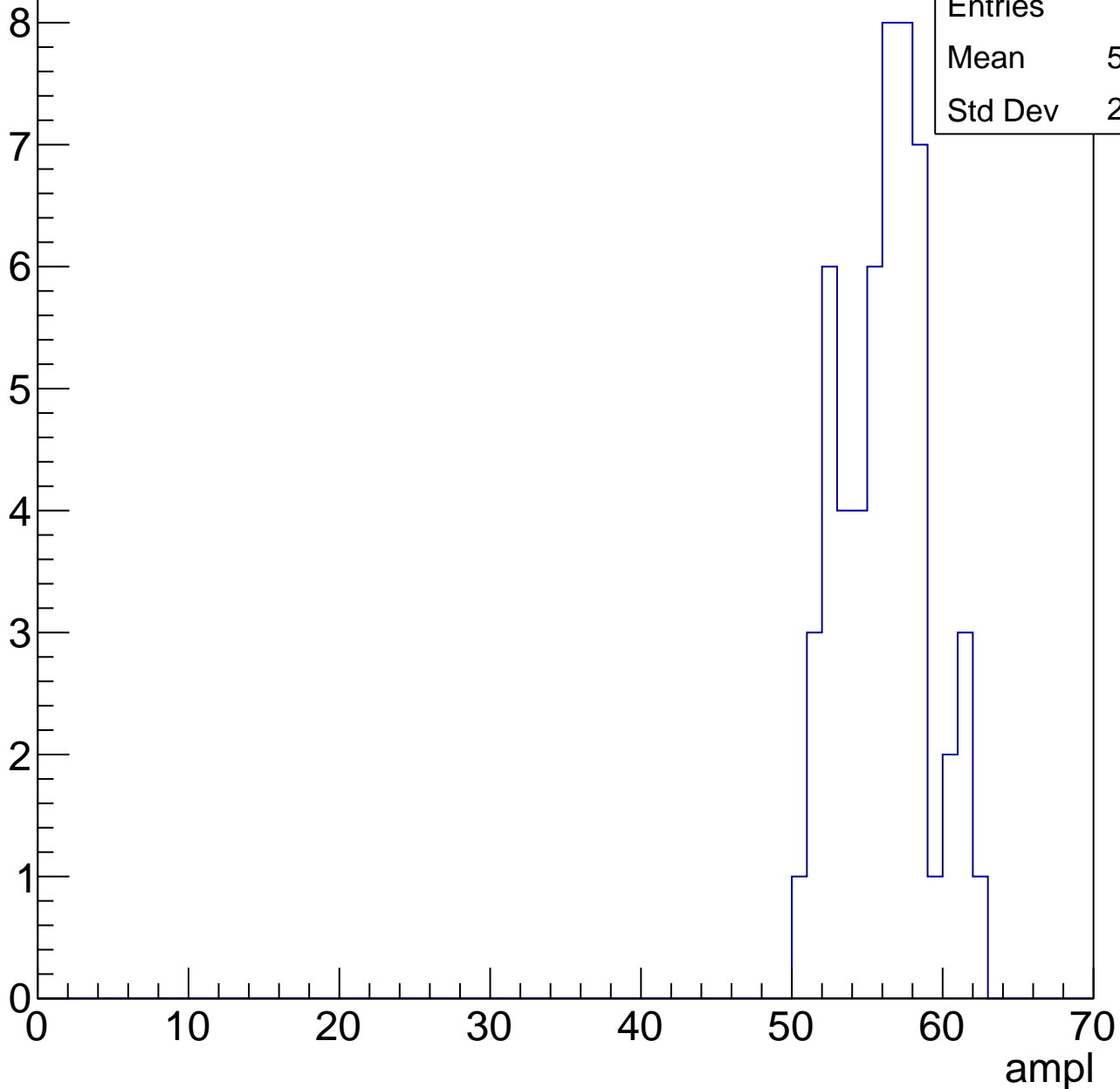


B1L102S, U10-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.69
Std Dev	2.879

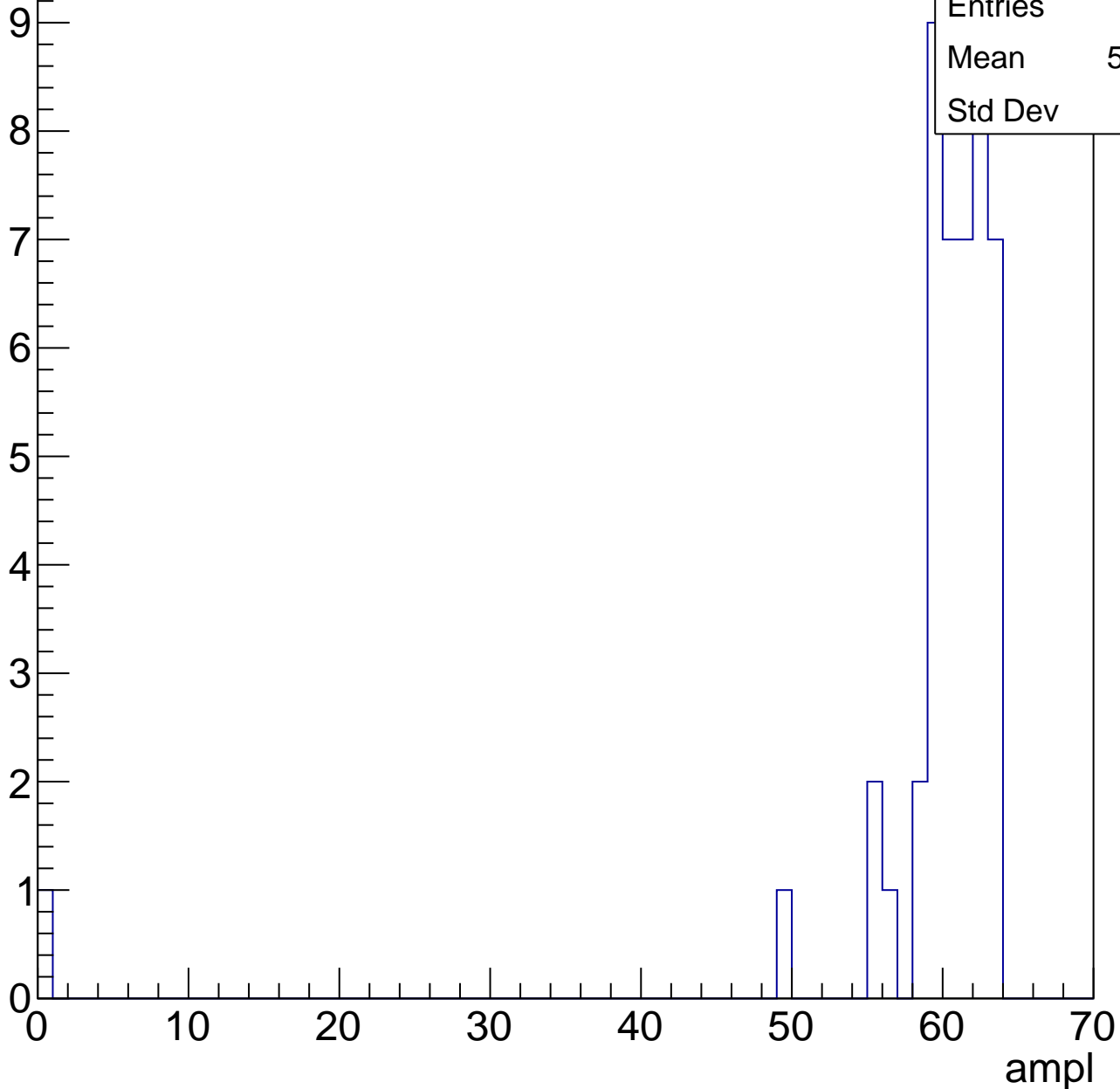


B1L102S, U10-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.87
Std Dev	9.15



B1L102S, U10-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch118, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	28.74
Std Dev	5.014

Gaus mean : 28.7456

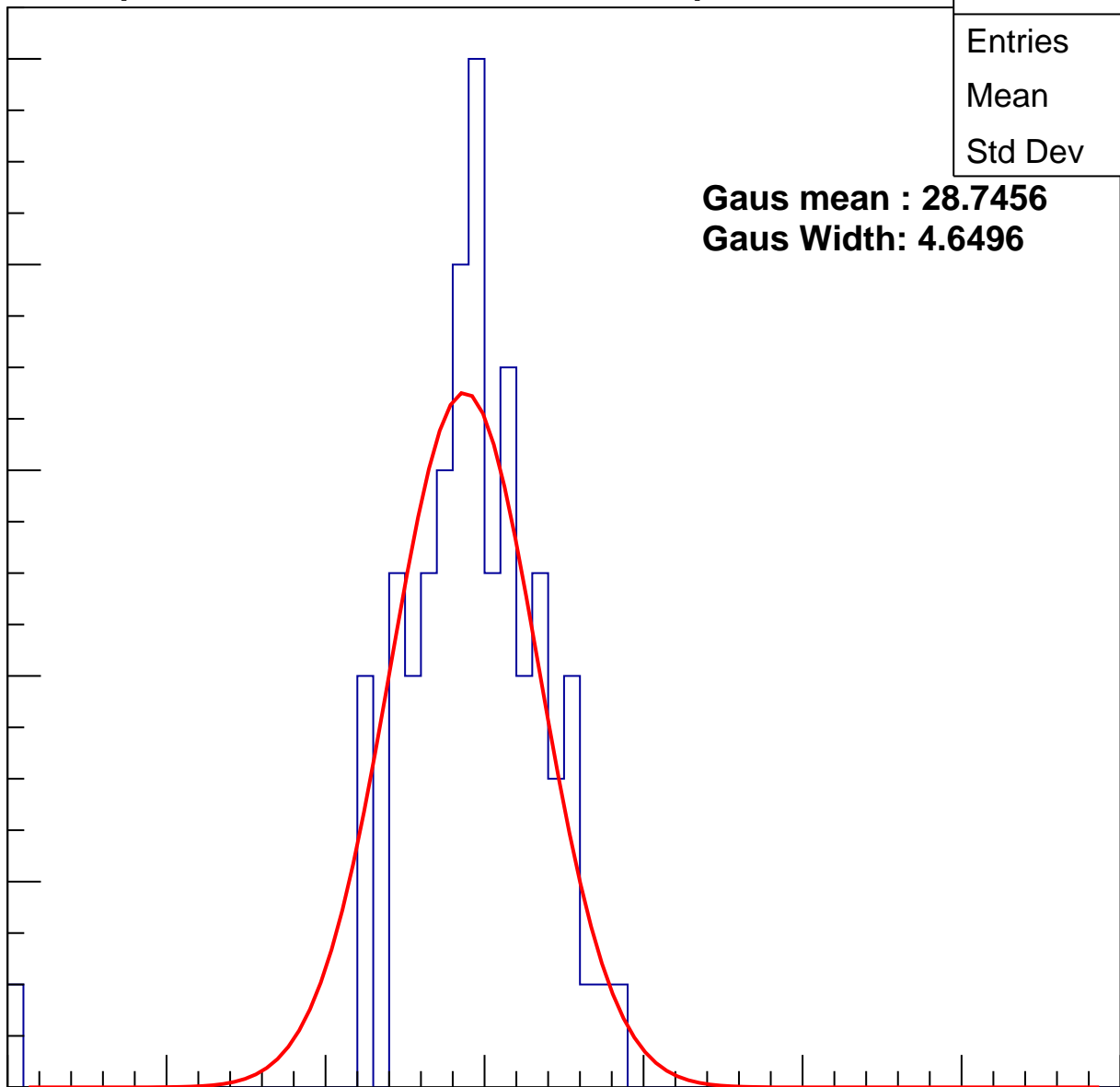
Gaus Width: 4.6496

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch118, adc1

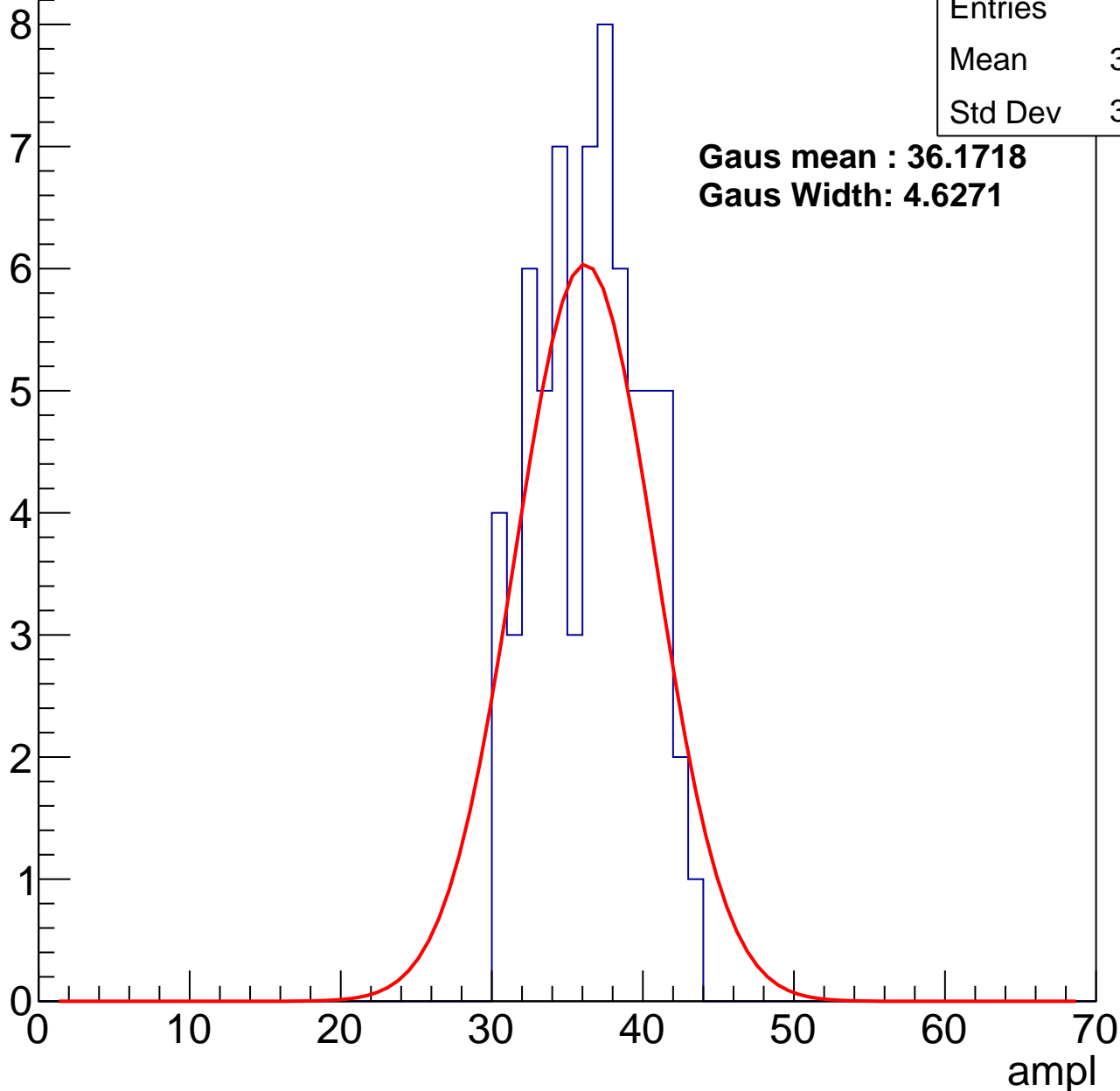
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.06
Std Dev	3.442

Gaus mean : 36.1718

Gaus Width: 4.6271



B1L102S, U10-ch118, adc2

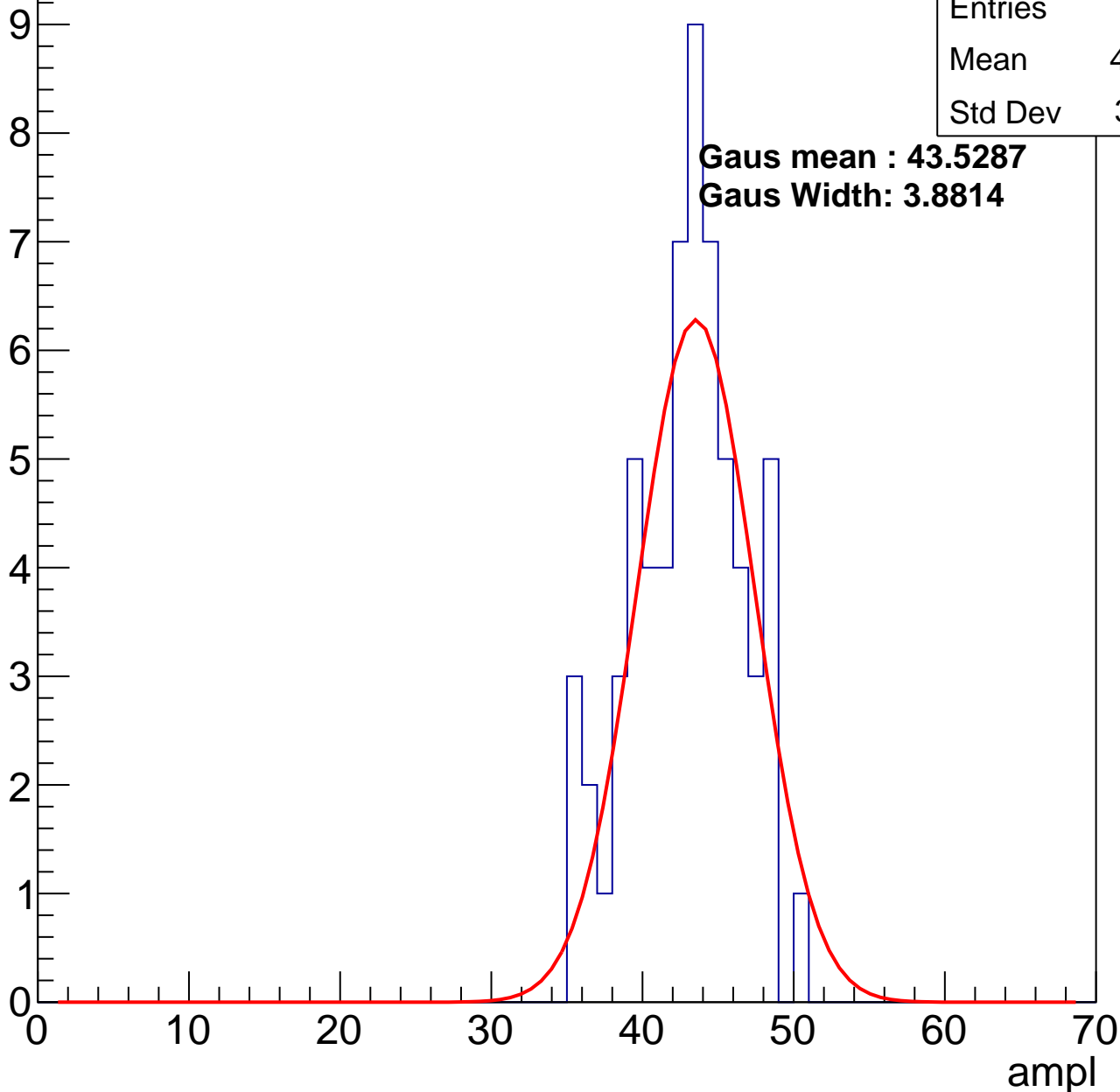
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.48
Std Dev	3.581

Gaus mean : 43.5287

Gaus Width: 3.8814

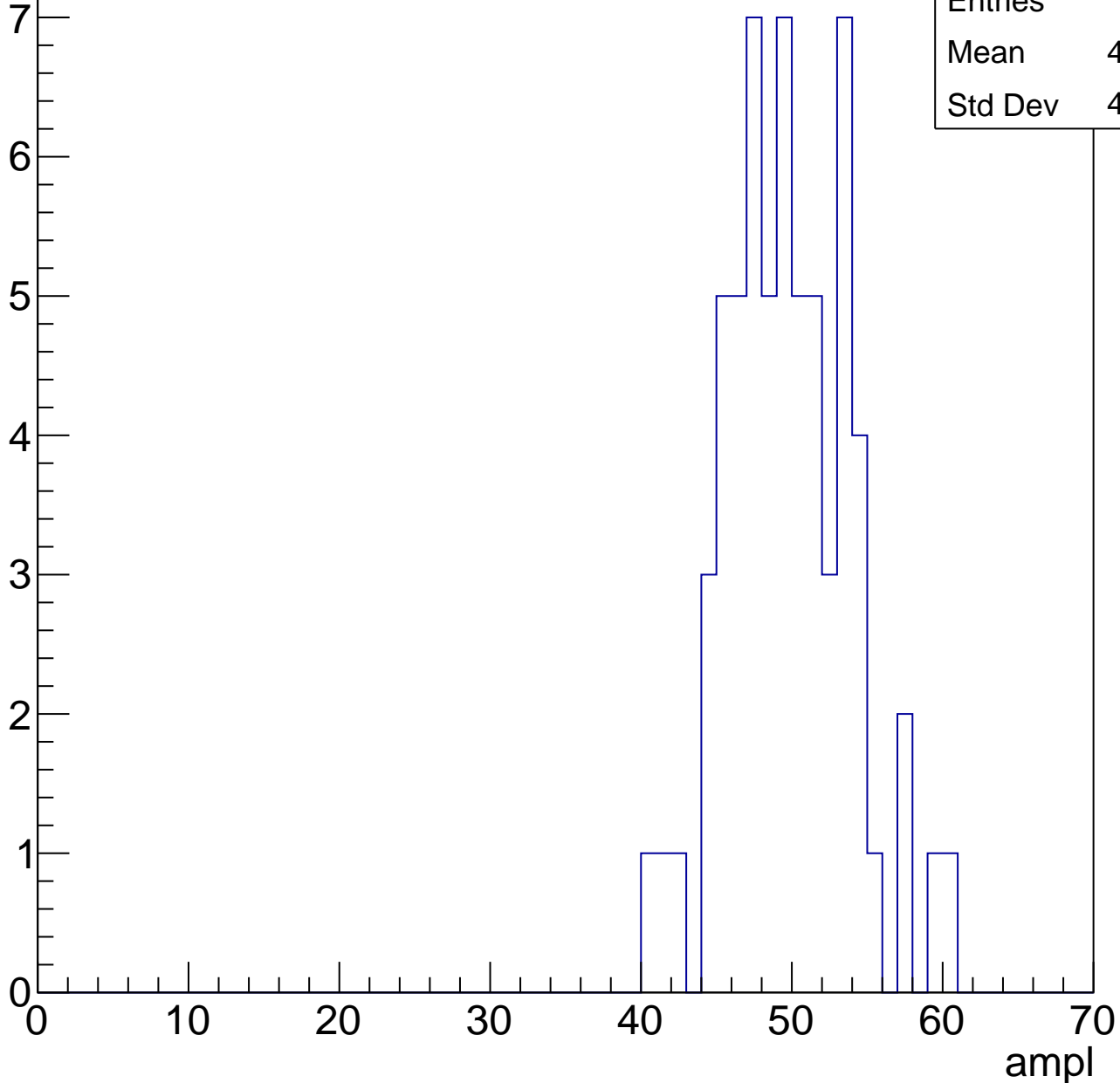


B1L102S, U10-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.34
Std Dev	4.086

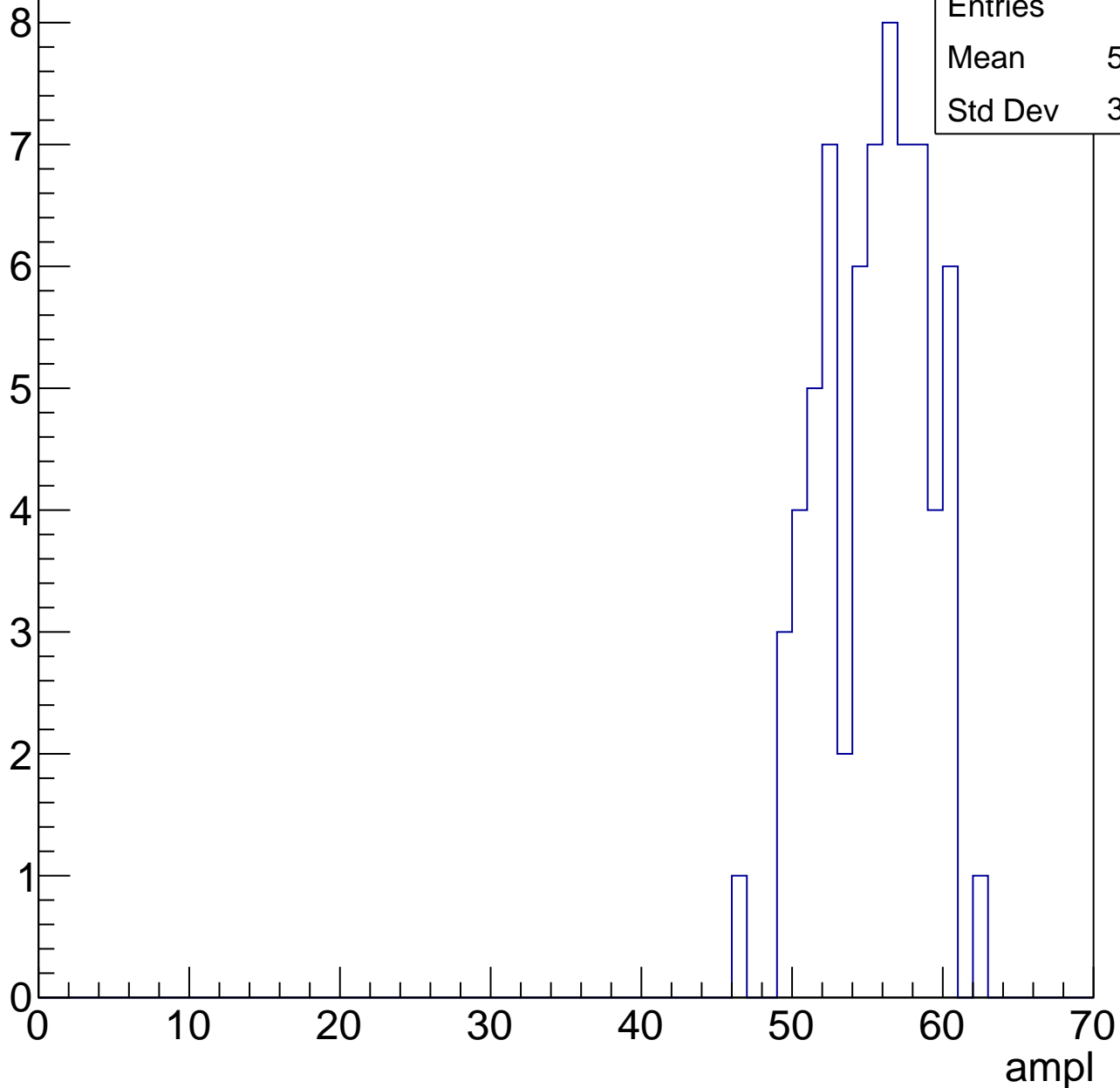


B1L102S, U10-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.97
Std Dev	3.447



B1L102S, U10-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	39
Mean	58.44
Std Dev	9.8

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

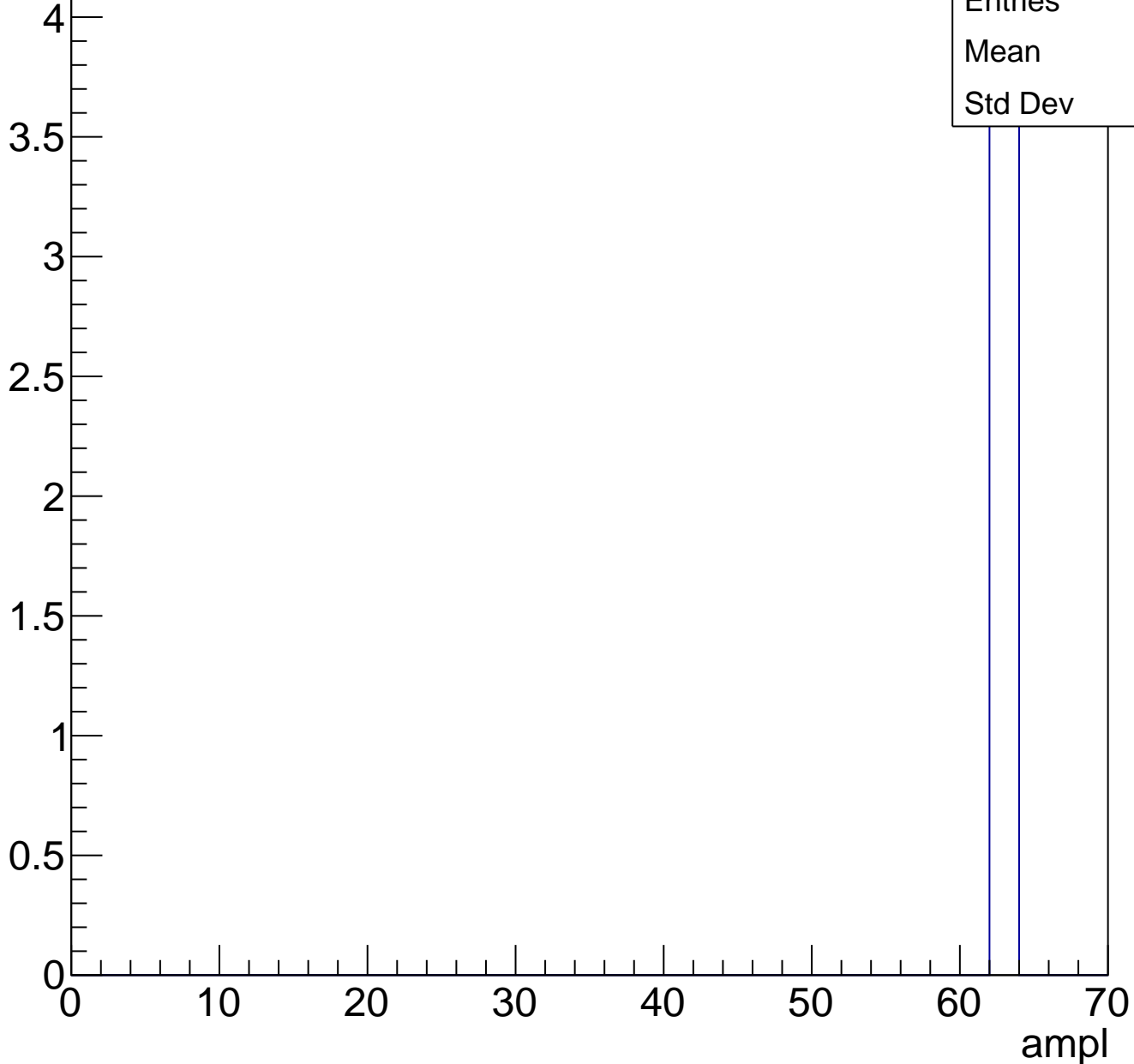
ampl

70

B1L102S, U10-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

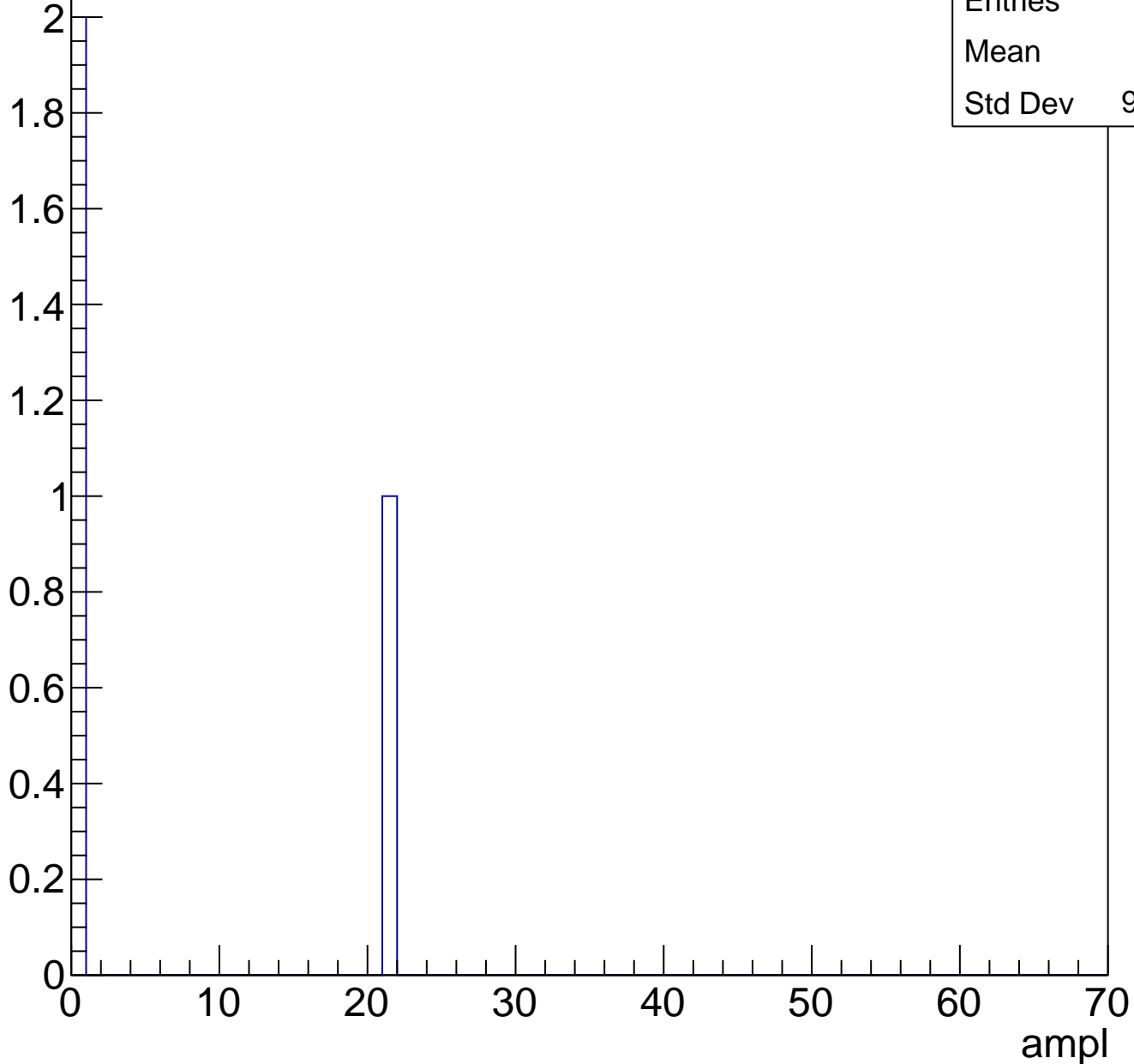
Entry



B1L102S, U10-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U10-ch119, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	28.55
Std Dev	5.101

Gaus mean : 29.7791

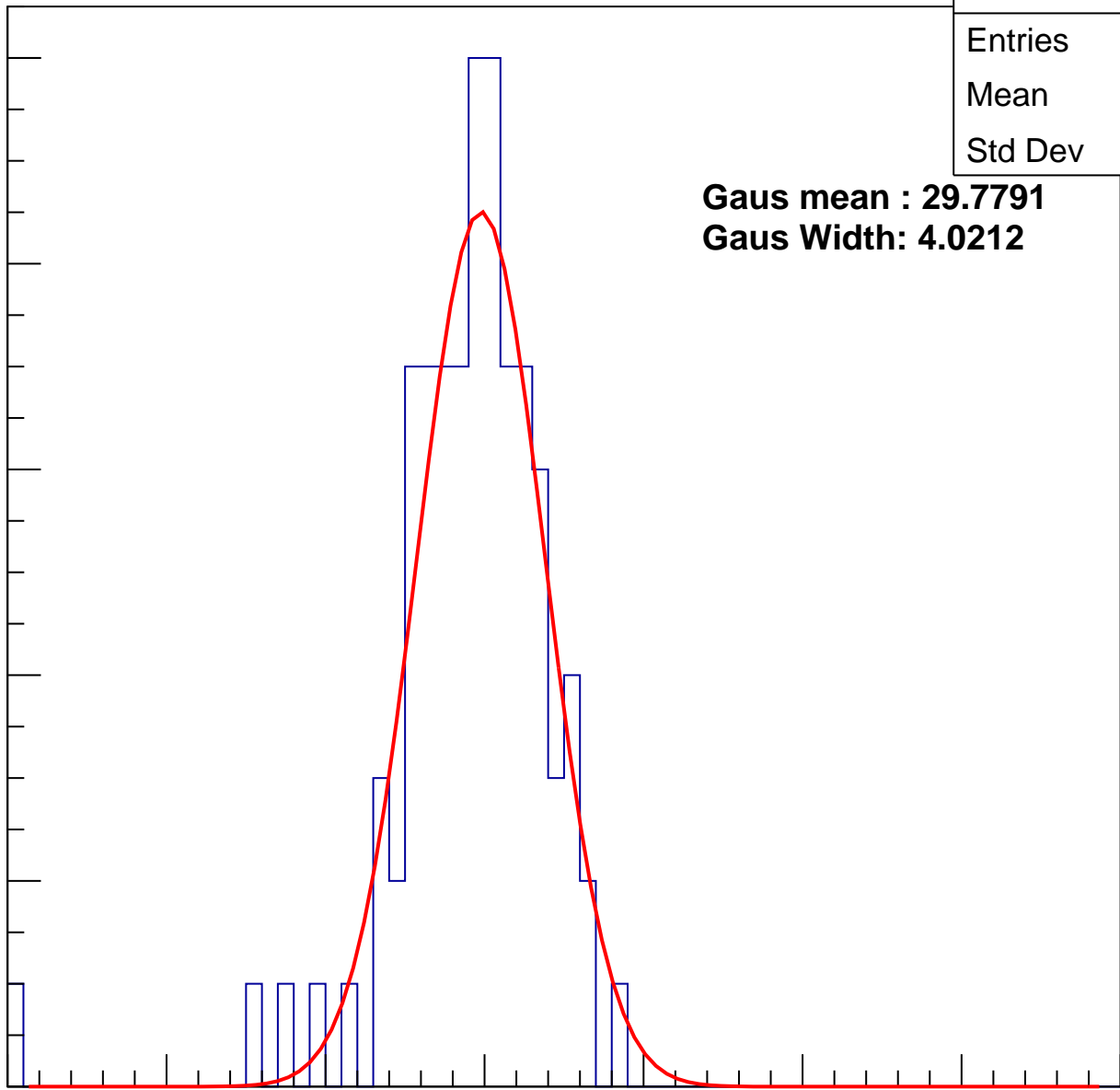
Gaus Width: 4.0212

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch119, adc1

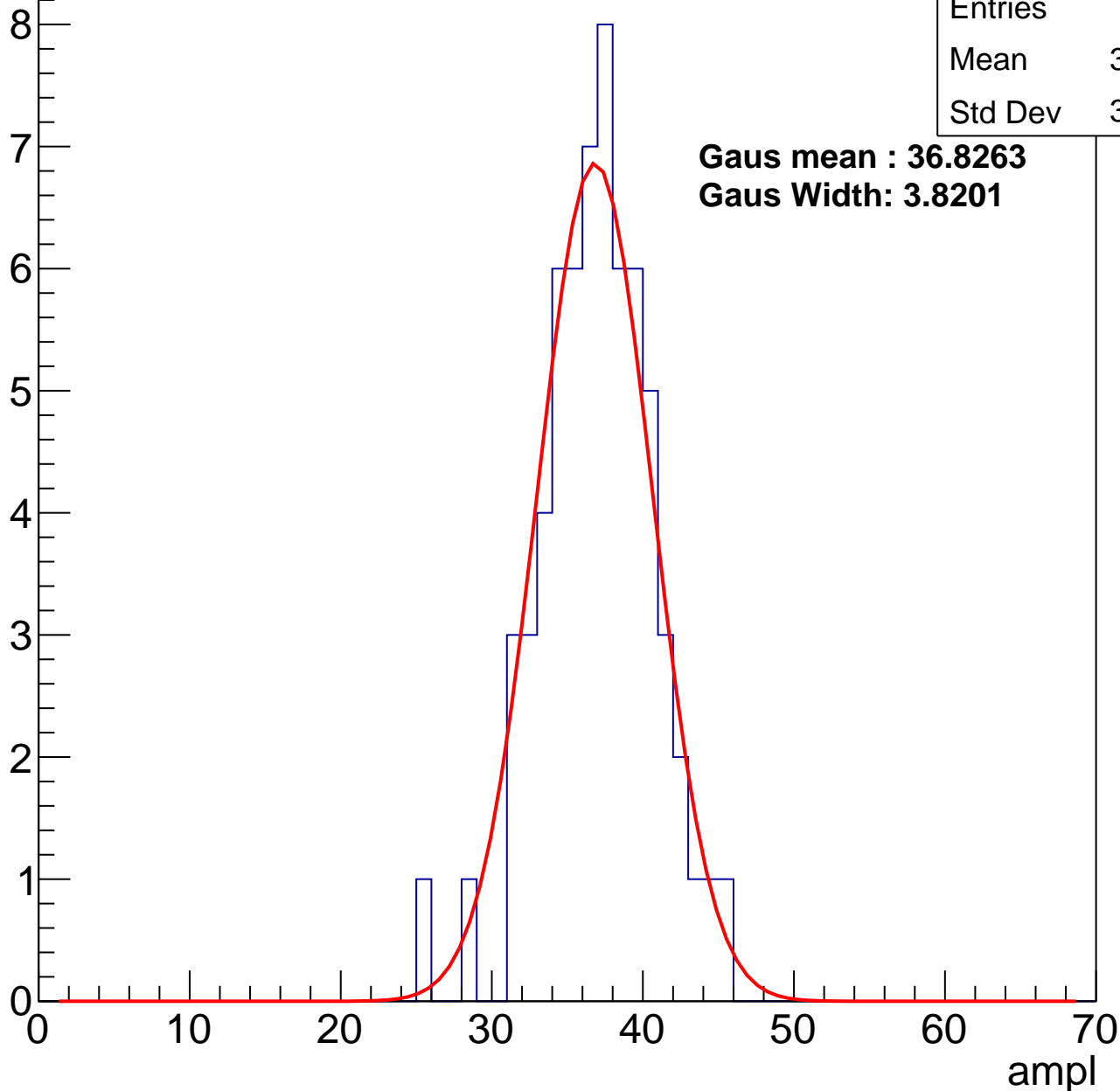
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.52
Std Dev	3.674

Gaus mean : 36.8263

Gaus Width: 3.8201



B1L102S, U10-ch119, adc2

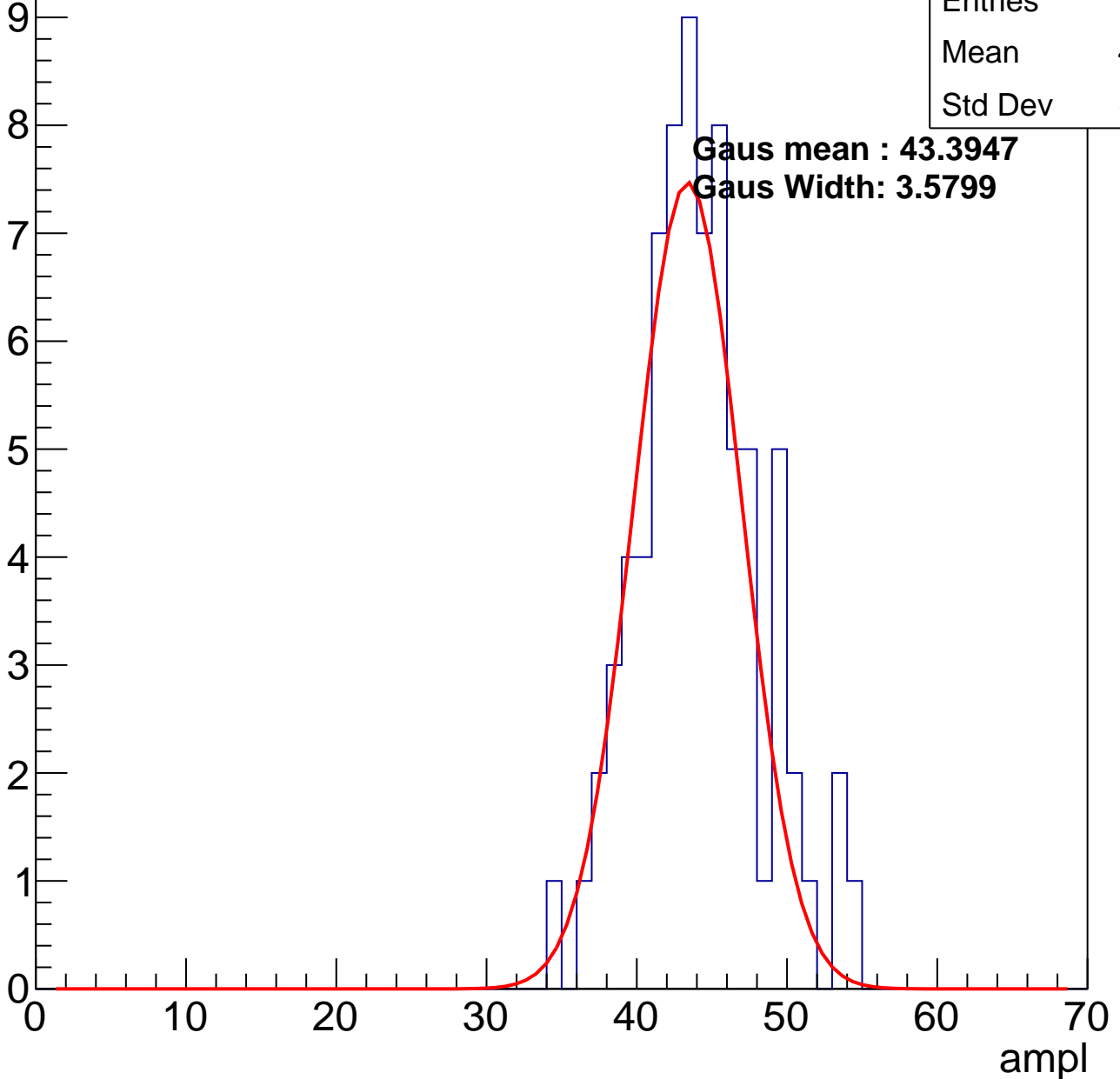
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.7
Std Dev	4.02

Gaus mean : 43.3947

Gaus Width: 3.5799

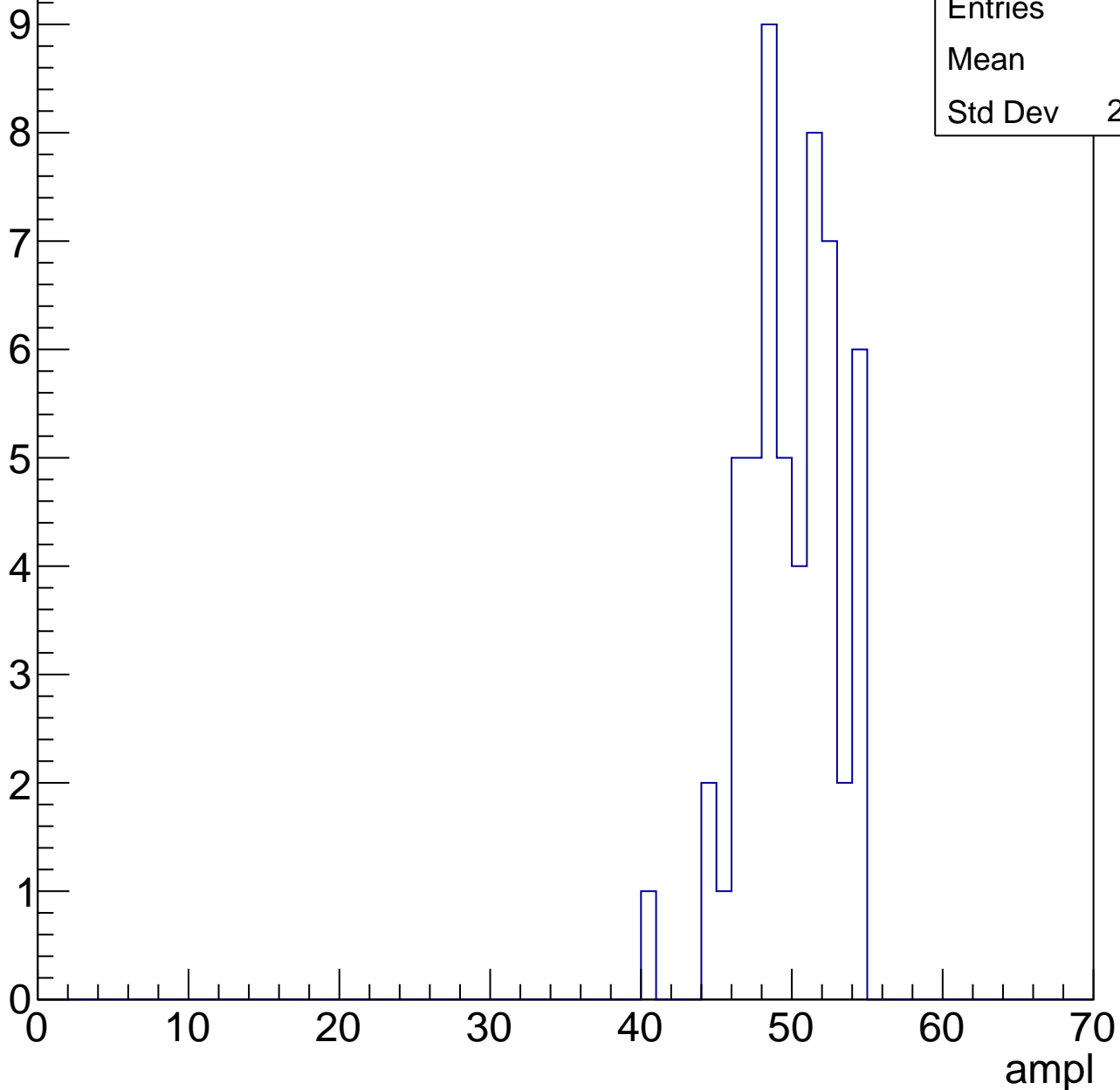


B1L102S, U10-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

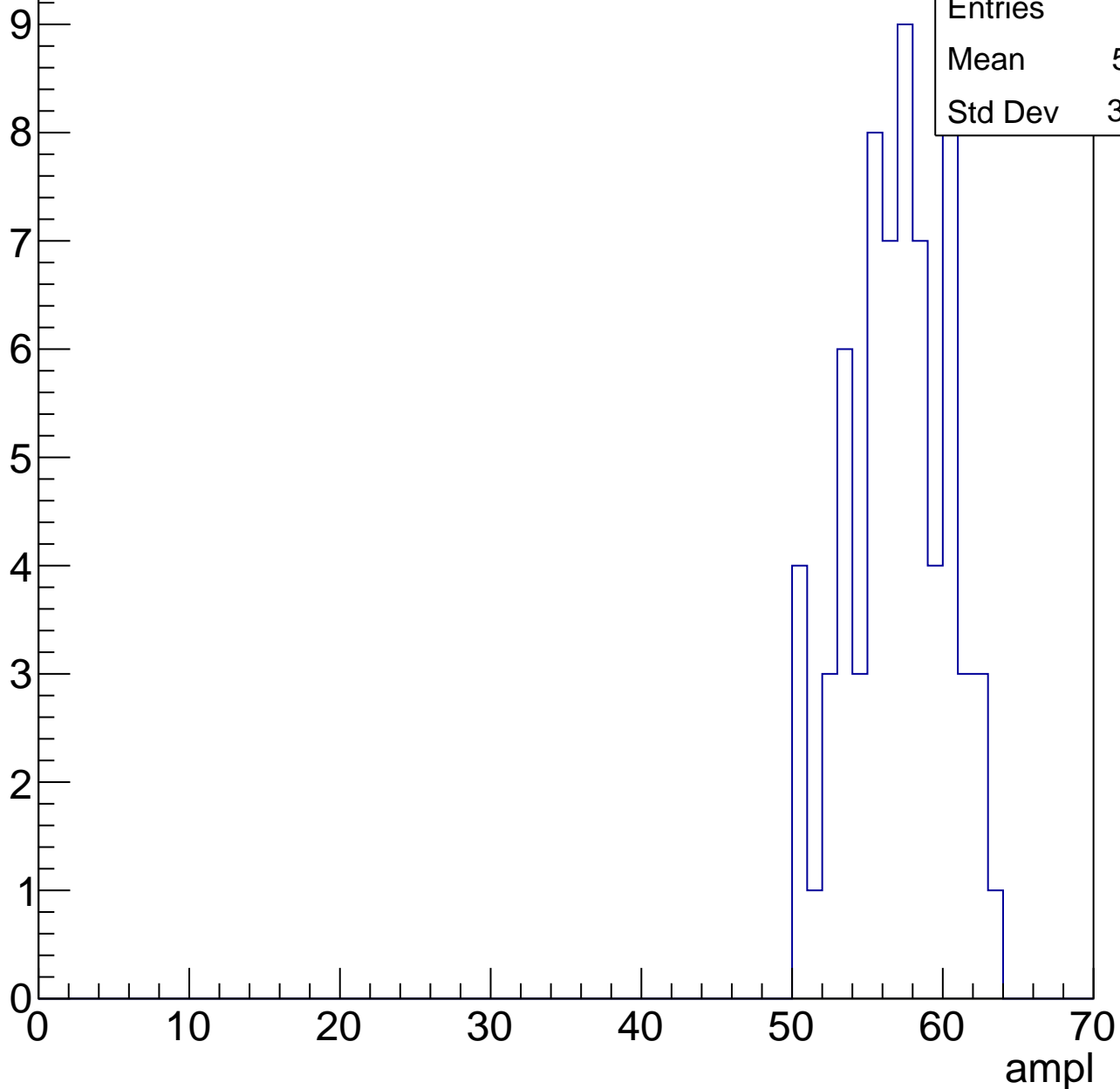
Entries	55
Mean	49.4
Std Dev	2.995



B1L102S, U10-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



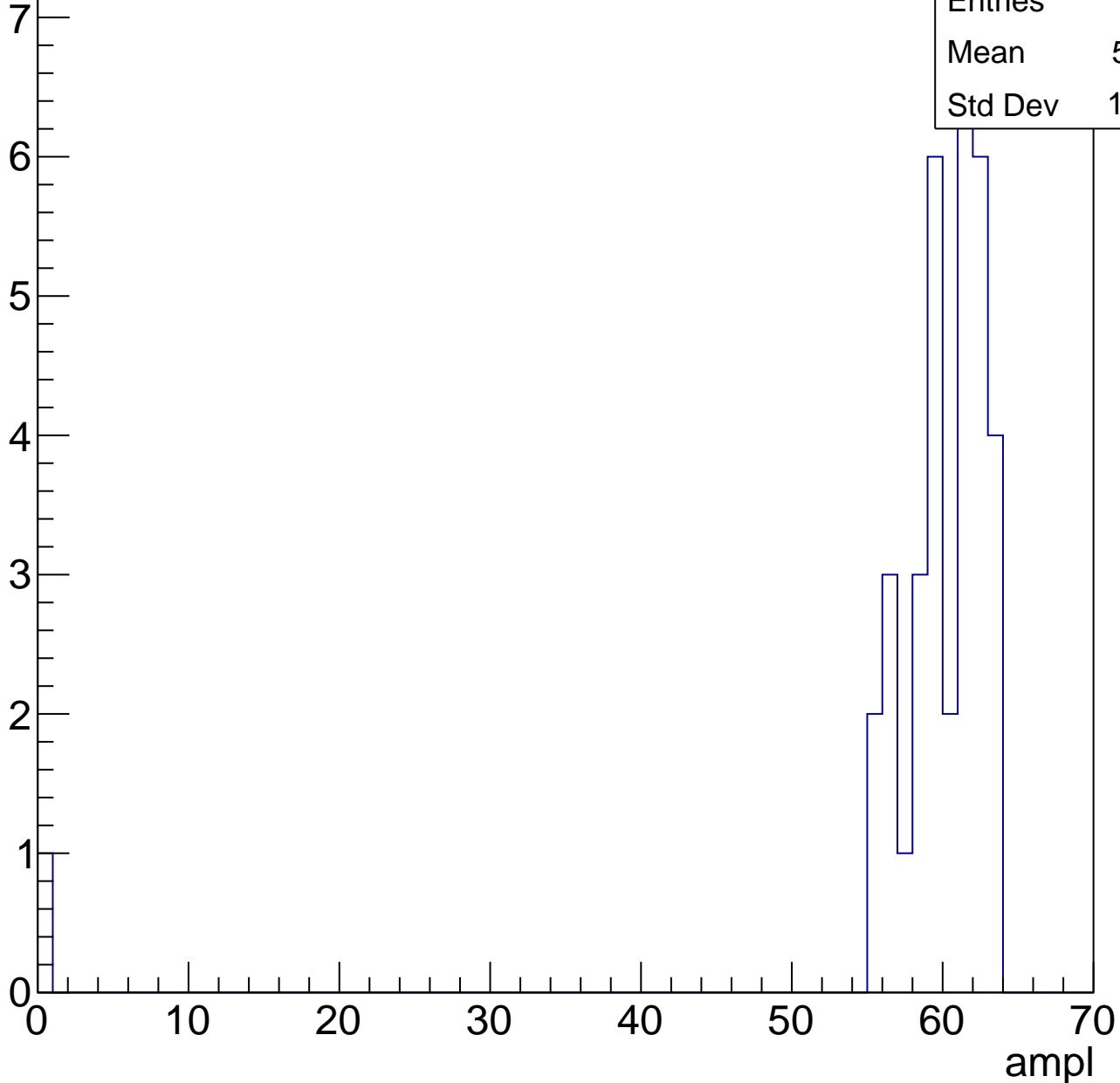
Entries	67
Mean	56.51
Std Dev	3.248

B1L102S, U10-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.11
Std Dev	10.24

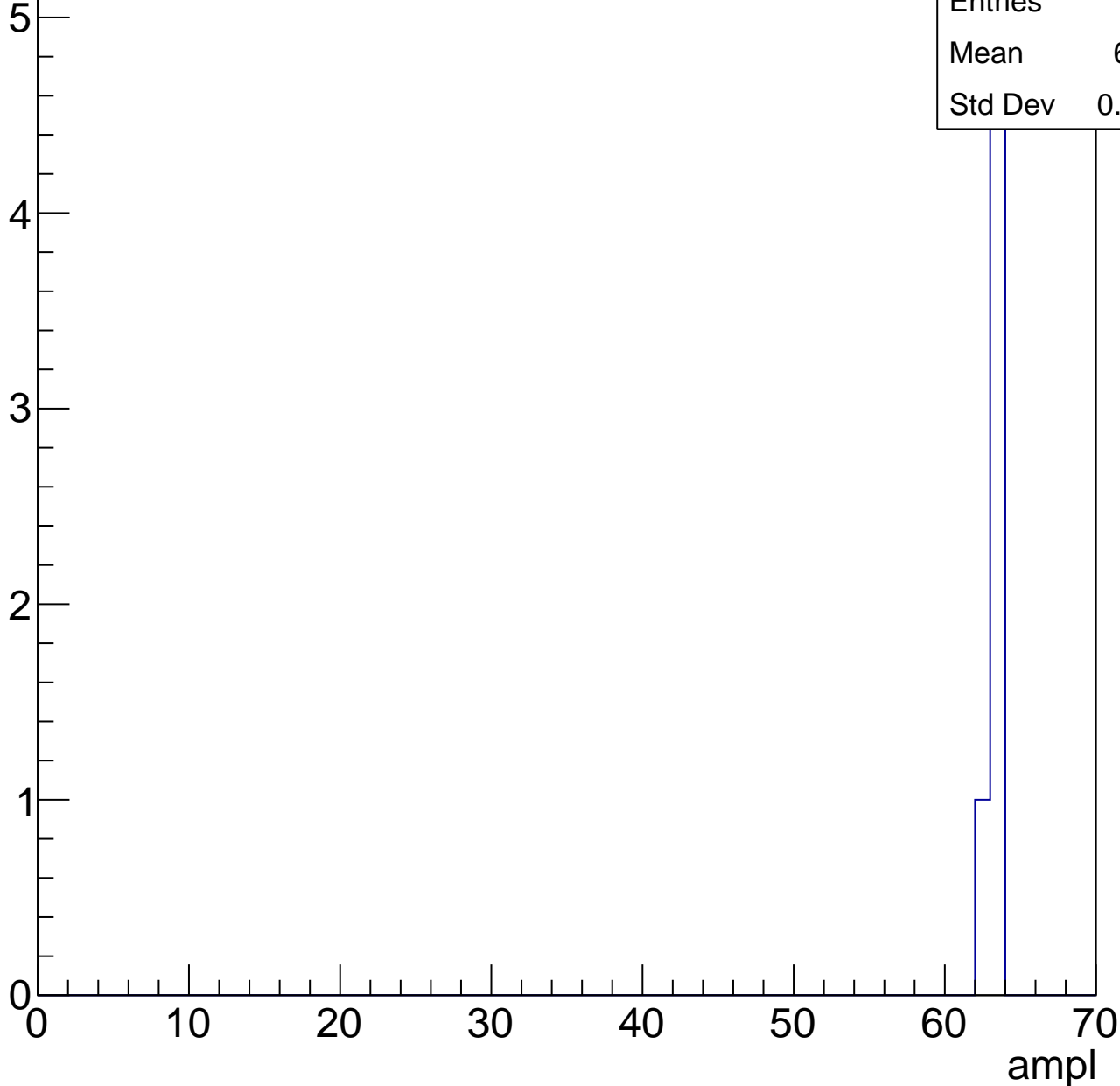


B1L102S, U10-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L102S, U10-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch120, adc0

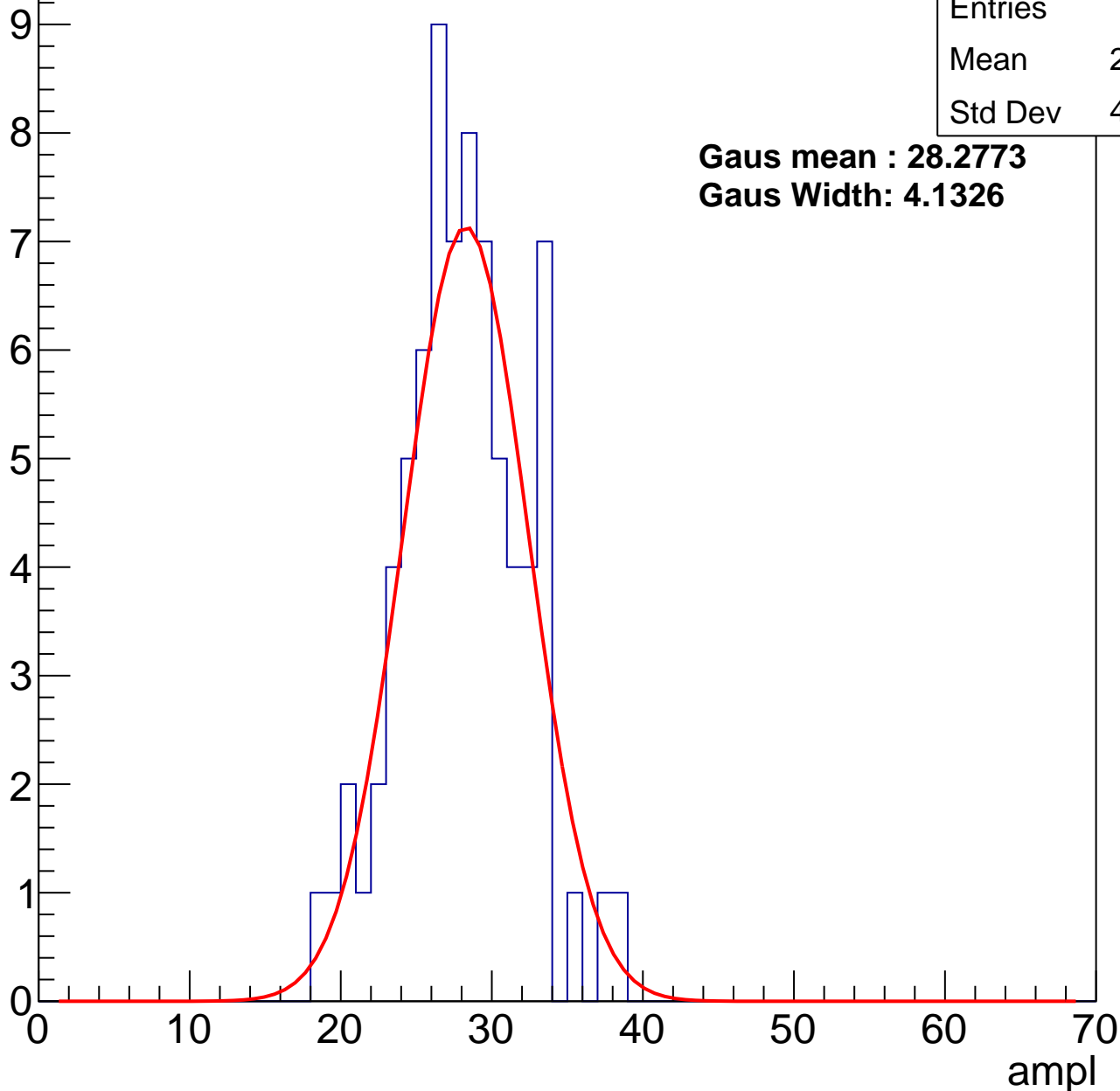
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.59
Std Dev	4.017

Gaus mean : 28.2773

Gaus Width: 4.1326



B1L102S, U10-ch120, adc1

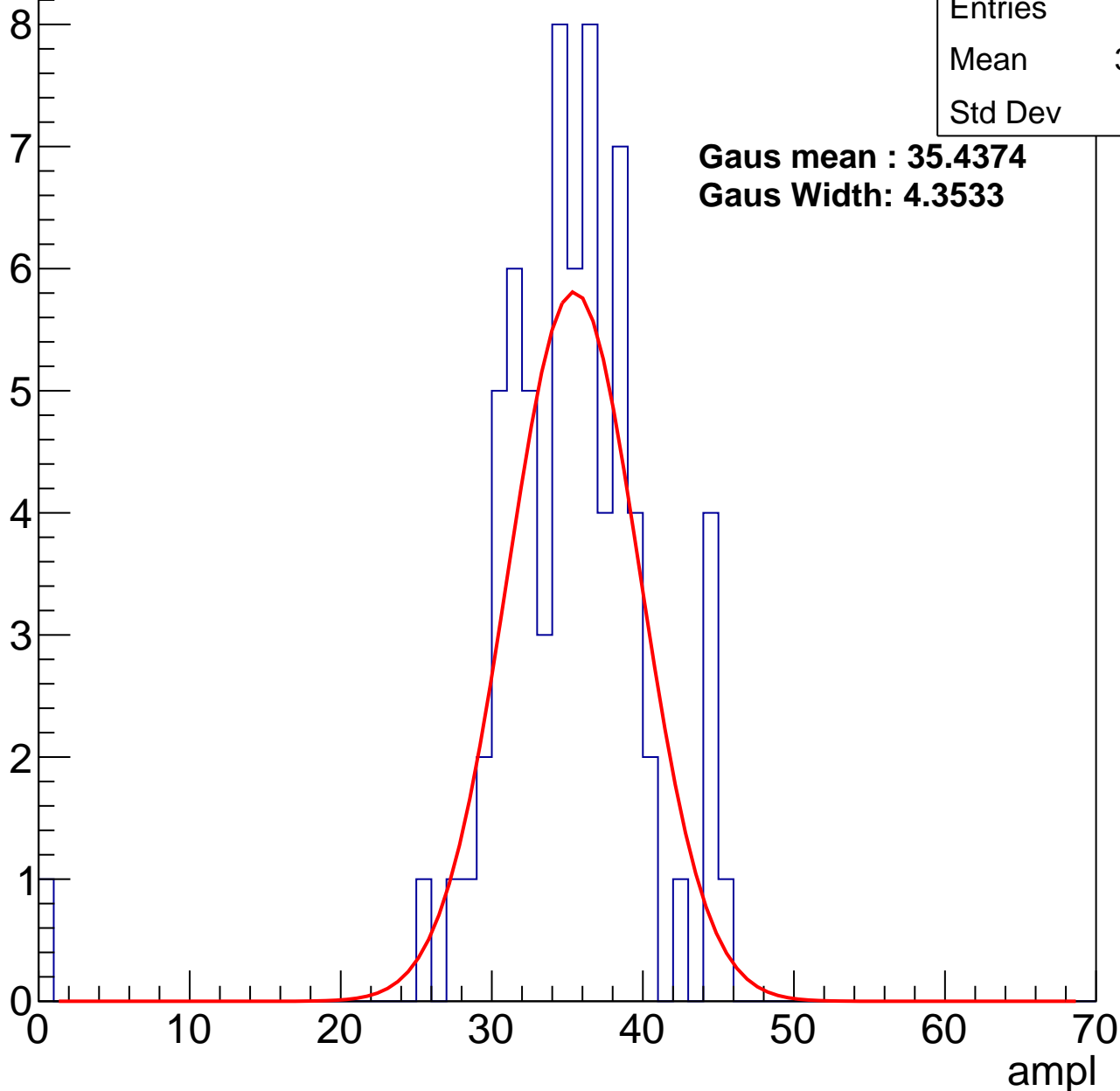
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.51
Std Dev	5.92

Gaus mean : 35.4374

Gaus Width: 4.3533



B1L102S, U10-ch120, adc2

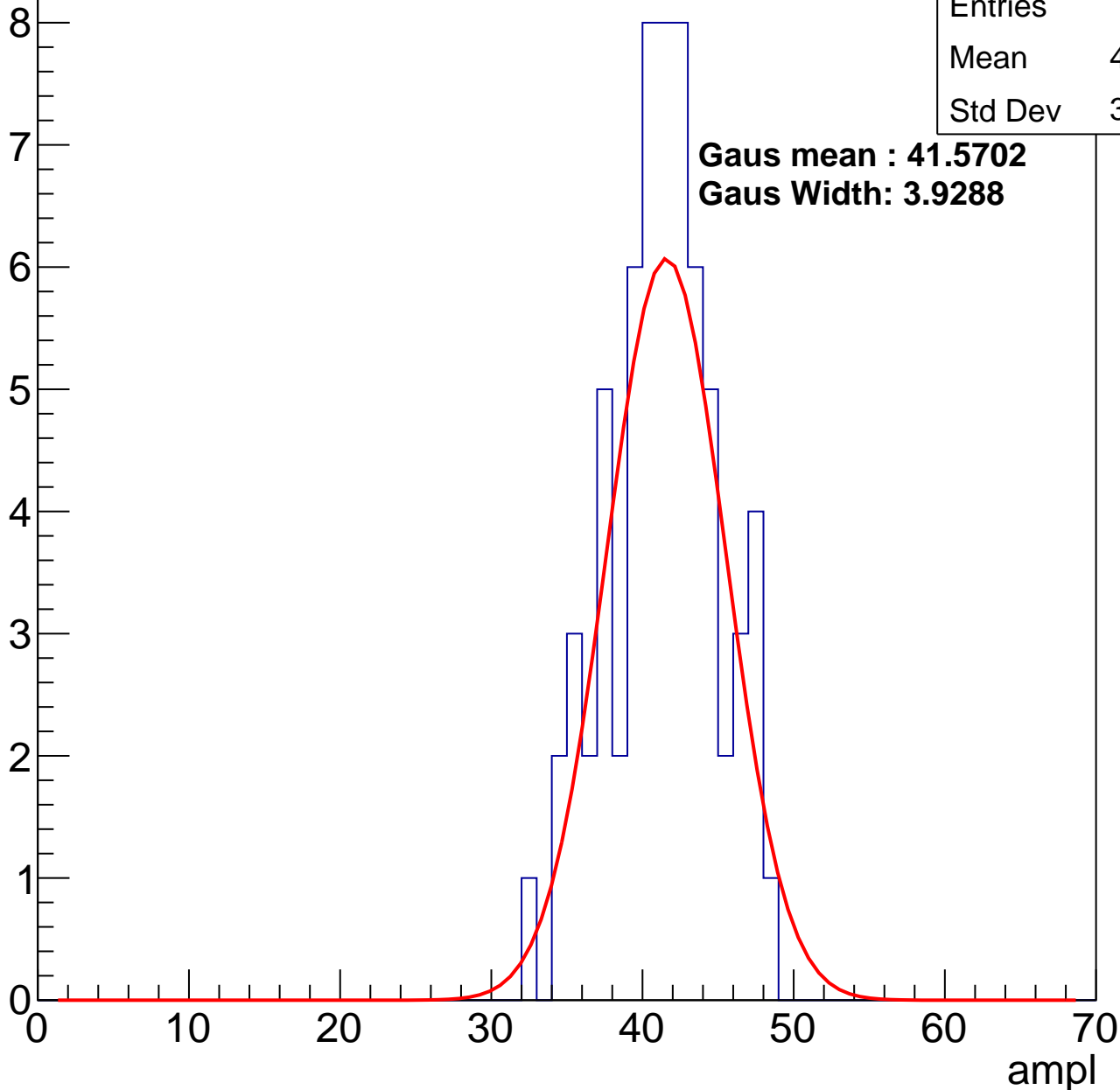
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.88
Std Dev	3.574

Gaus mean : 41.5702

Gaus Width: 3.9288

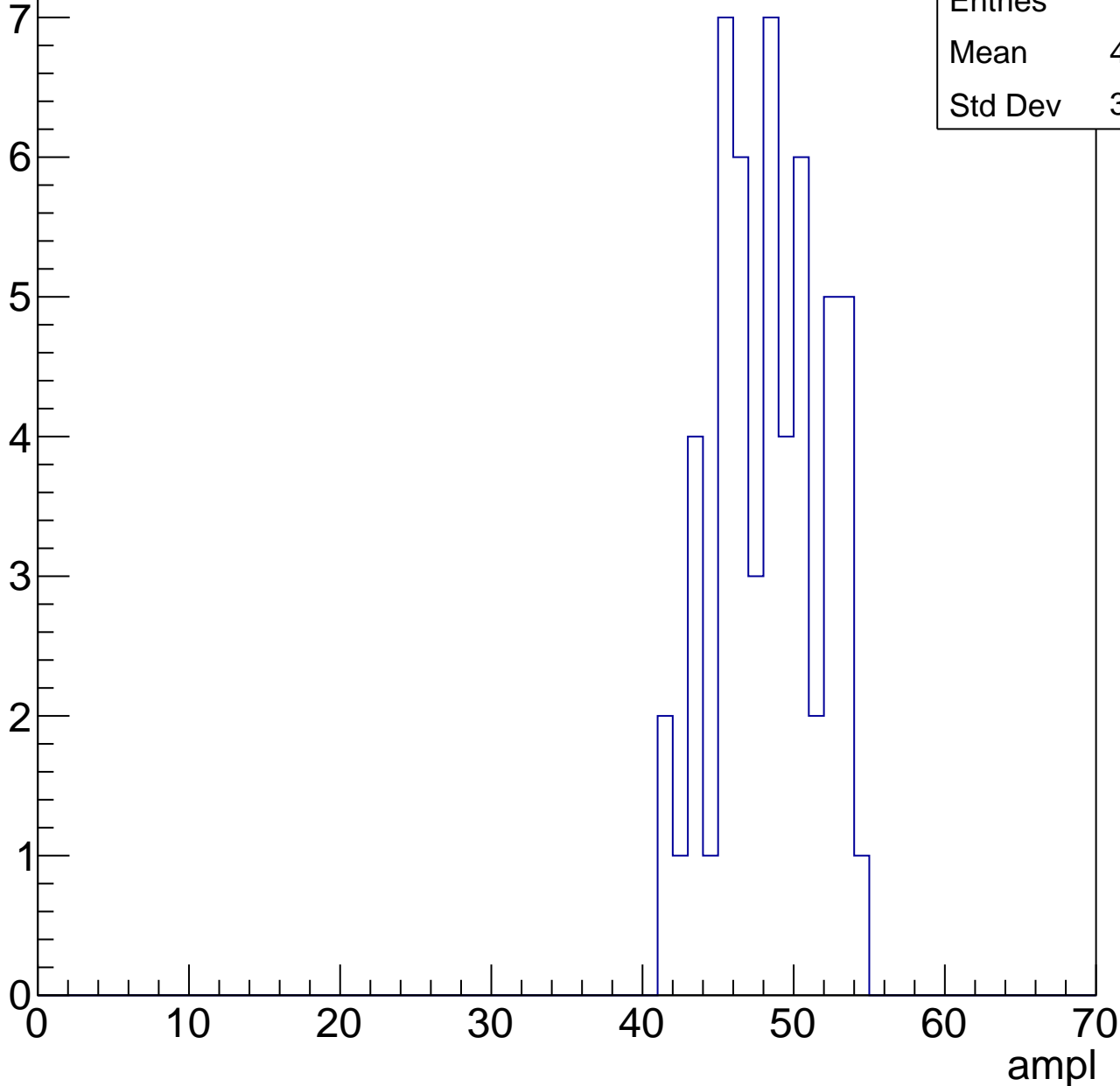


B1L102S, U10-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	47.87
Std Dev	3.405

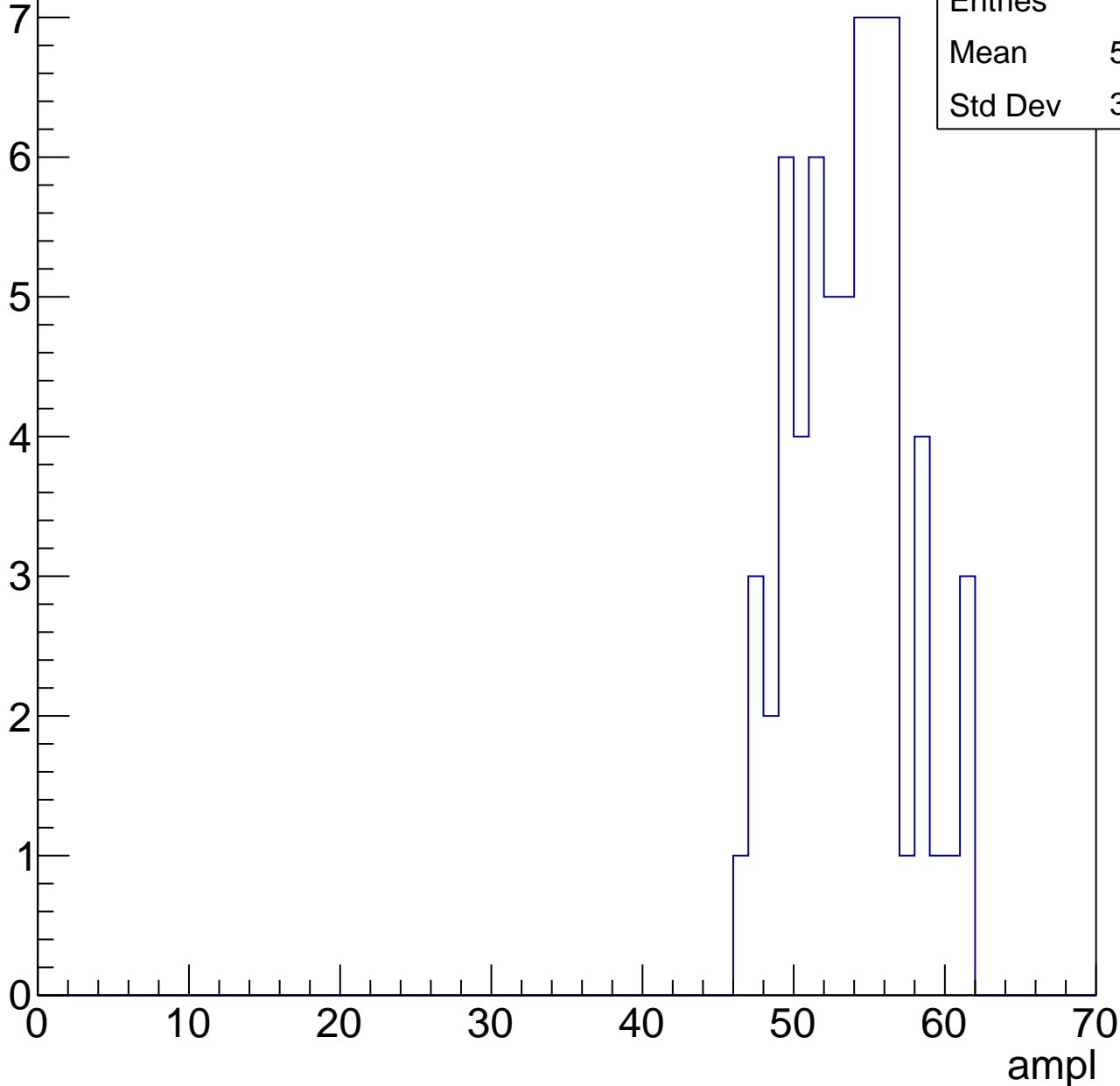


B1L102S, U10-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.24
Std Dev	3.685

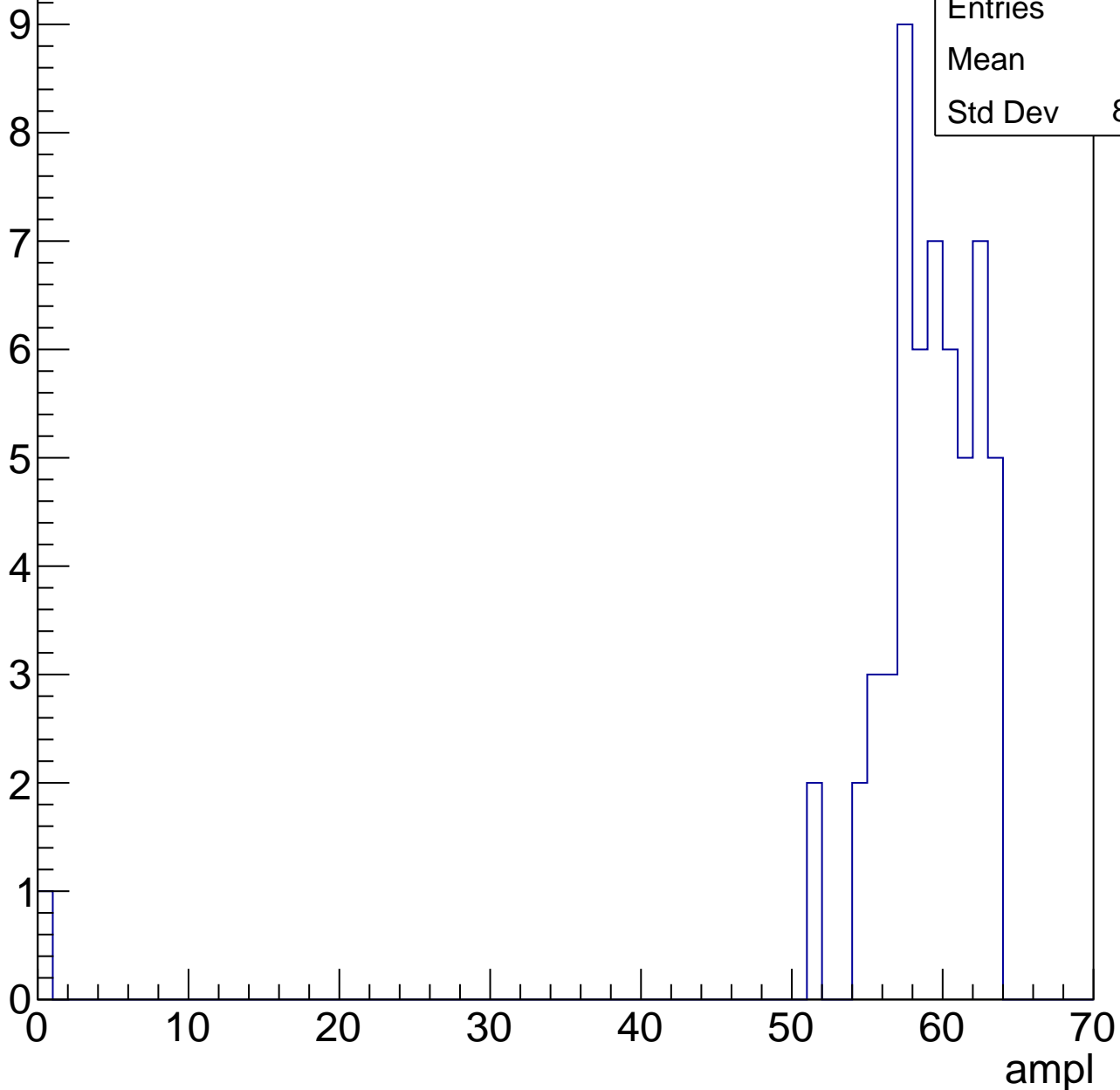


B1L102S, U10-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	57.7
Std Dev	8.291

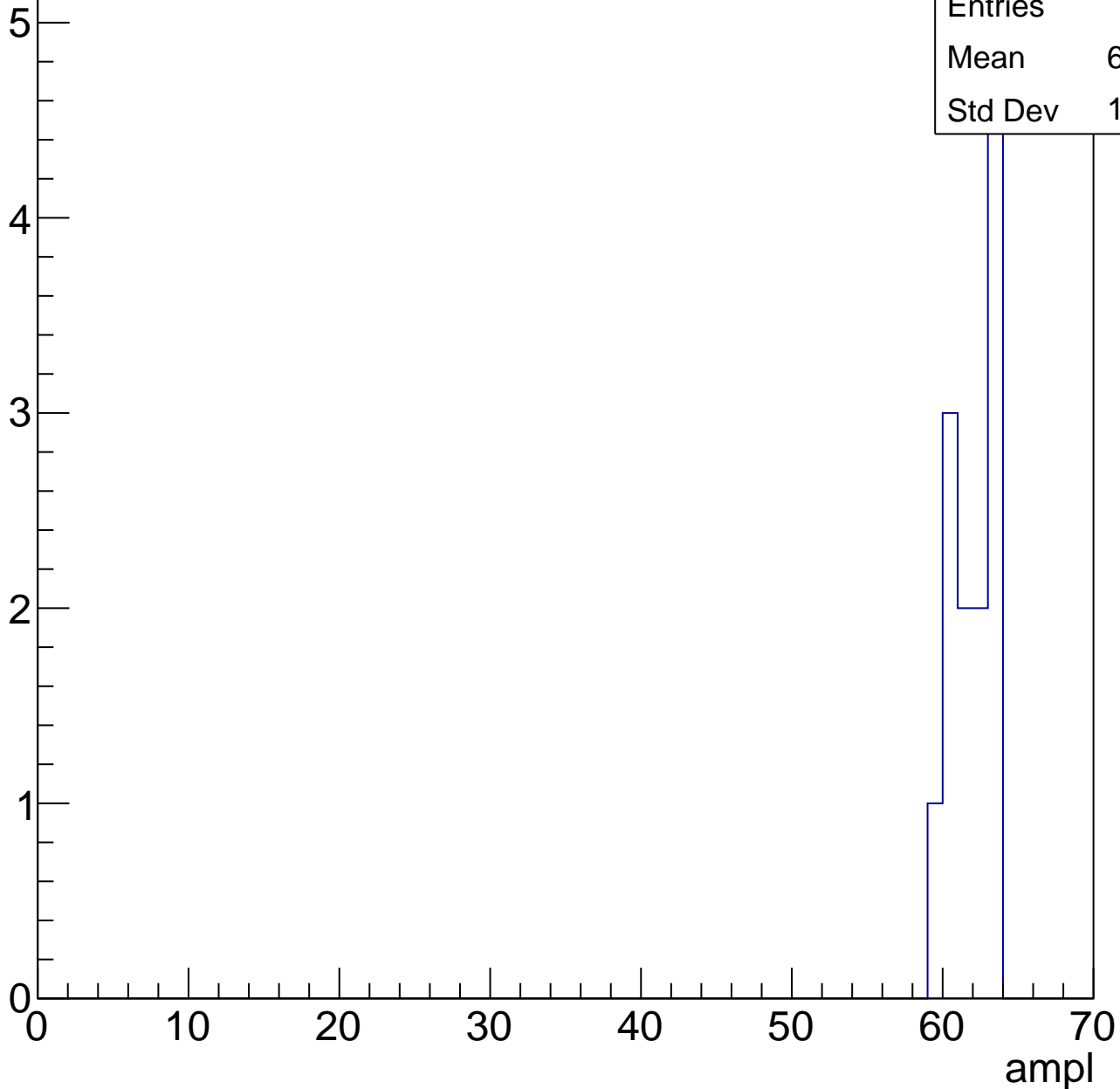


B1L102S, U10-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

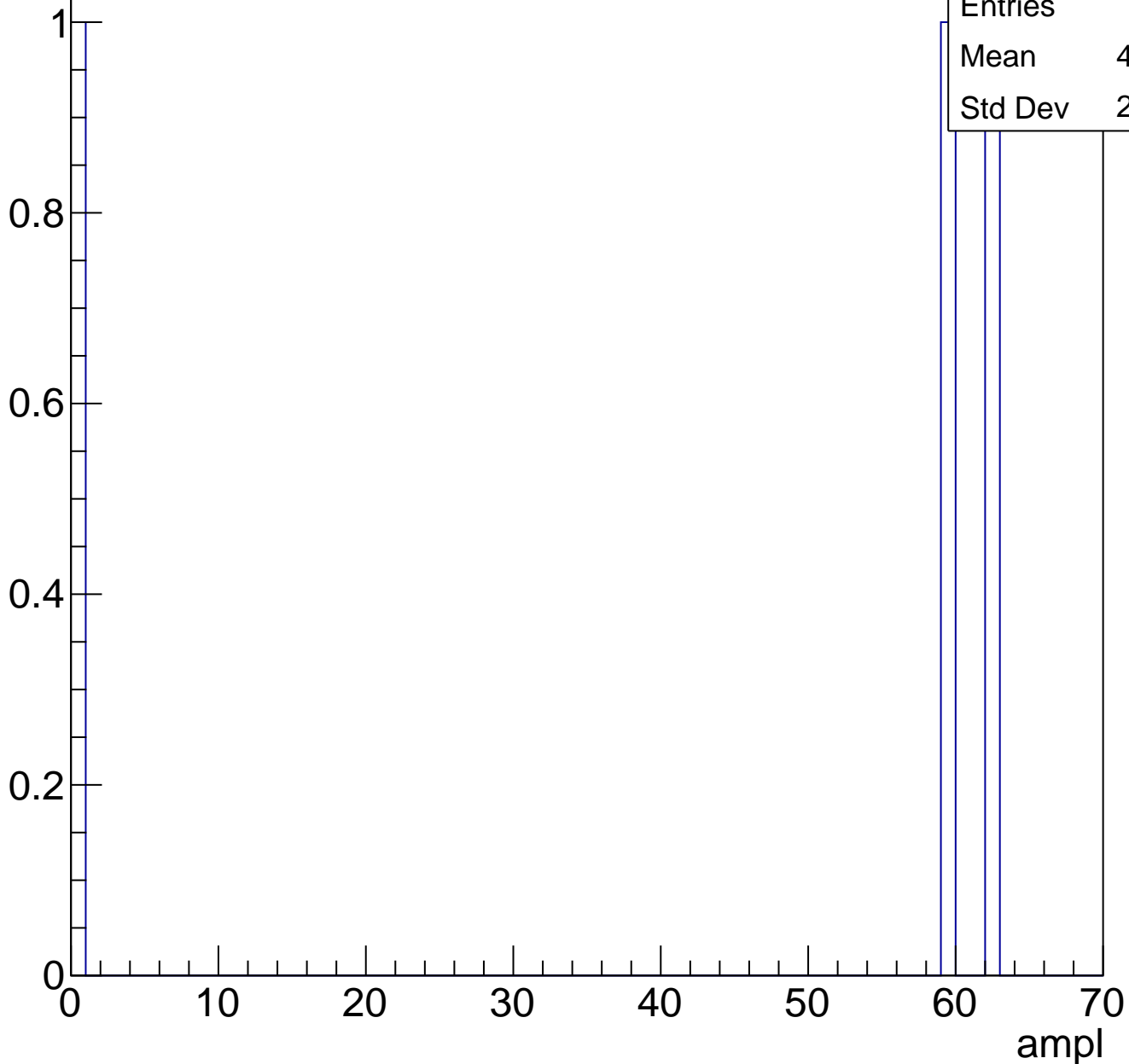
Entries	13
Mean	61.54
Std Dev	1.393



B1L102S, U10-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch121, adc0

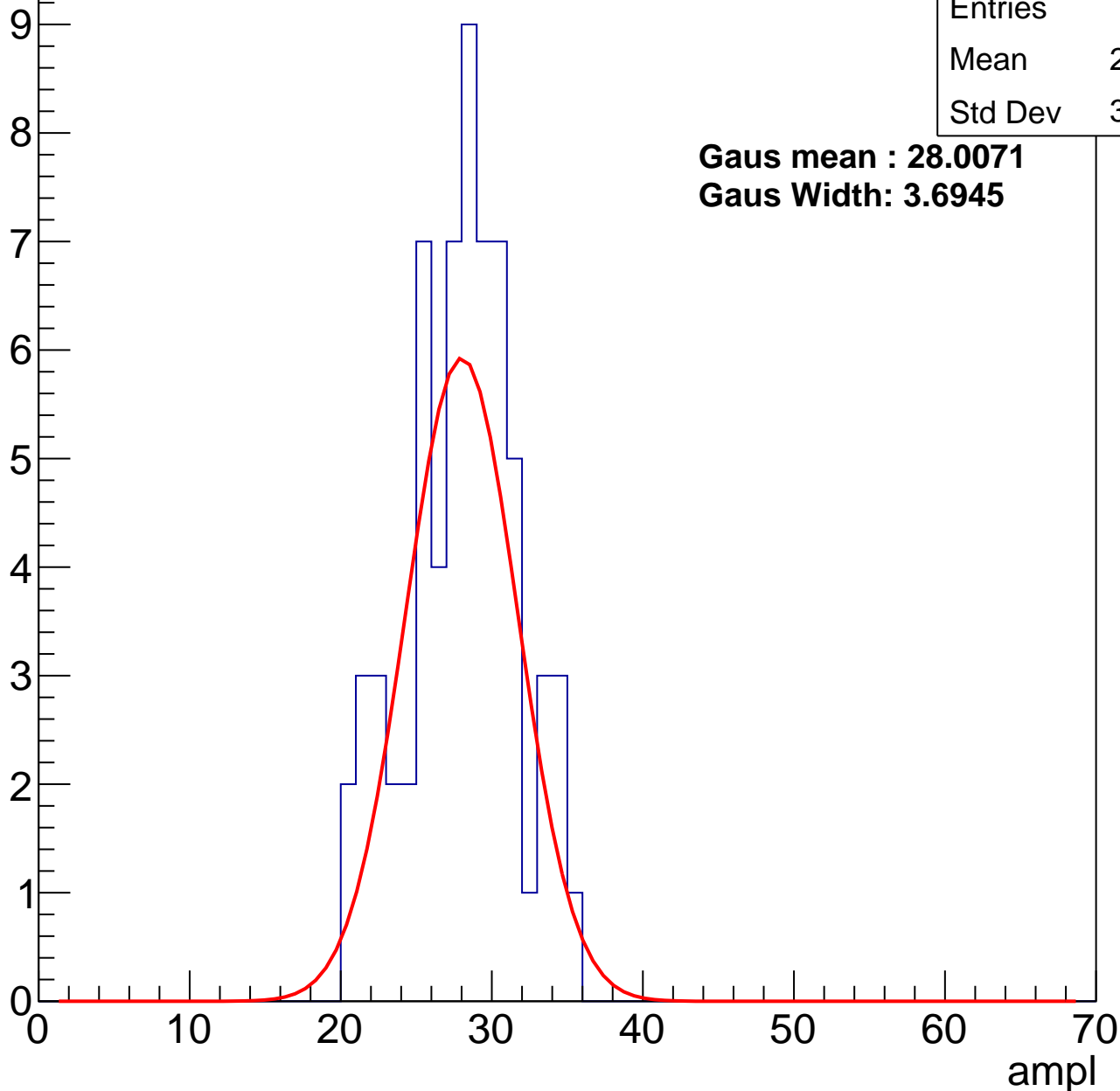
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.56
Std Dev	3.623

Gaus mean : 28.0071

Gaus Width: 3.6945



B1L102S, U10-ch121, adc1

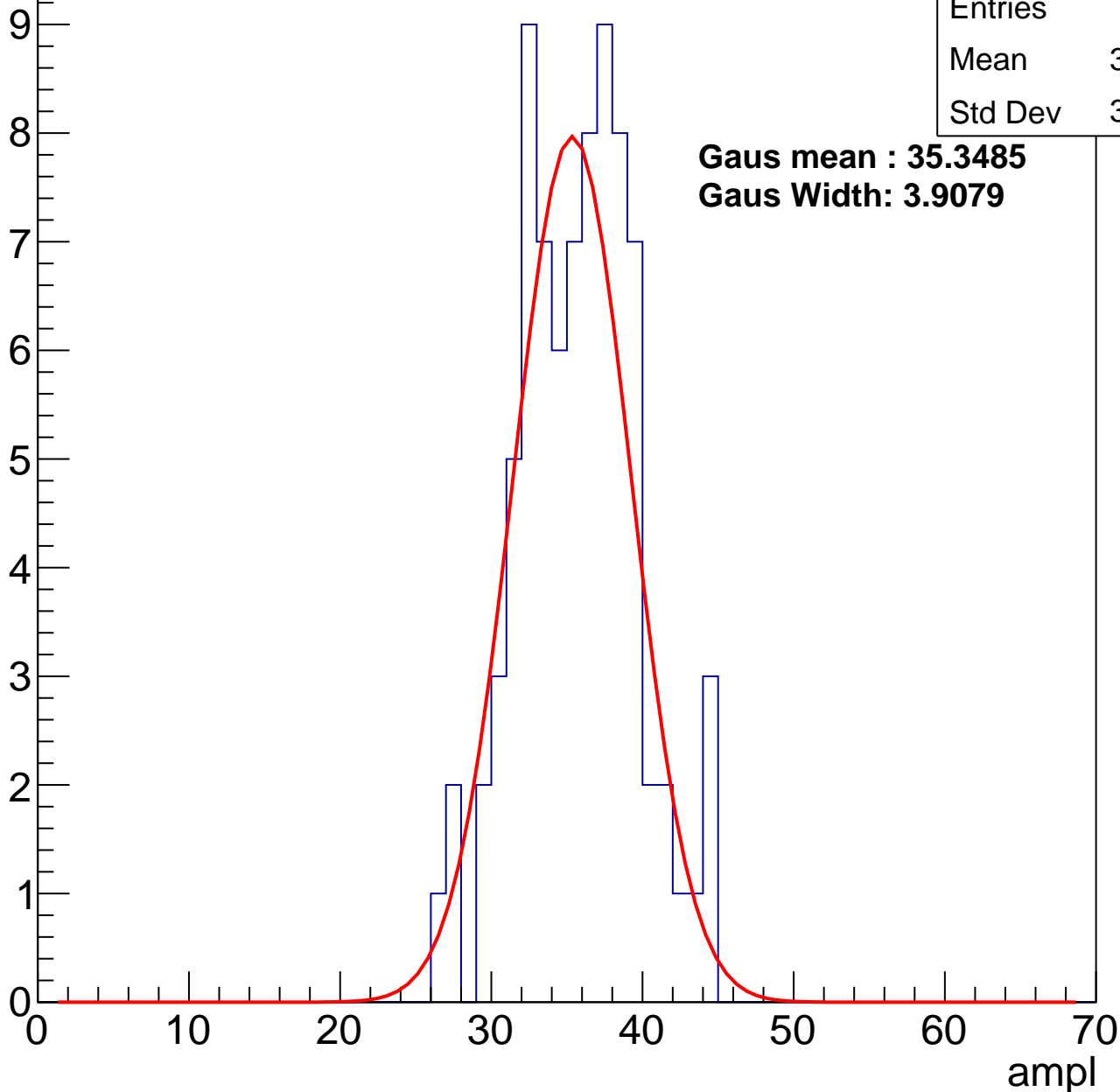
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	35.28
Std Dev	3.882

Gaus mean : 35.3485

Gaus Width: 3.9079

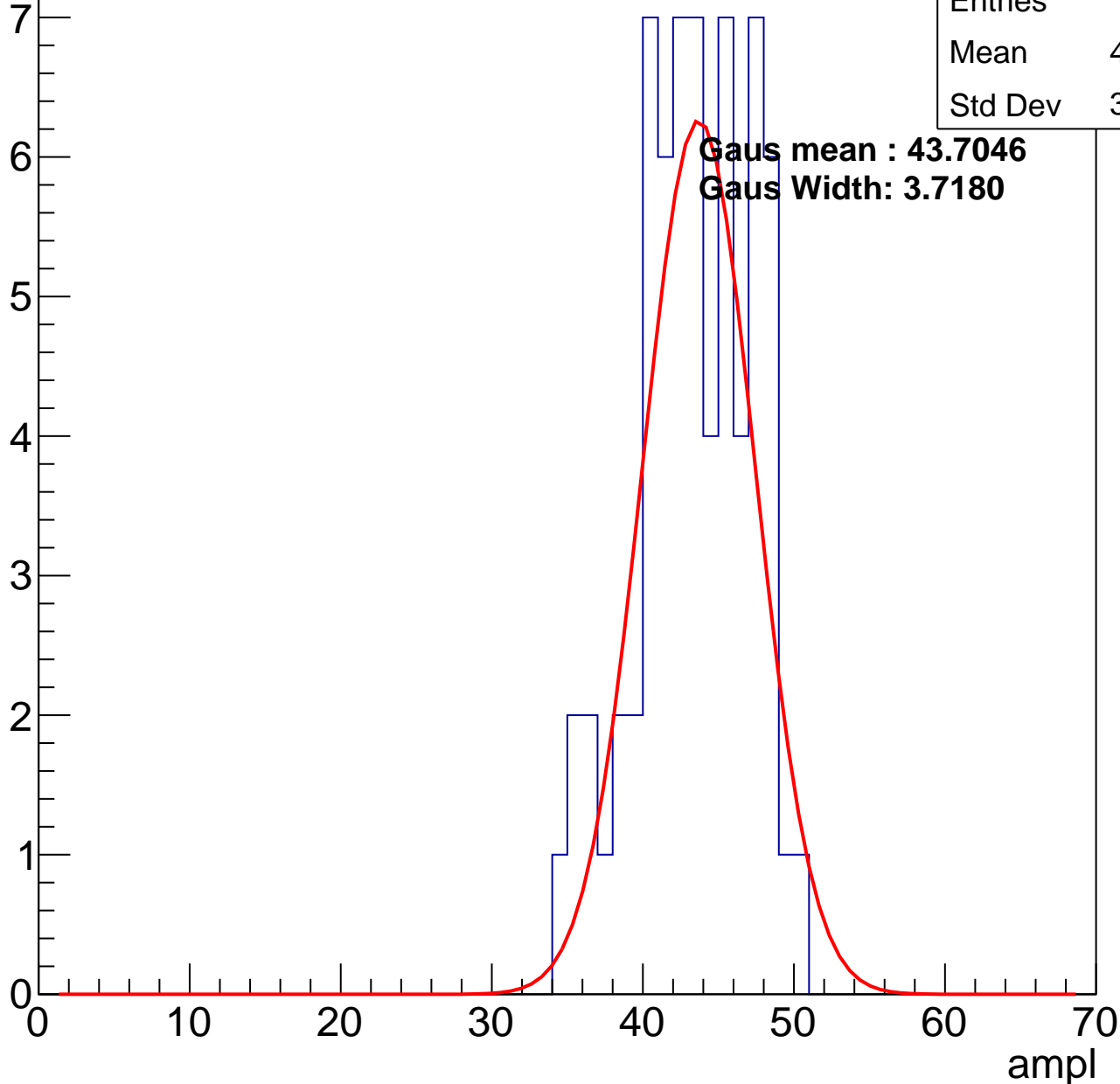


B1L102S, U10-ch121, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.97
Std Dev	3.734

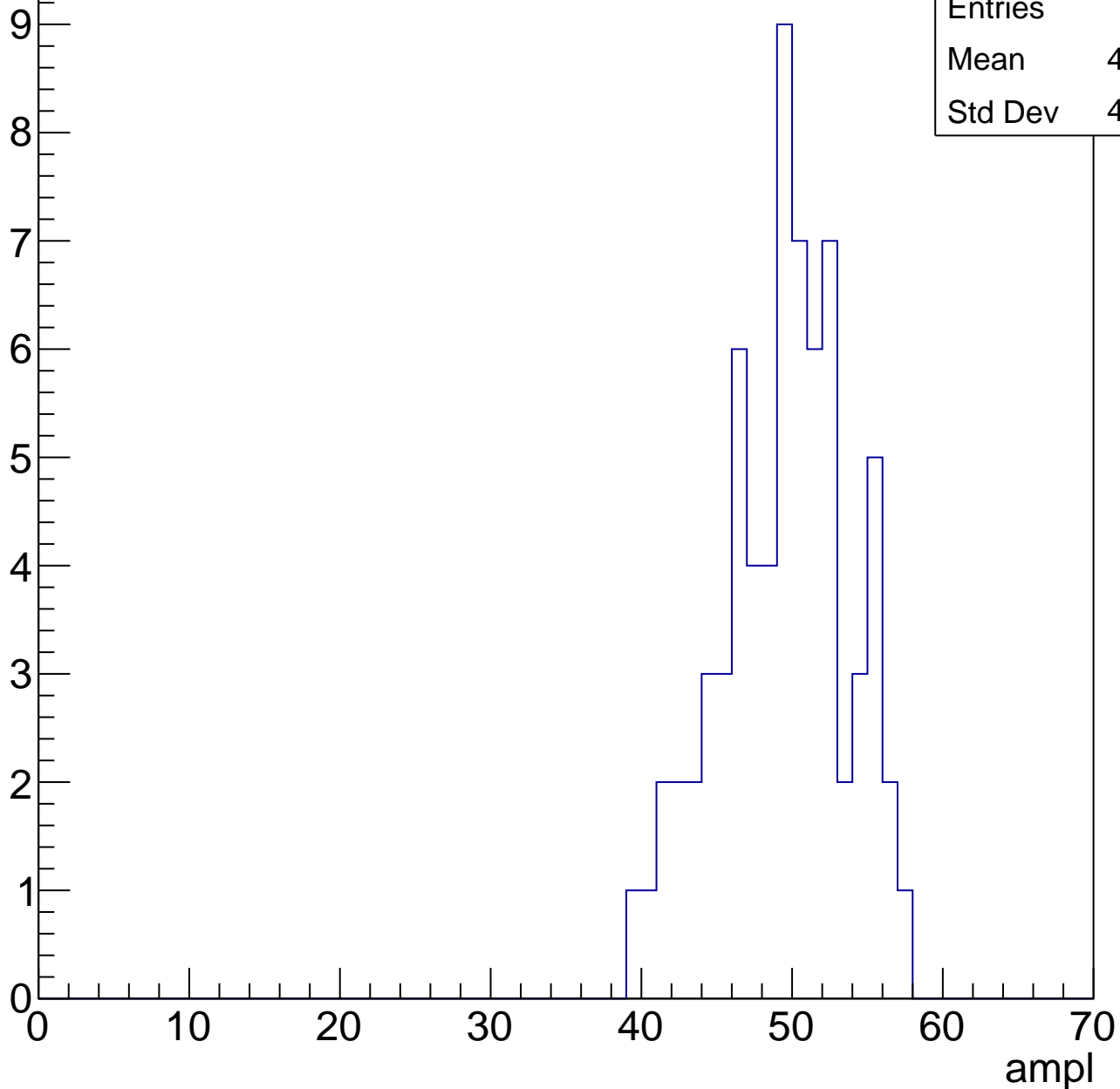


B1L102S, U10-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.96
Std Dev	4.173

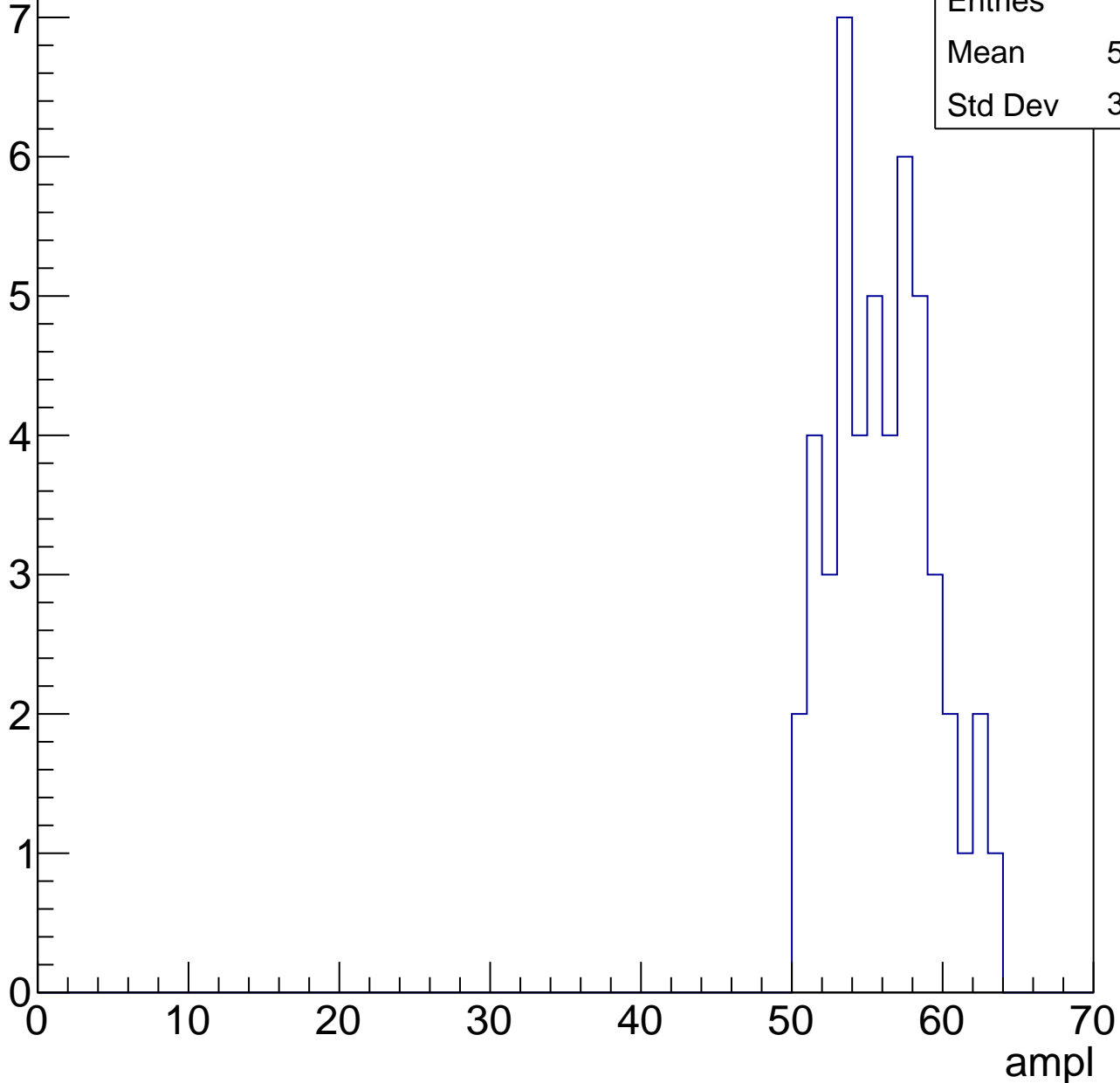


B1L102S, U10-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.57
Std Dev	3.276

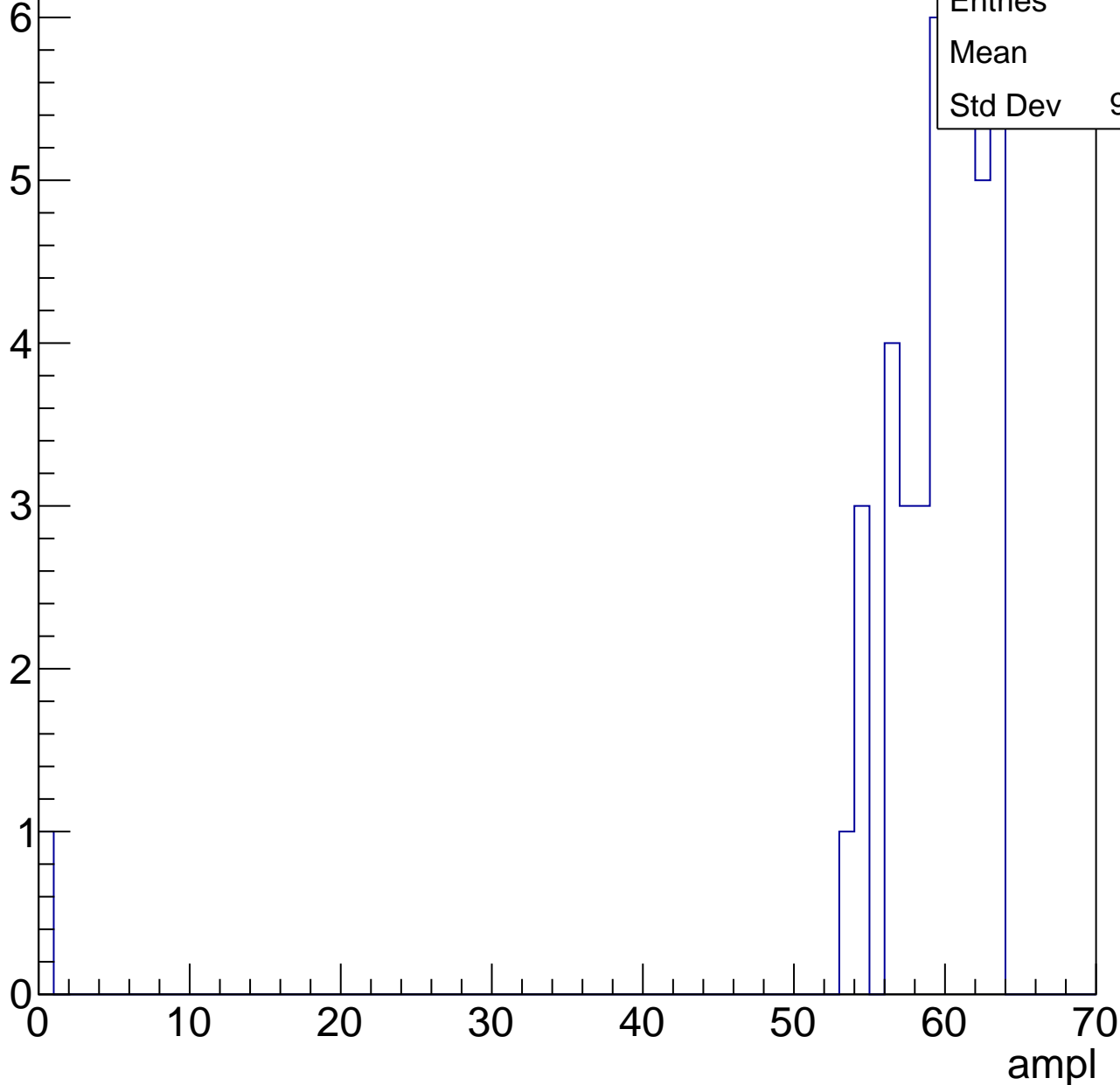


B1L102S, U10-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58
Std Dev	9.256



B1L102S, U10-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

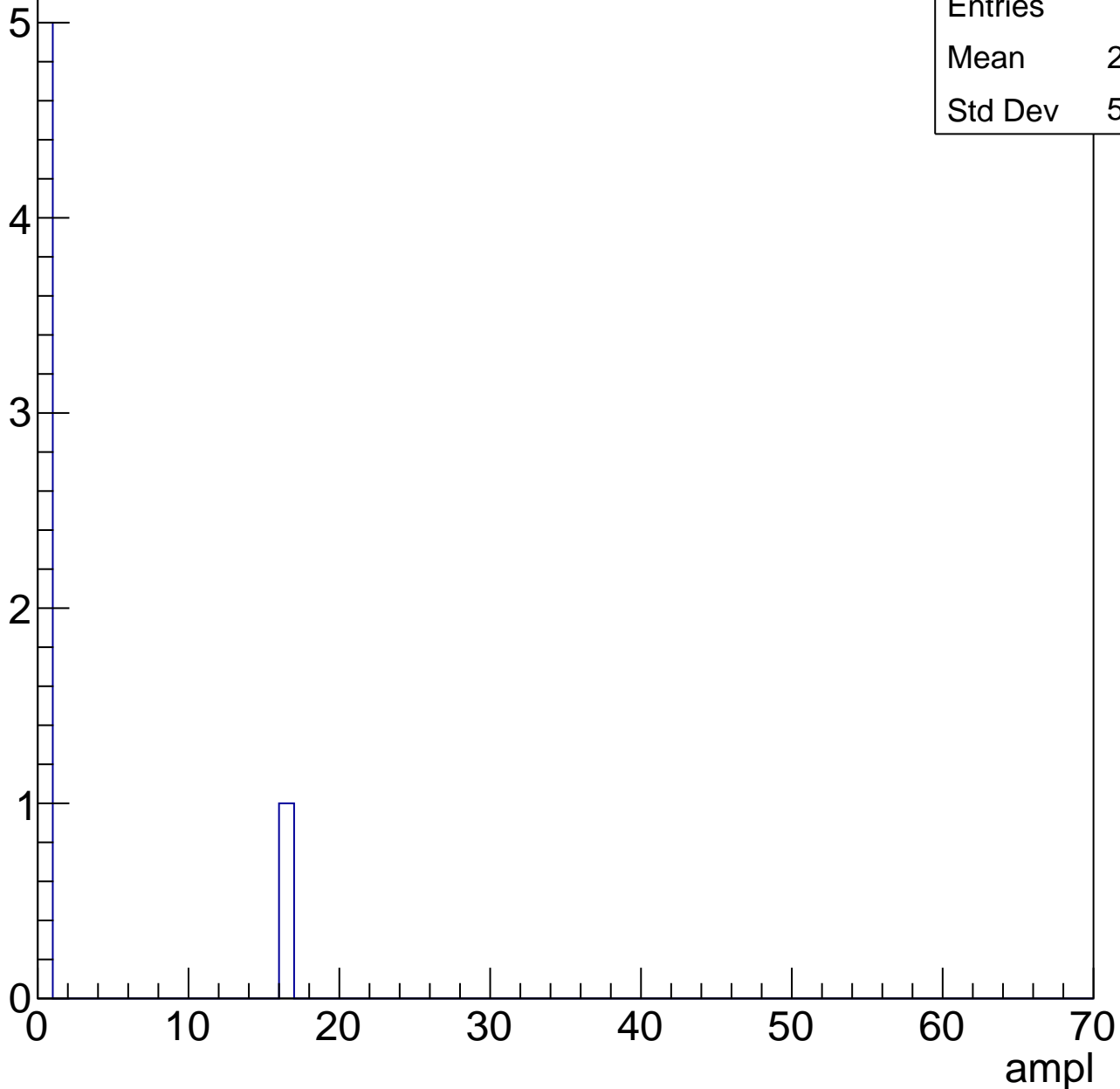
Entries	14
Mean	60.93
Std Dev	1.668

B1L102S, U10-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	6
Mean	2.667
Std Dev	5.963



B1L102S, U10-ch122, adc0

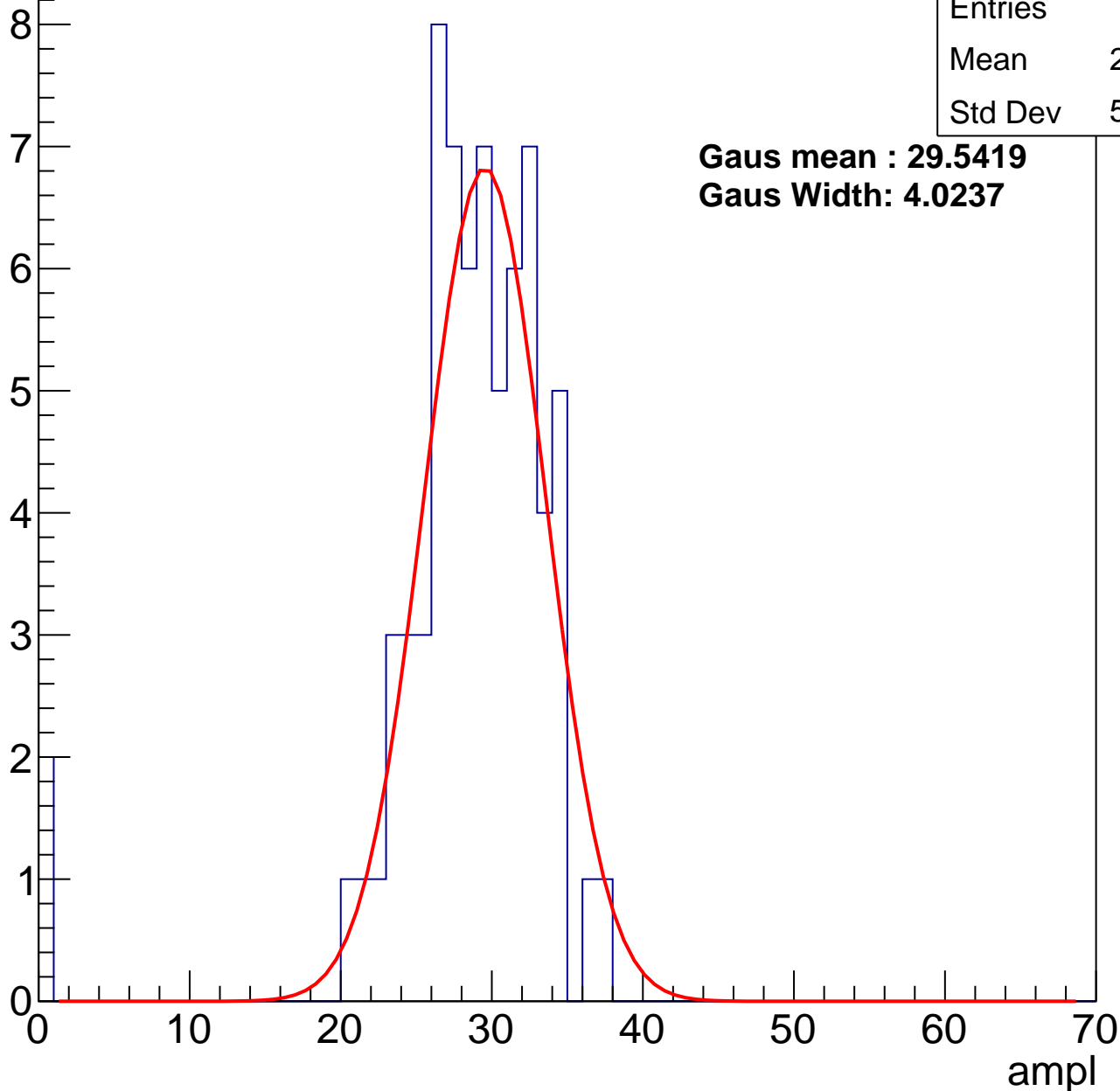
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.92
Std Dev	5.962

Gaus mean : 29.5419

Gaus Width: 4.0237



B1L102S, U10-ch122, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	35.68
Std Dev	3.216

Gaus mean : 36.1850

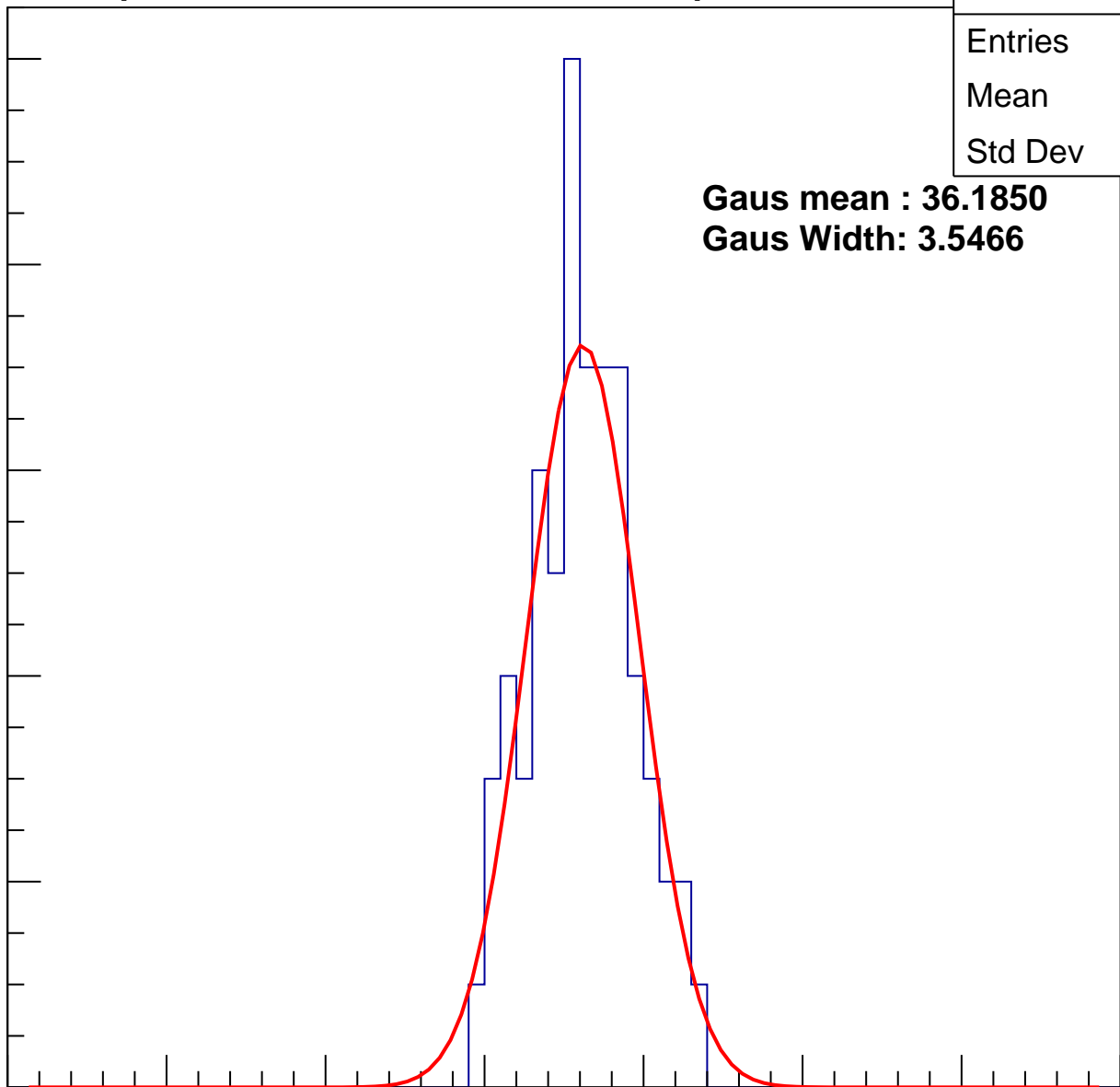
Gaus Width: 3.5466

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch122, adc2

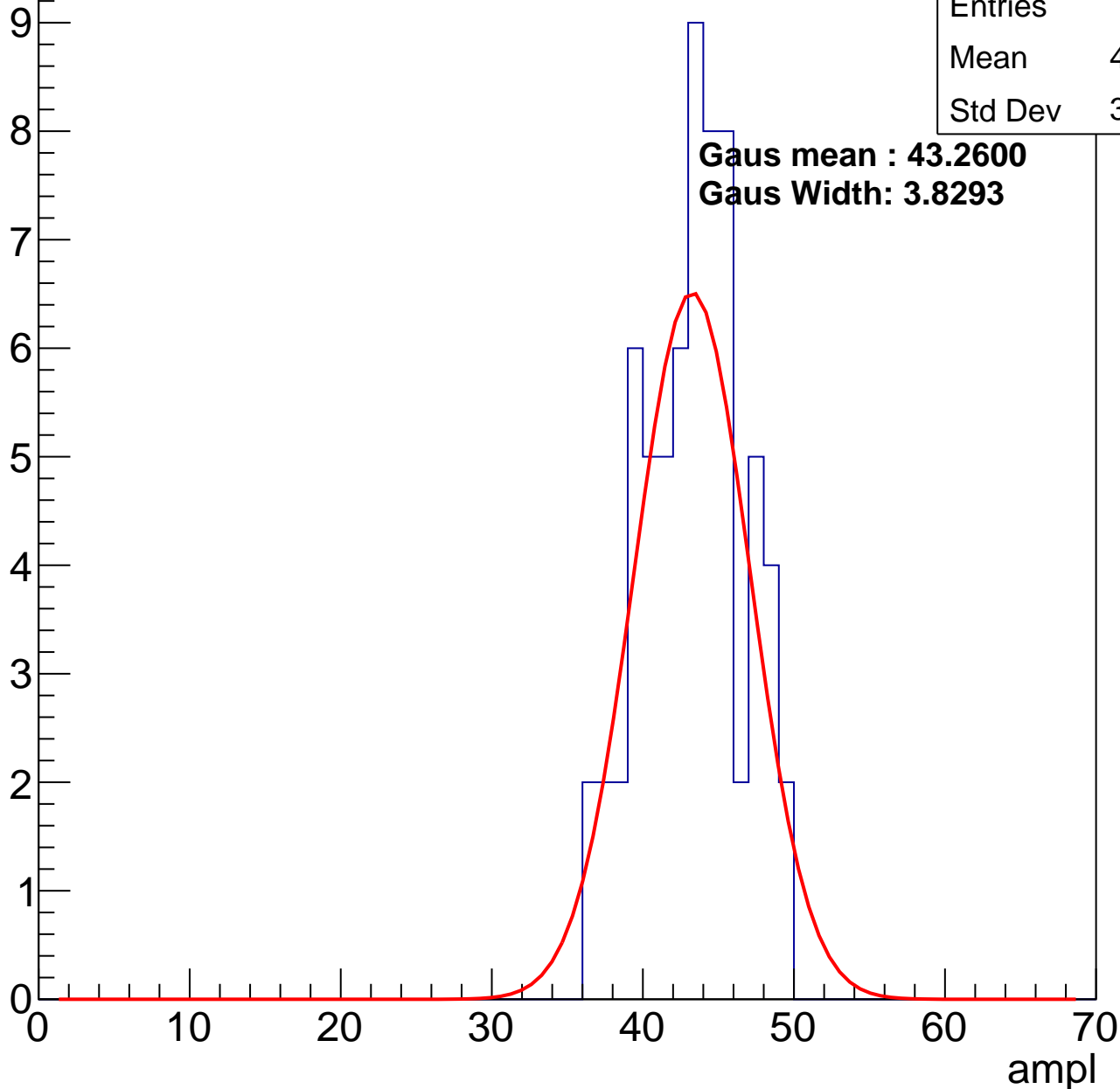
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.86
Std Dev	3.247

Gaus mean : 43.2600

Gaus Width: 3.8293

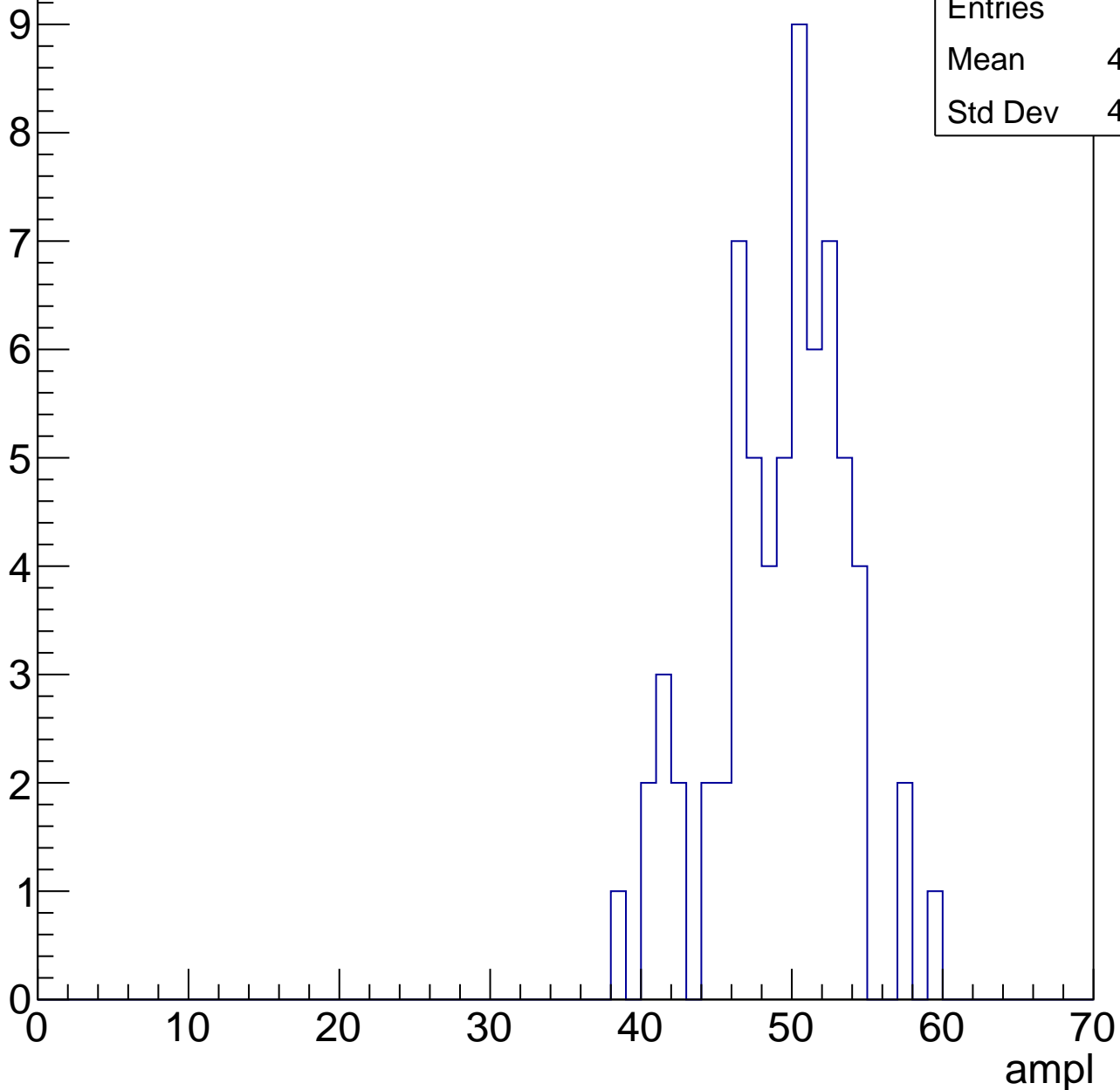


B1L102S, U10-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.82
Std Dev	4.309

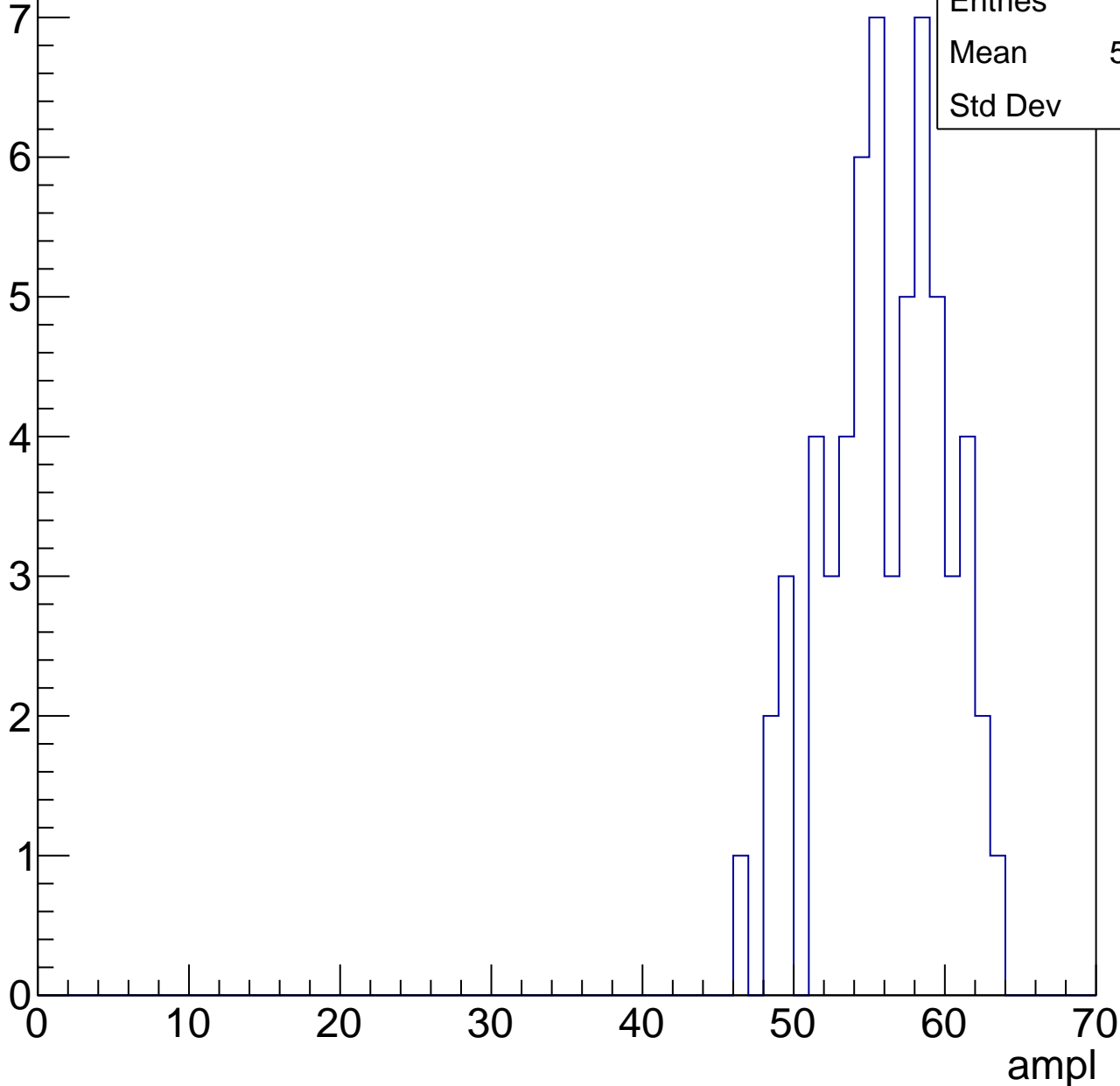


B1L102S, U10-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.58
Std Dev	3.93

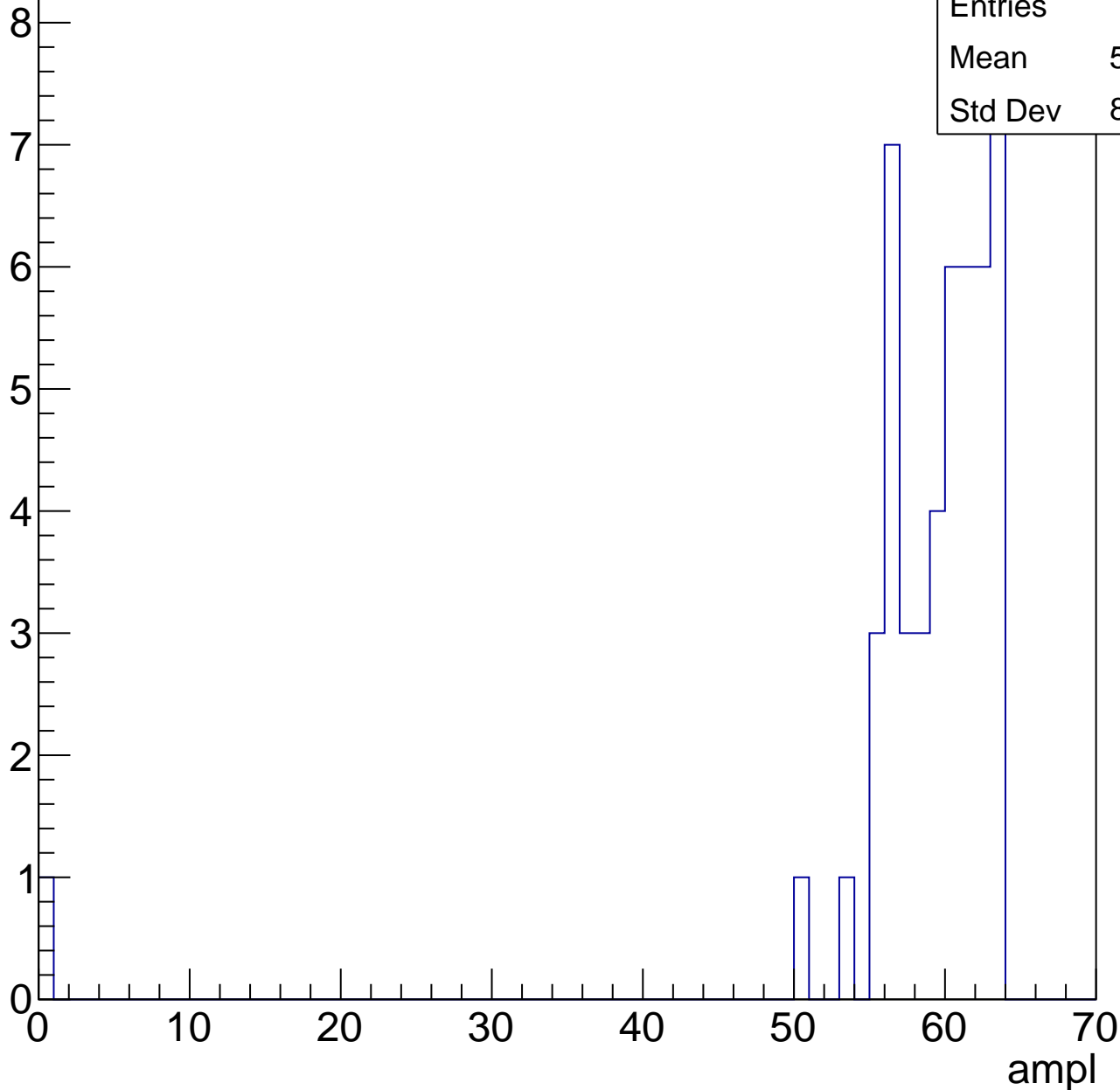


B1L102S, U10-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.02
Std Dev	8.909



B1L102S, U10-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

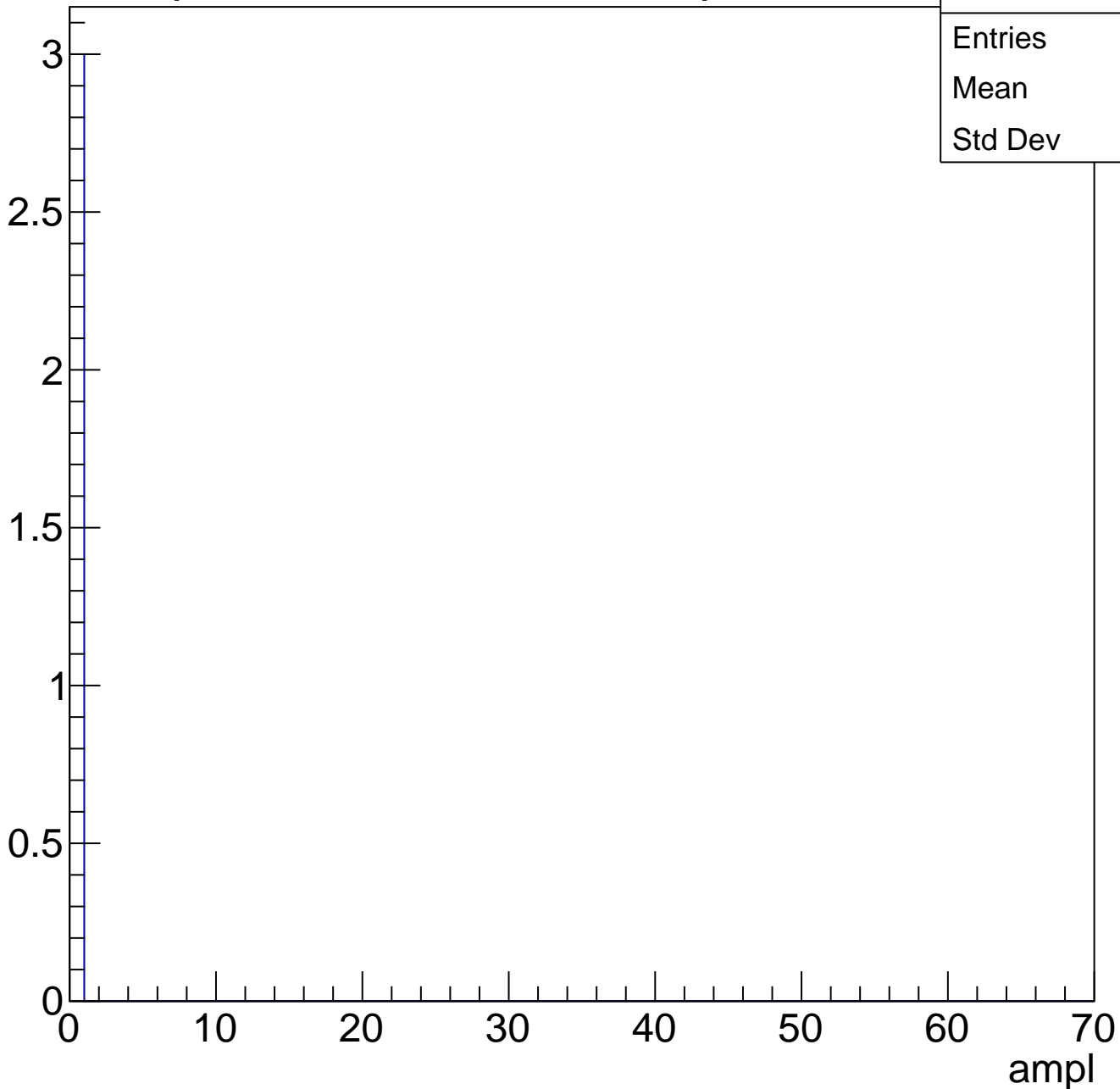
ampl

0 10 20 30 40 50 60 70

B1L102S, U10-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U10-ch123, adc0

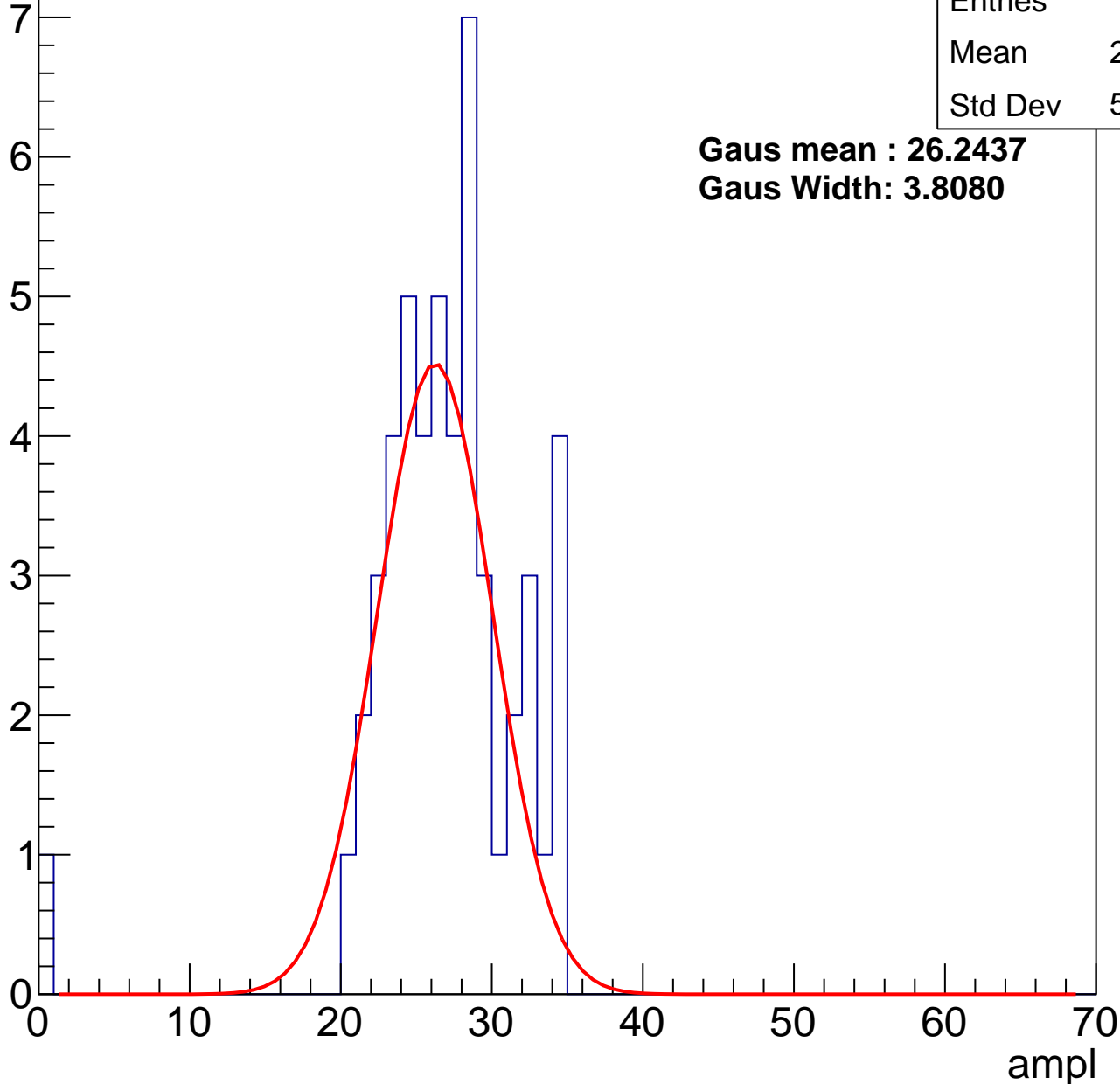
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	26.36
Std Dev	5.294

Gaus mean : 26.2437

Gaus Width: 3.8080



B1L102S, U10-ch123, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	94
Mean	33.8
Std Dev	4.194

Gaus mean : 34.3772

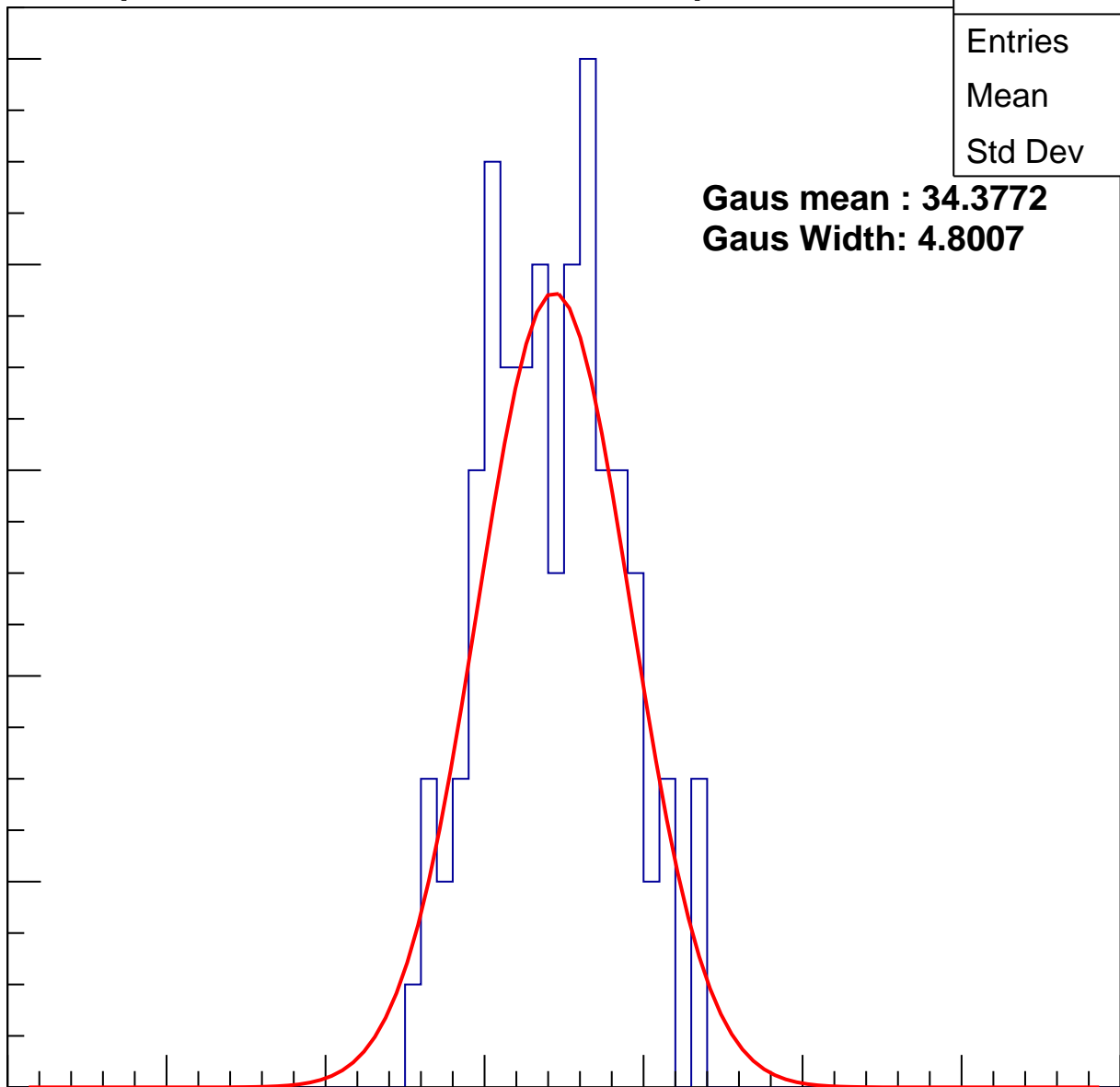
Gaus Width: 4.8007

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U10-ch123, adc2

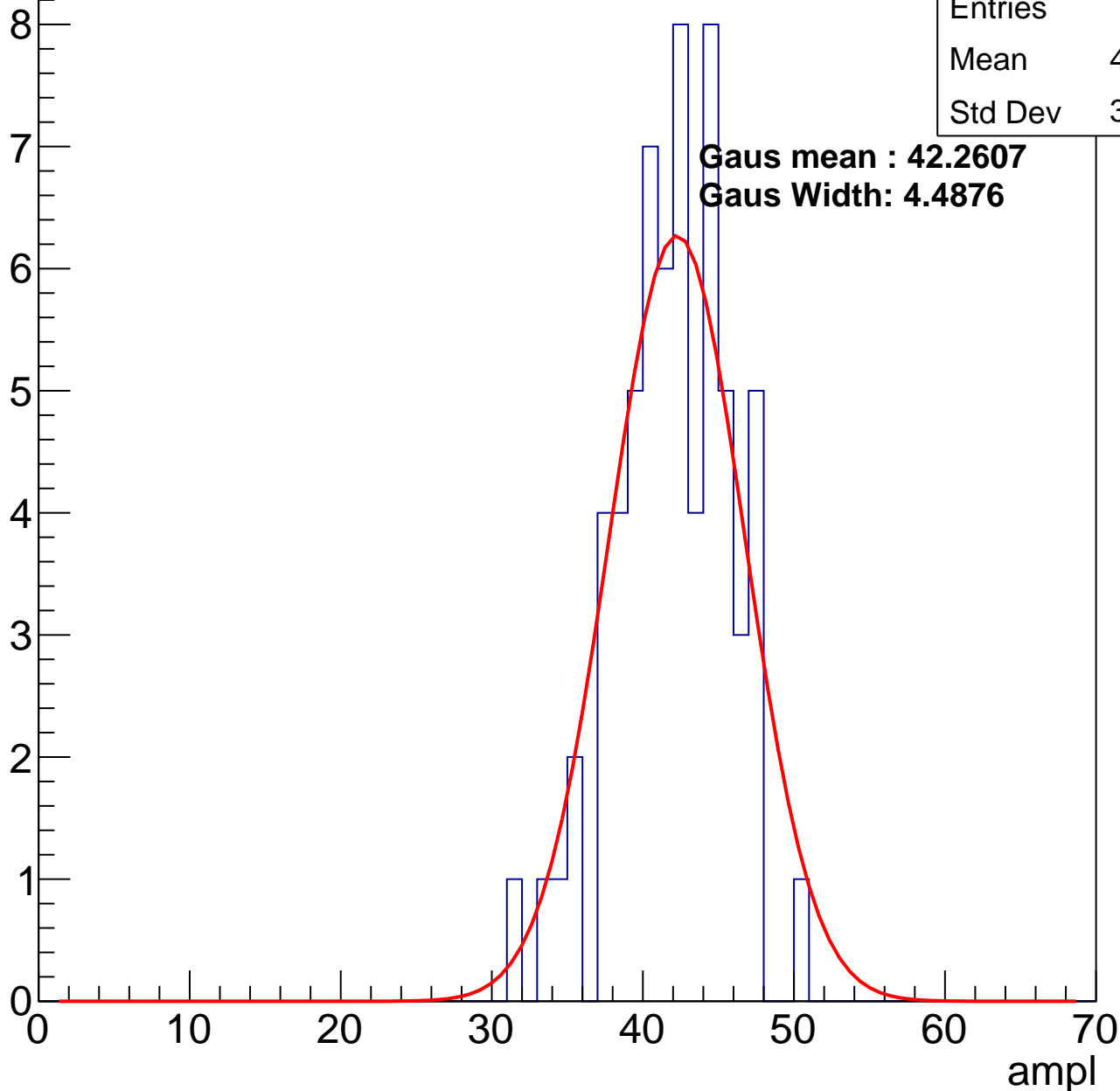
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.49
Std Dev	3.738

Gaus mean : 42.2607

Gaus Width: 4.4876

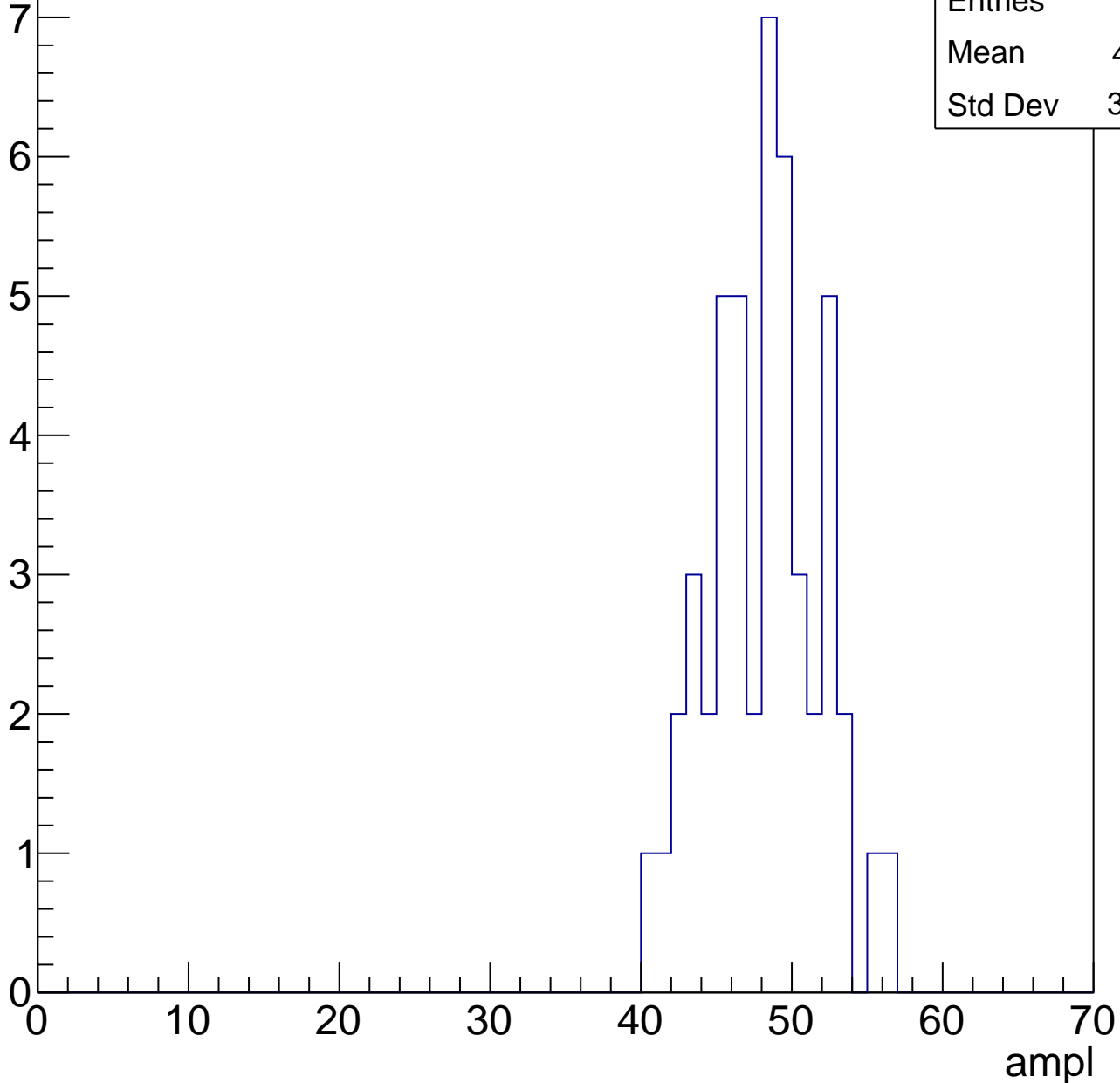


B1L102S, U10-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	47.71
Std Dev	3.634



B1L102S, U10-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	53.59
Std Dev	3.777

Entry

10

8

6

4

2

0

0

10

20

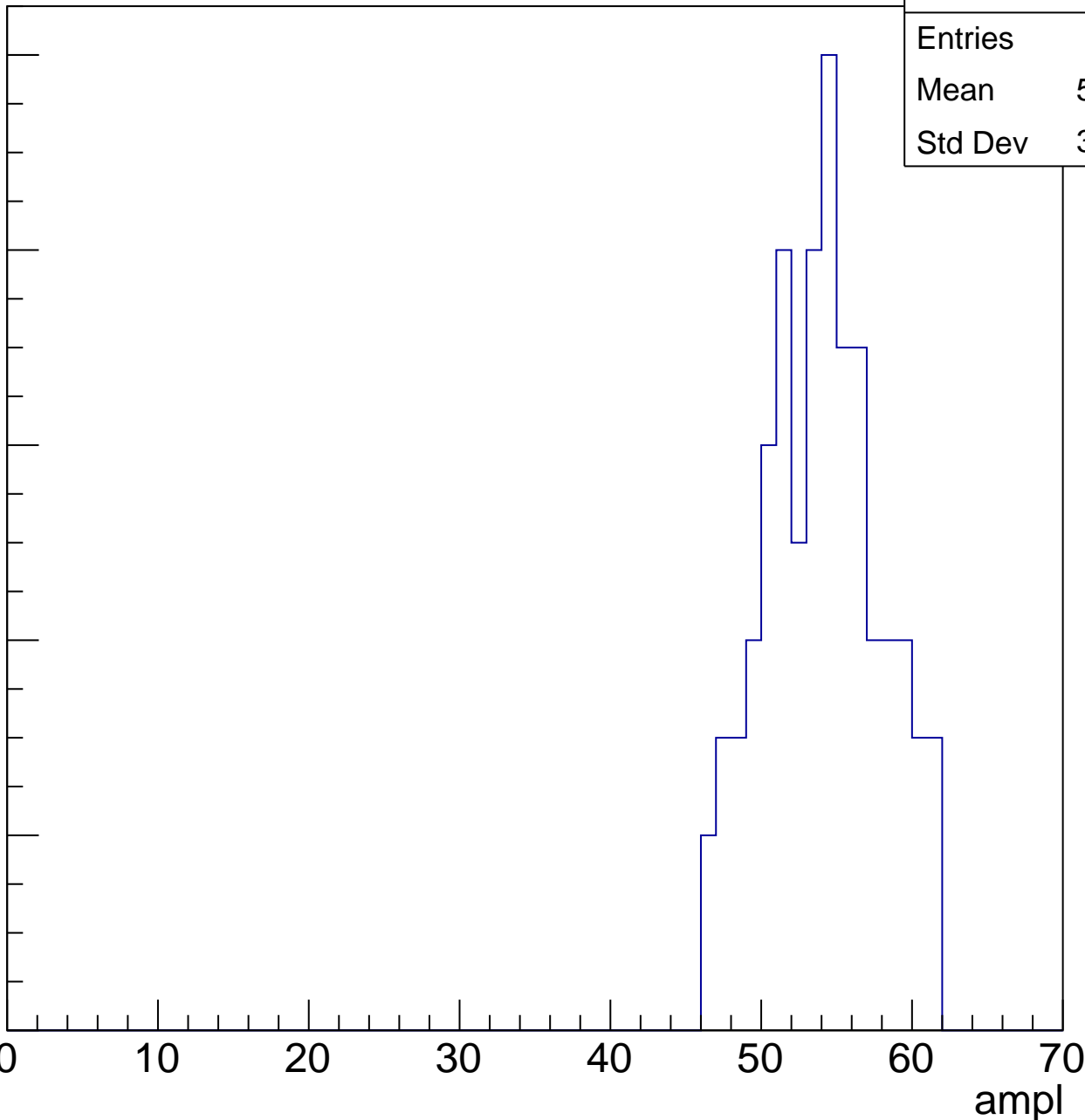
30

40

50

60

ampl

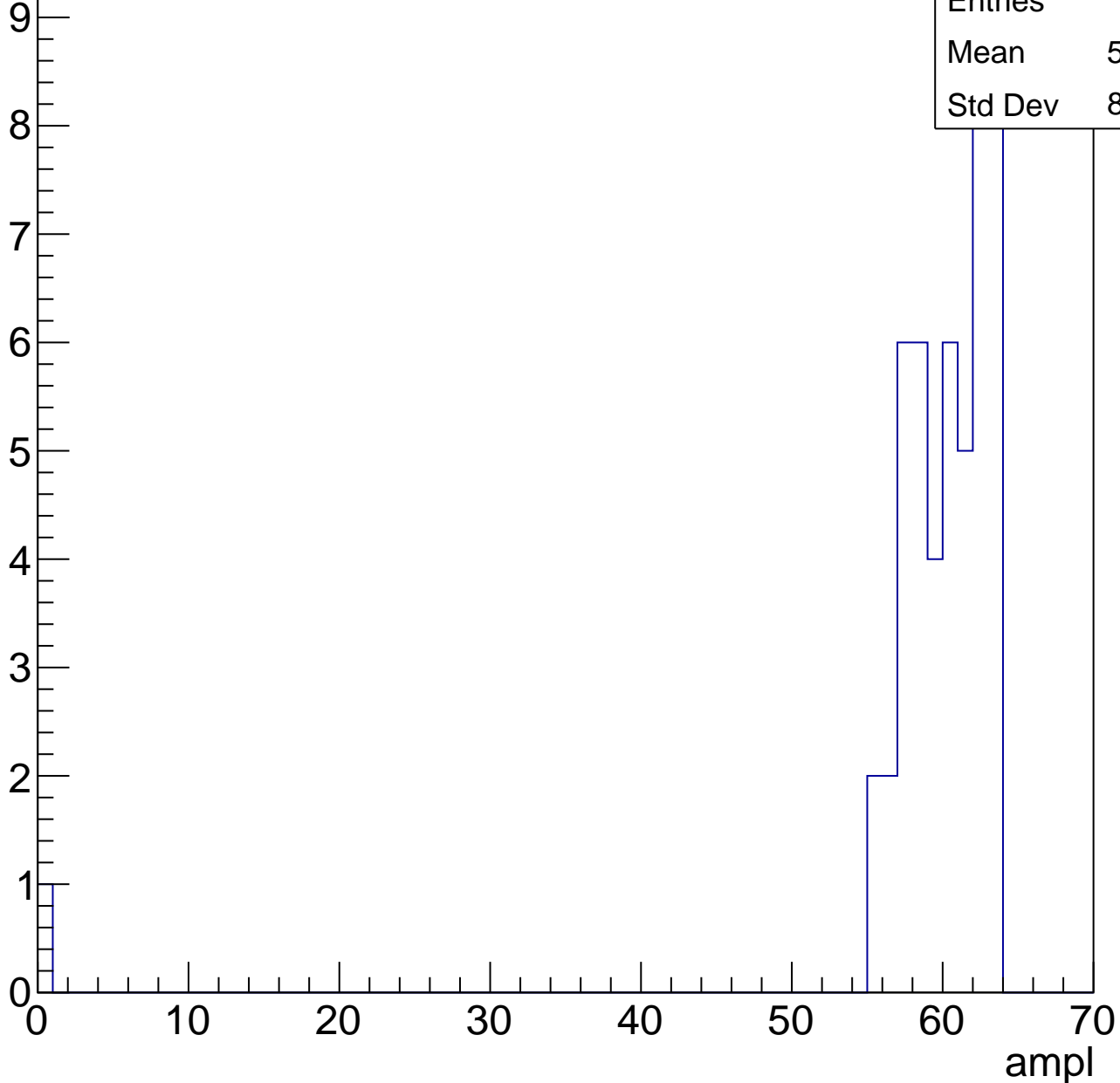


B1L102S, U10-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.67
Std Dev	8.796



B1L102S, U10-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

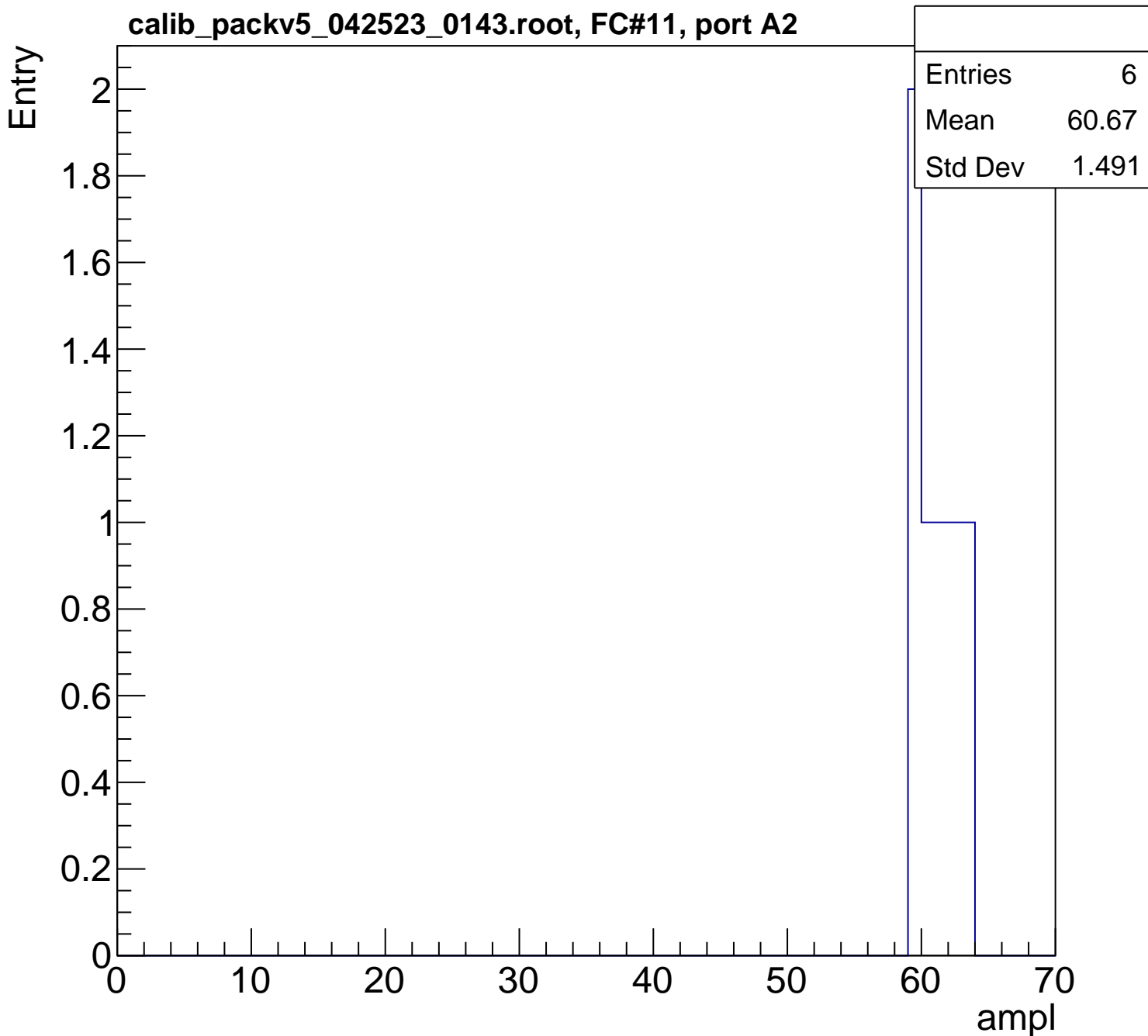
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	1.491

0 10 20 30 40 50 60 70

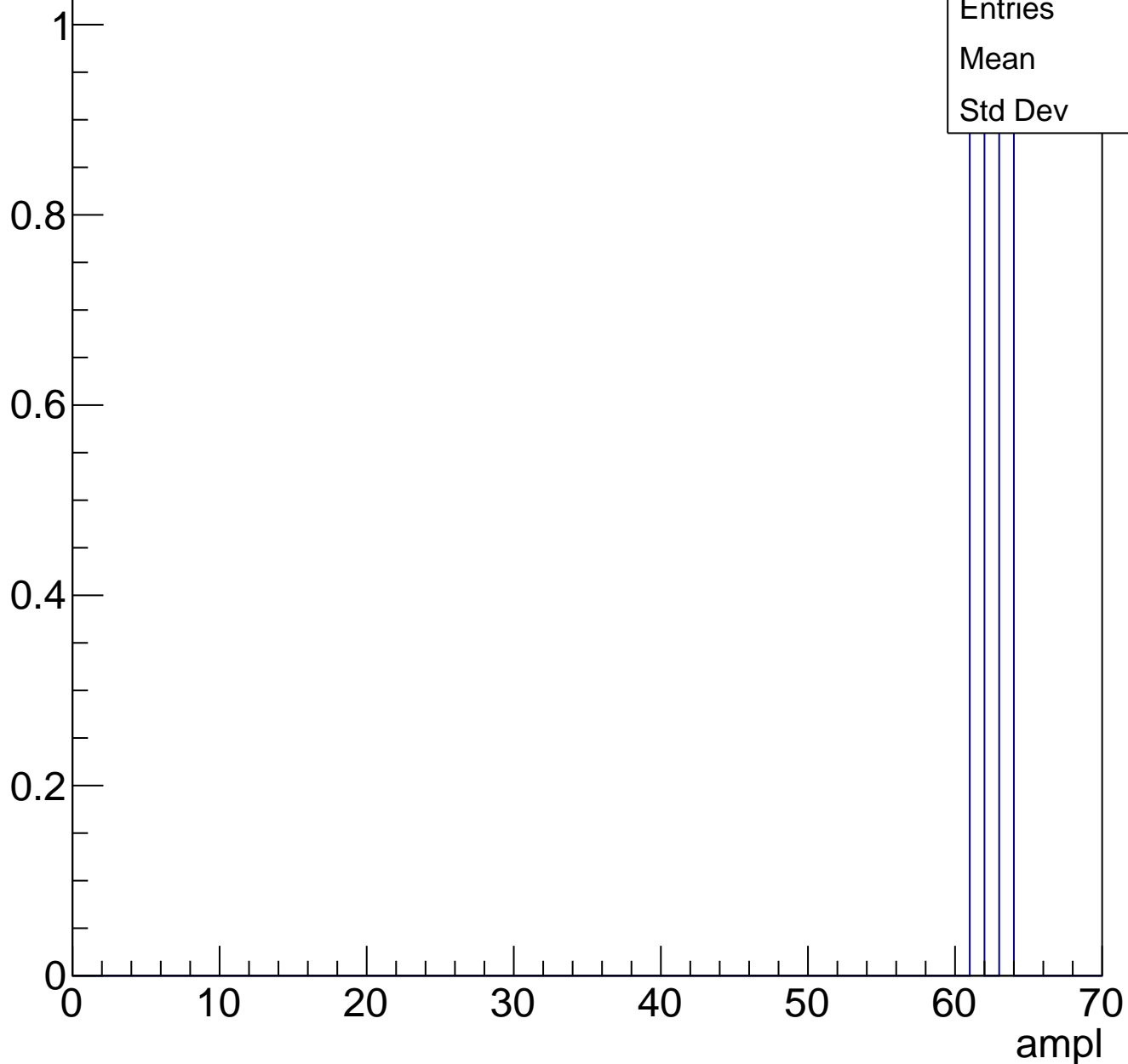
ampl



B1L102S, U10-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch124, adc0

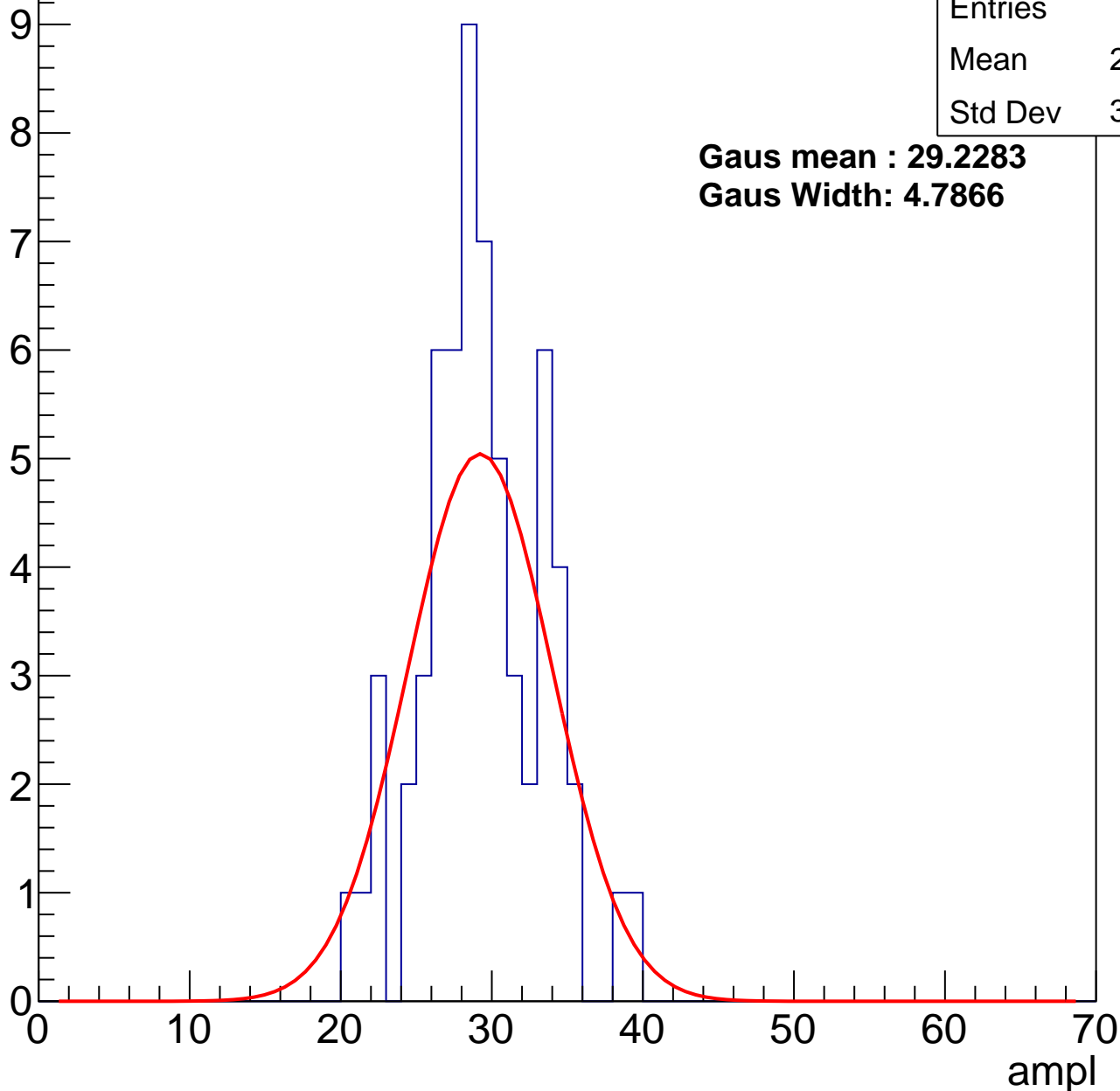
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.89
Std Dev	3.935

Gaus mean : 29.2283

Gaus Width: 4.7866



B1L102S, U10-ch124, adc1

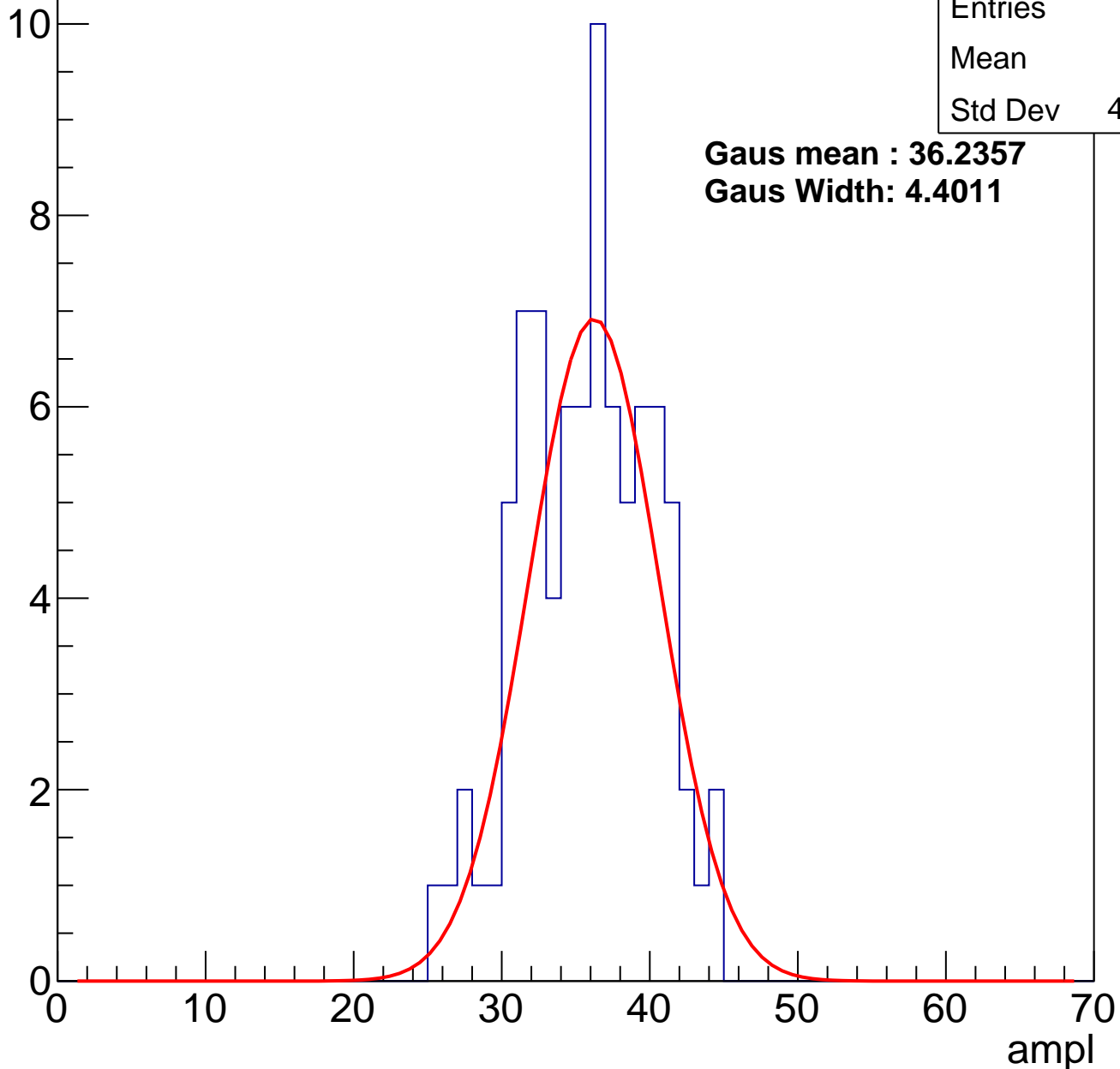
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	35.3
Std Dev	4.278

Gaus mean : 36.2357

Gaus Width: 4.4011

Entry



B1L102S, U10-ch124, adc2

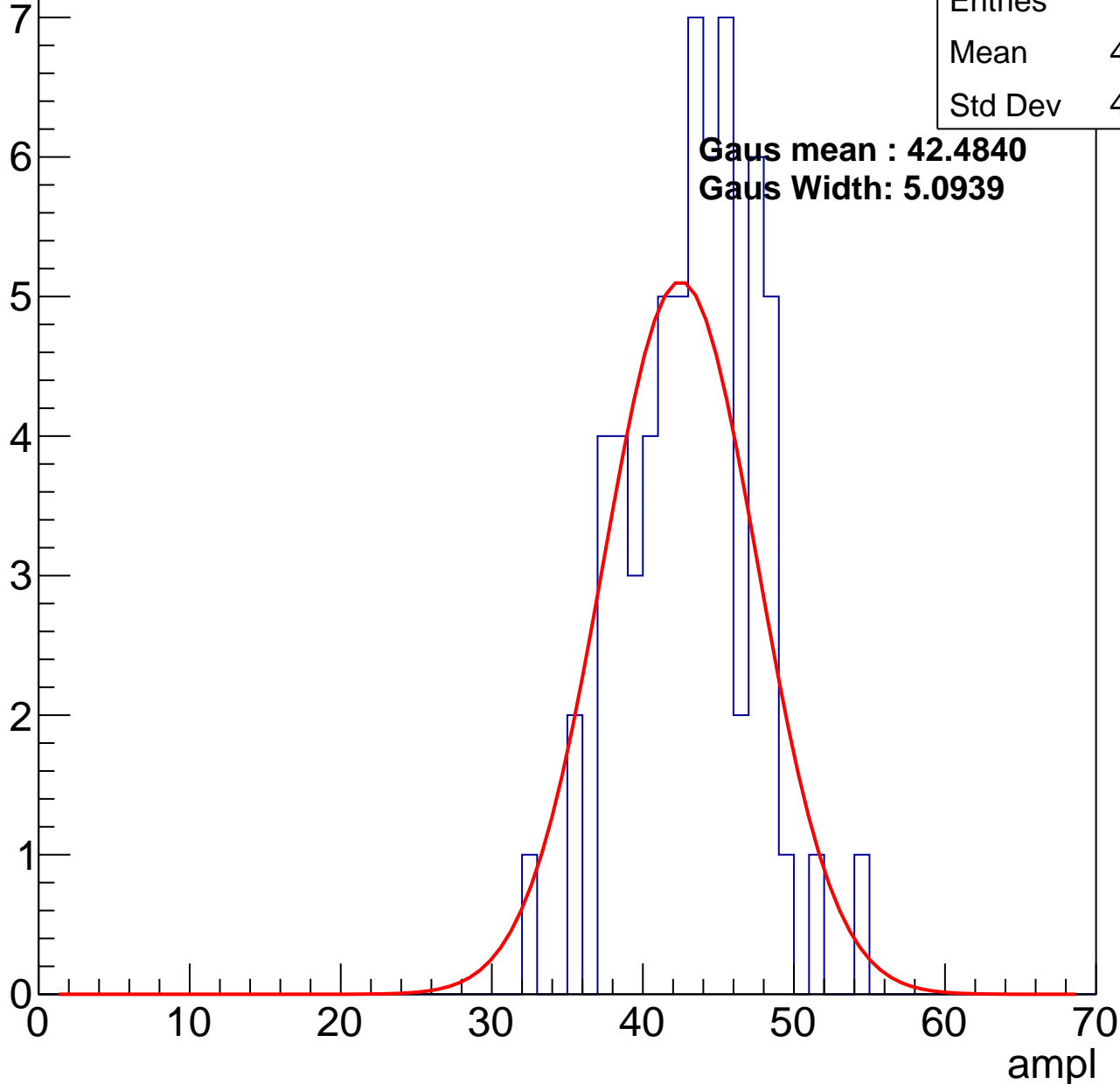
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.84
Std Dev	4.135

Gaus mean : 42.4840

Gaus Width: 5.0939

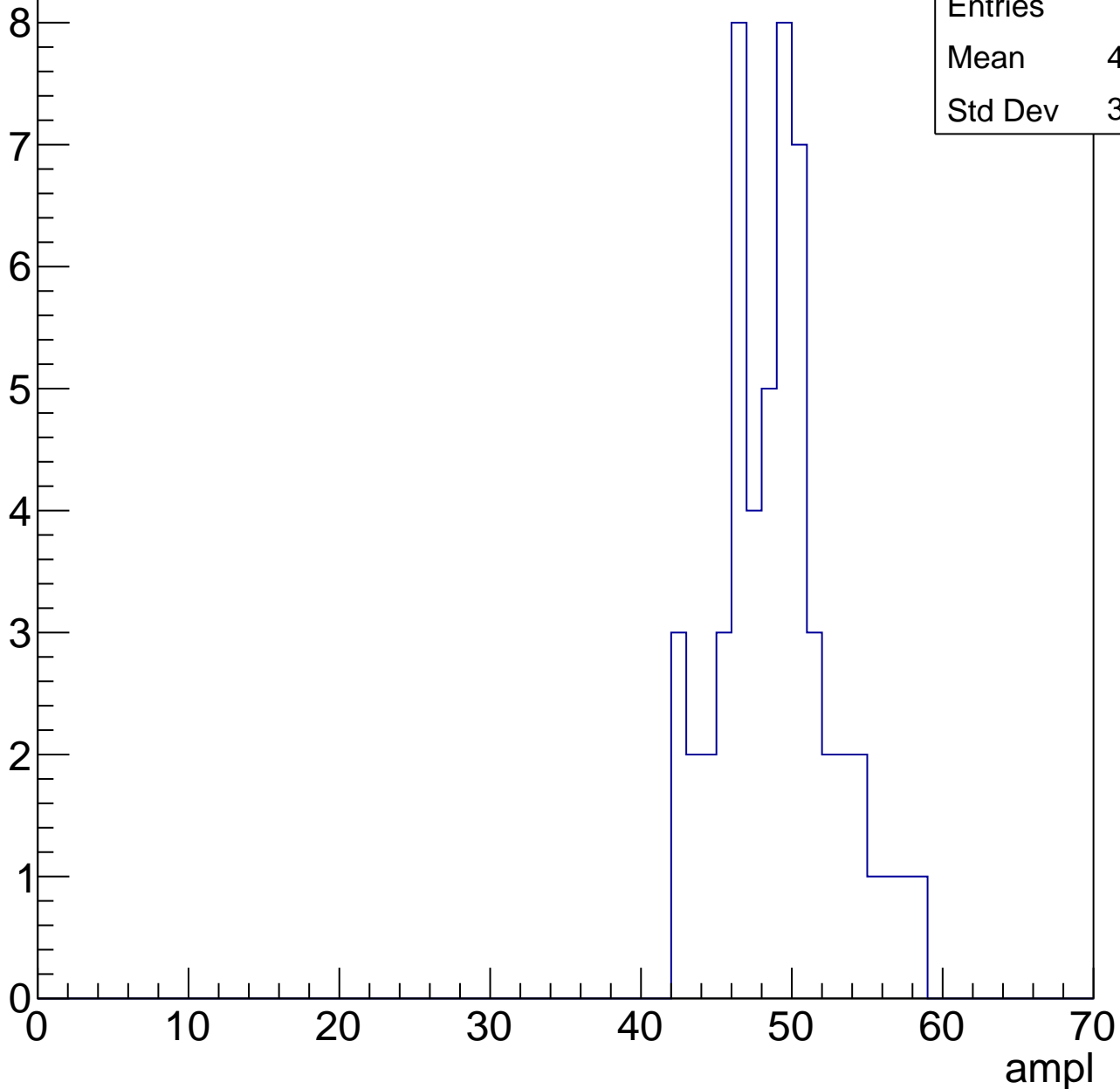


B1L102S, U10-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.55
Std Dev	3.687

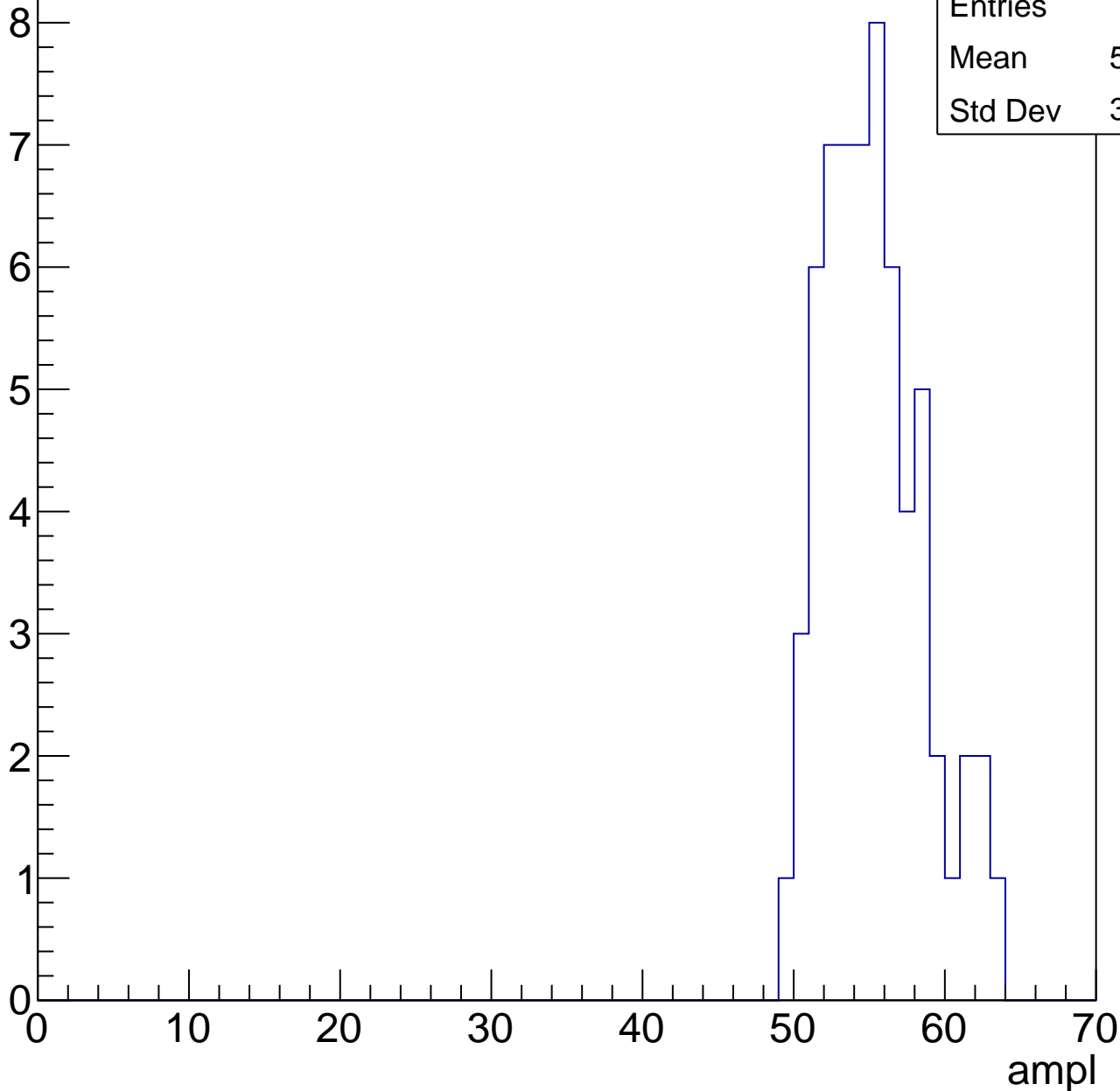


B1L102S, U10-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.82
Std Dev	3.265

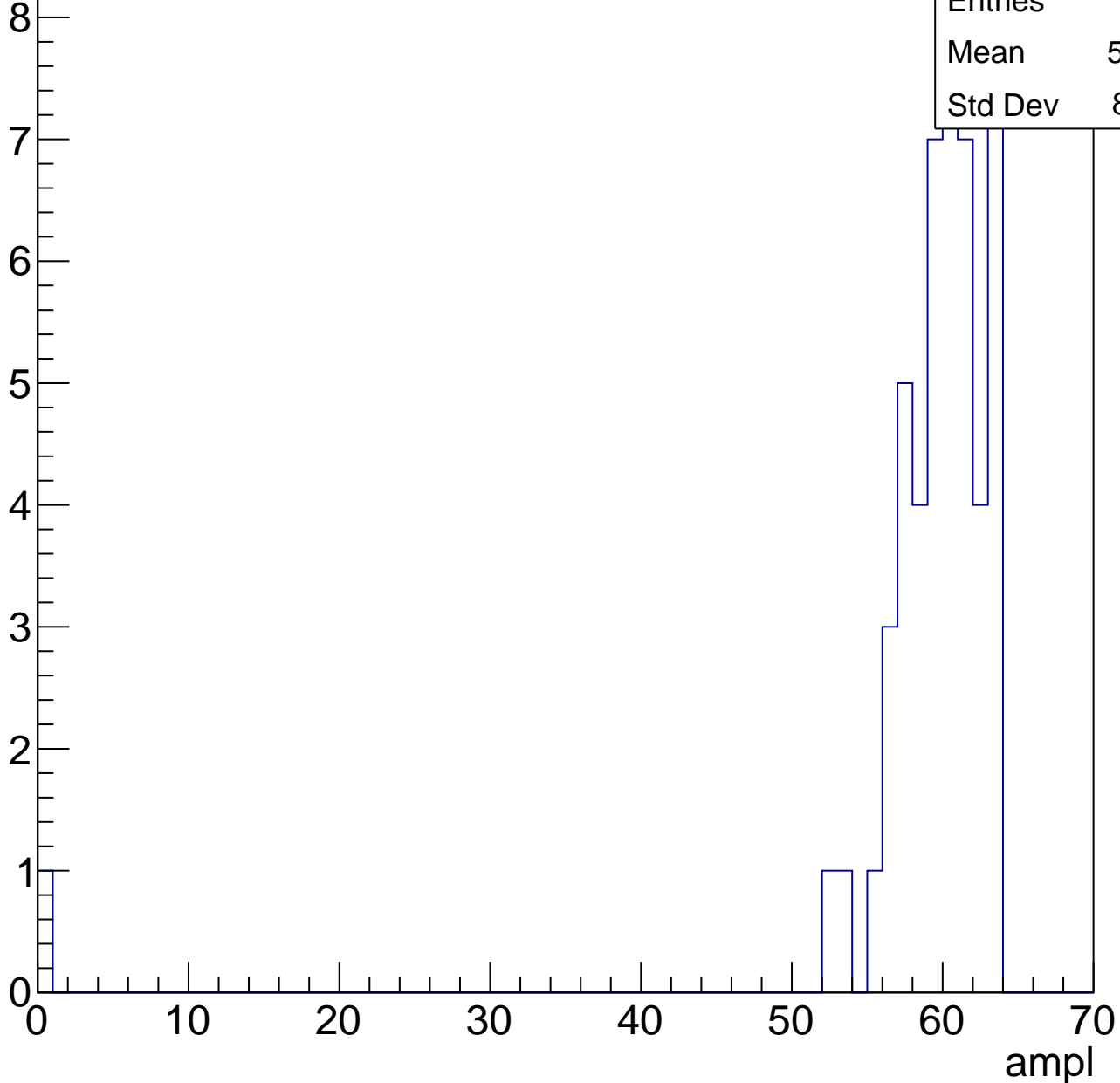


B1L102S, U10-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

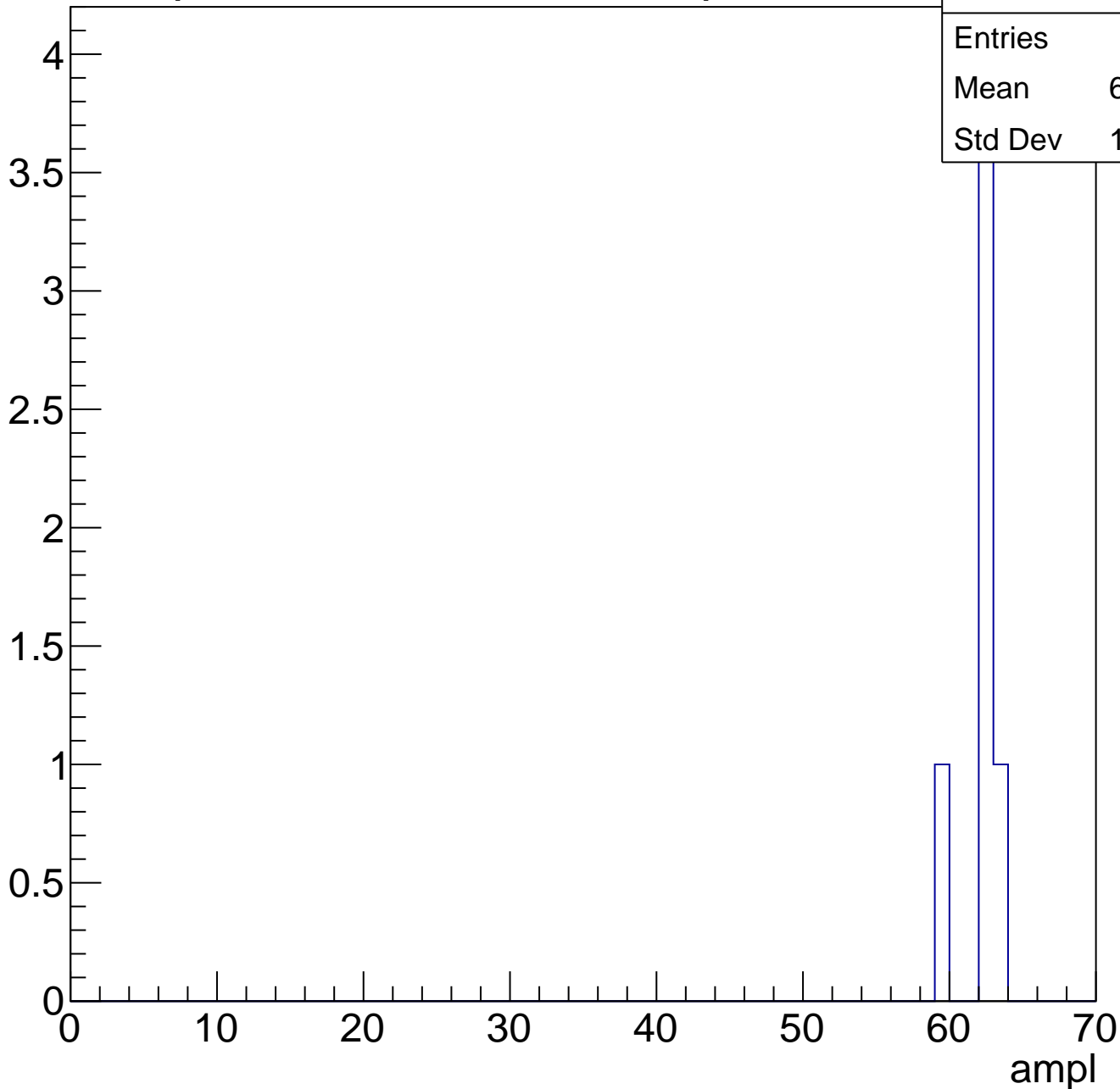
Entries	50
Mean	58.34
Std Dev	8.731



B1L102S, U10-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

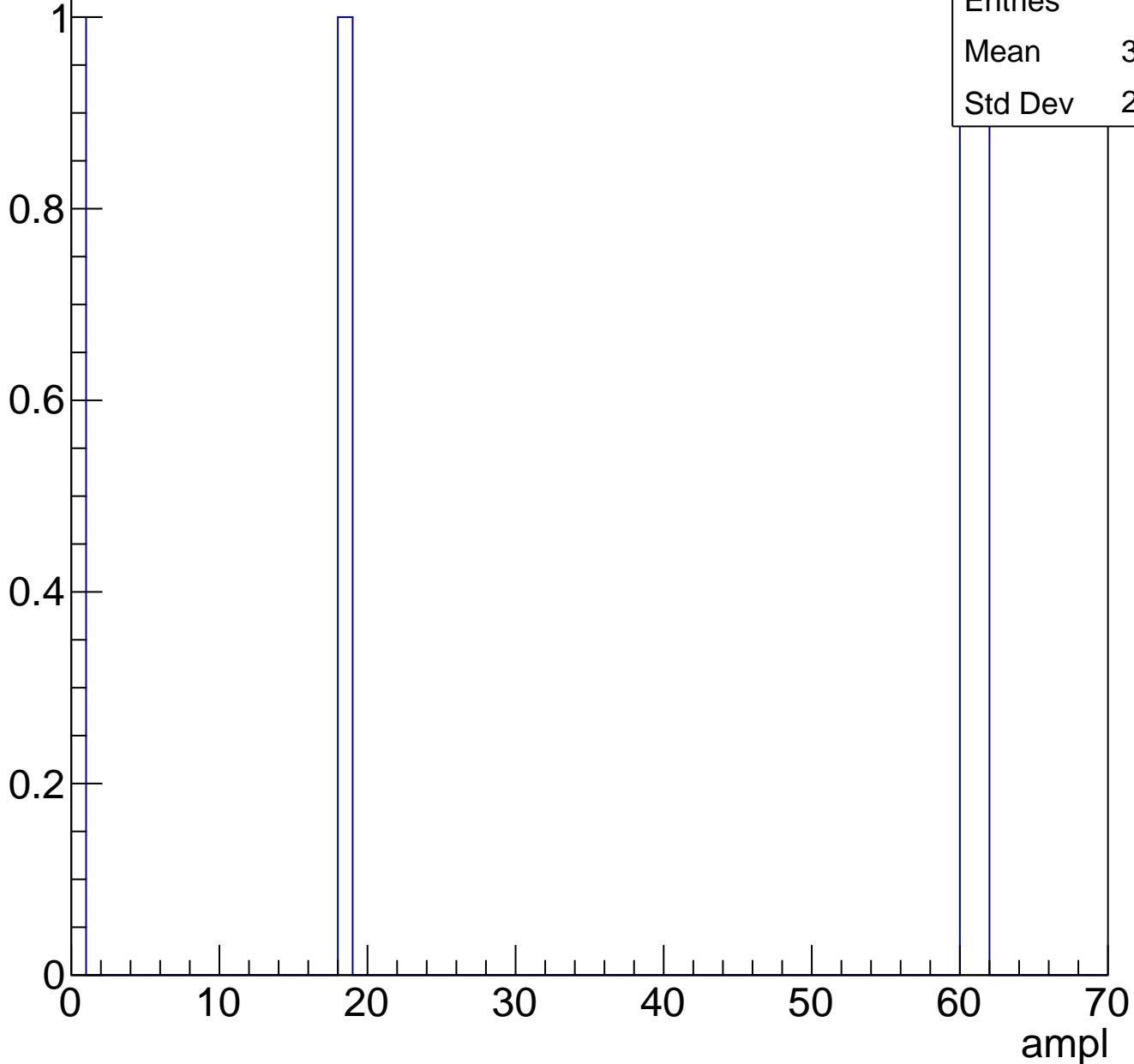
Entry



B1L102S, U10-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch125, adc0

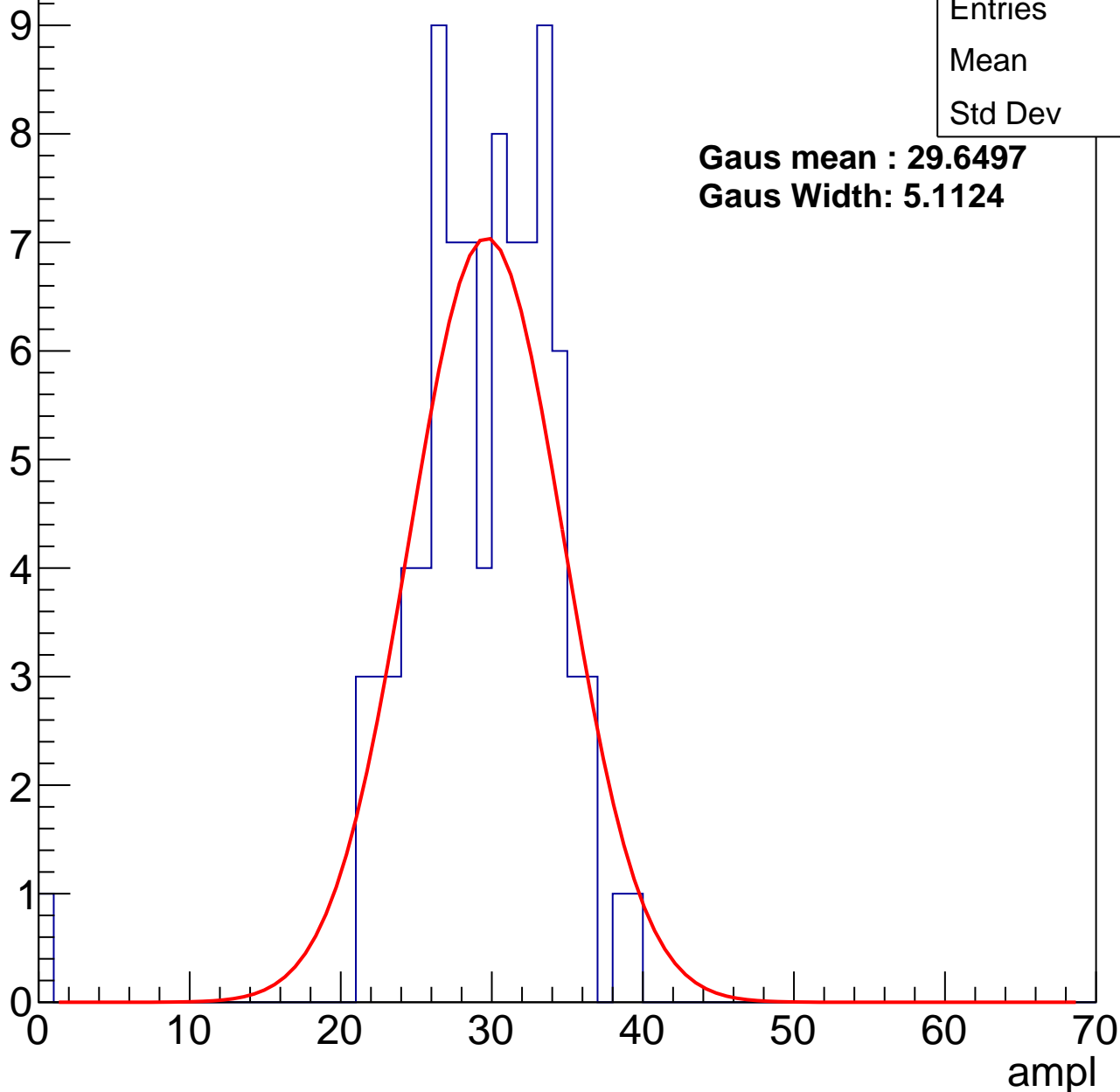
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	28.9
Std Dev	5.16

Gaus mean : 29.6497

Gaus Width: 5.1124



B1L102S, U10-ch125, adc1

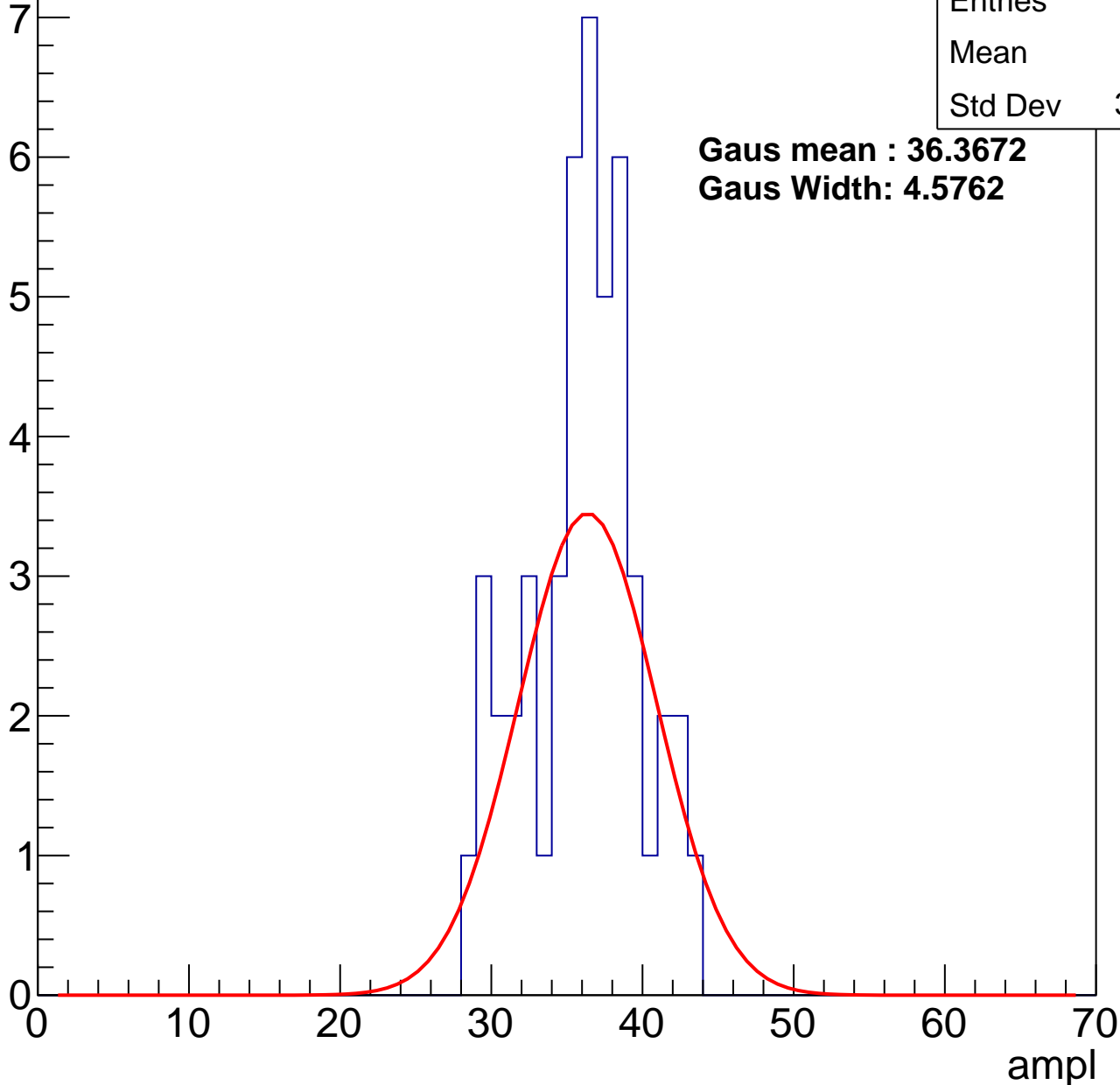
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	35.6
Std Dev	3.661

Gaus mean : 36.3672

Gaus Width: 4.5762



B1L102S, U10-ch125, adc2

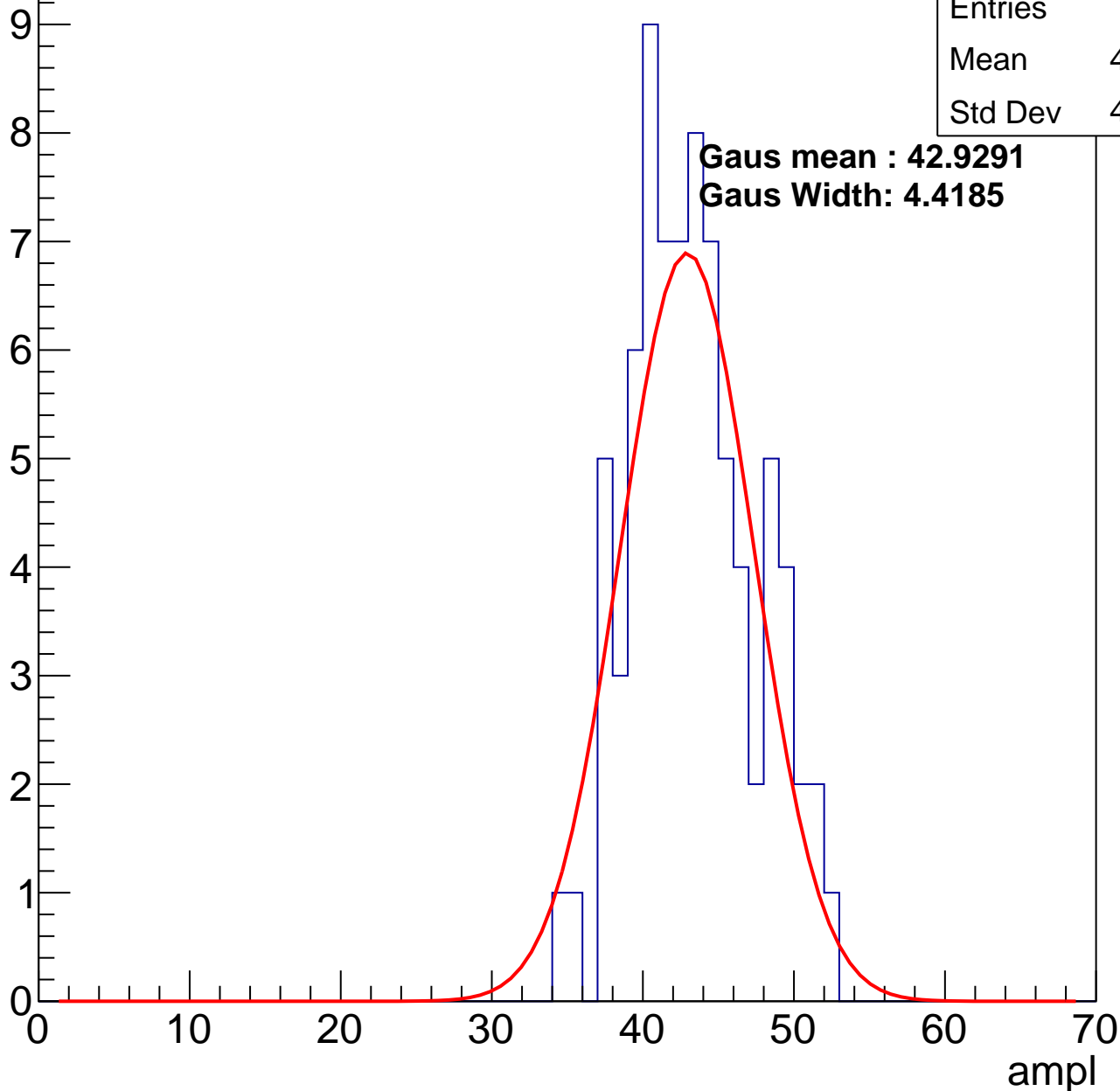
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	42.89
Std Dev	4.028

Gaus mean : 42.9291

Gaus Width: 4.4185

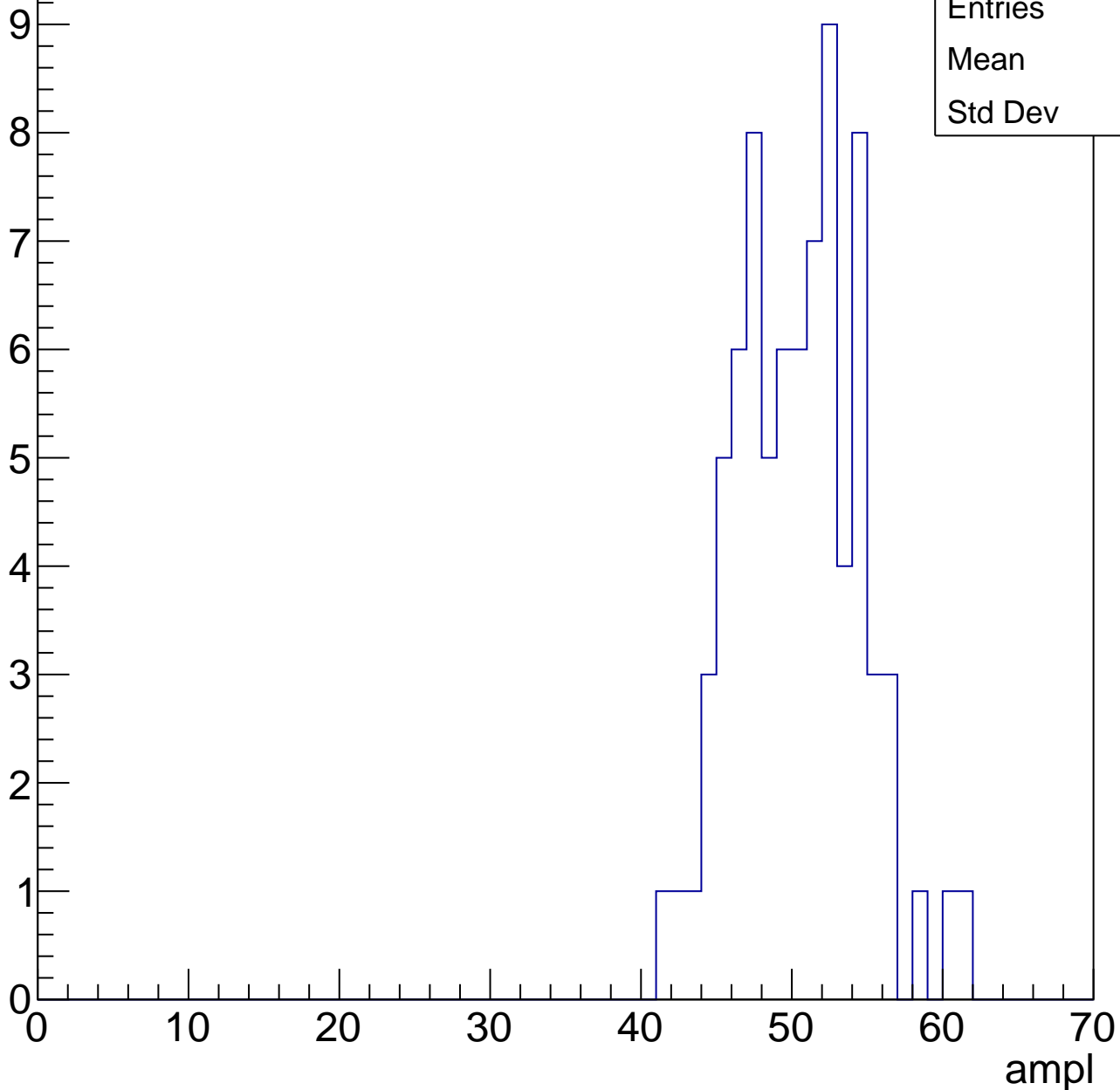


B1L102S, U10-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50
Std Dev	4.05

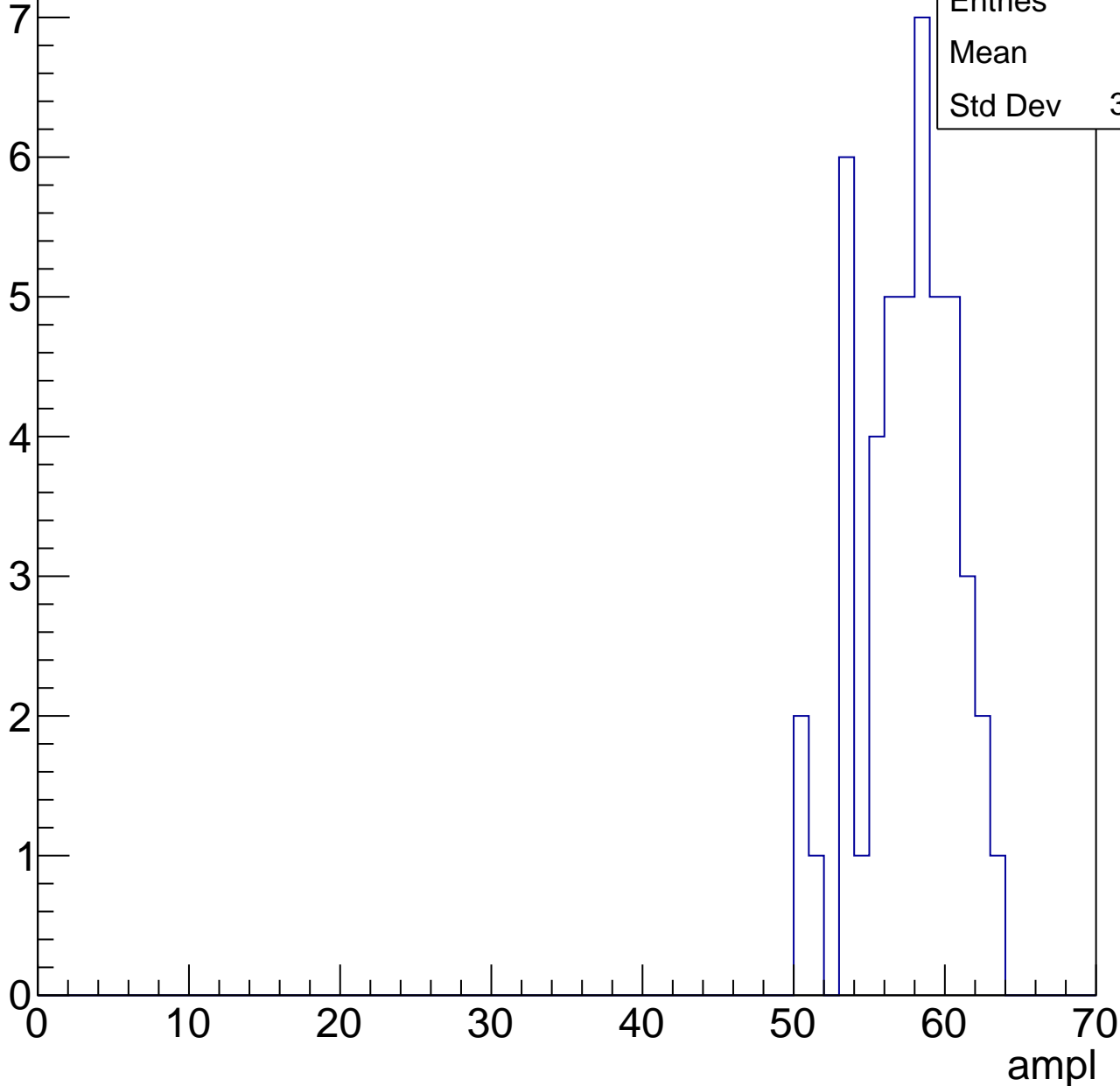


B1L102S, U10-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57
Std Dev	3.149

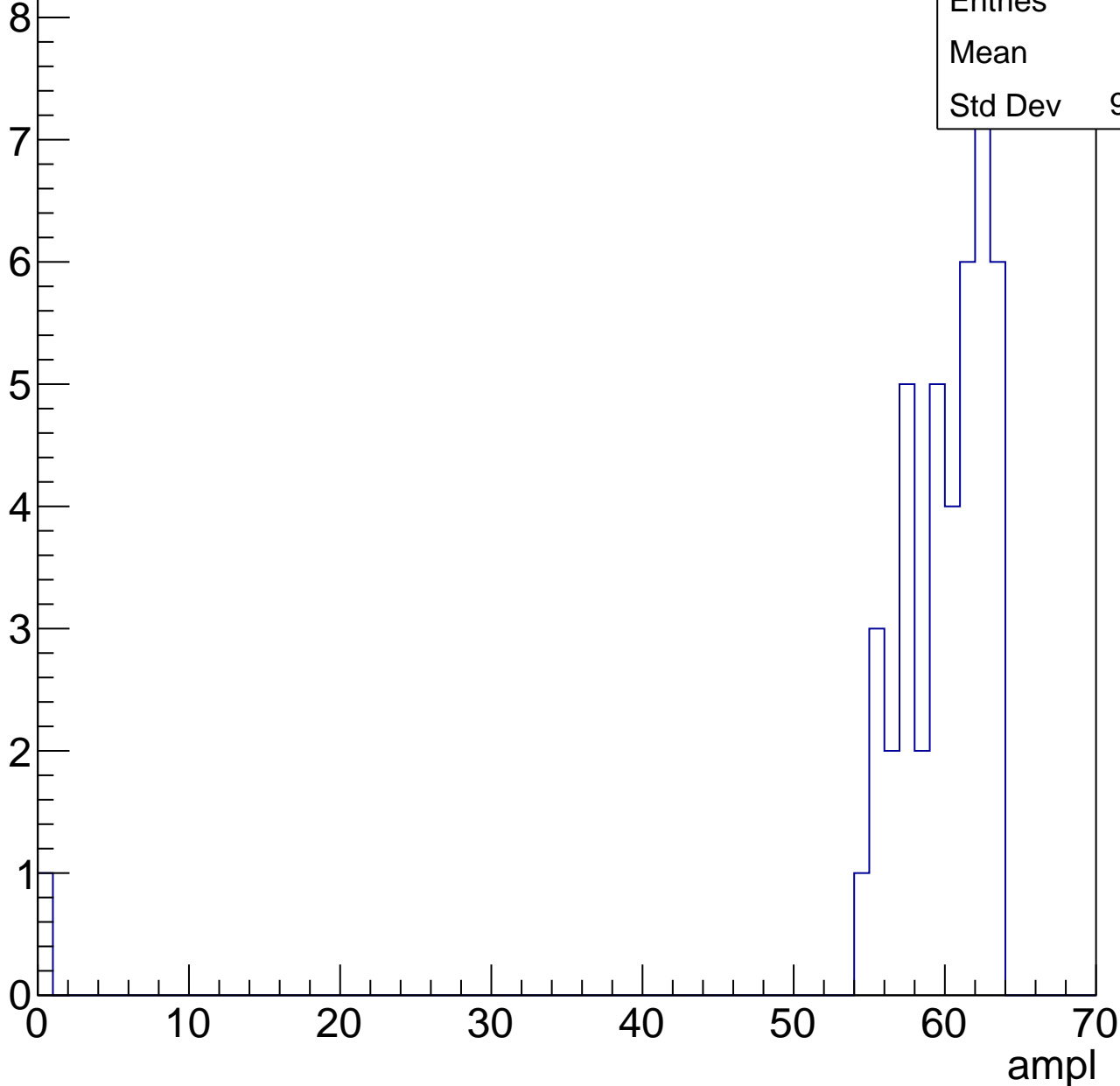


B1L102S, U10-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.3
Std Dev	9.362



B1L102S, U10-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch126, adc0

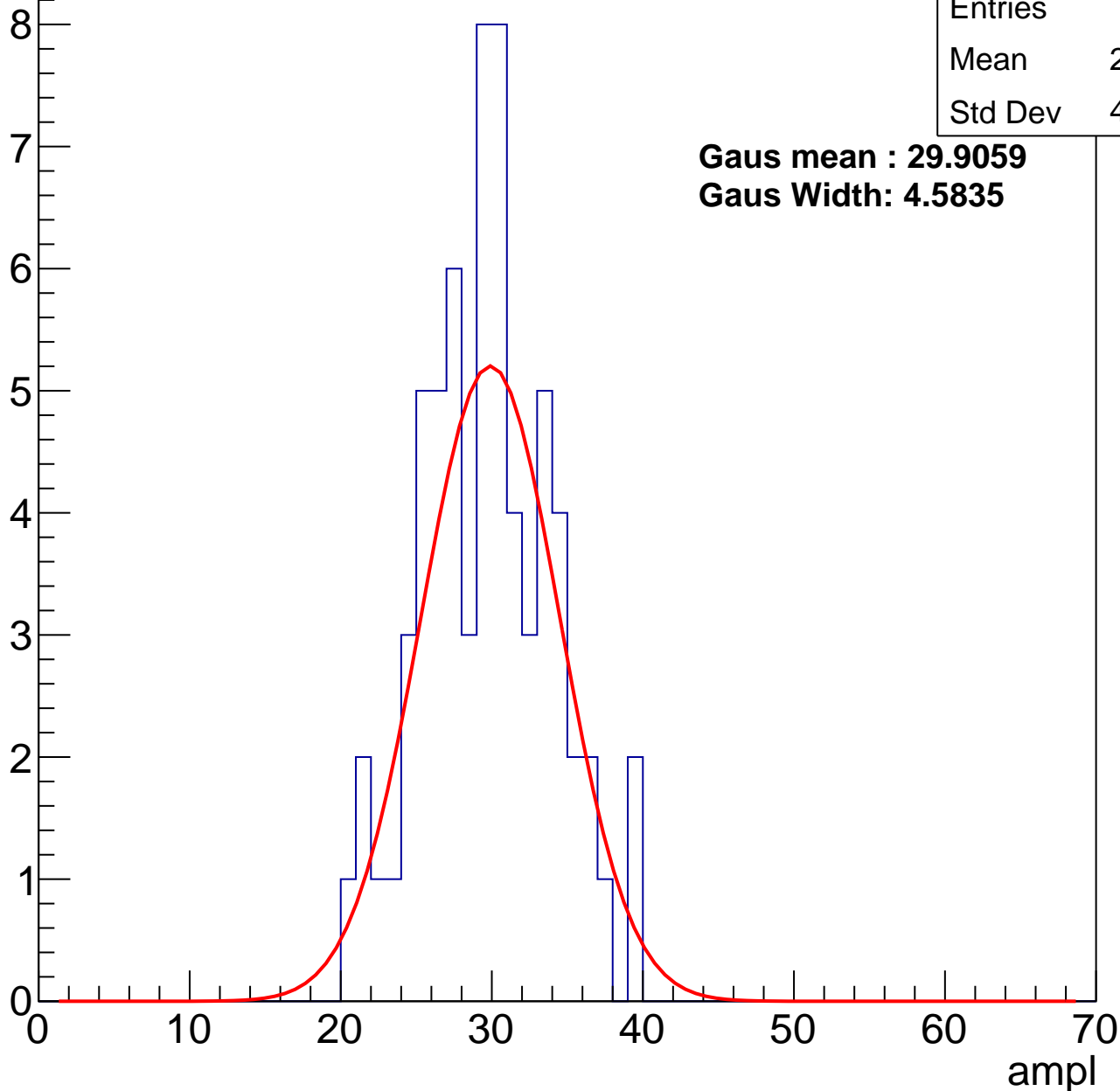
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.24
Std Dev	4.232

Gaus mean : 29.9059

Gaus Width: 4.5835



B1L102S, U10-ch126, adc1

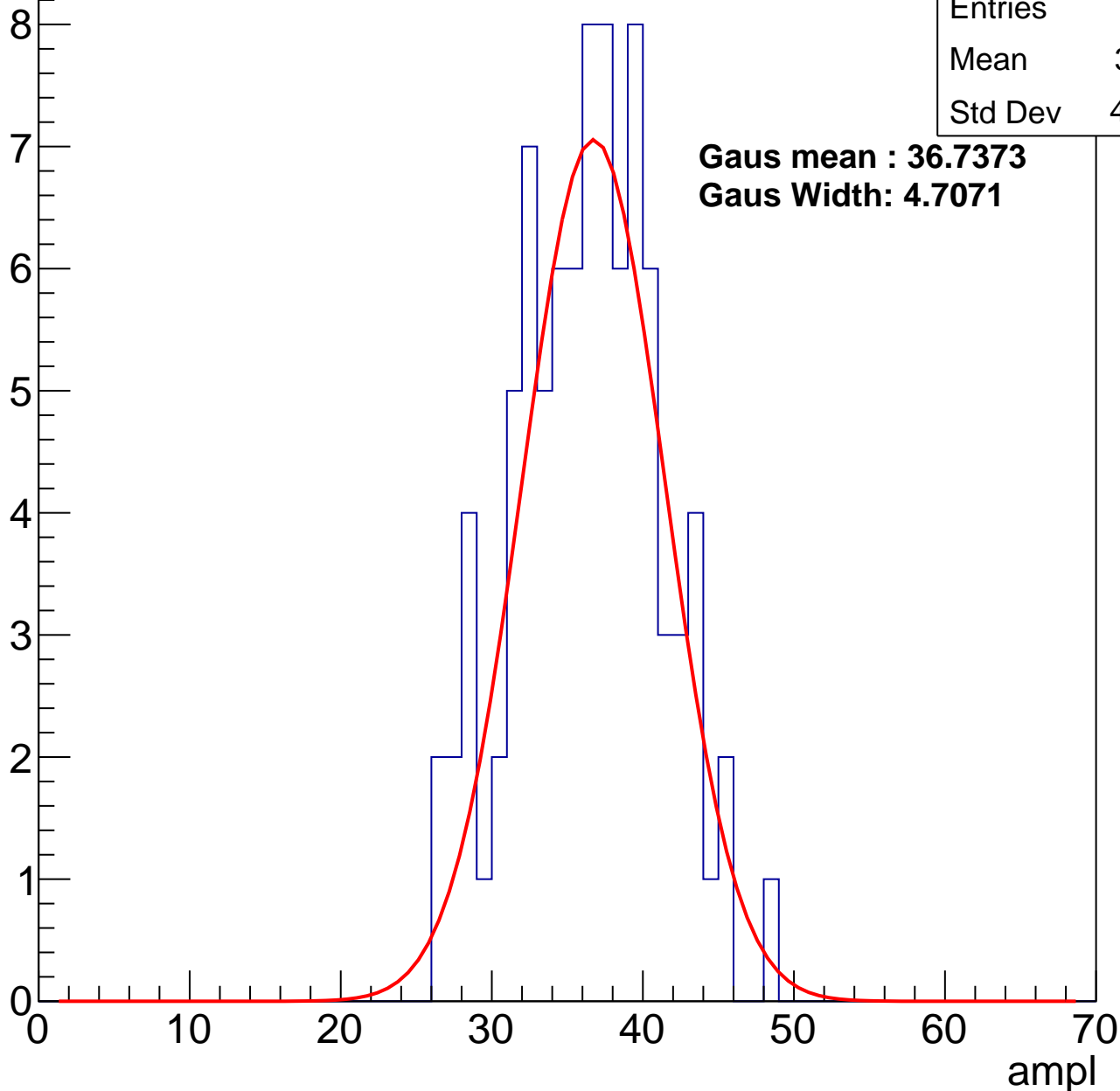
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	35.91
Std Dev	4.713

Gaus mean : 36.7373

Gaus Width: 4.7071



B1L102S, U10-ch126, adc2

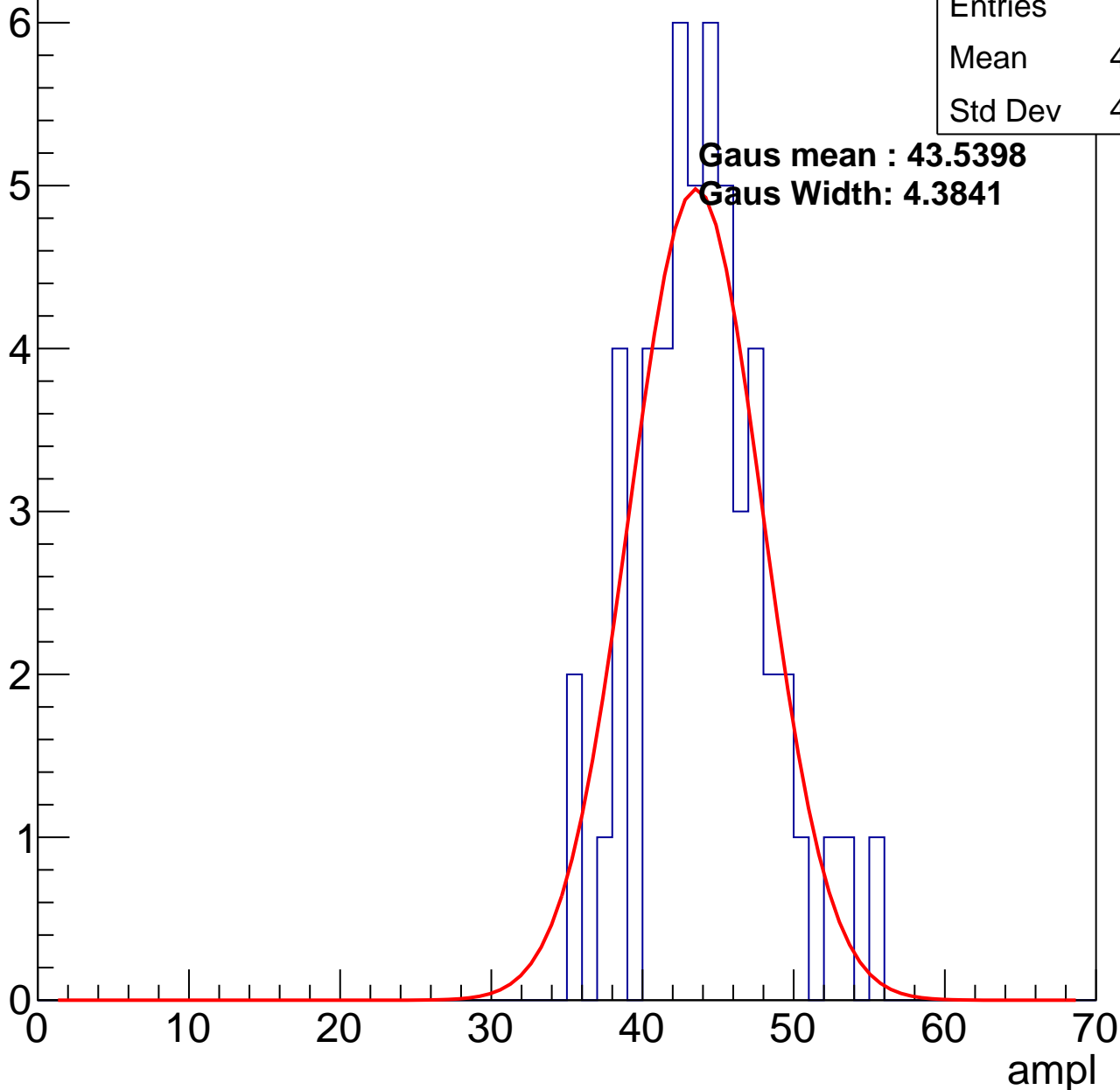
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.63
Std Dev	4.193

Gaus mean : 43.5398

Gaus Width: 4.3841

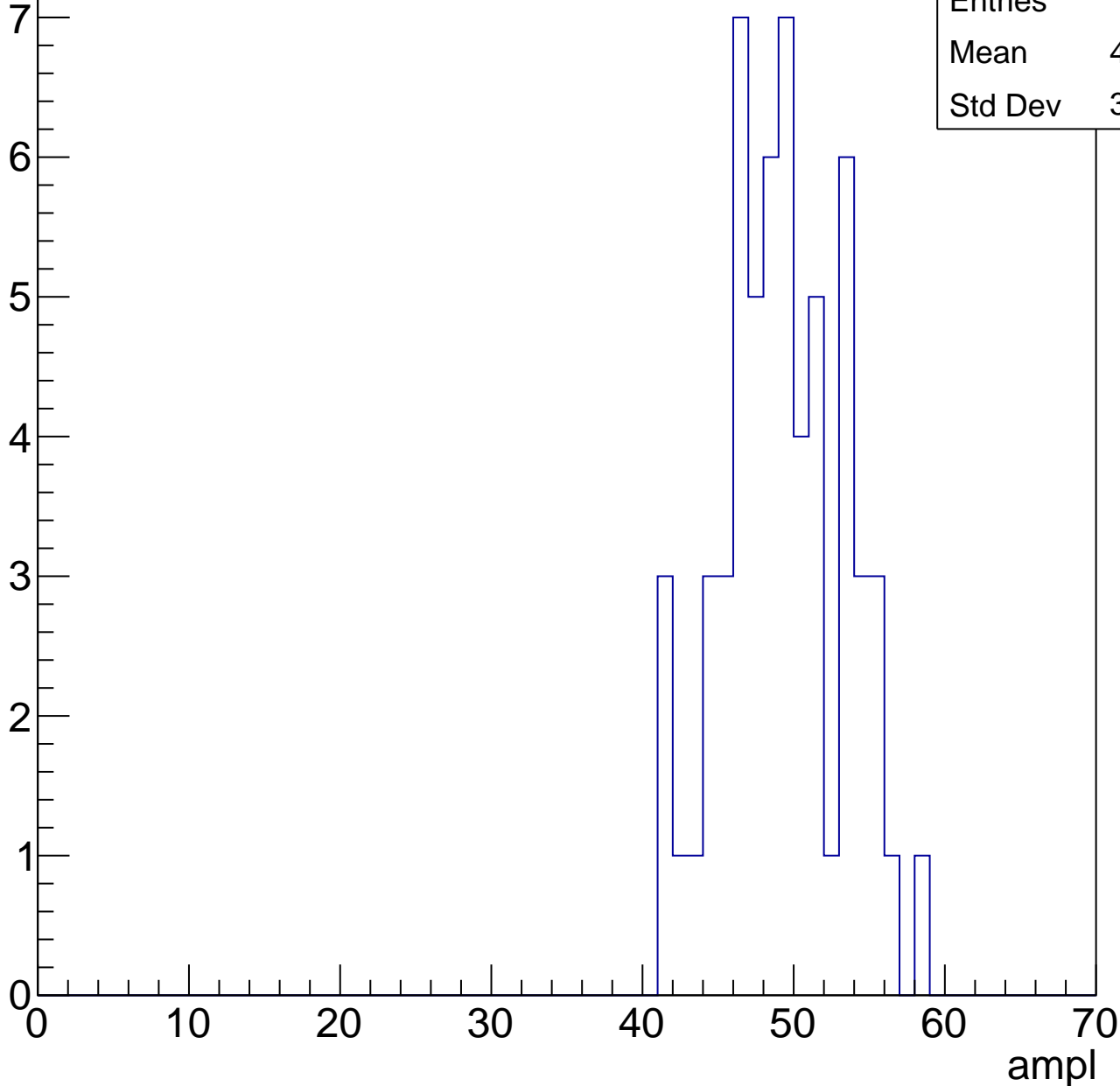


B1L102S, U10-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.82
Std Dev	3.935

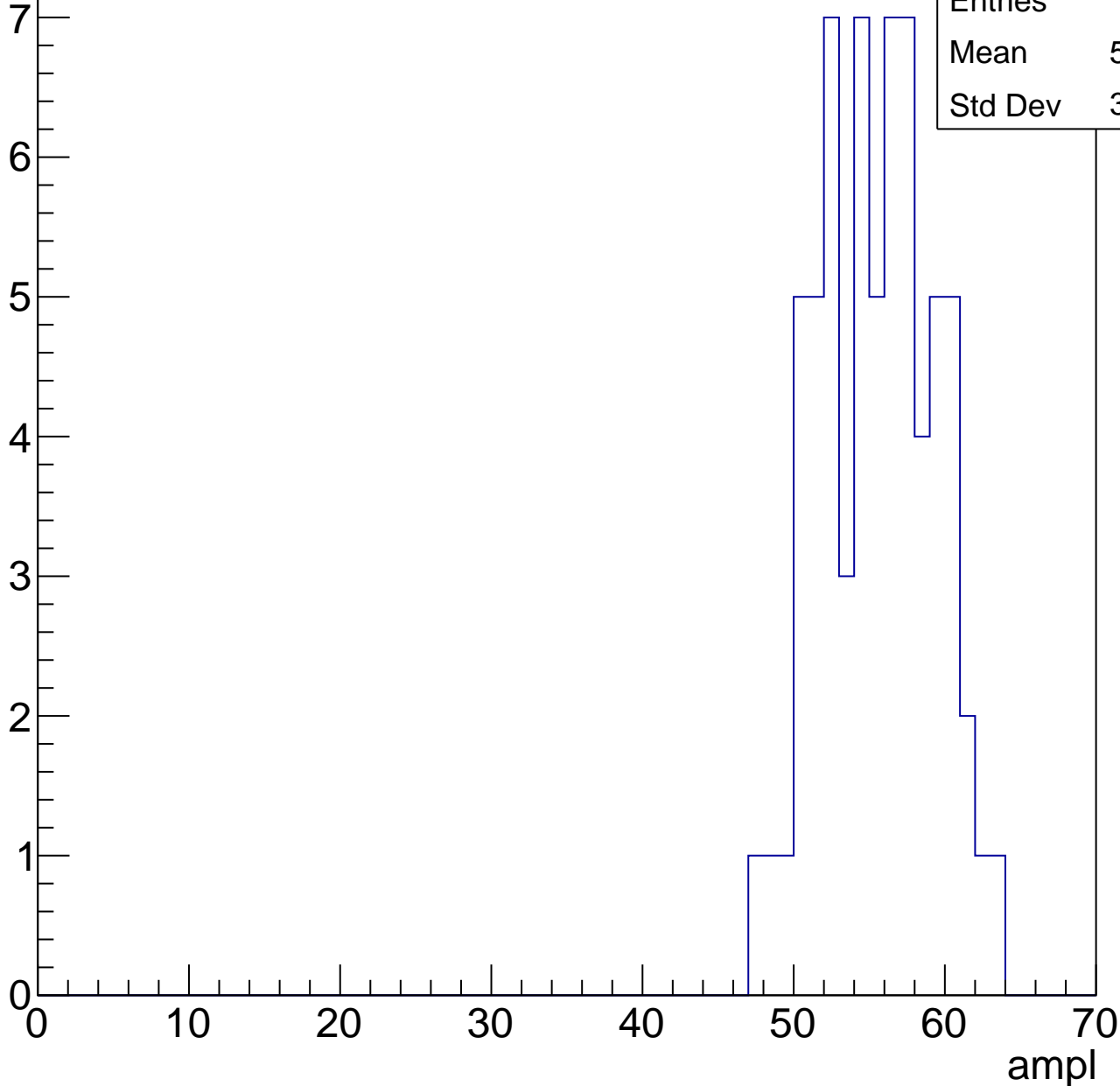


B1L102S, U10-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55.07
Std Dev	3.658

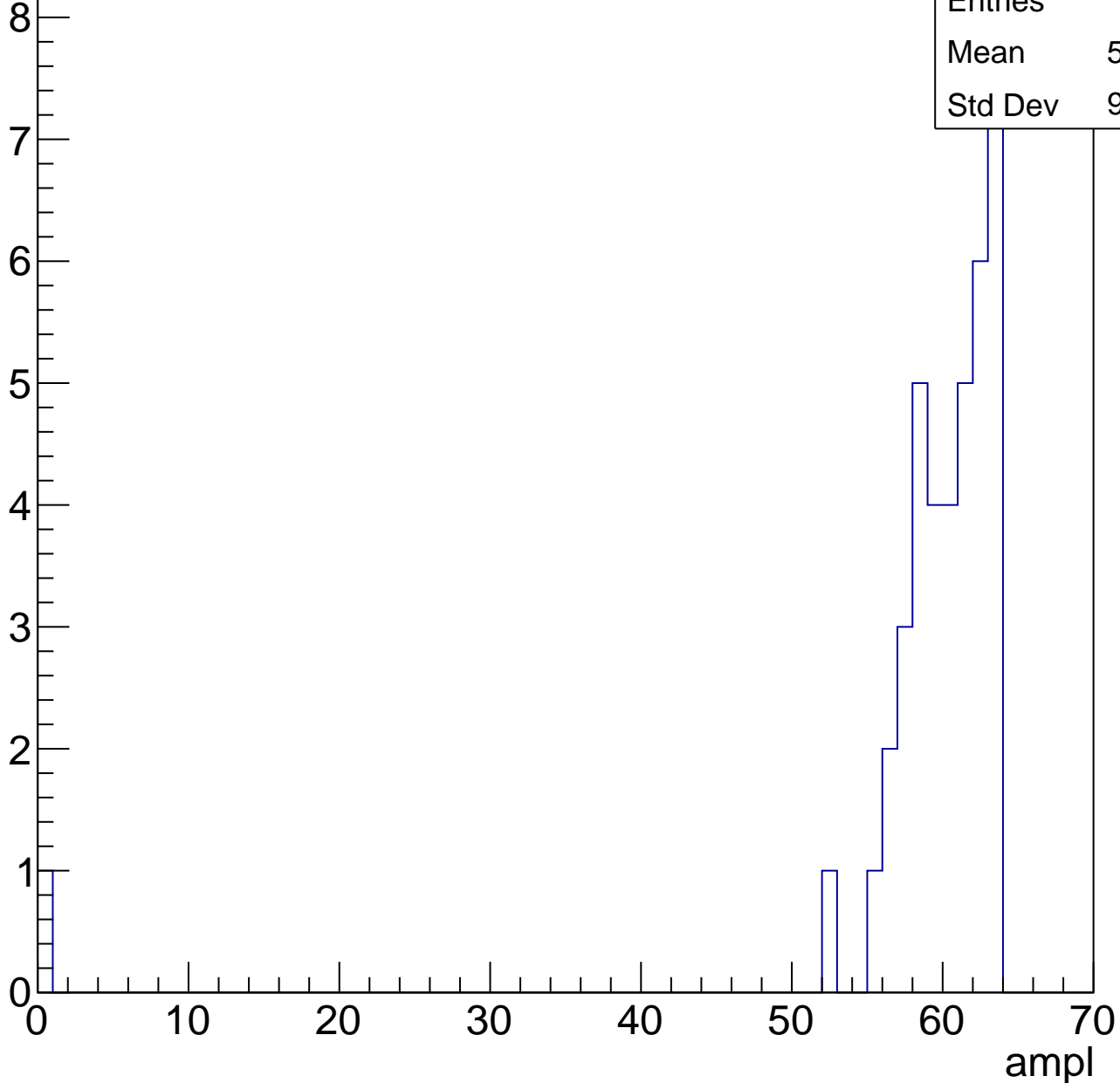


B1L102S, U10-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

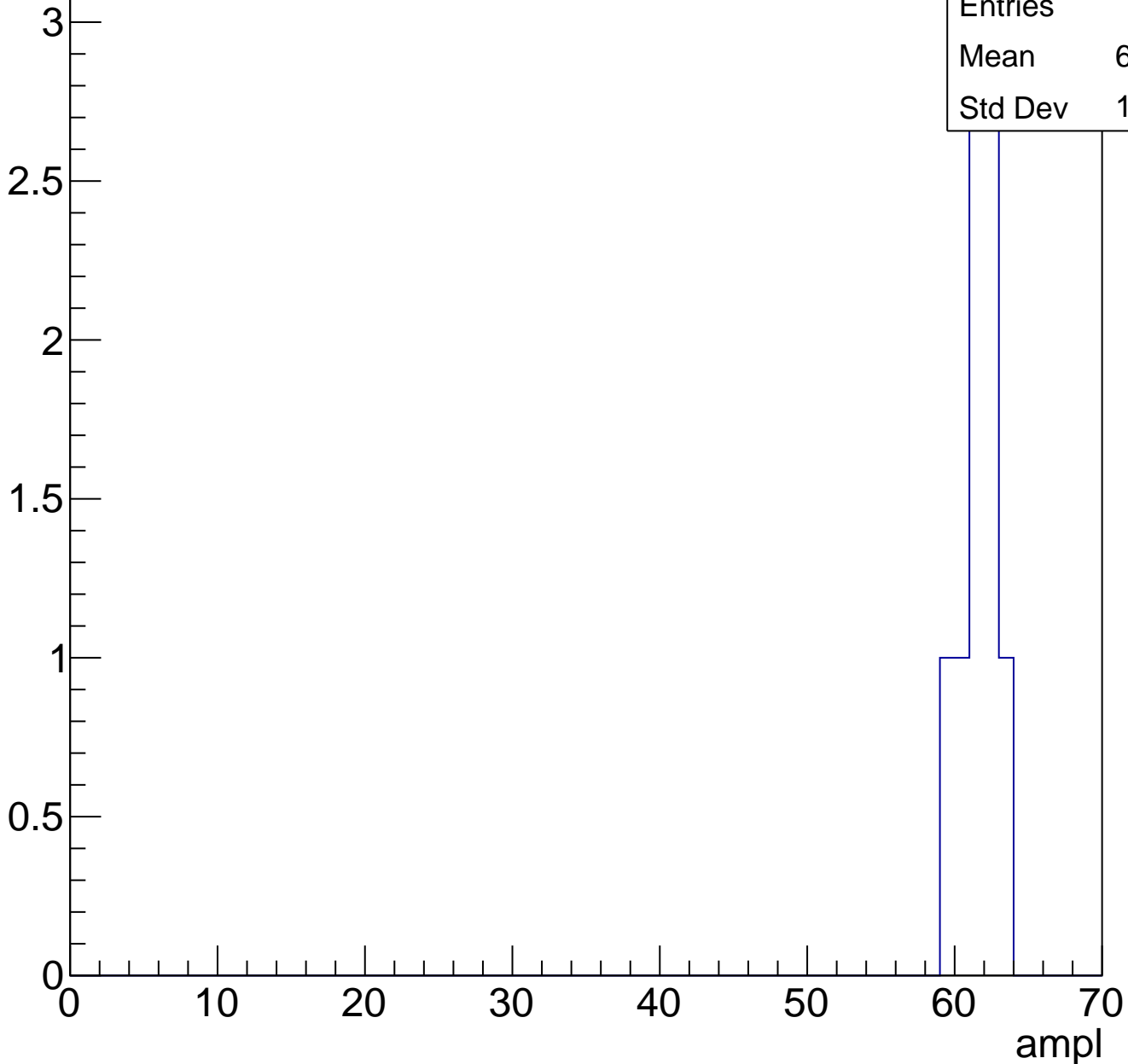
Entries	40
Mean	58.42
Std Dev	9.713



B1L102S, U10-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch127, adc0

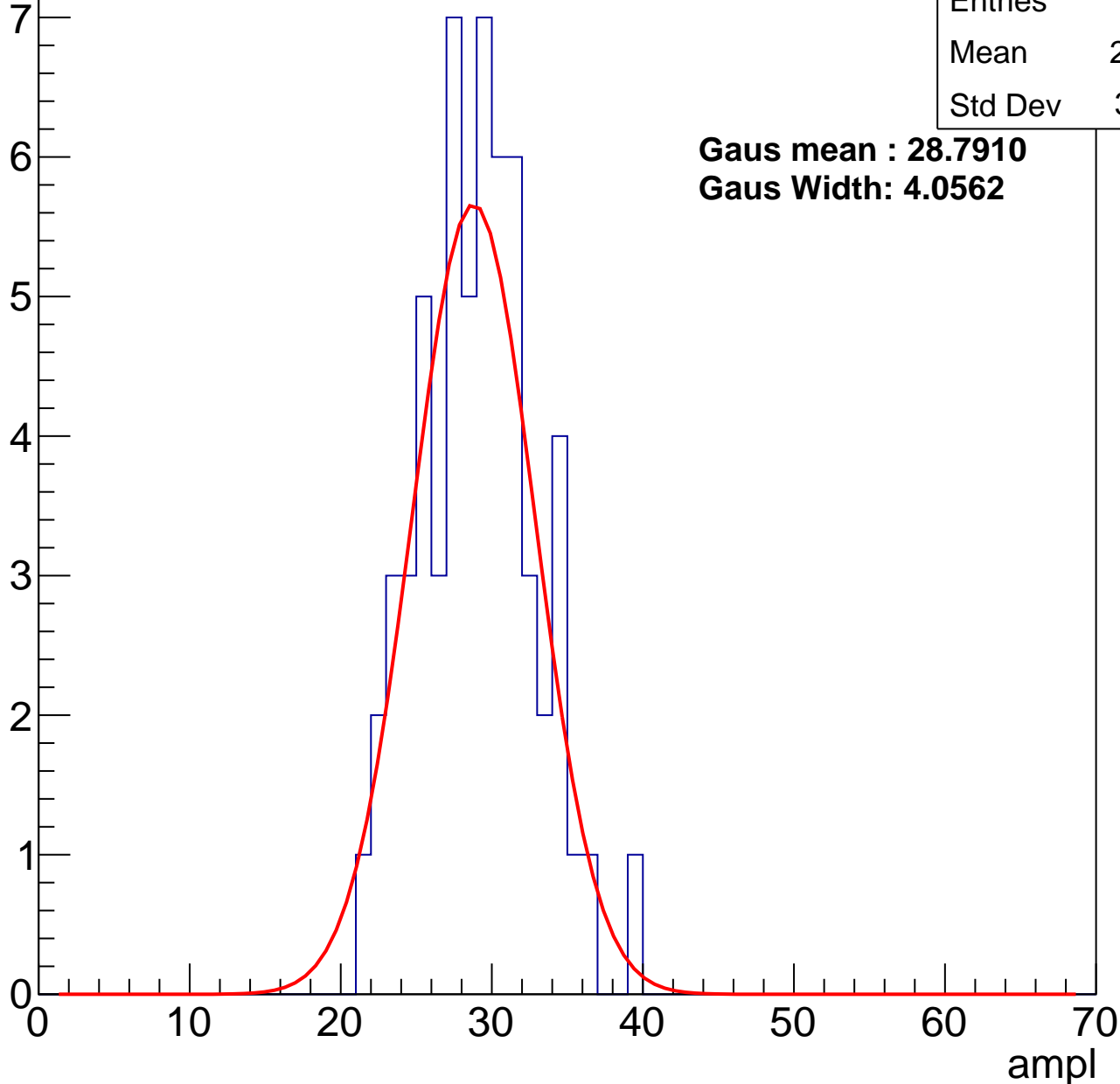
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.58
Std Dev	3.761

Gaus mean : 28.7910

Gaus Width: 4.0562



B1L102S, U10-ch127, adc1

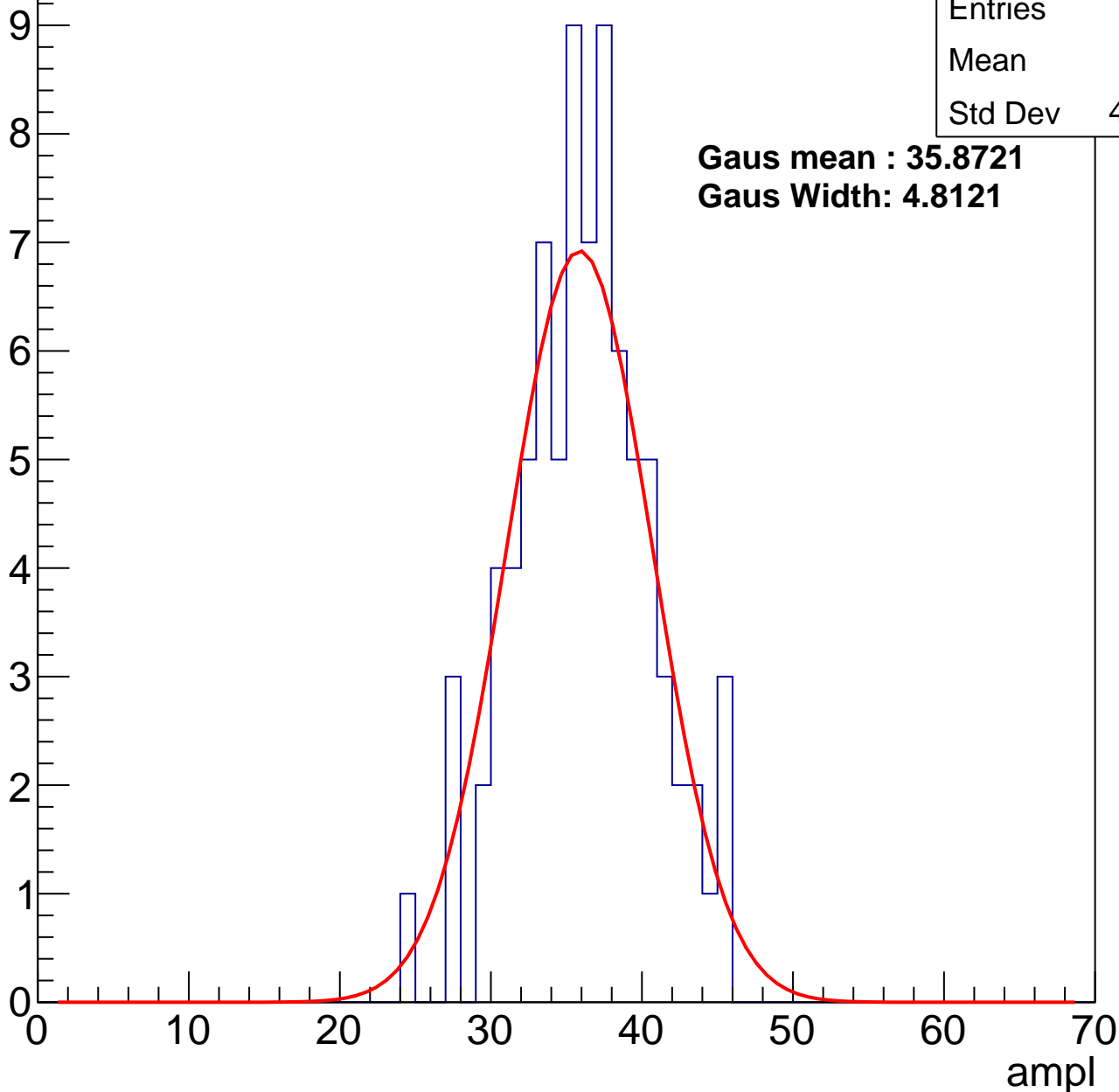
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	35.7
Std Dev	4.395

Gaus mean : 35.8721

Gaus Width: 4.8121

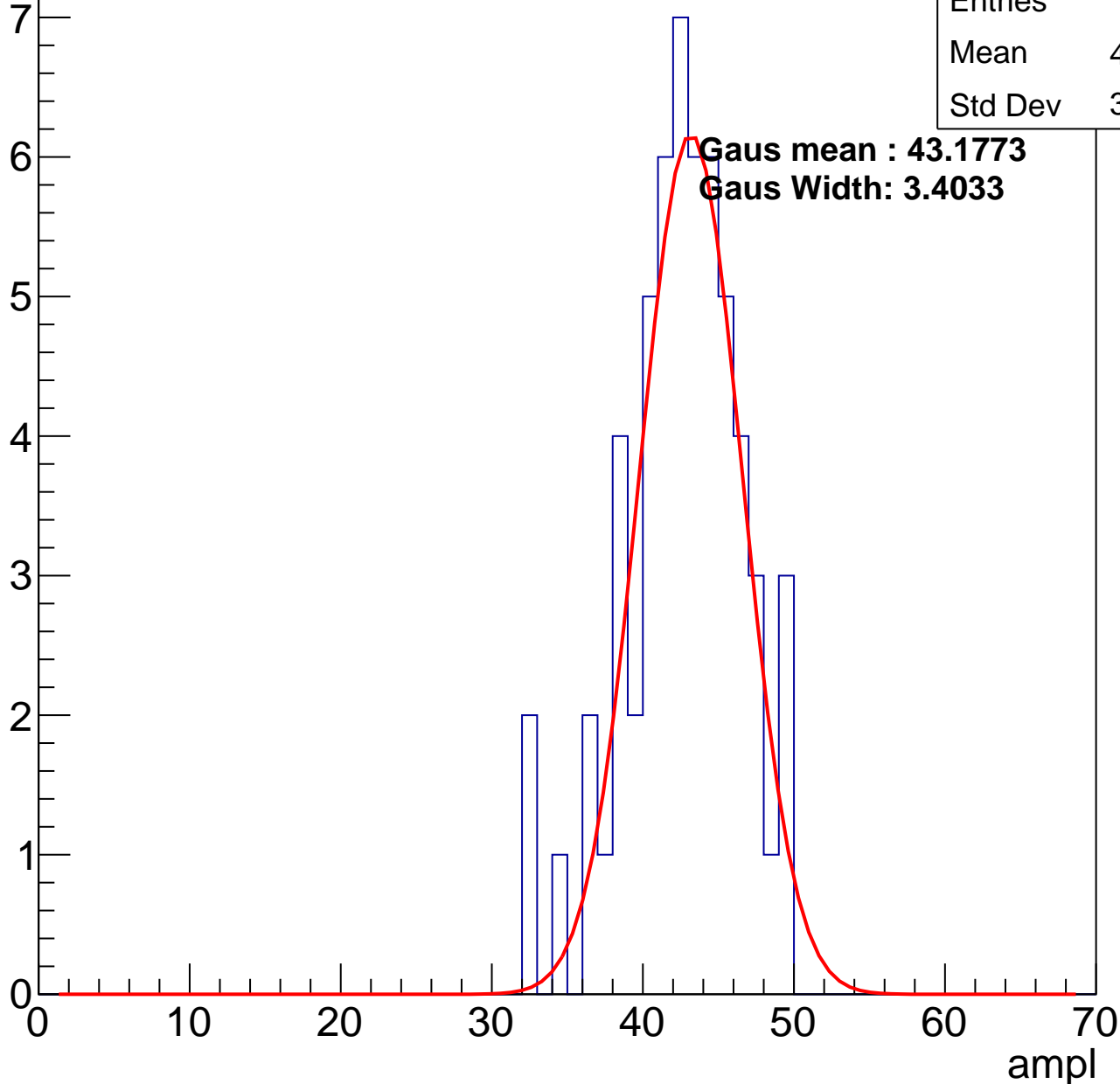


B1L102S, U10-ch127, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.14
Std Dev	3.862

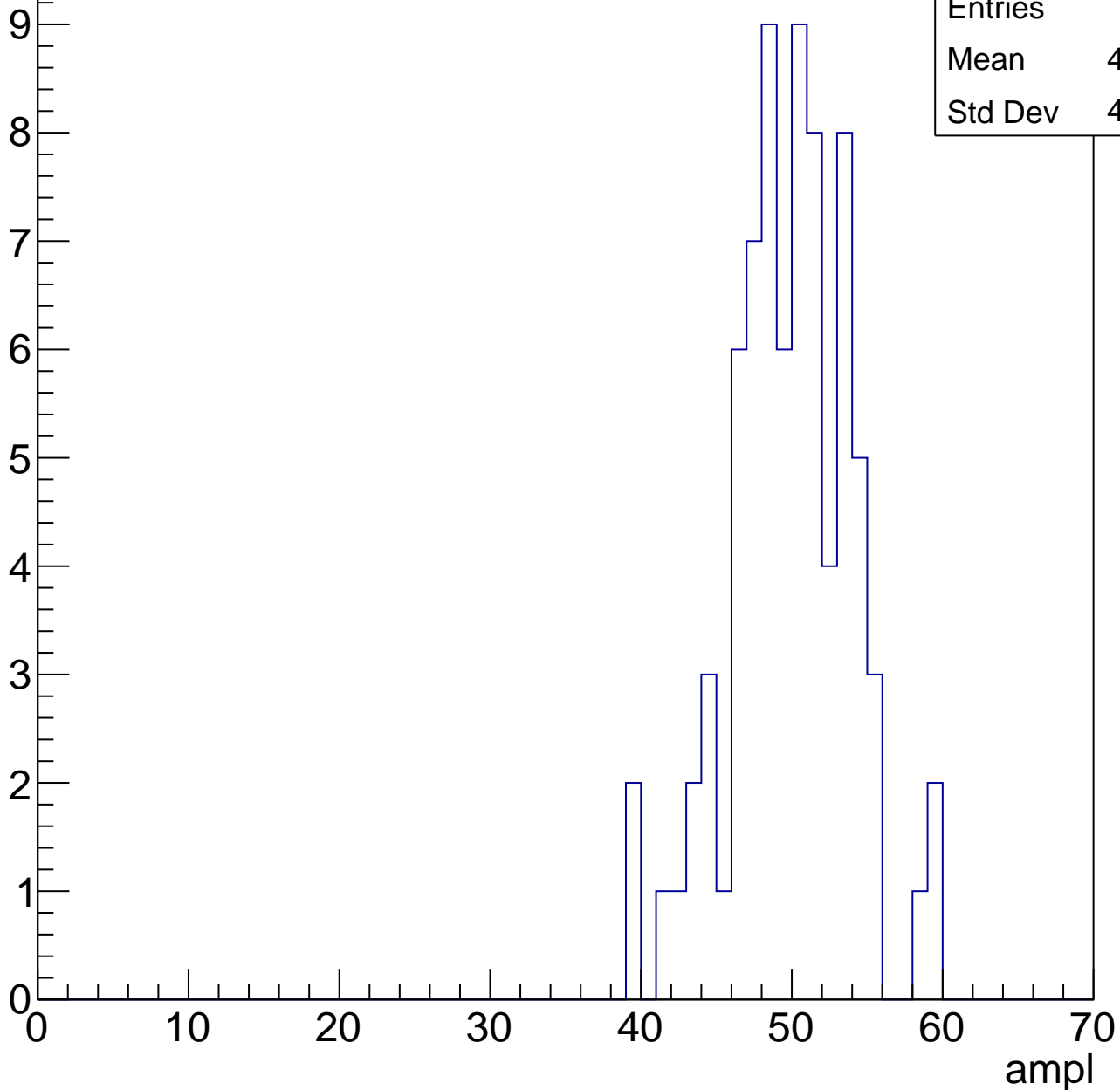


B1L102S, U10-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	49.44
Std Dev	4.037

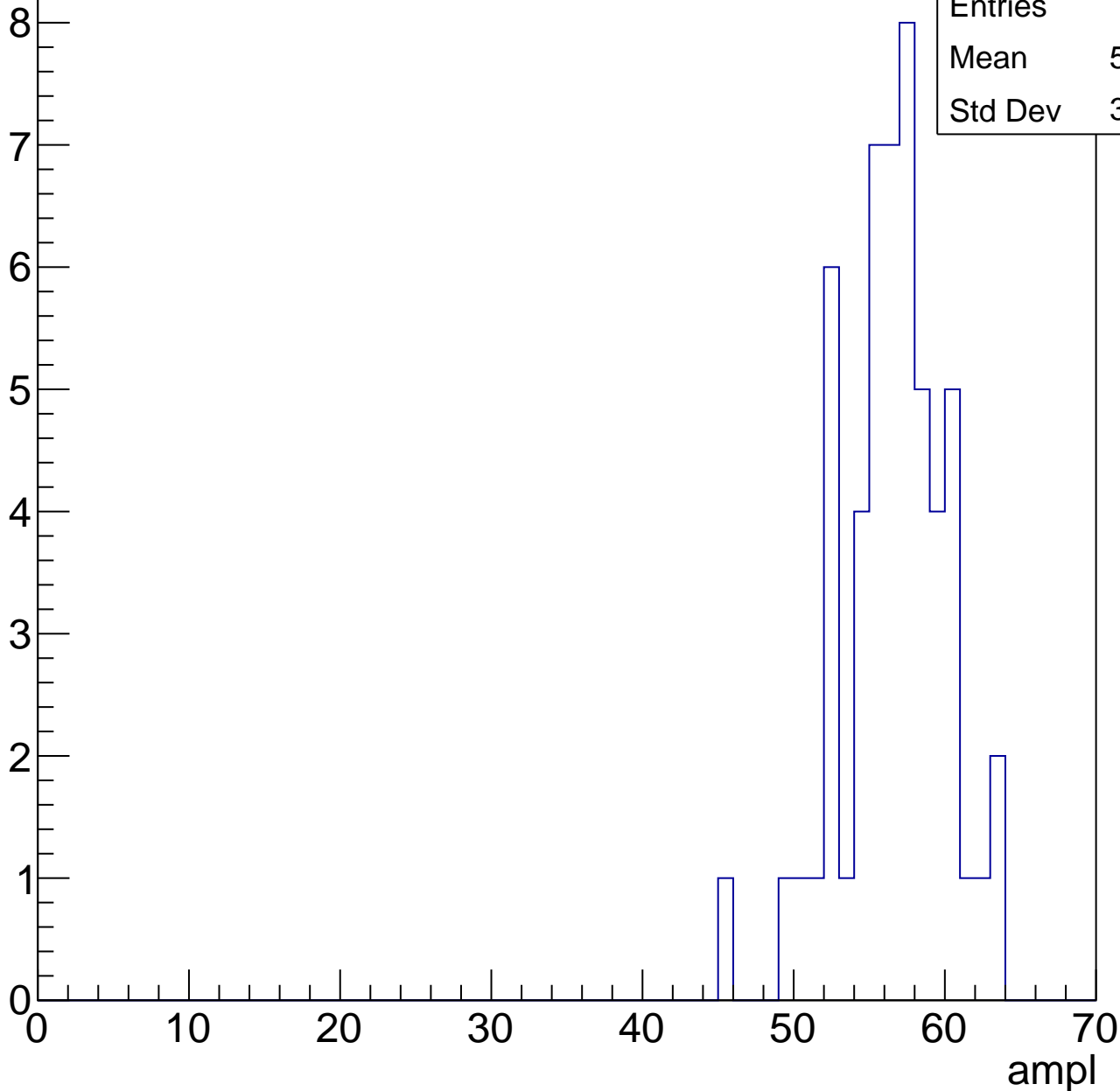


B1L102S, U10-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.07
Std Dev	3.469

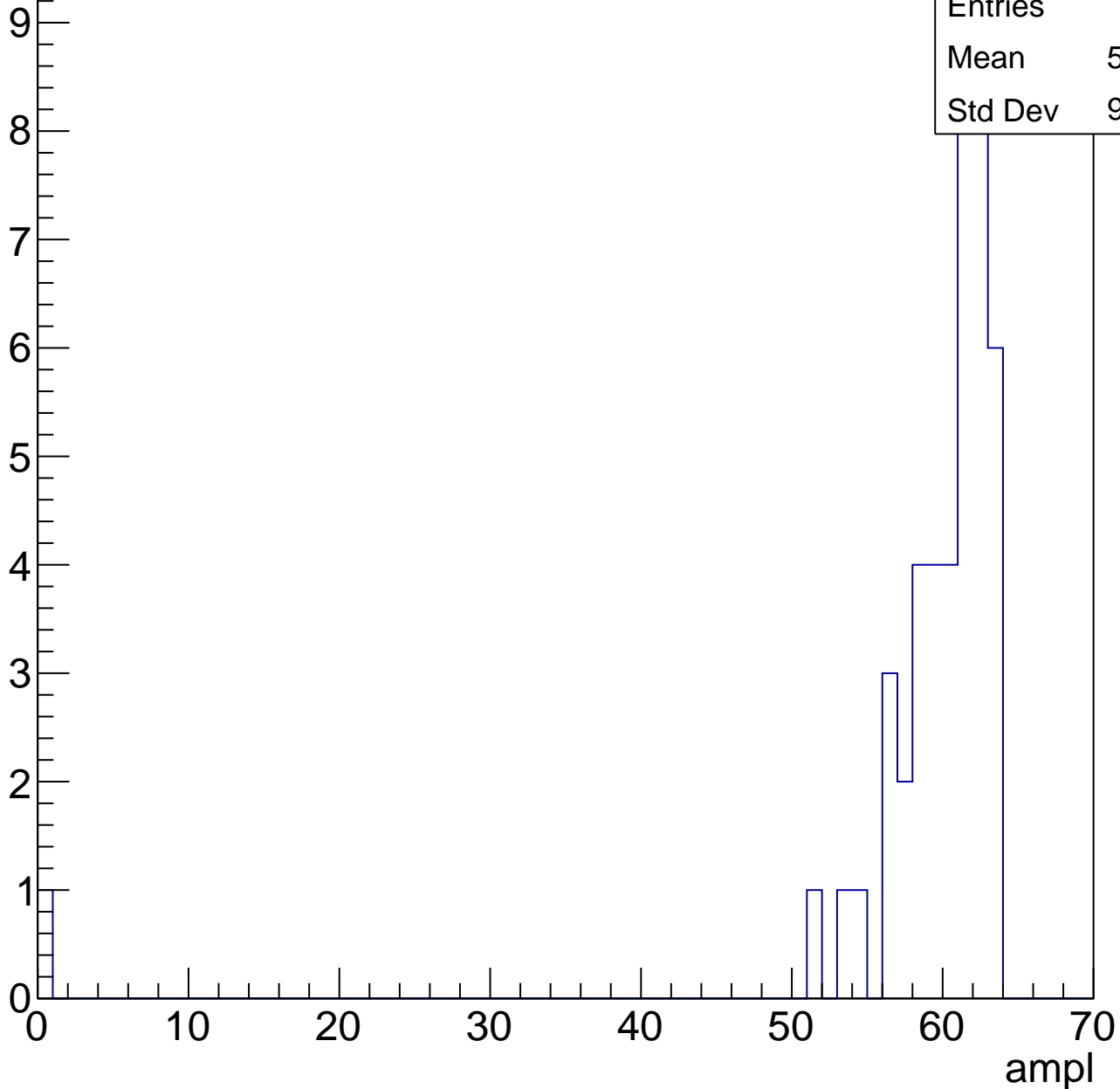


B1L102S, U10-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

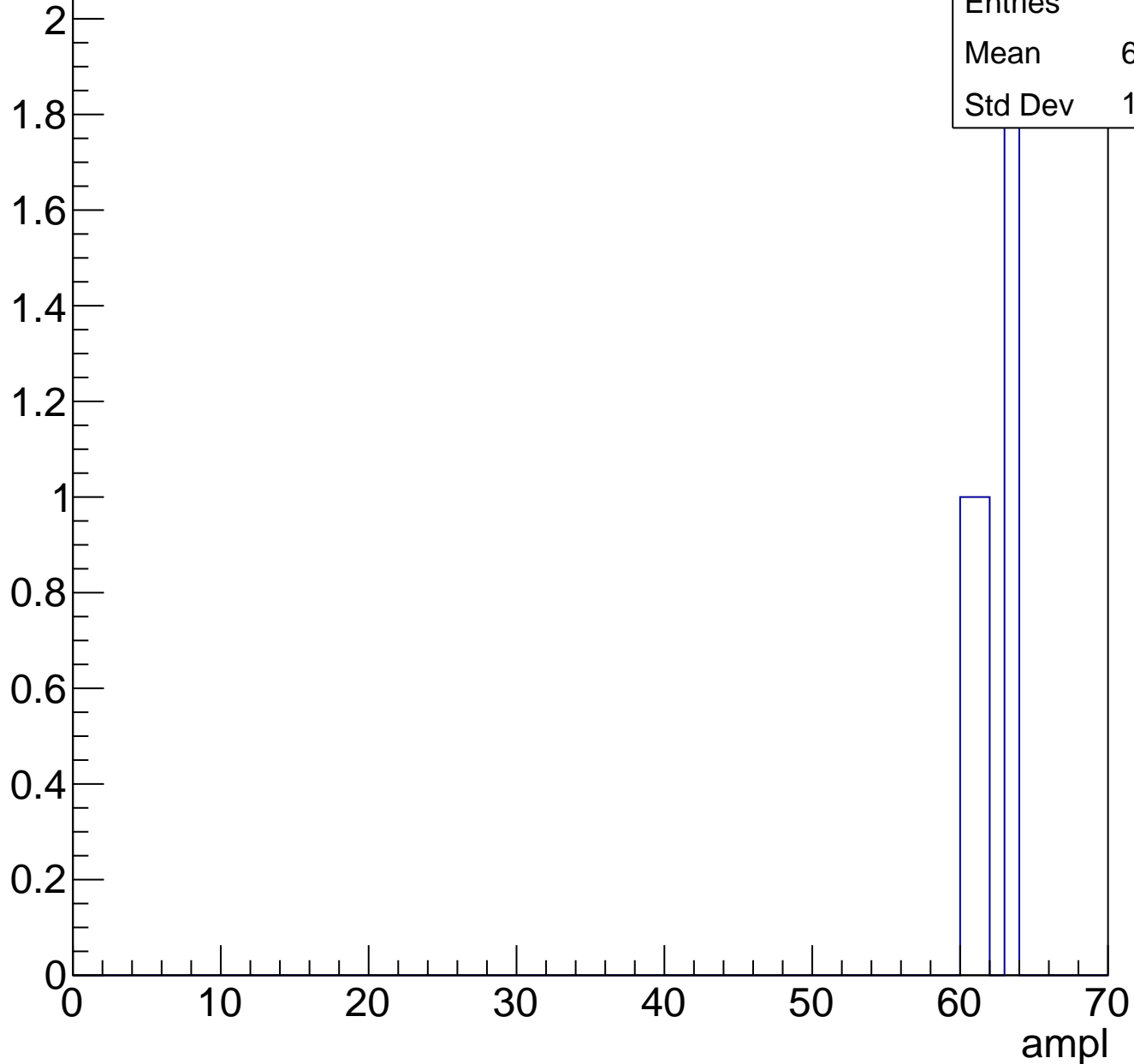
Entries	44
Mean	58.45
Std Dev	9.348



B1L102S, U10-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

