

B1L101S, U6-ch0, adc0

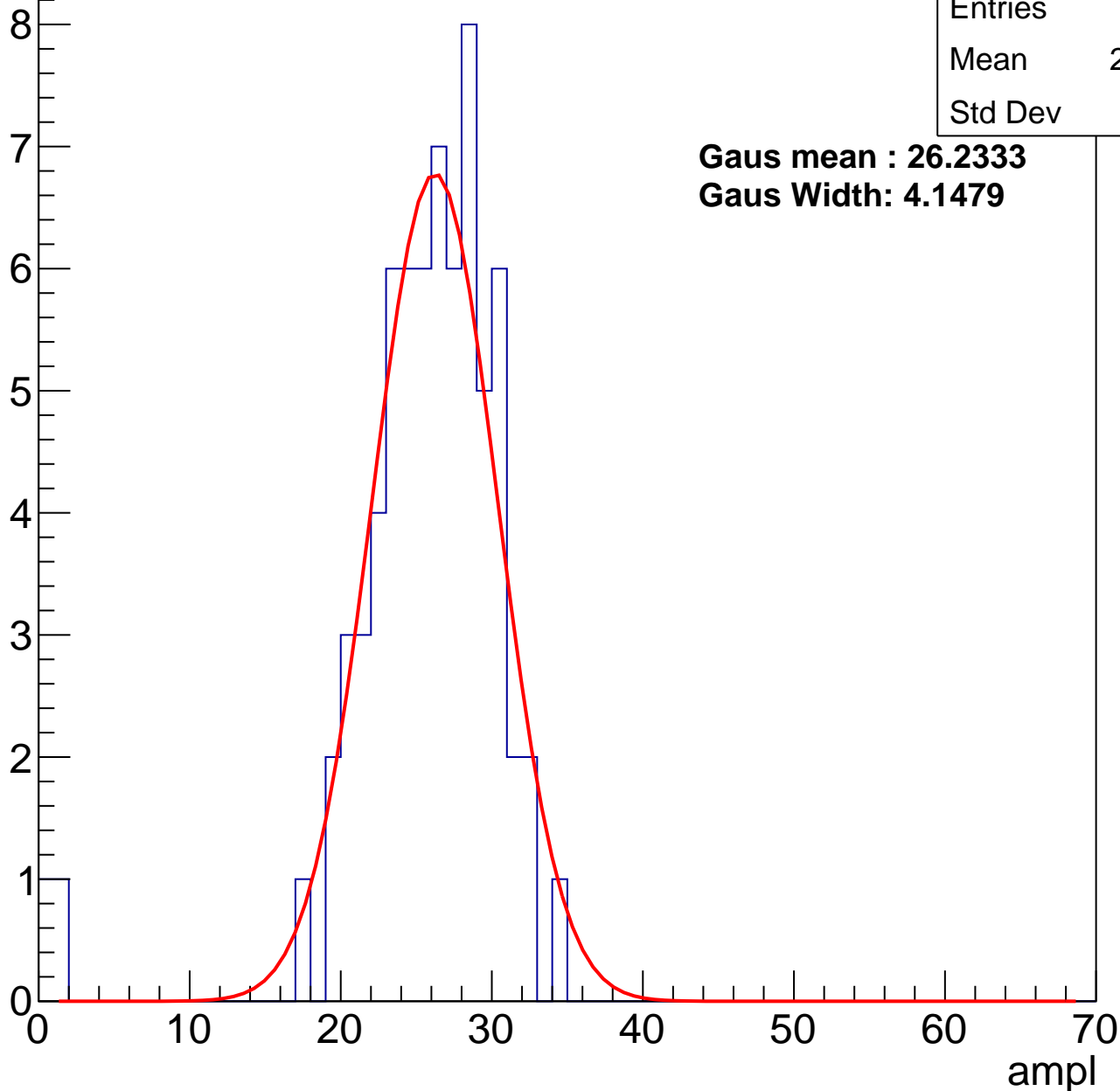
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	25.03
Std Dev	5.49

Gaus mean : 26.2333

Gaus Width: 4.1479



B1L101S, U6-ch0, adc1

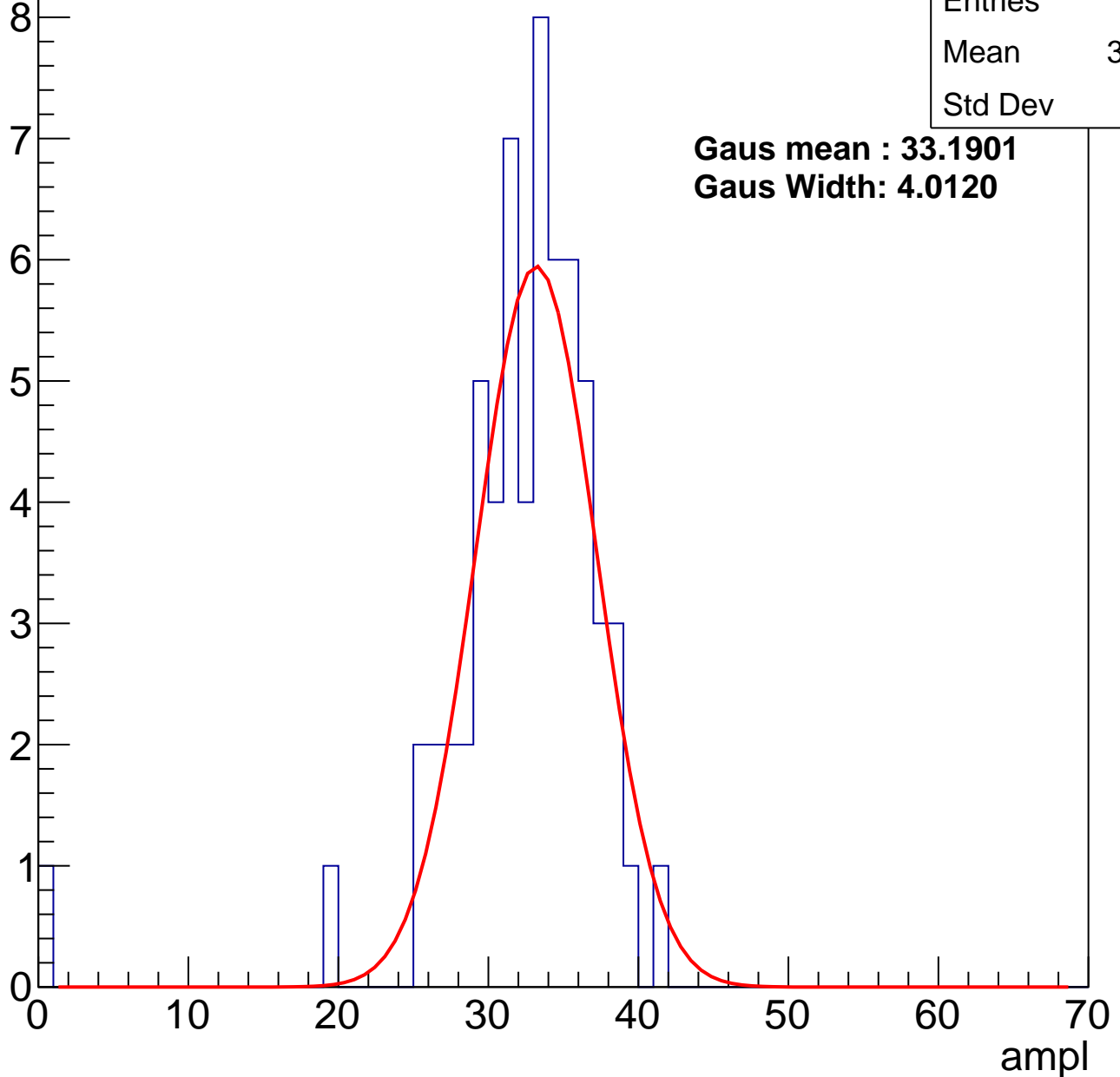
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.81
Std Dev	5.62

Gaus mean : 33.1901

Gaus Width: 4.0120



B1L101S, U6-ch0, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	39.34
Std Dev	3.822

Gaus mean : 39.8494

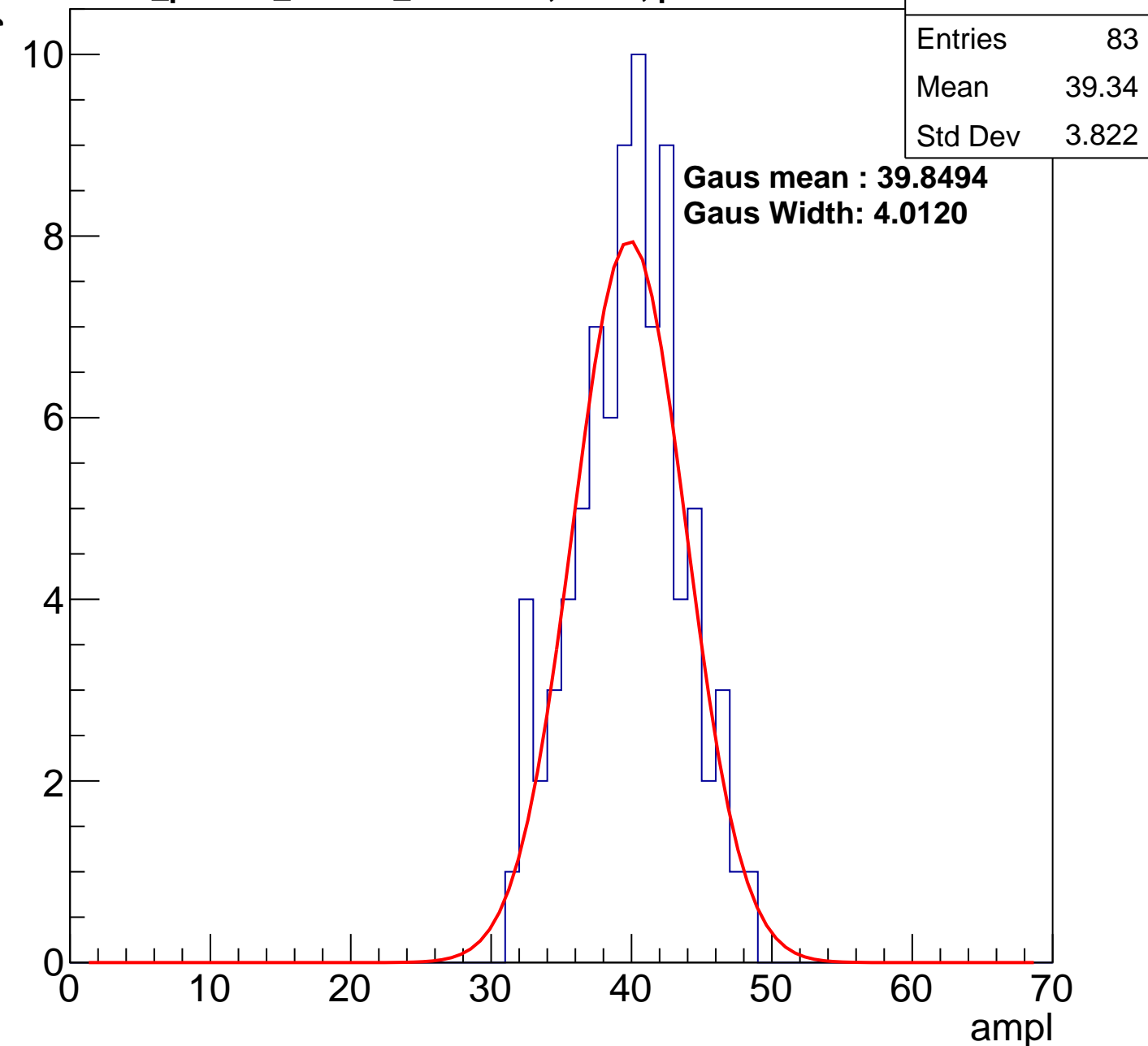
Gaus Width: 4.0120

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

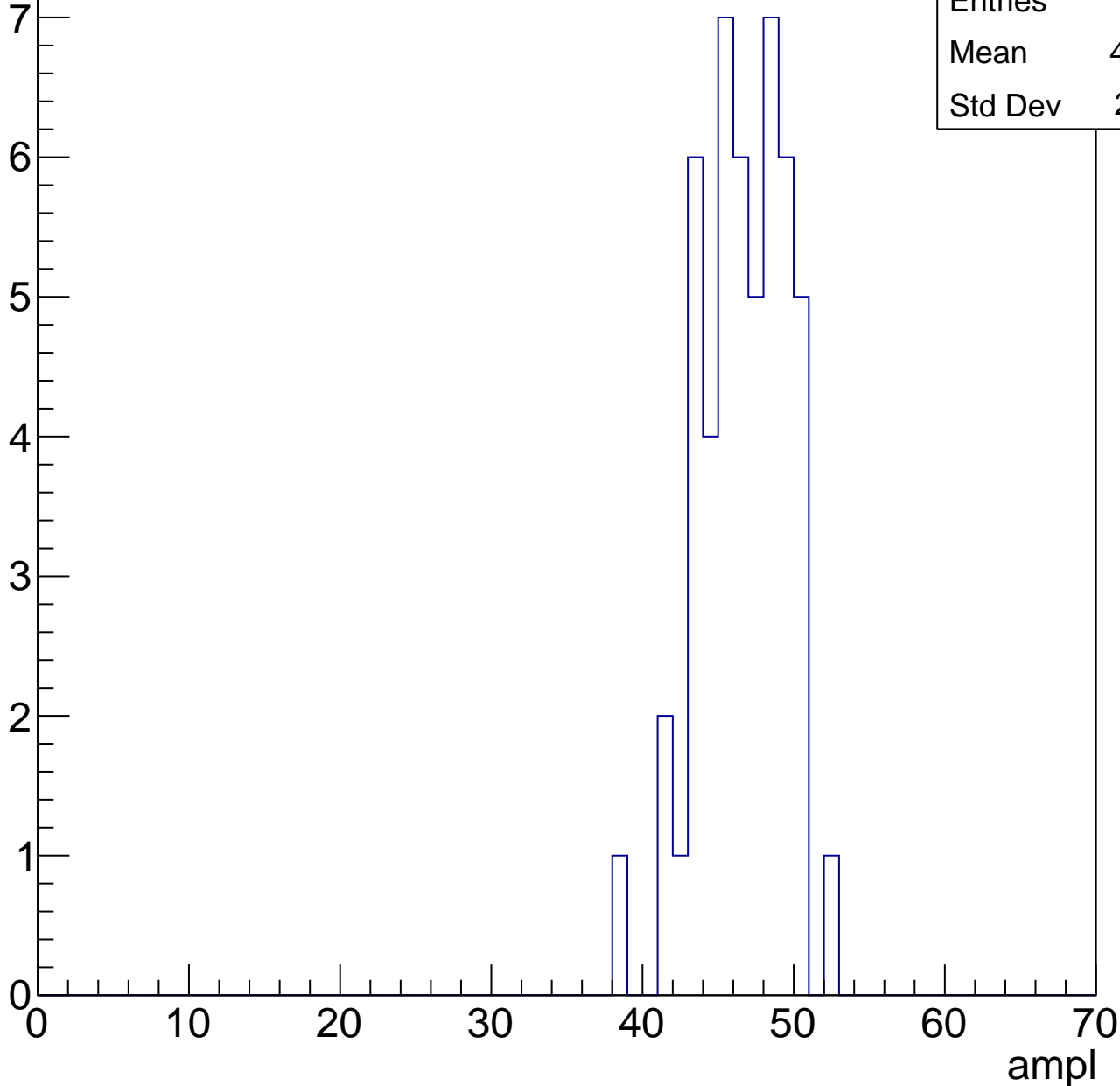


B1L101S, U6-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

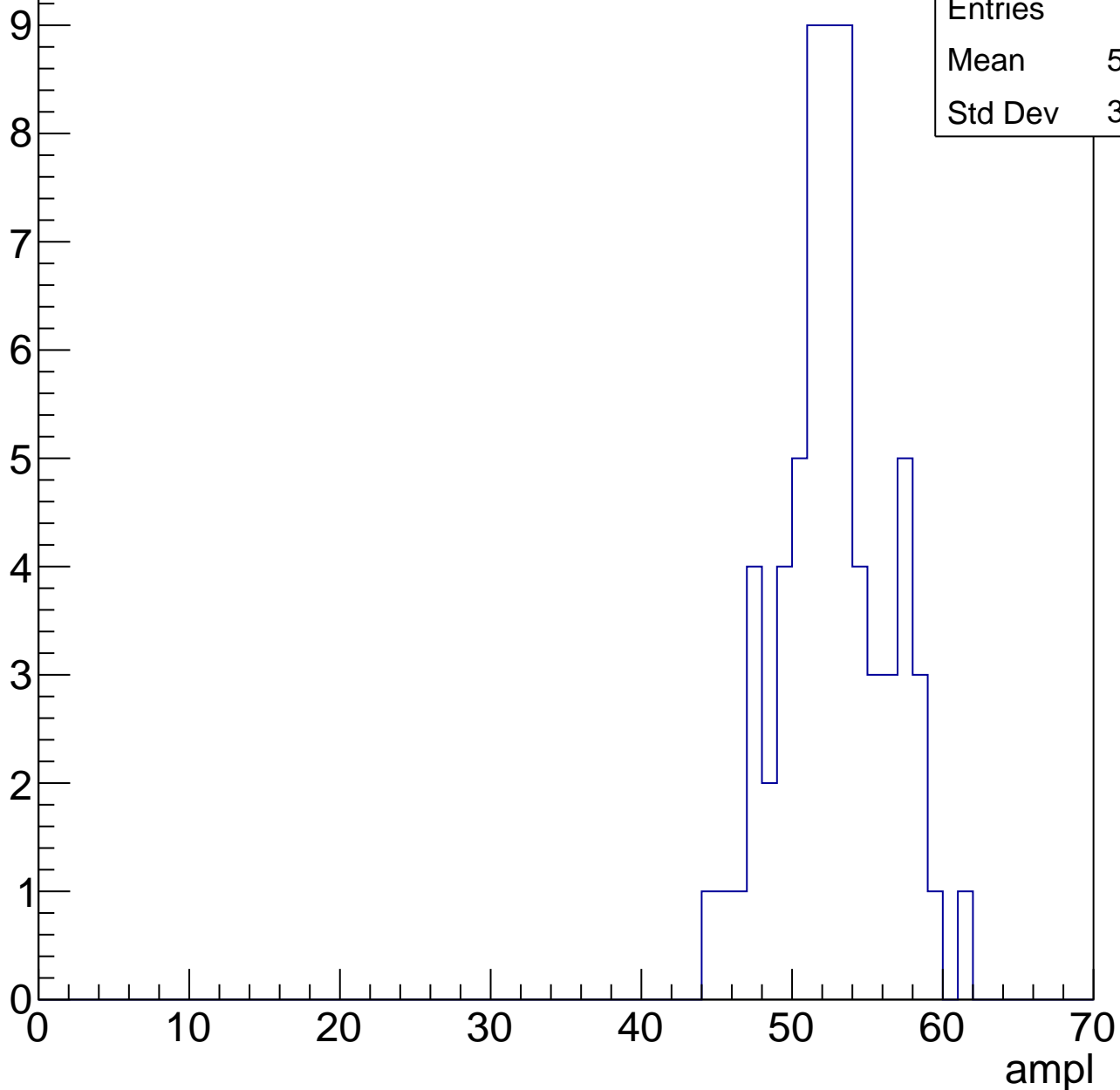
Entries	51
Mean	46.16
Std Dev	2.831



B1L101S, U6-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

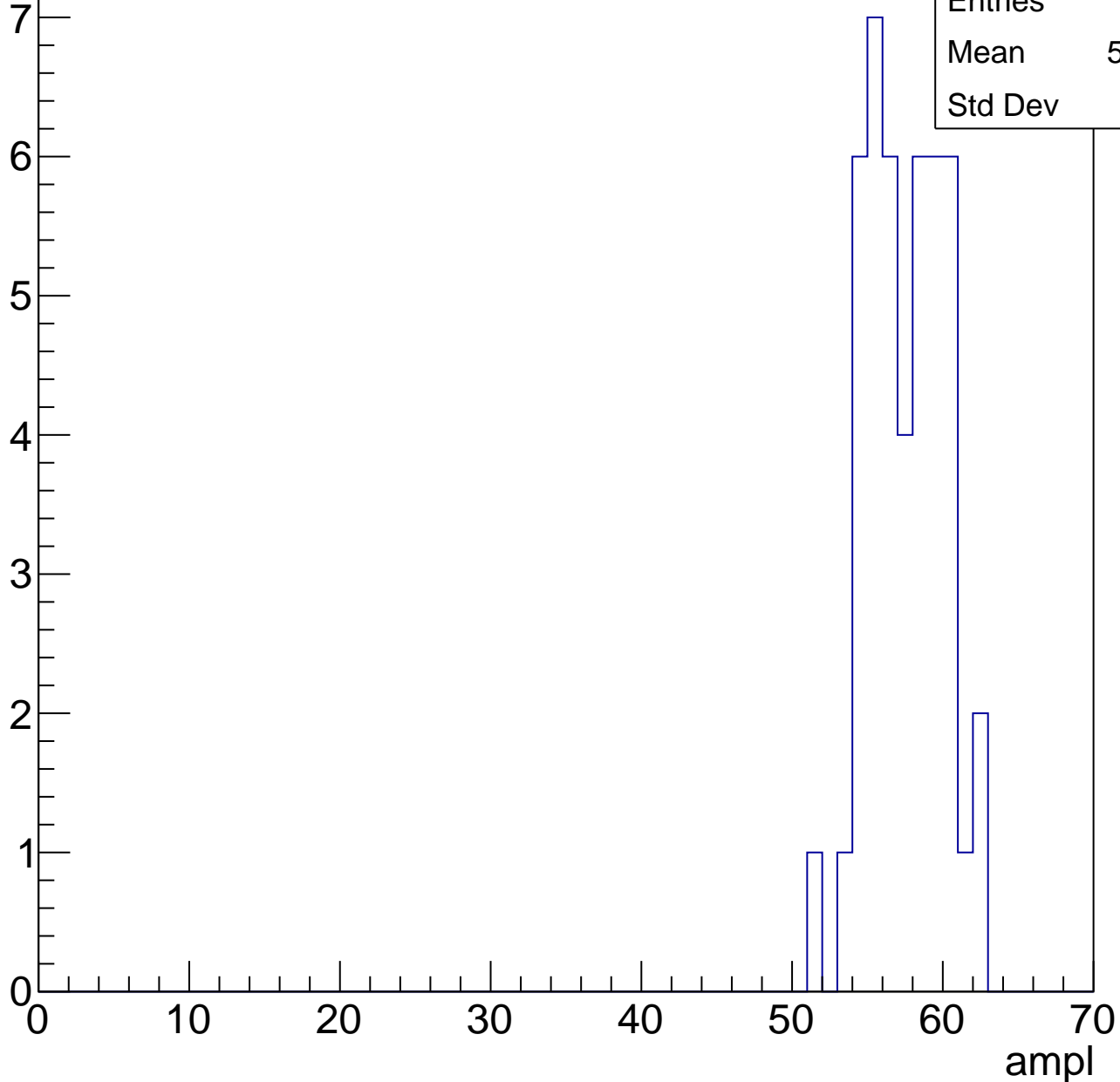


B1L101S, U6-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.04
Std Dev	2.51

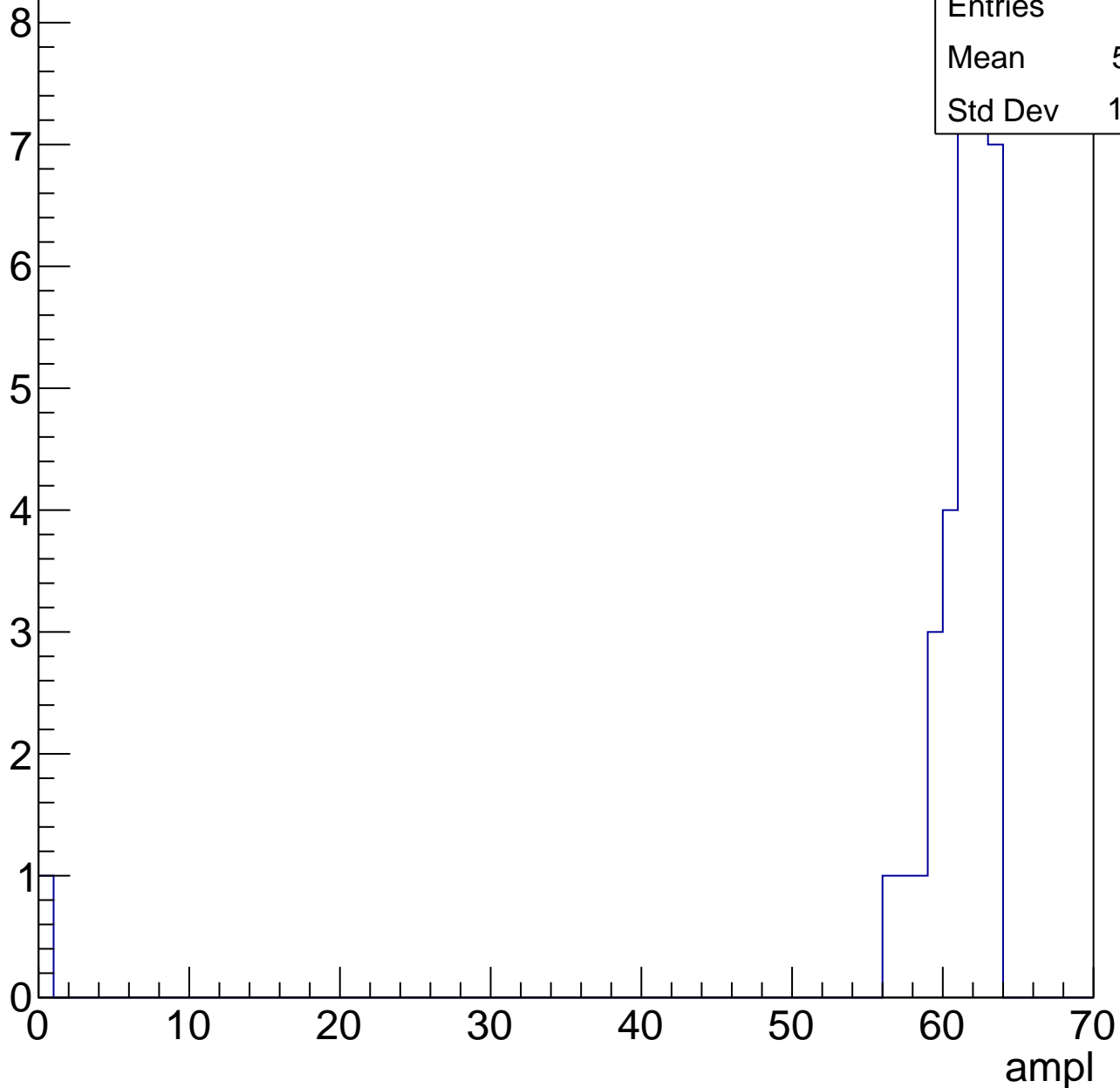


B1L101S, U6-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.21
Std Dev	10.45



B1L101S, U6-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch1, adc0

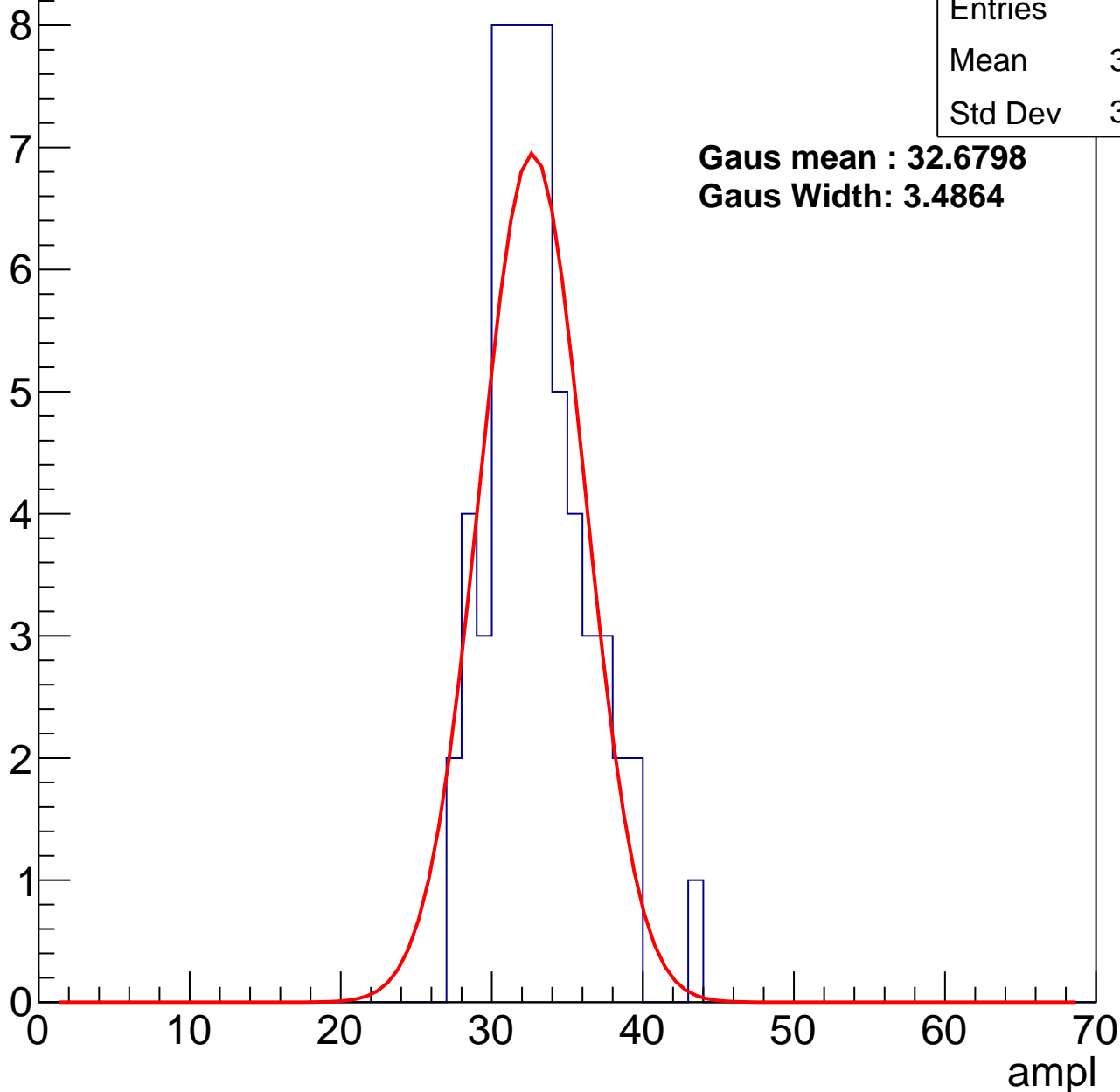
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	32.57
Std Dev	3.236

Gaus mean : 32.6798

Gaus Width: 3.4864



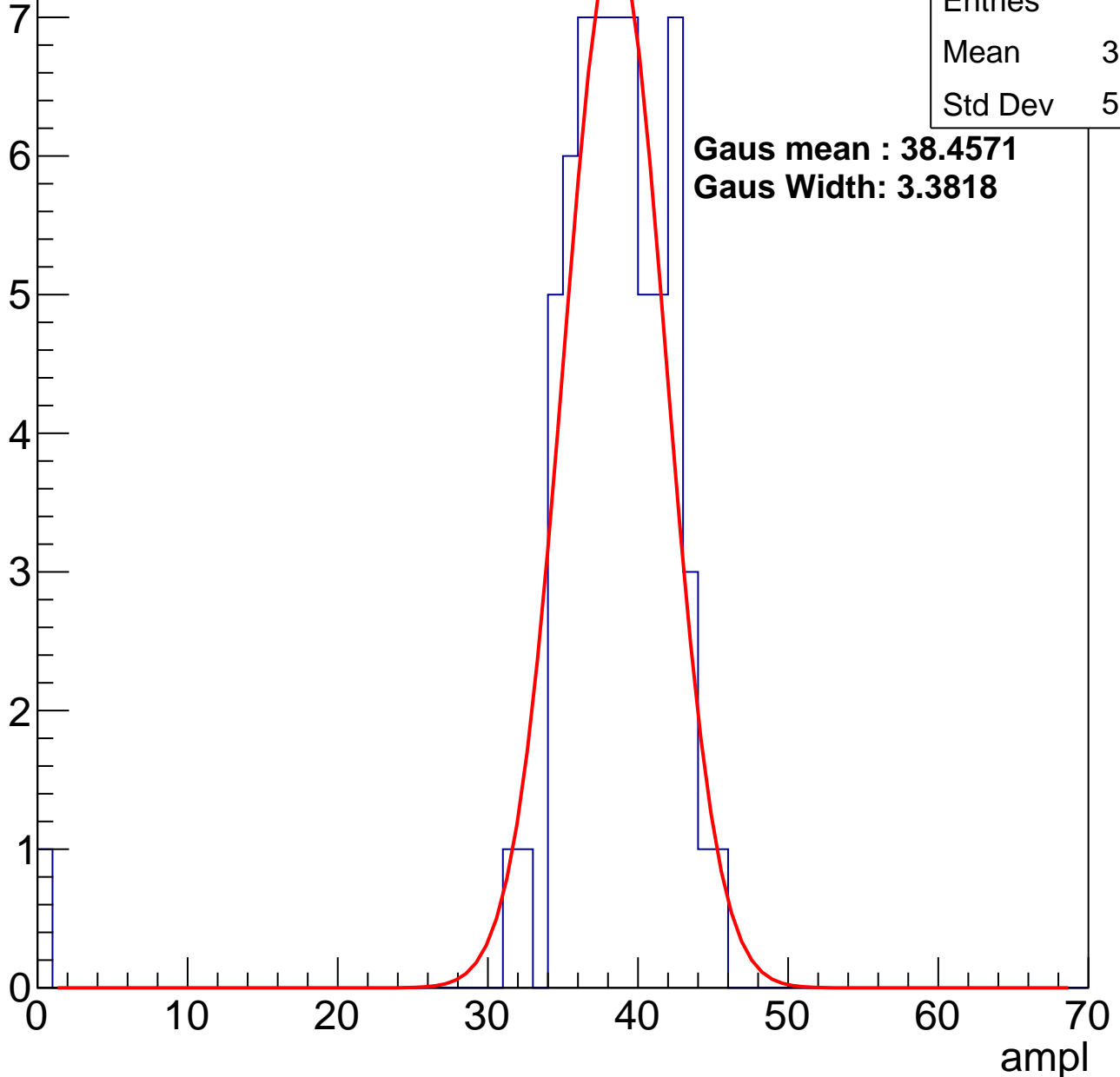
B1L101S, U6-ch1, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.66
Std Dev	5.638

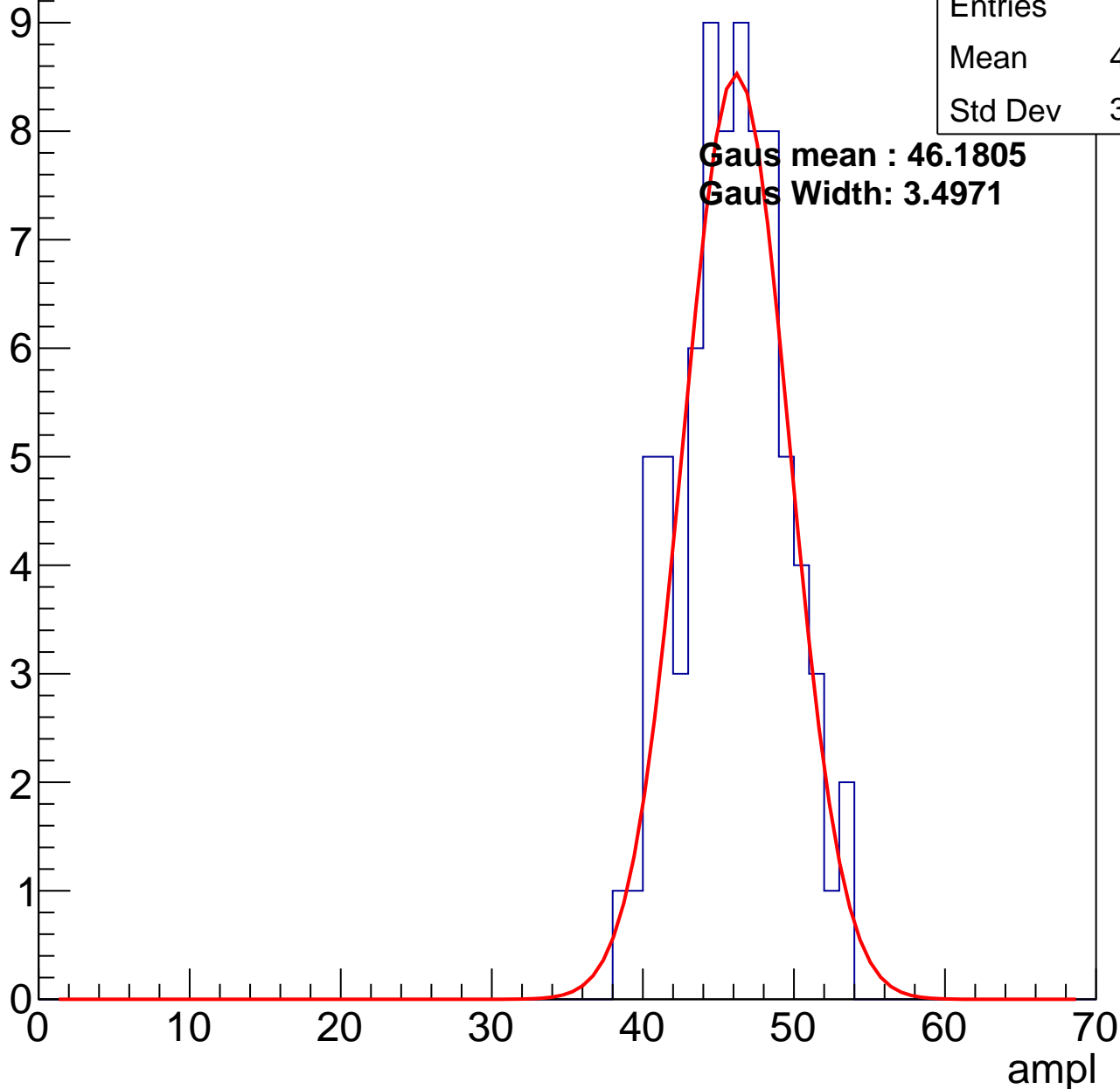
Gaus mean : 38.4571
Gaus Width: 3.3818



B1L101S, U6-ch1, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

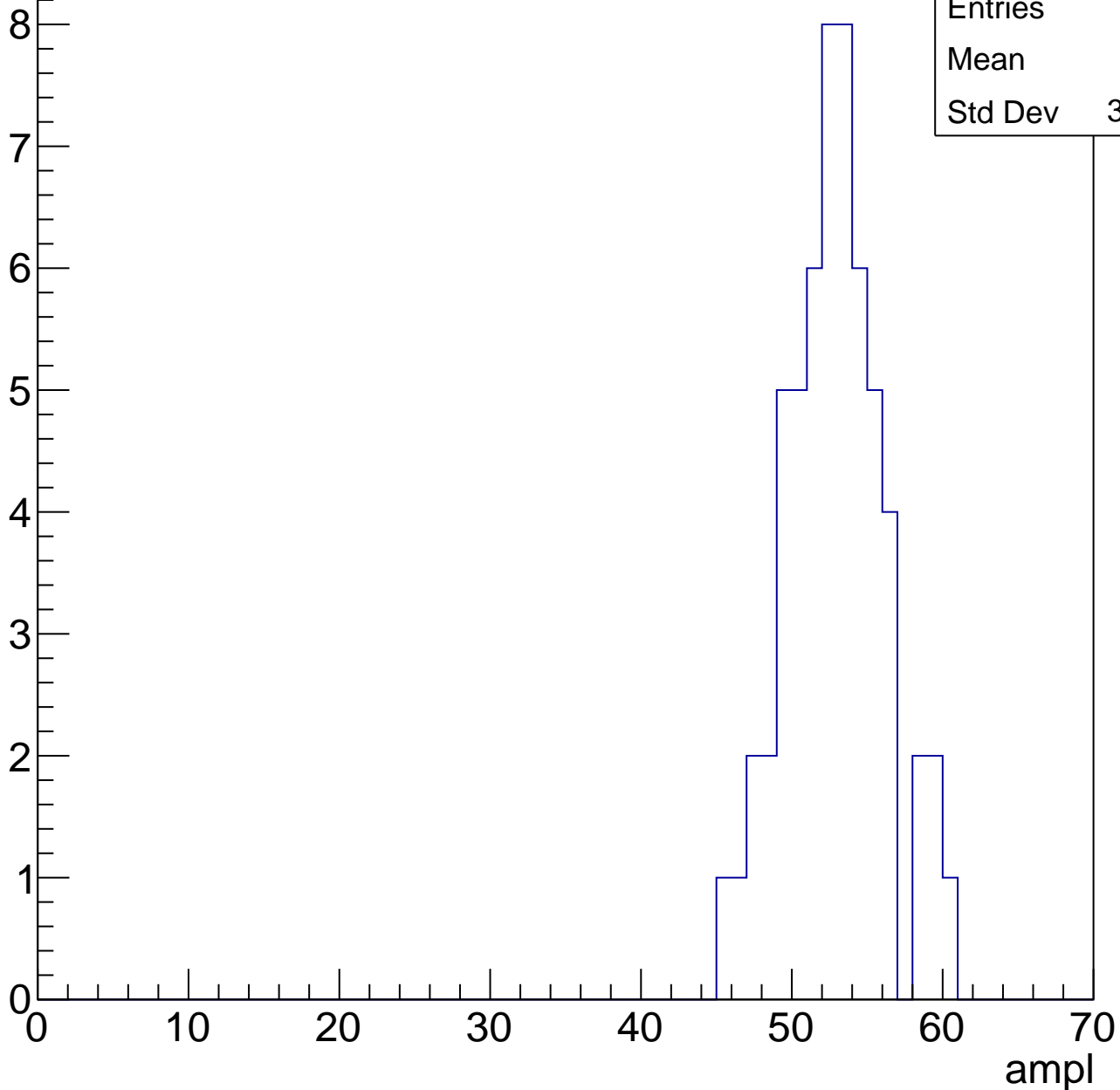


B1L101S, U6-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	52.4
Std Dev	3.227

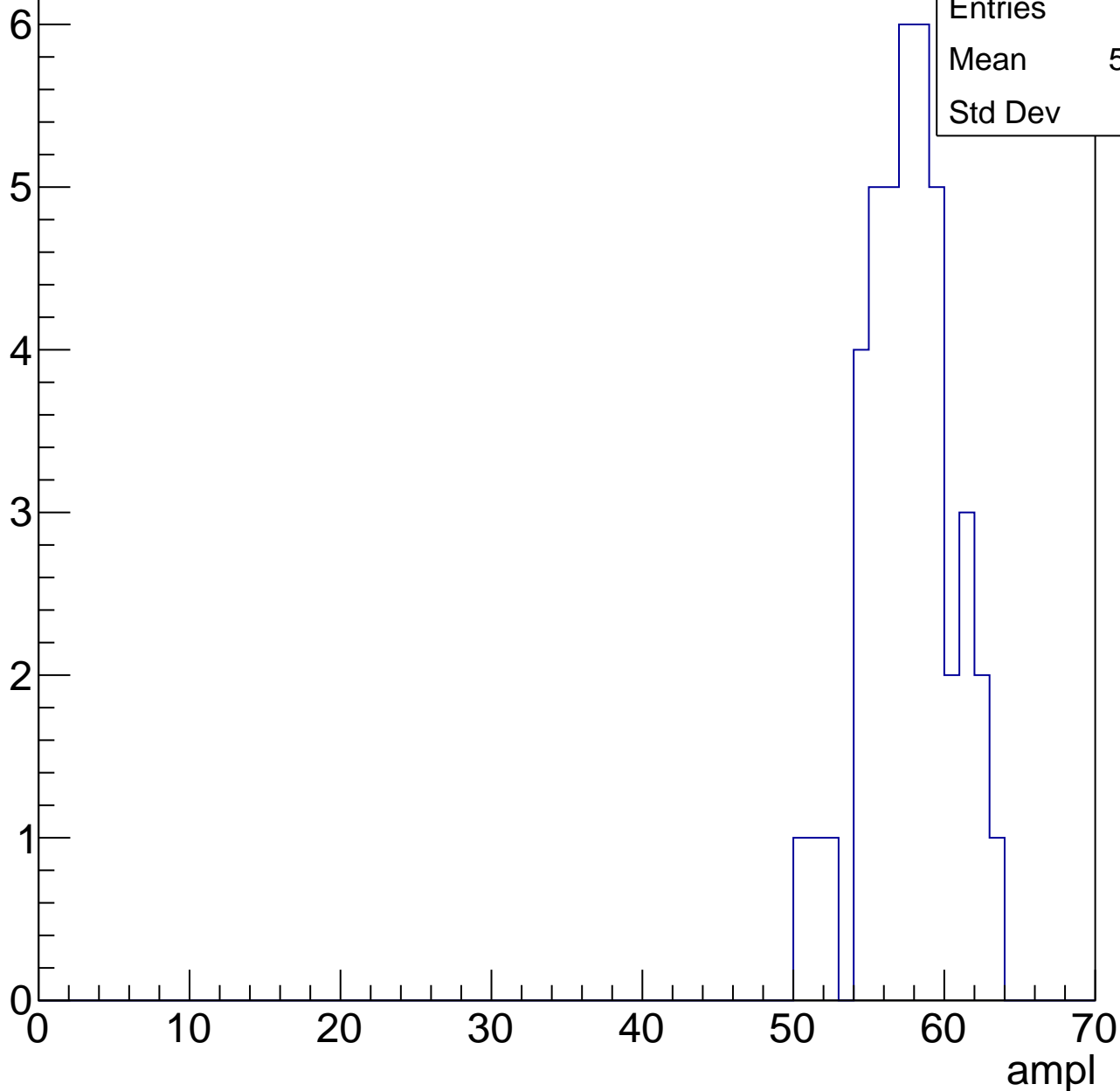


B1L101S, U6-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	57.12
Std Dev	2.88

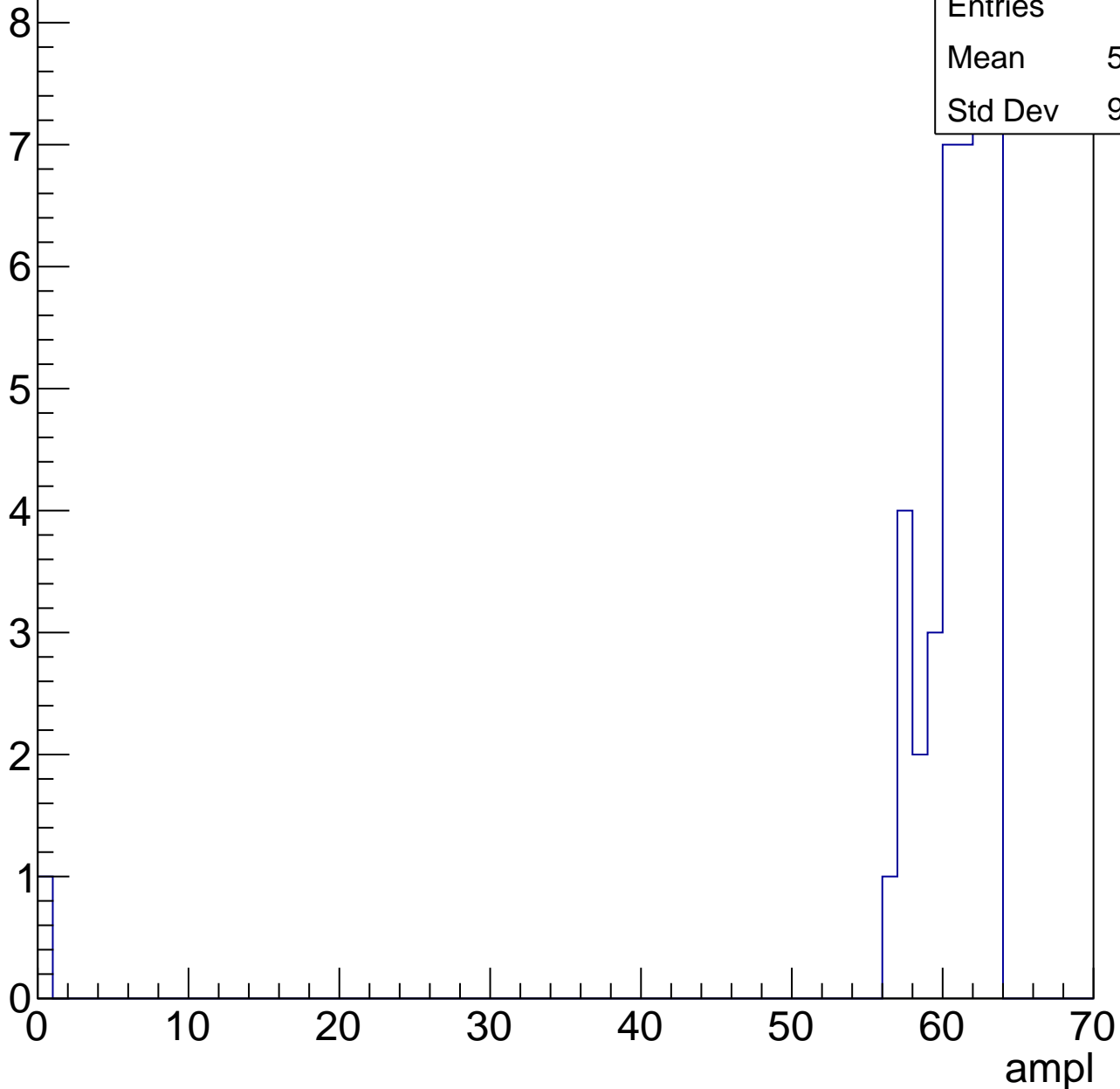


B1L101S, U6-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.12
Std Dev	9.554



B1L101S, U6-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch2, adc0

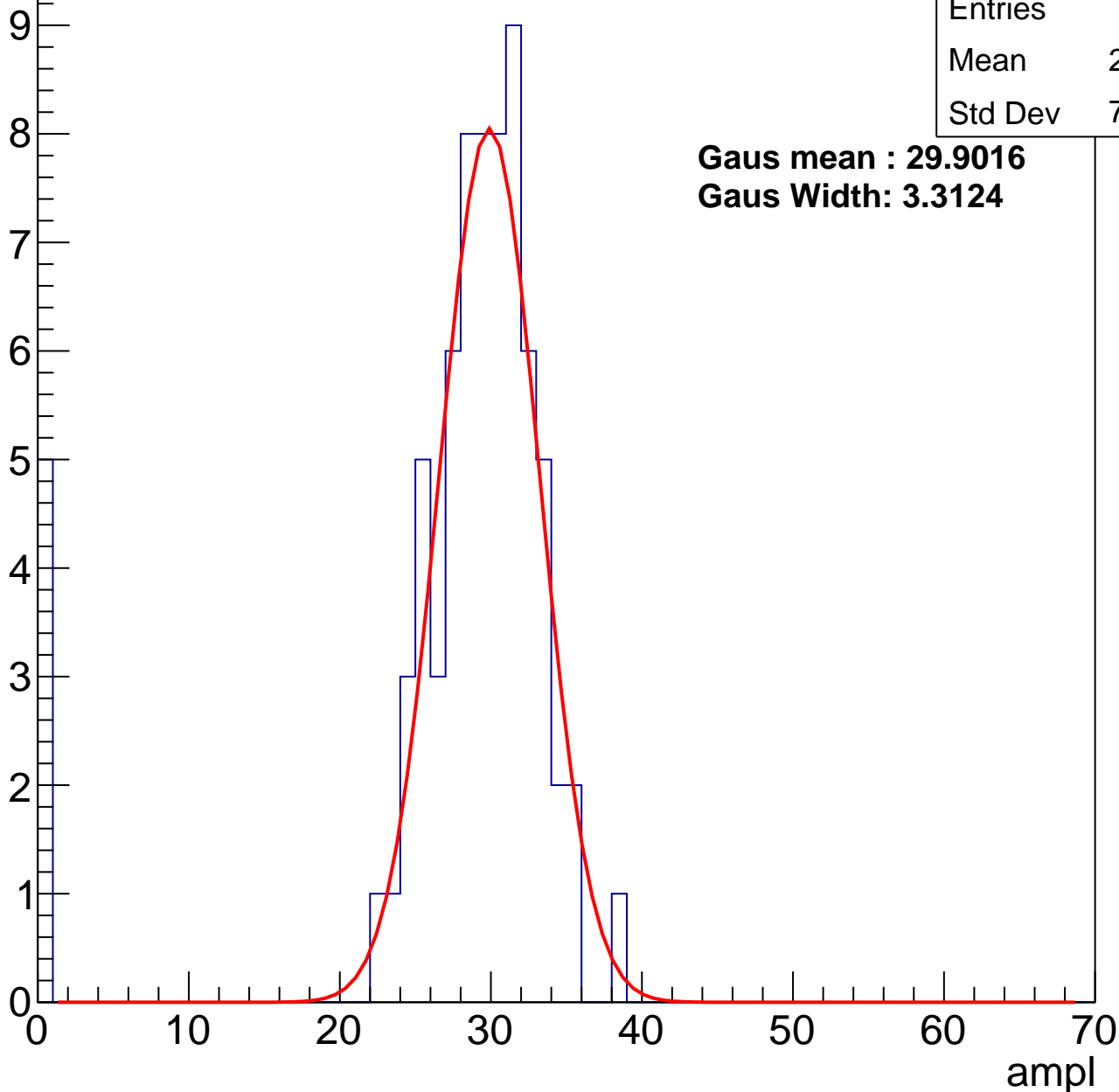
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.26
Std Dev	7.993

Gaus mean : 29.9016

Gaus Width: 3.3124



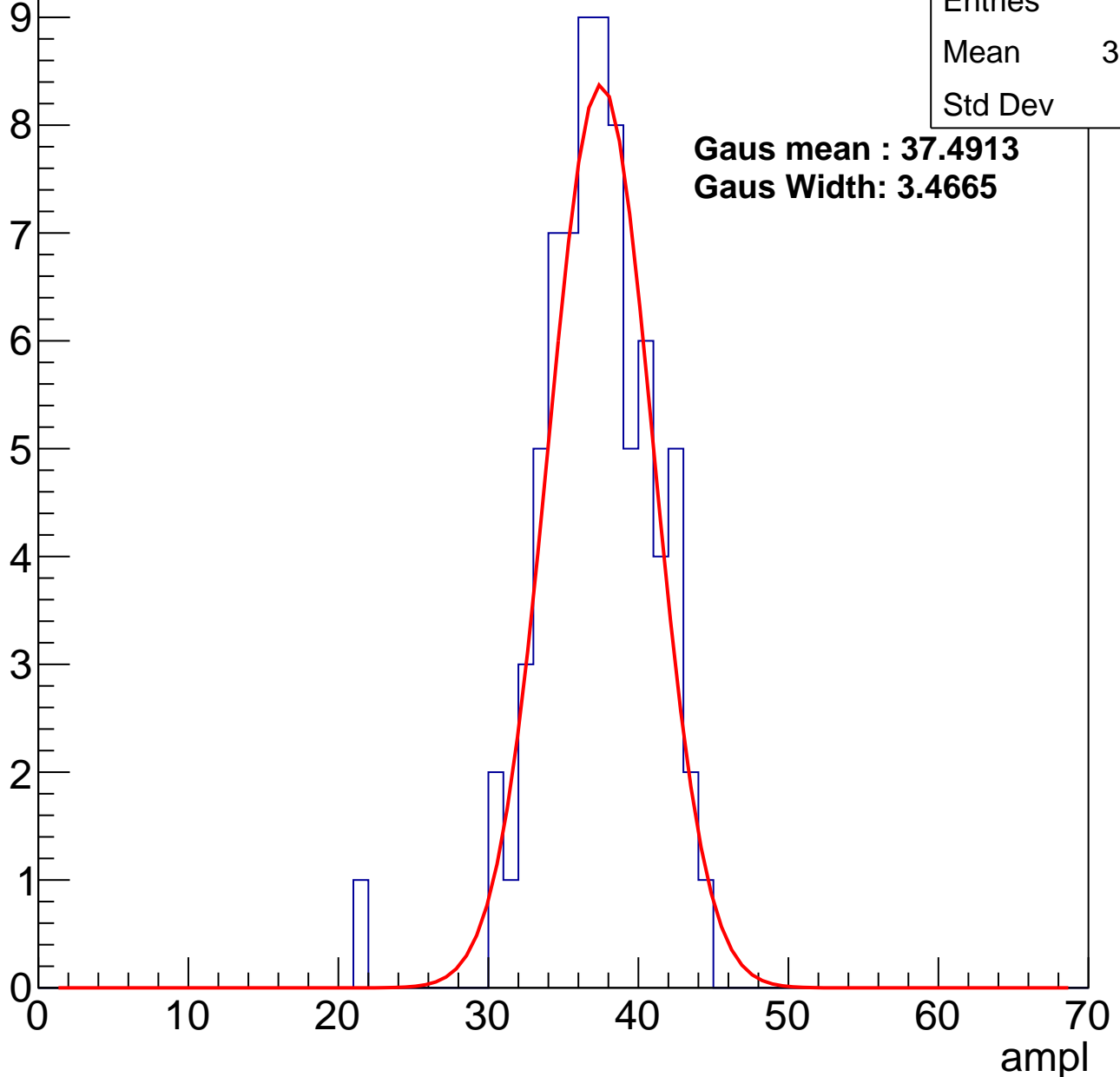
B1L101S, U6-ch2, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.75
Std Dev	3.71

Gaus mean : 37.4913
Gaus Width: 3.4665



B1L101S, U6-ch2, adc2

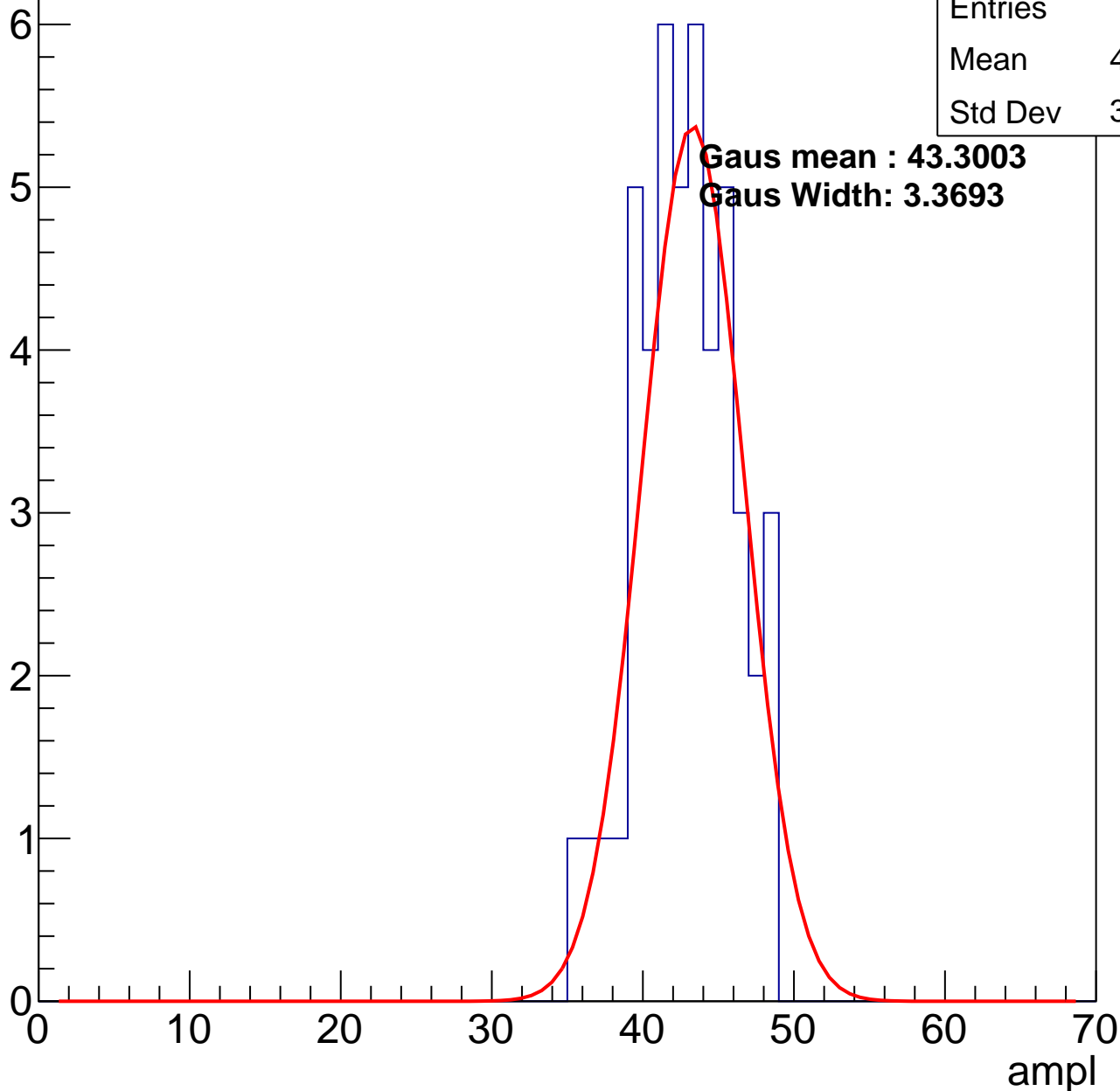
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	42.38
Std Dev	3.125

Gaus mean : 43.3003

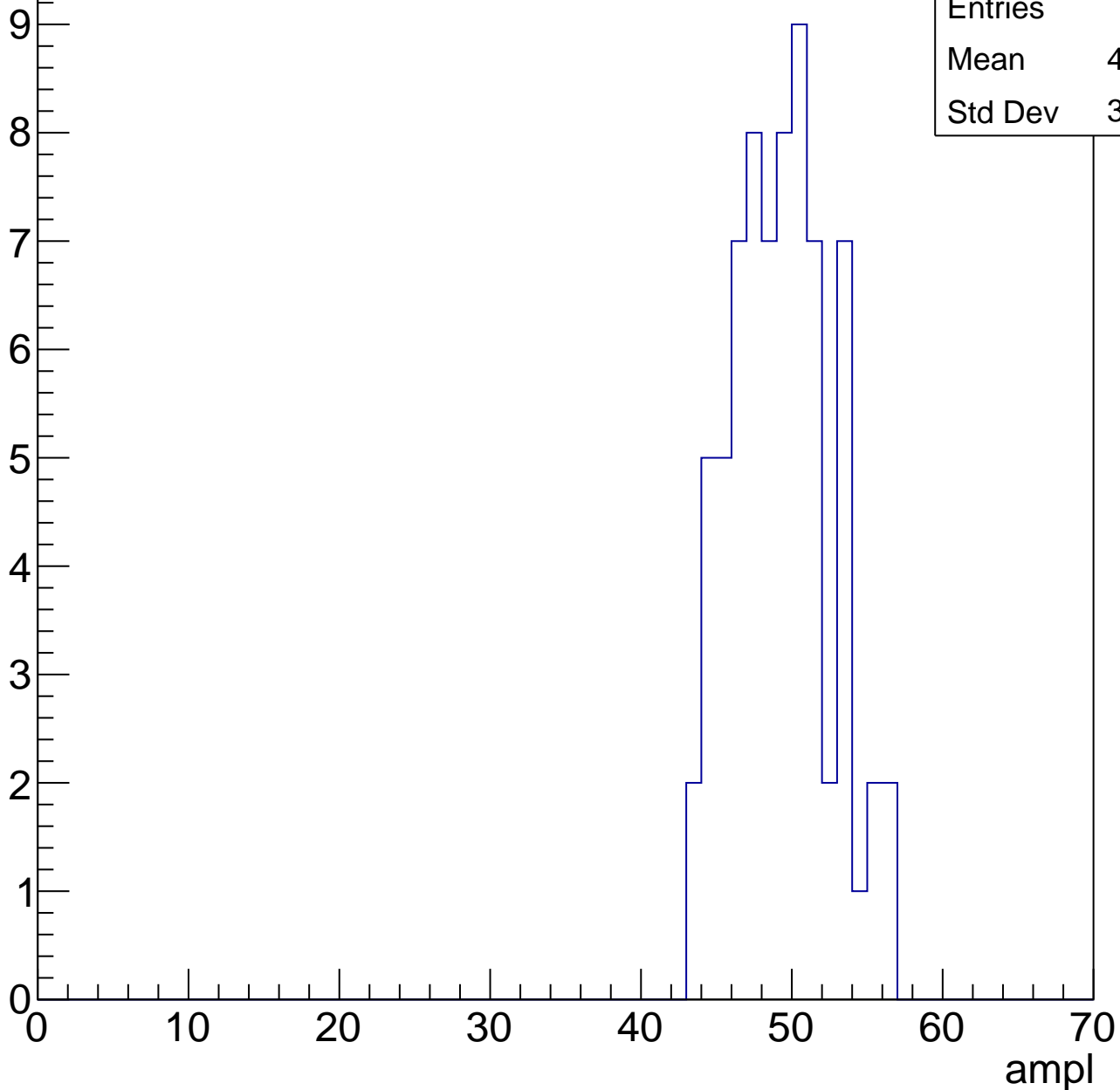
Gaus Width: 3.3693



B1L101S, U6-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

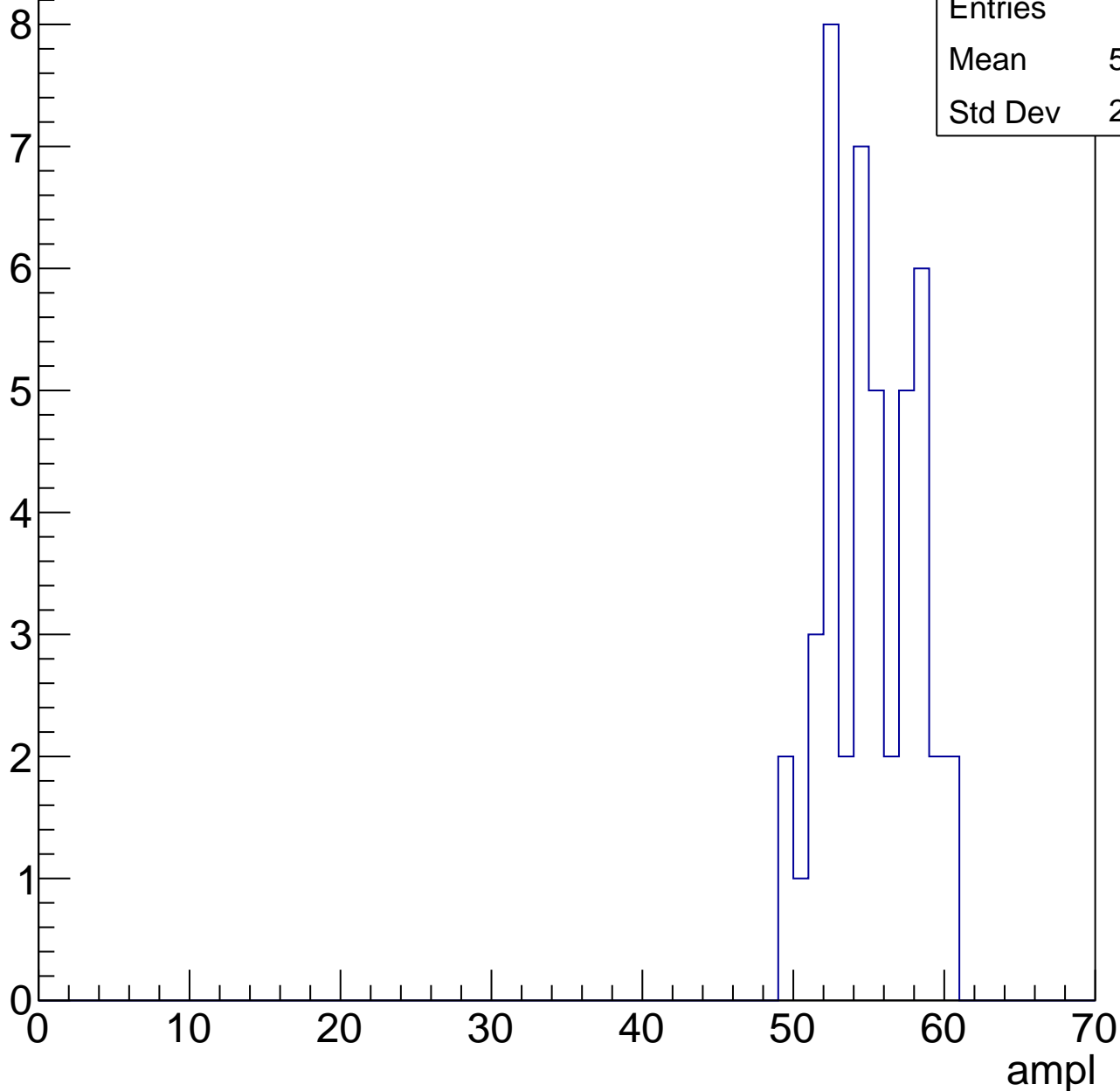


B1L101S, U6-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	54.64
Std Dev	2.922

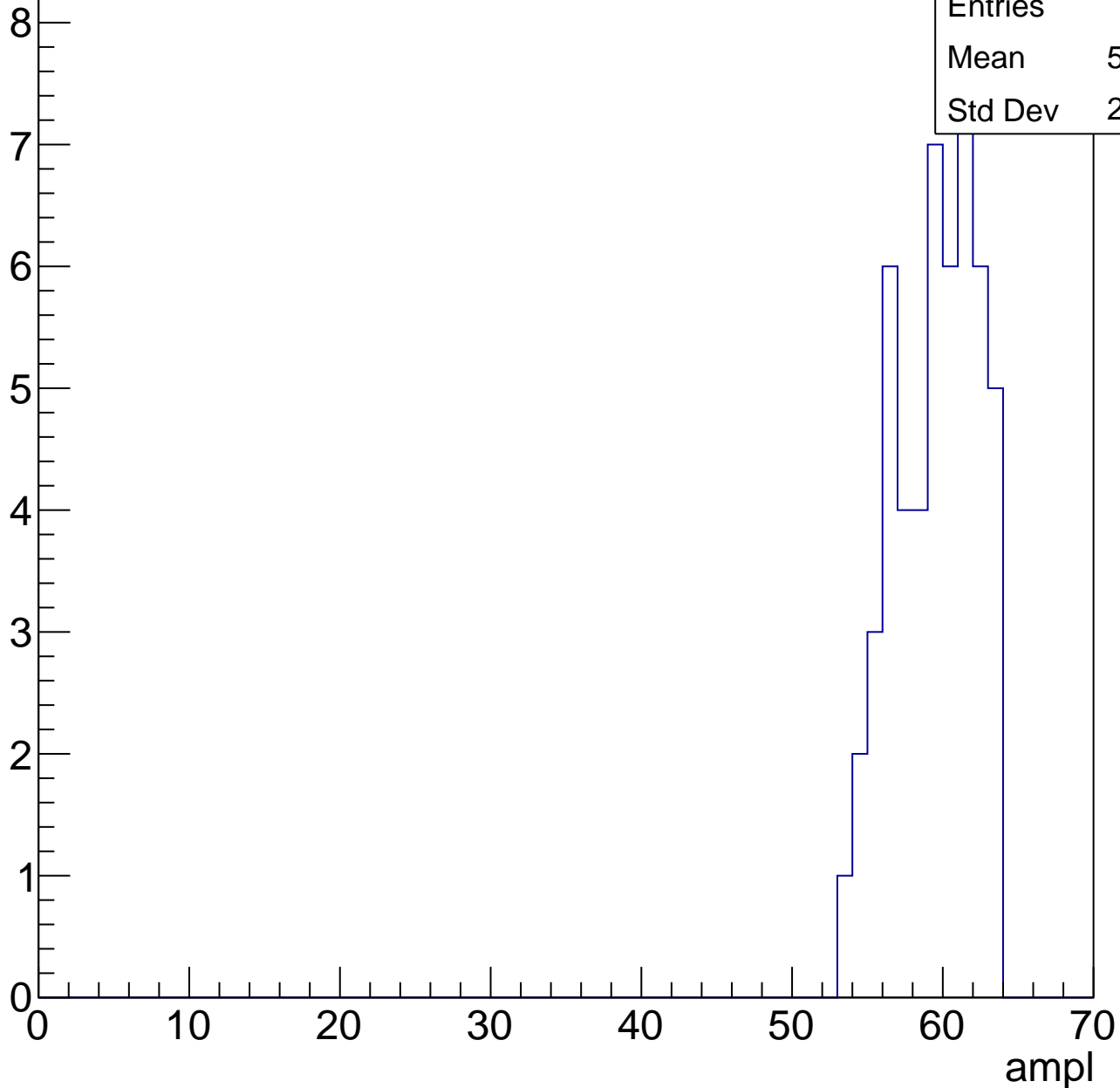


B1L101S, U6-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

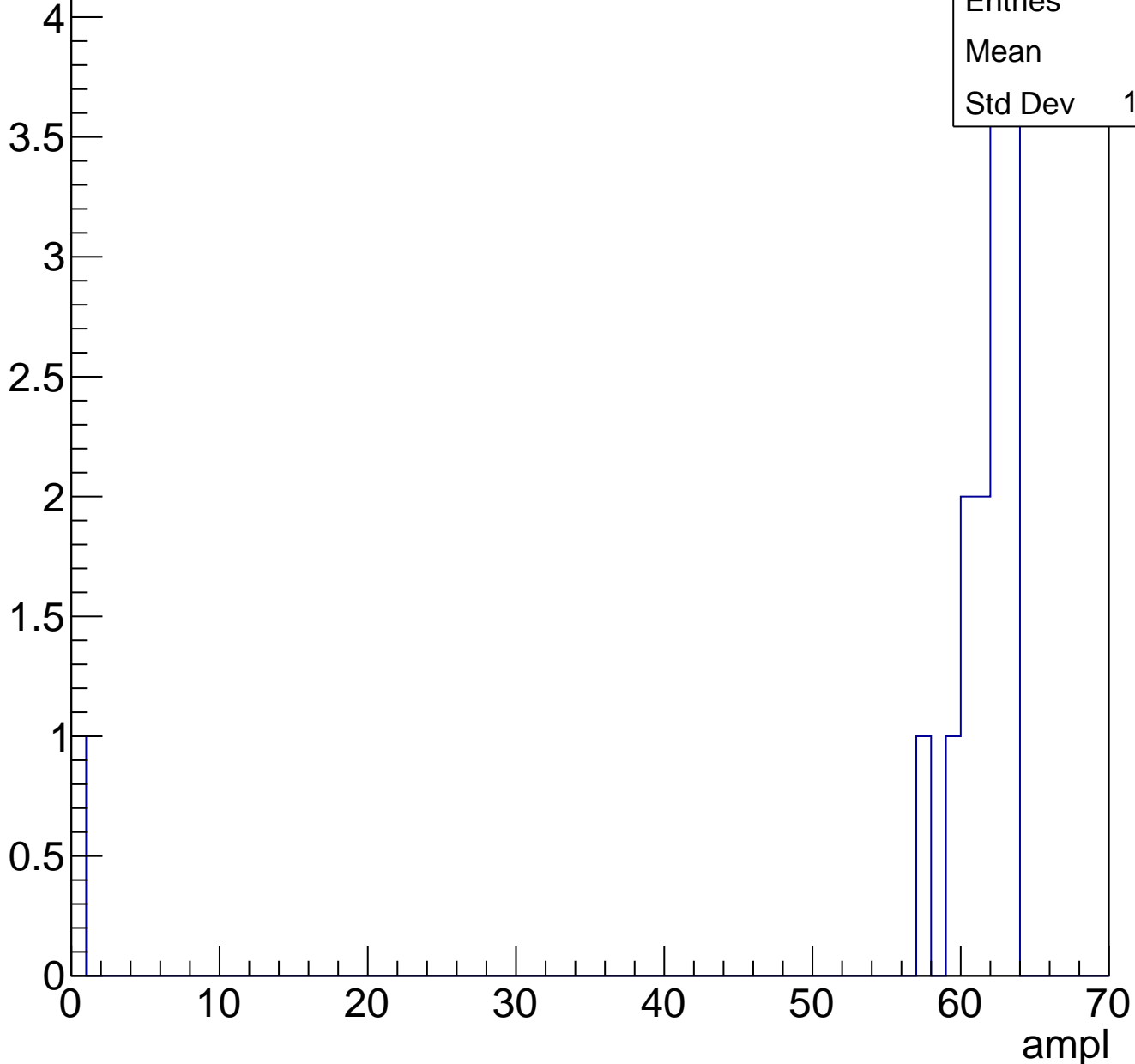
Entries	52
Mean	59.04
Std Dev	2.703



B1L101S, U6-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch3, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	29.25
Std Dev	5.684

Gaus mean : 30.8543

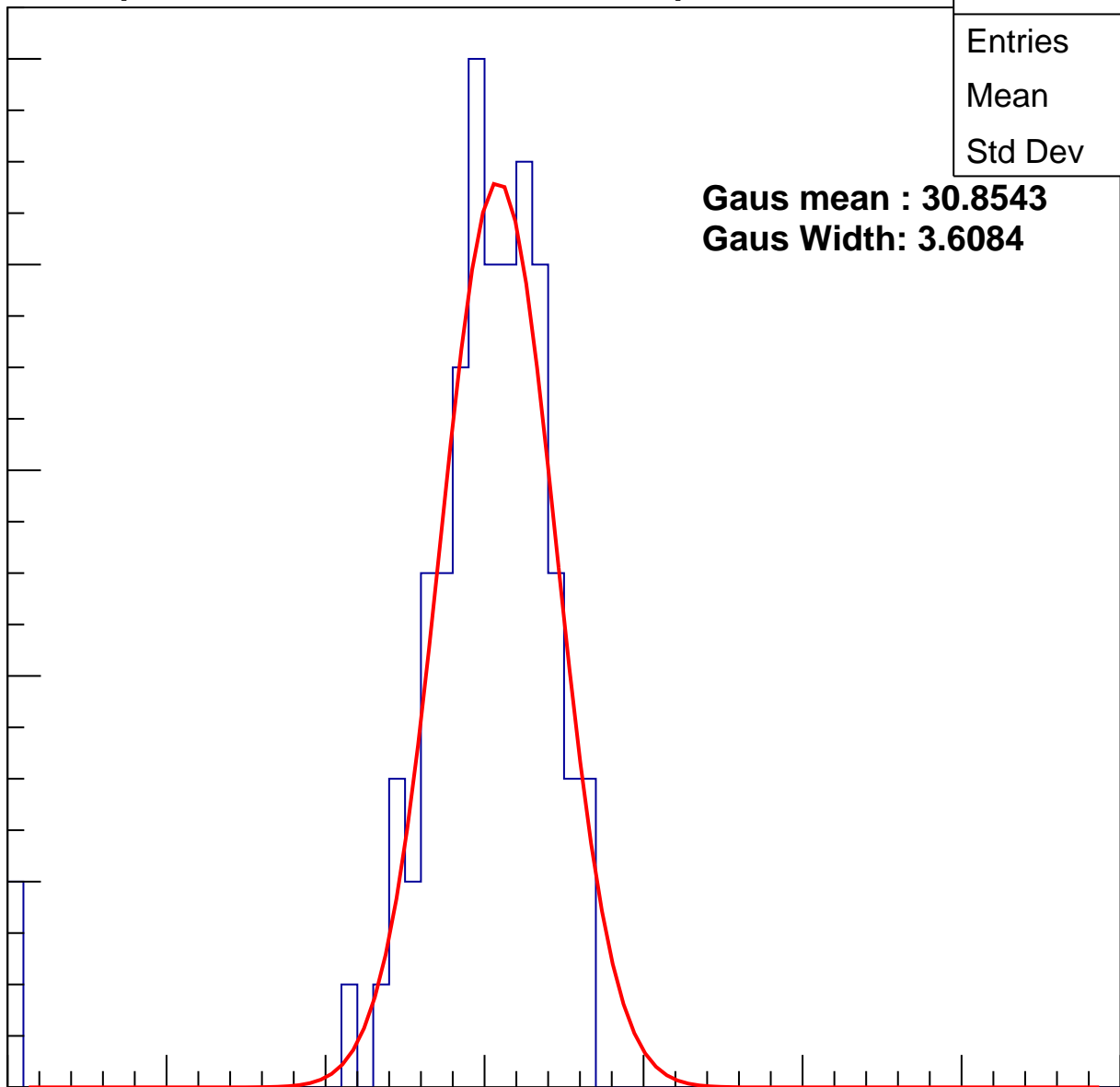
Gaus Width: 3.6084

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



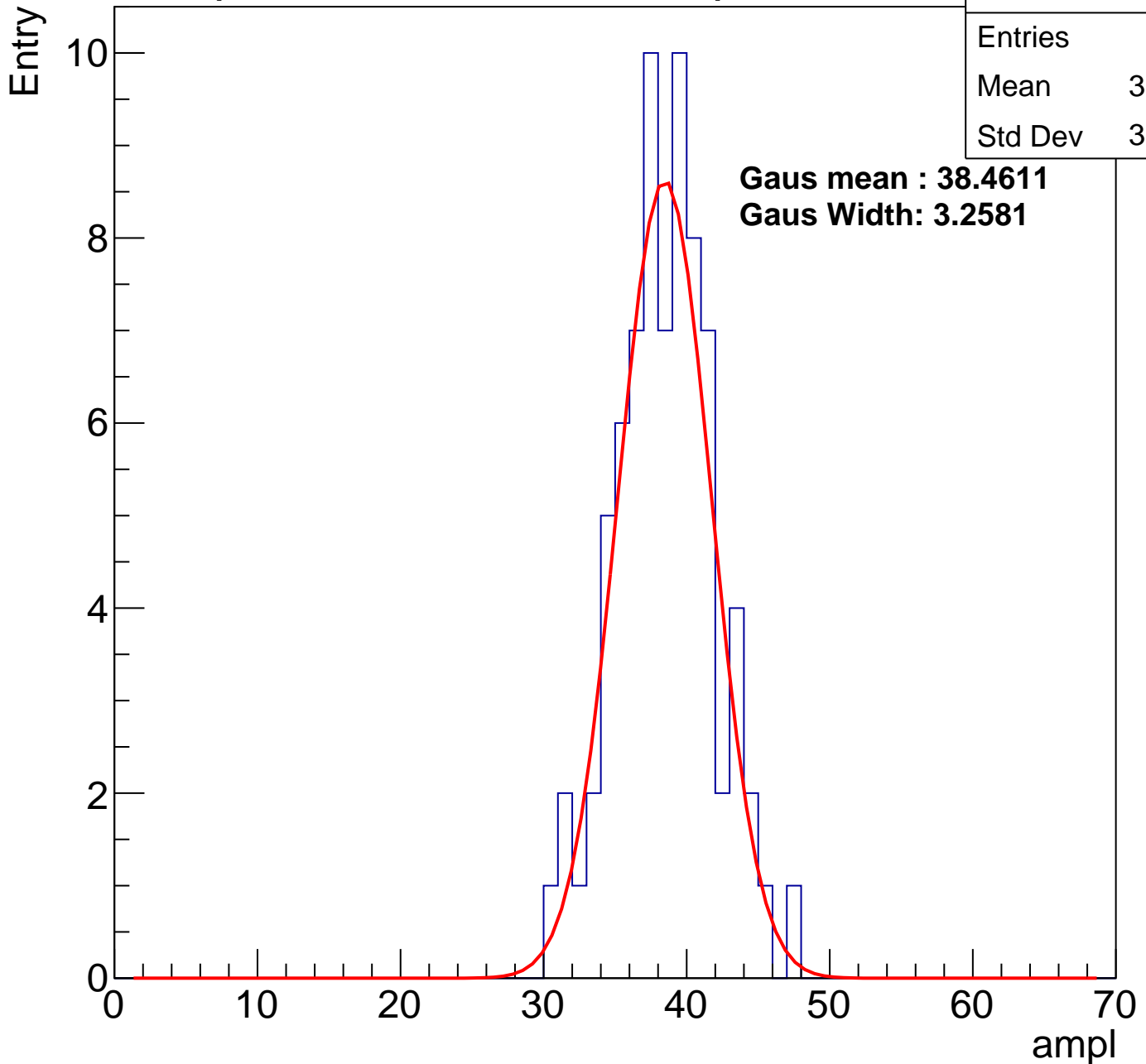
B1L101S, U6-ch3, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	38.04
Std Dev	3.362

Gaus mean : 38.4611

Gaus Width: 3.2581



B1L101S, U6-ch3, adc2

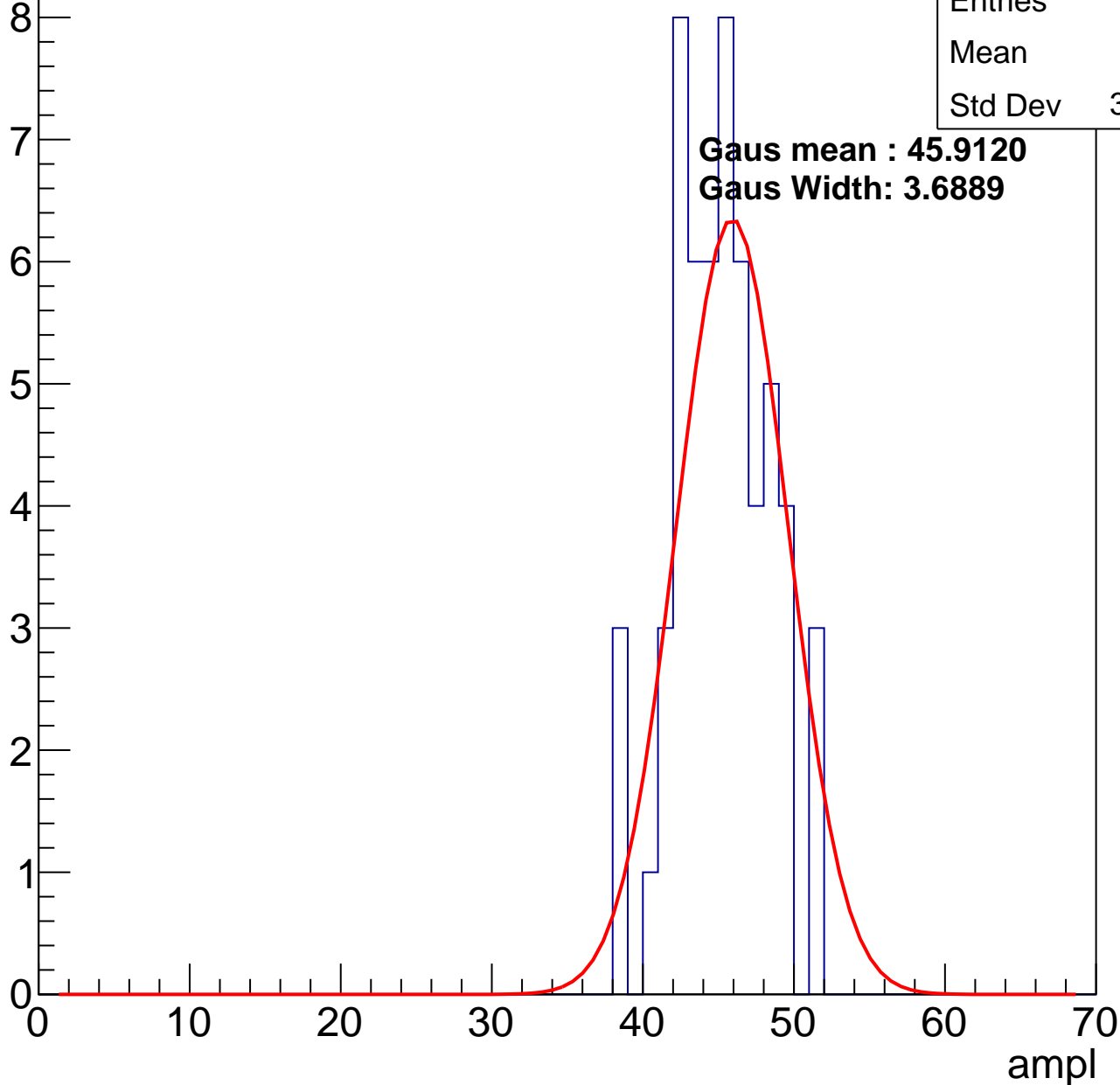
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.7
Std Dev	3.123

Gaus mean : 45.9120

Gaus Width: 3.6889

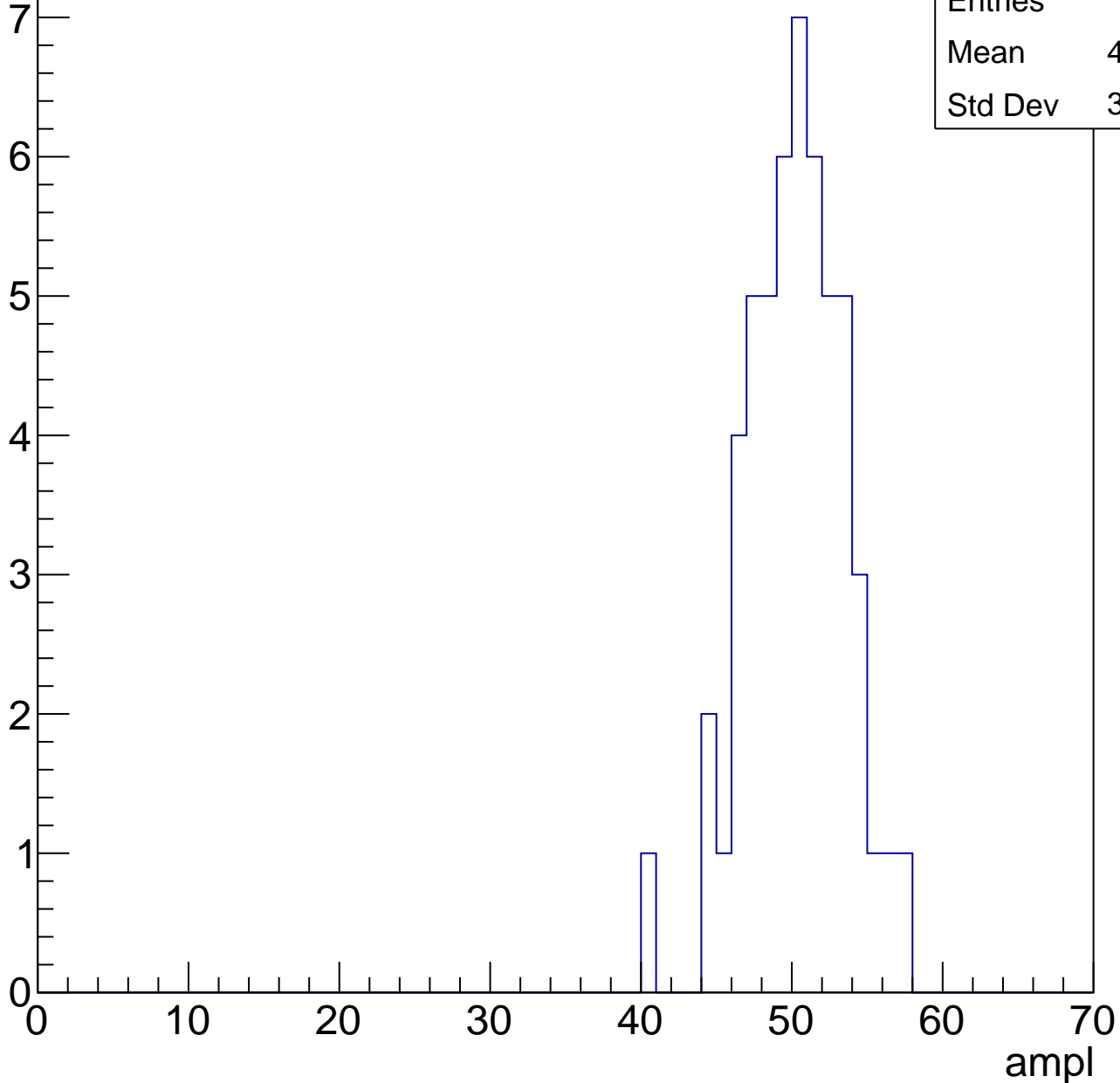


B1L101S, U6-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

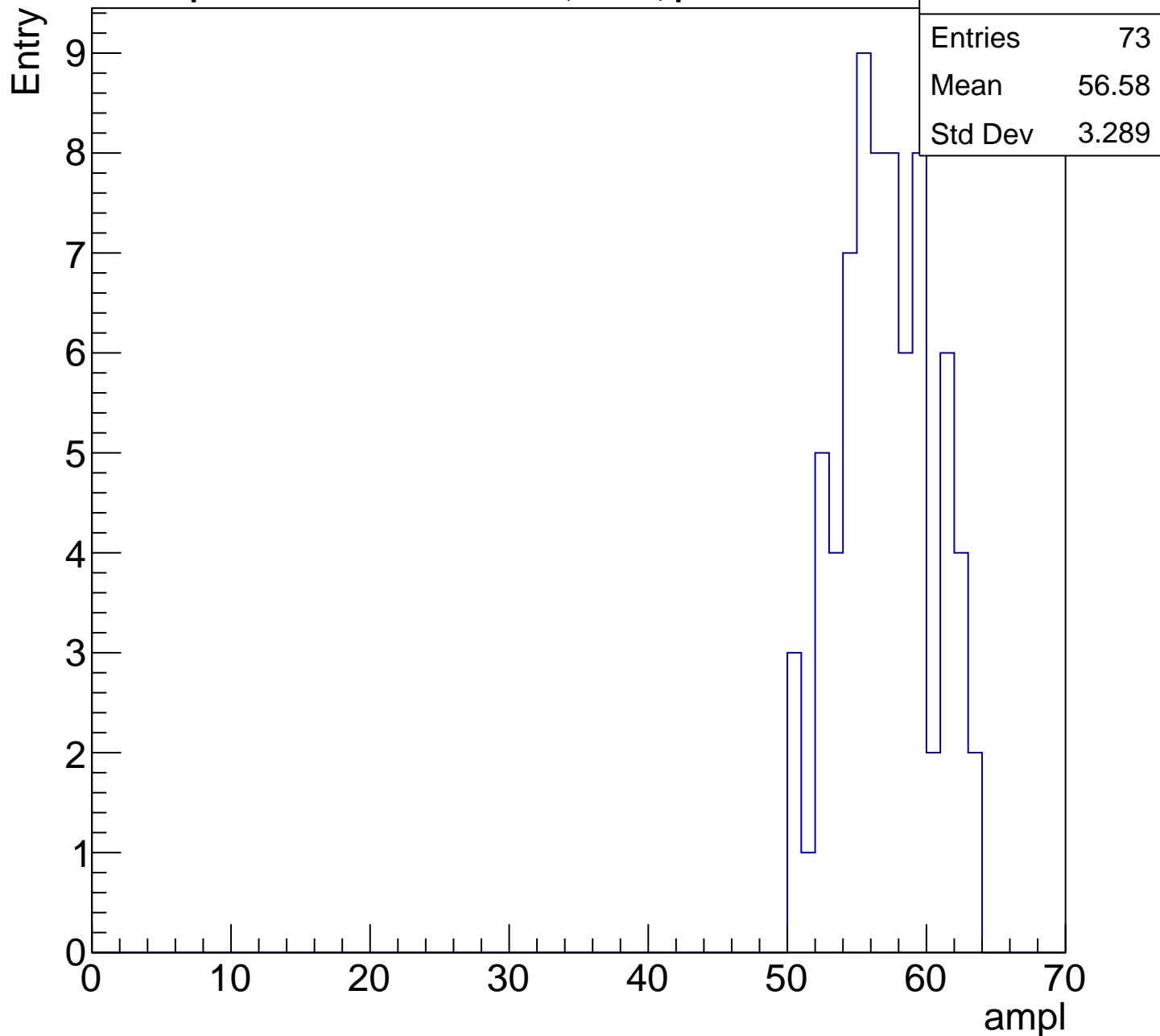
Entry

Entries	53
Mean	49.75
Std Dev	3.244



B1L101S, U6-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch3, adc5

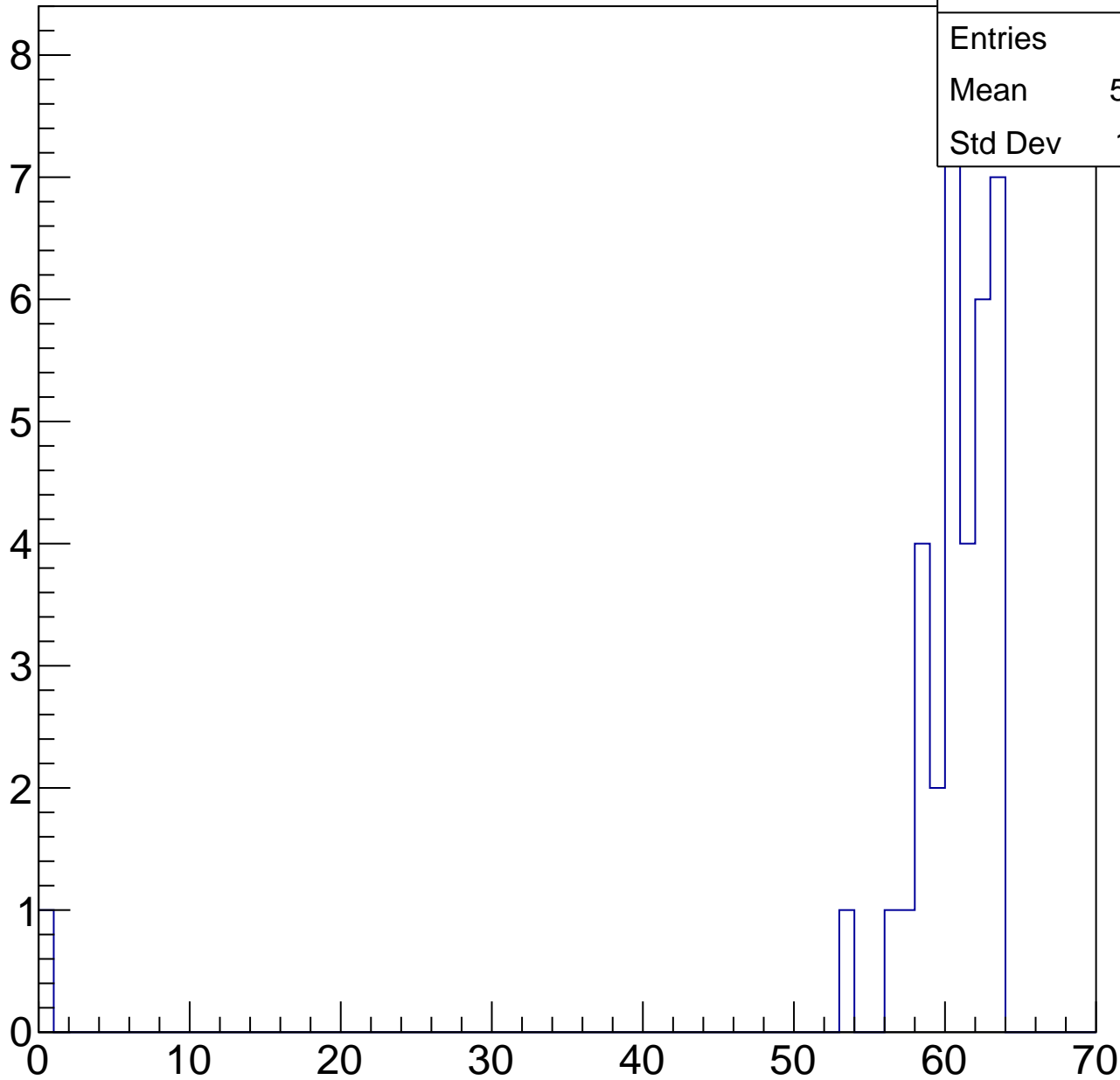
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	58.66
Std Dev	10.31

ampl



B1L101S, U6-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch4, adc0

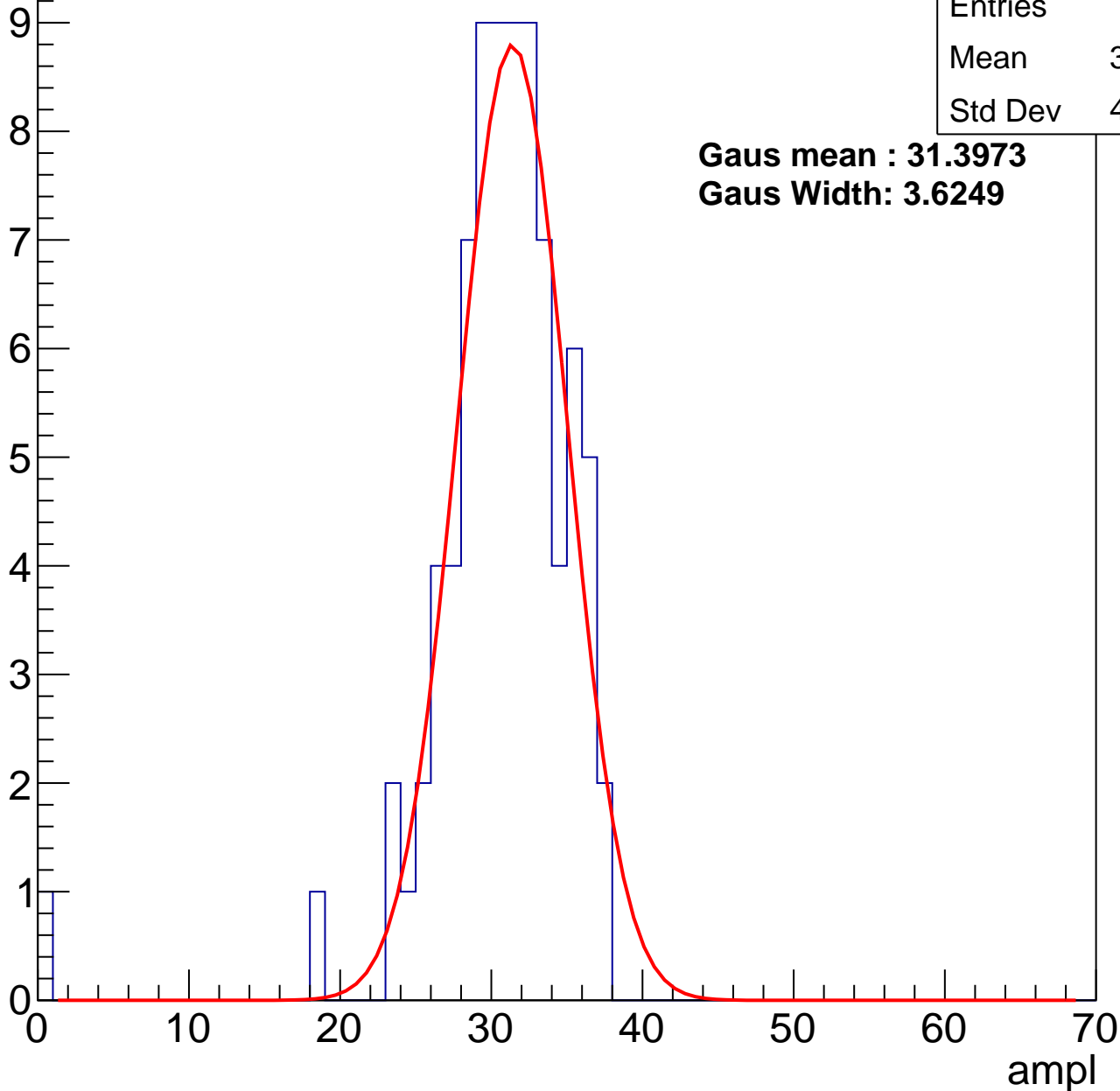
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.18
Std Dev	4.892

Gaus mean : 31.3973

Gaus Width: 3.6249



B1L101S, U6-ch4, adc1

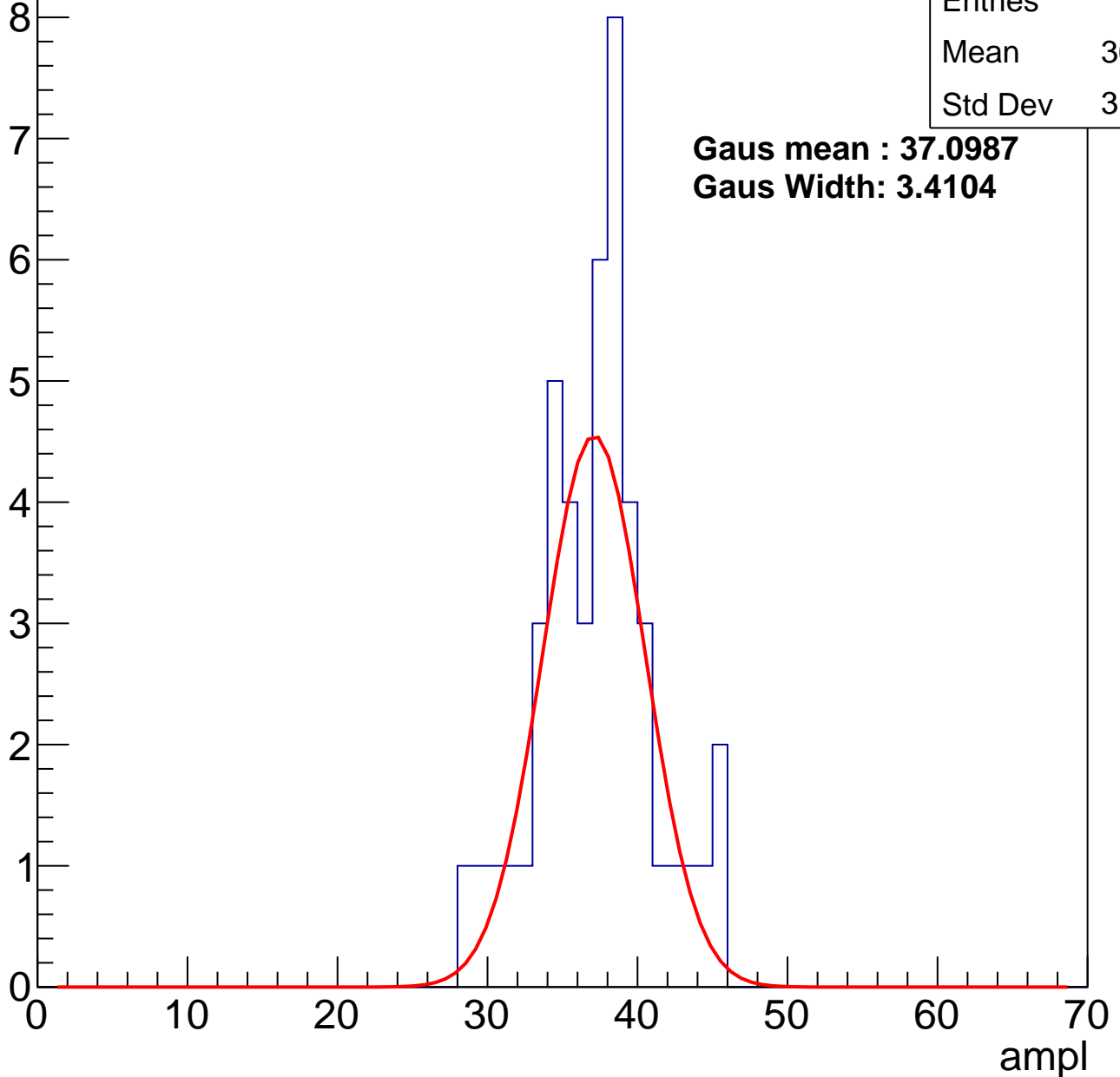
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	36.79
Std Dev	3.775

Gaus mean : 37.0987

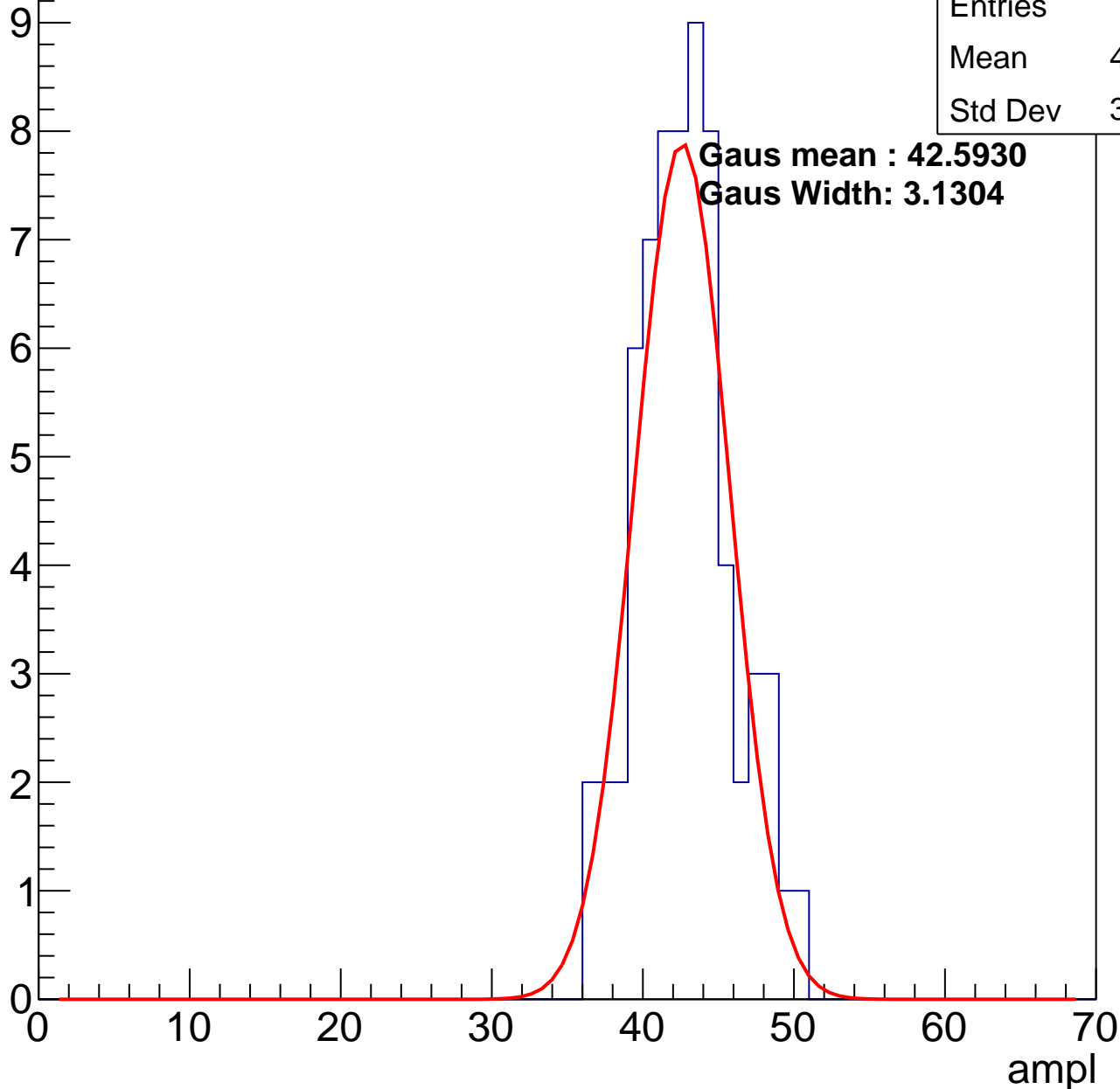
Gaus Width: 3.4104



B1L101S, U6-ch4, adc2

calib_packv5_042523_0143.root, FC#0, port D2

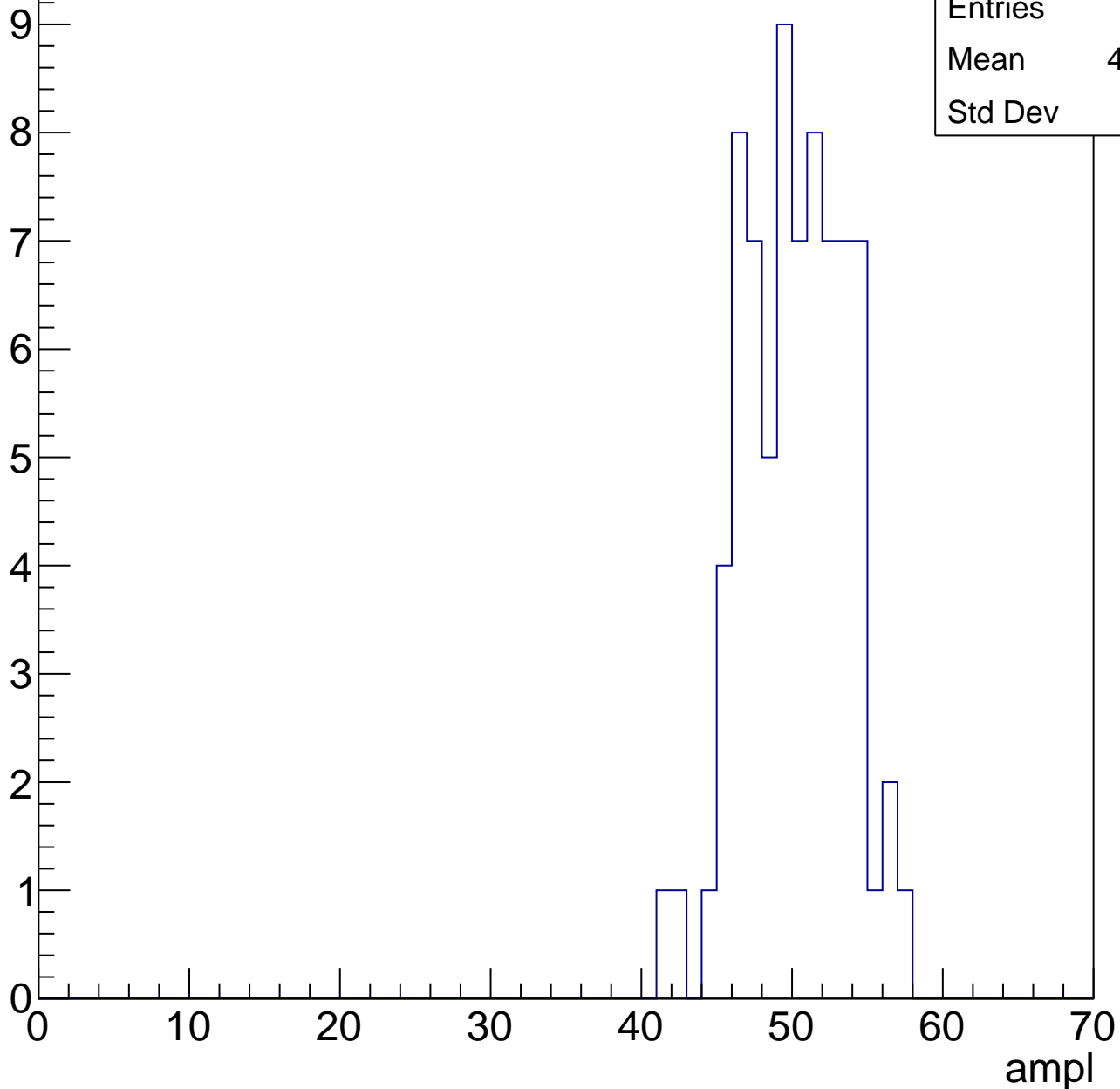
Entry



B1L101S, U6-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



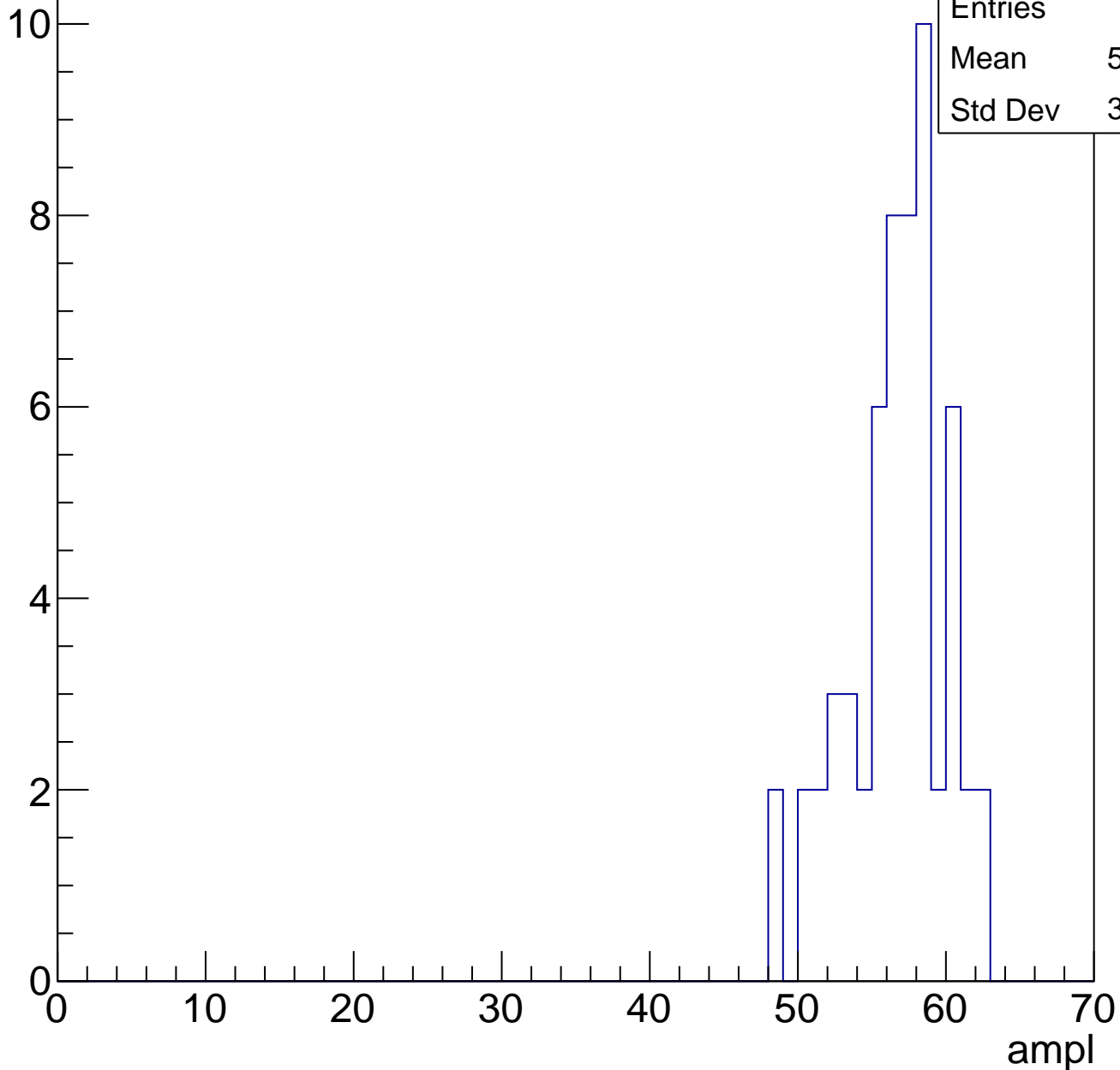
Entries	76
Mean	49.74
Std Dev	3.35

B1L101S, U6-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	58
Mean	56.19
Std Dev	3.272

Entry

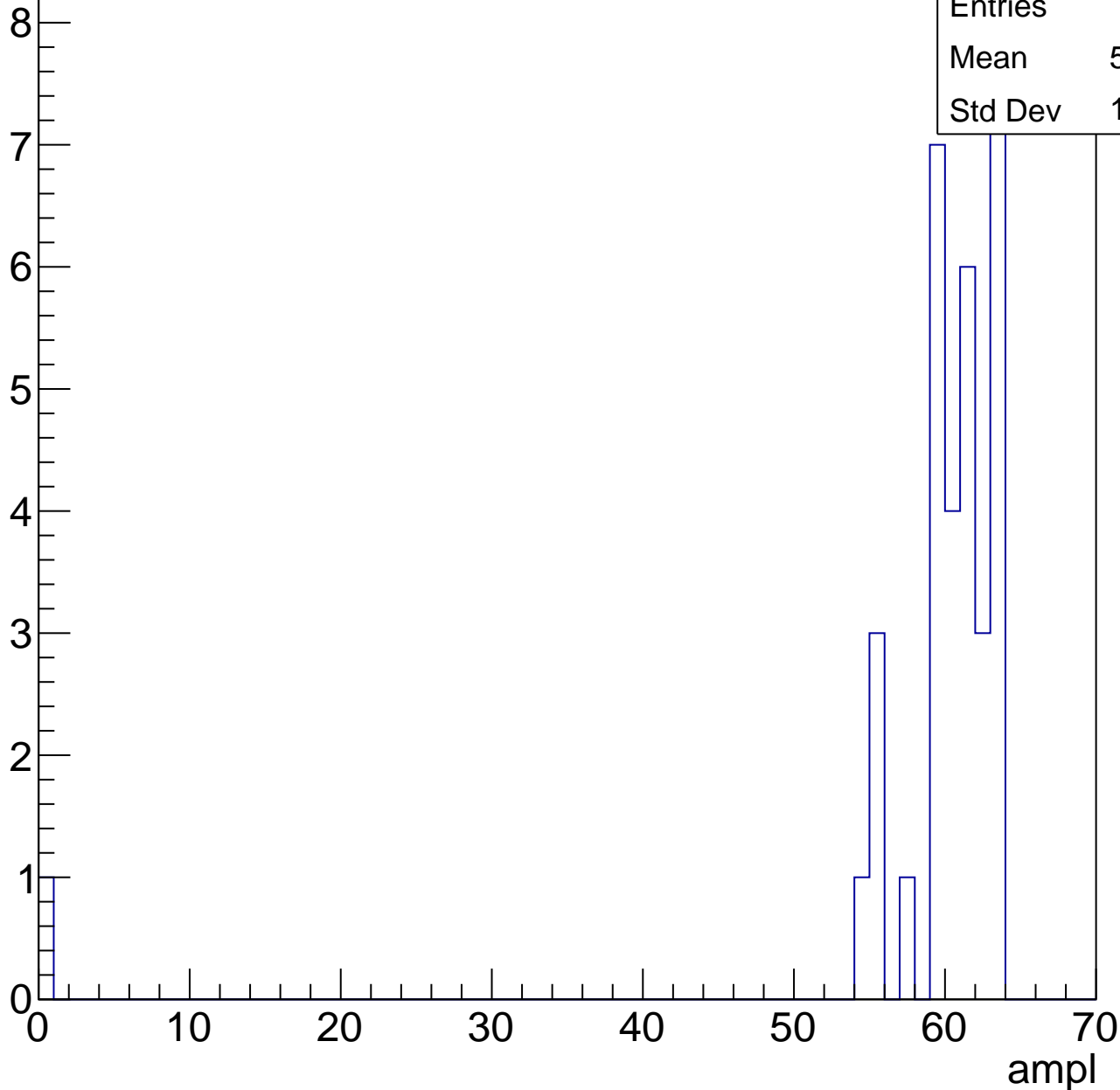


B1L101S, U6-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.38
Std Dev	10.47

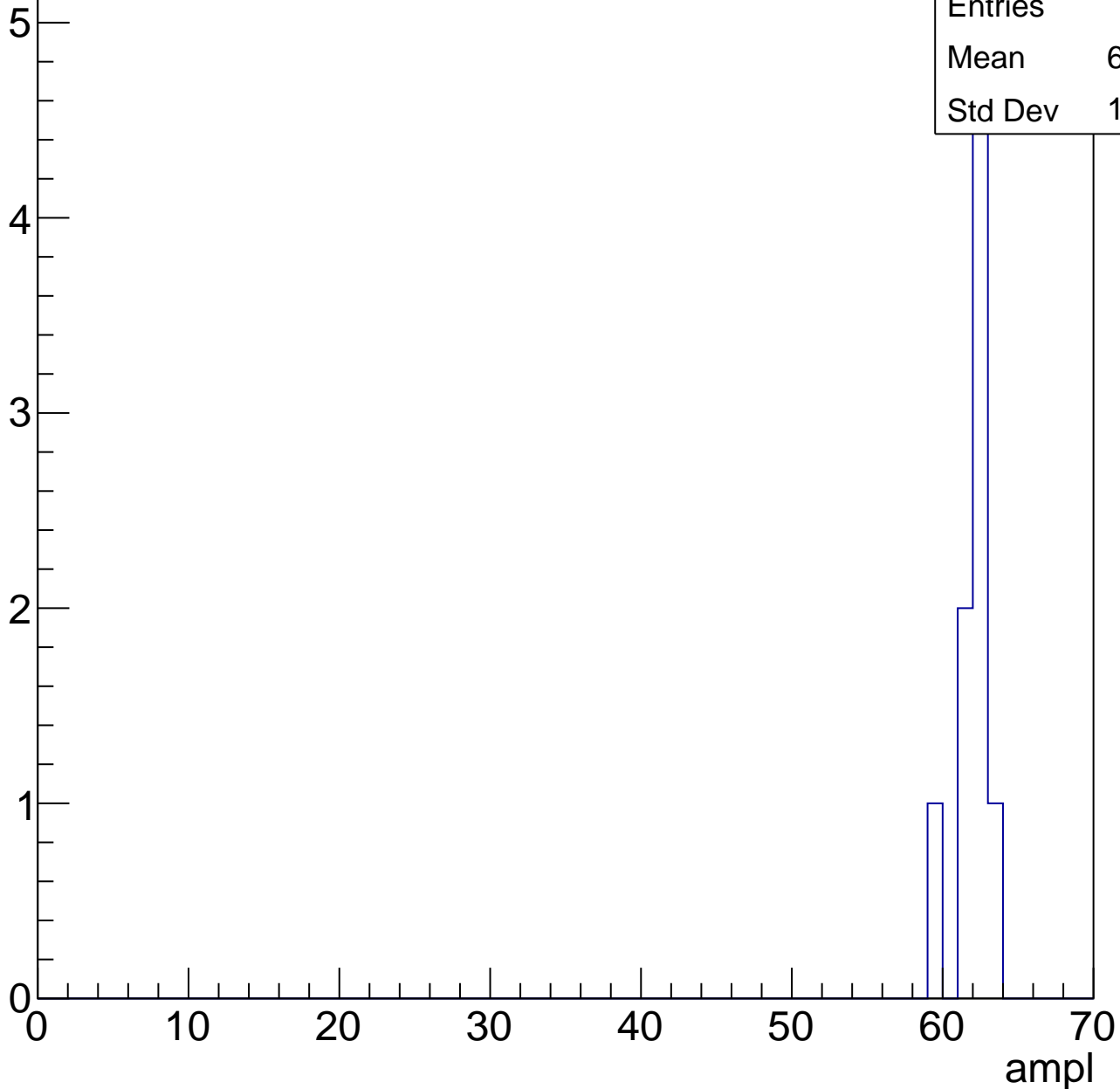


B1L101S, U6-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	61.56
Std Dev	1.066



B1L101S, U6-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch5, adc0

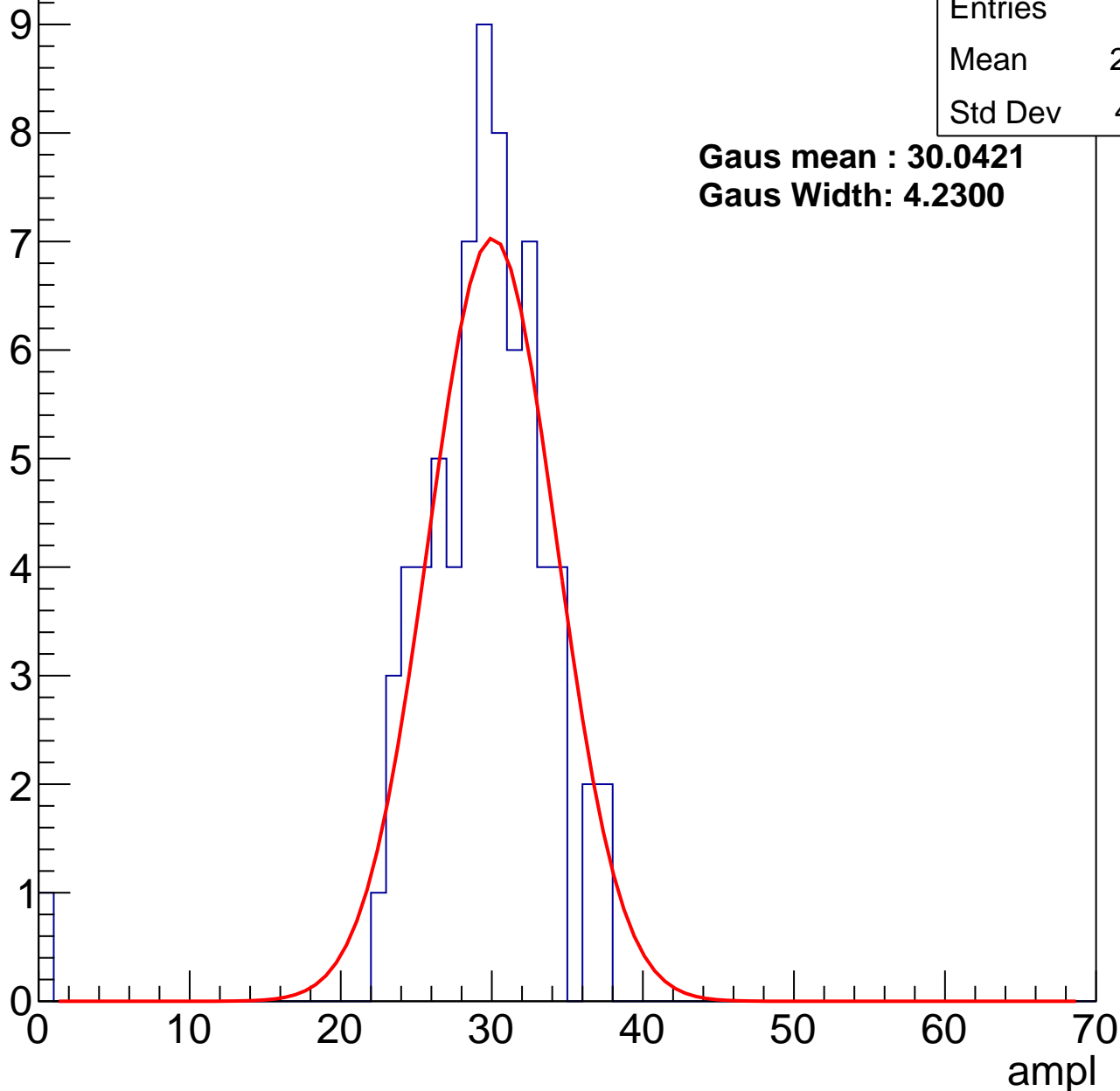
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.82
Std Dev	4.911

Gaus mean : 30.0421

Gaus Width: 4.2300



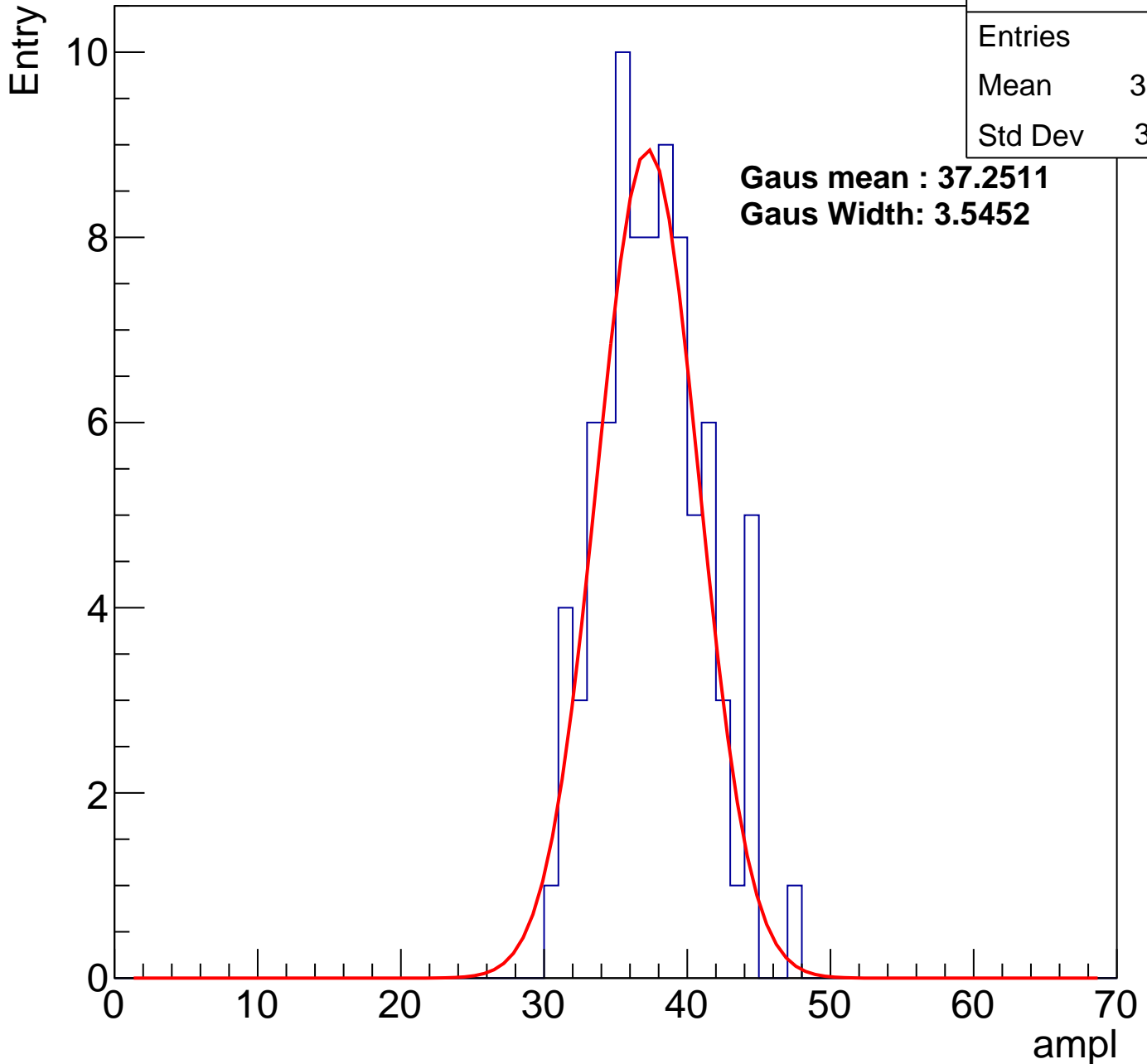
B1L101S, U6-ch5, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	37.17
Std Dev	3.621

Gaus mean : 37.2511

Gaus Width: 3.5452



B1L101S, U6-ch5, adc2

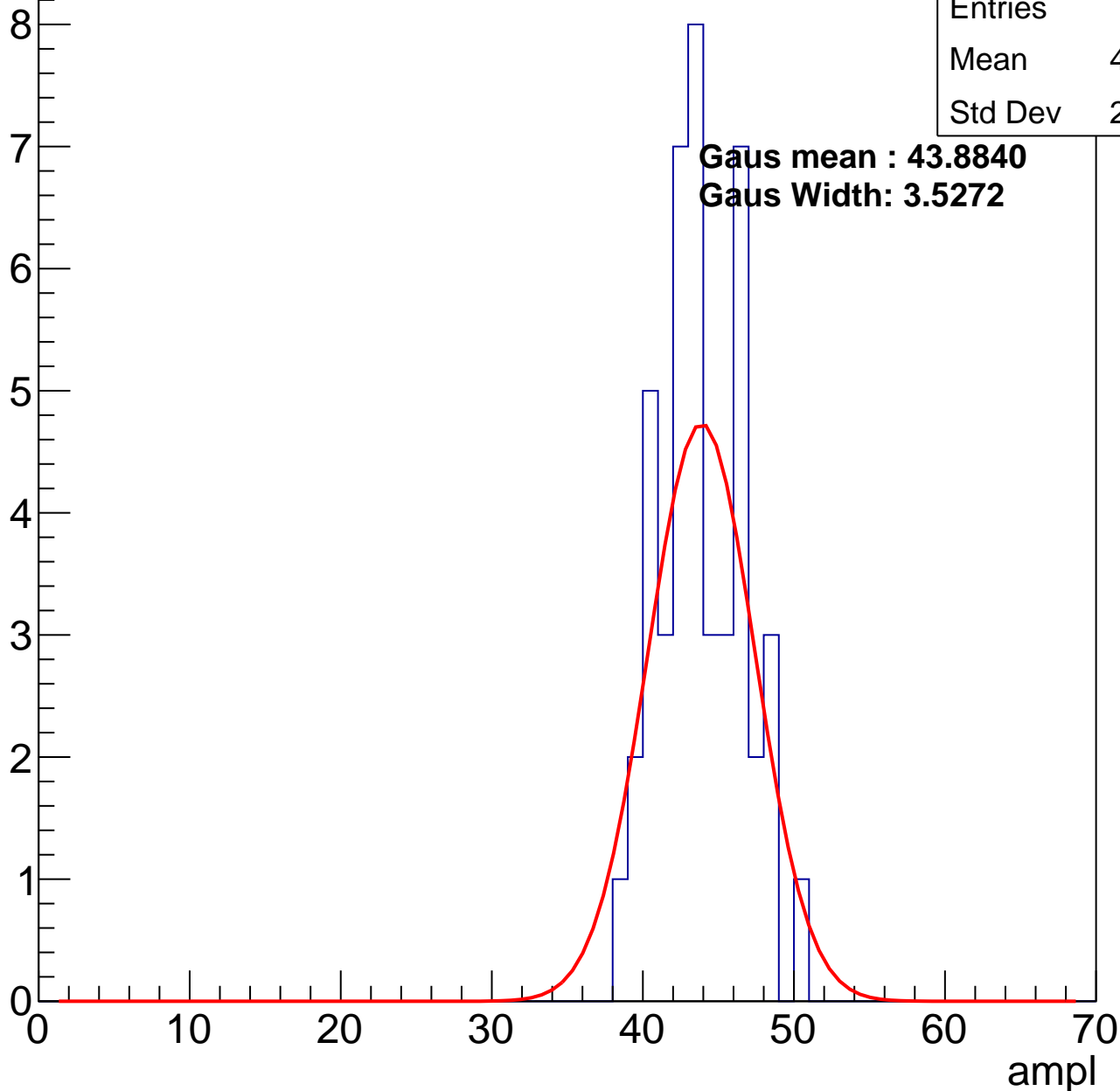
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	43.42
Std Dev	2.777

Gaus mean : 43.8840

Gaus Width: 3.5272

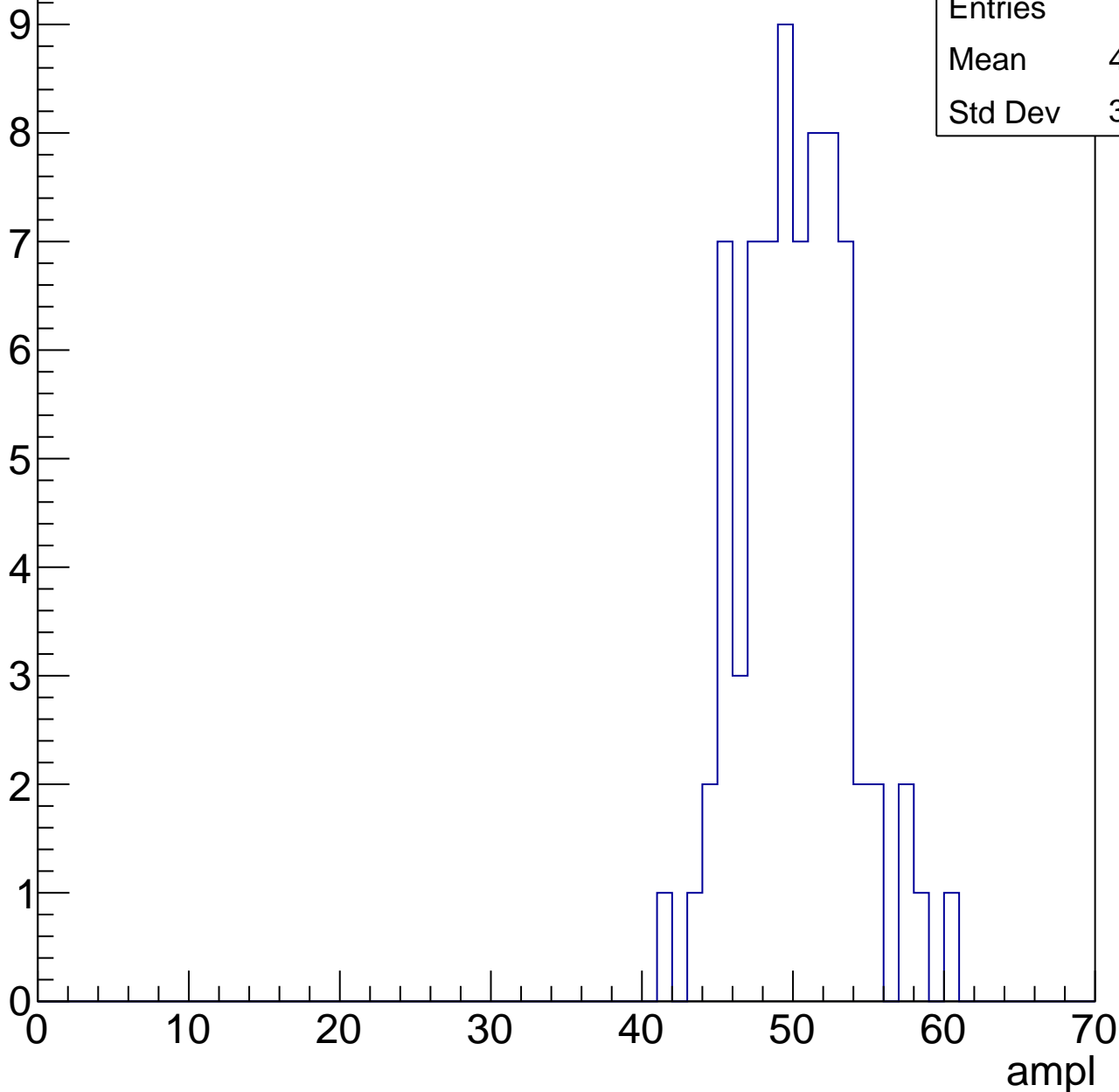


B1L101S, U6-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	49.68
Std Dev	3.586



B1L101S, U6-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	56.04
Std Dev	3.095

Entry

10

8

6

4

2

0

0

10

20

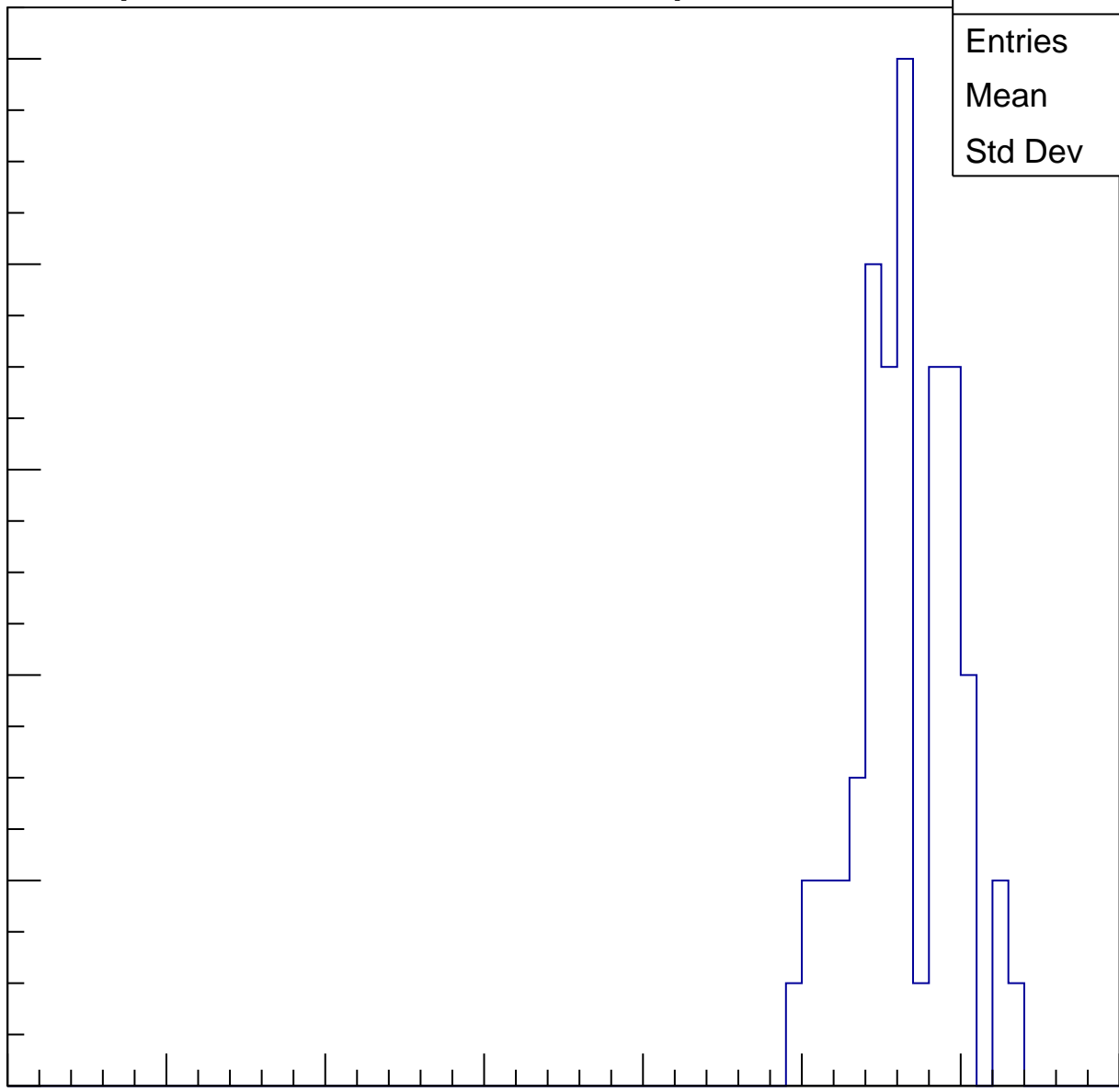
30

40

50

60

ampl

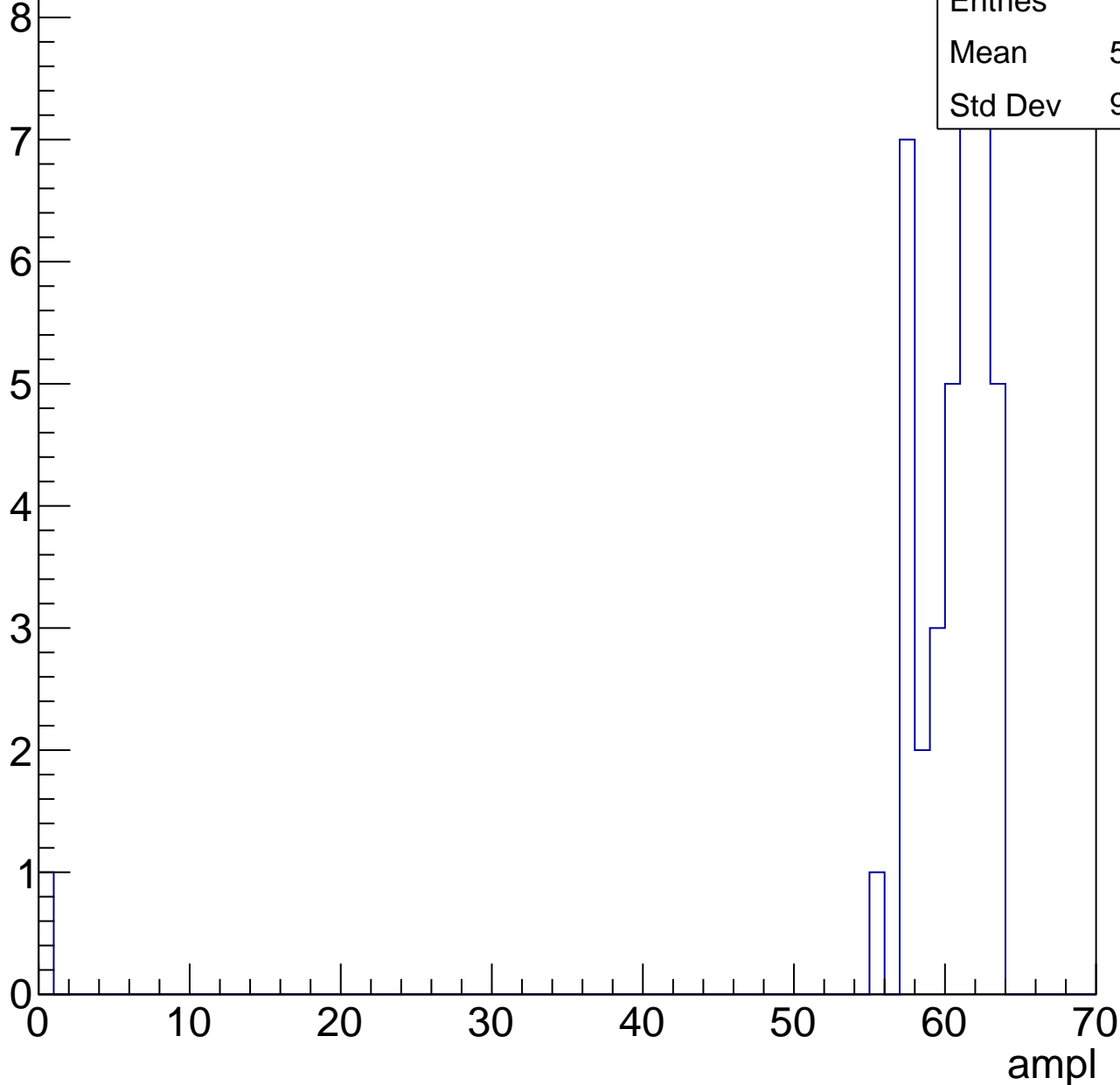


B1L101S, U6-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.65
Std Dev	9.632



B1L101S, U6-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

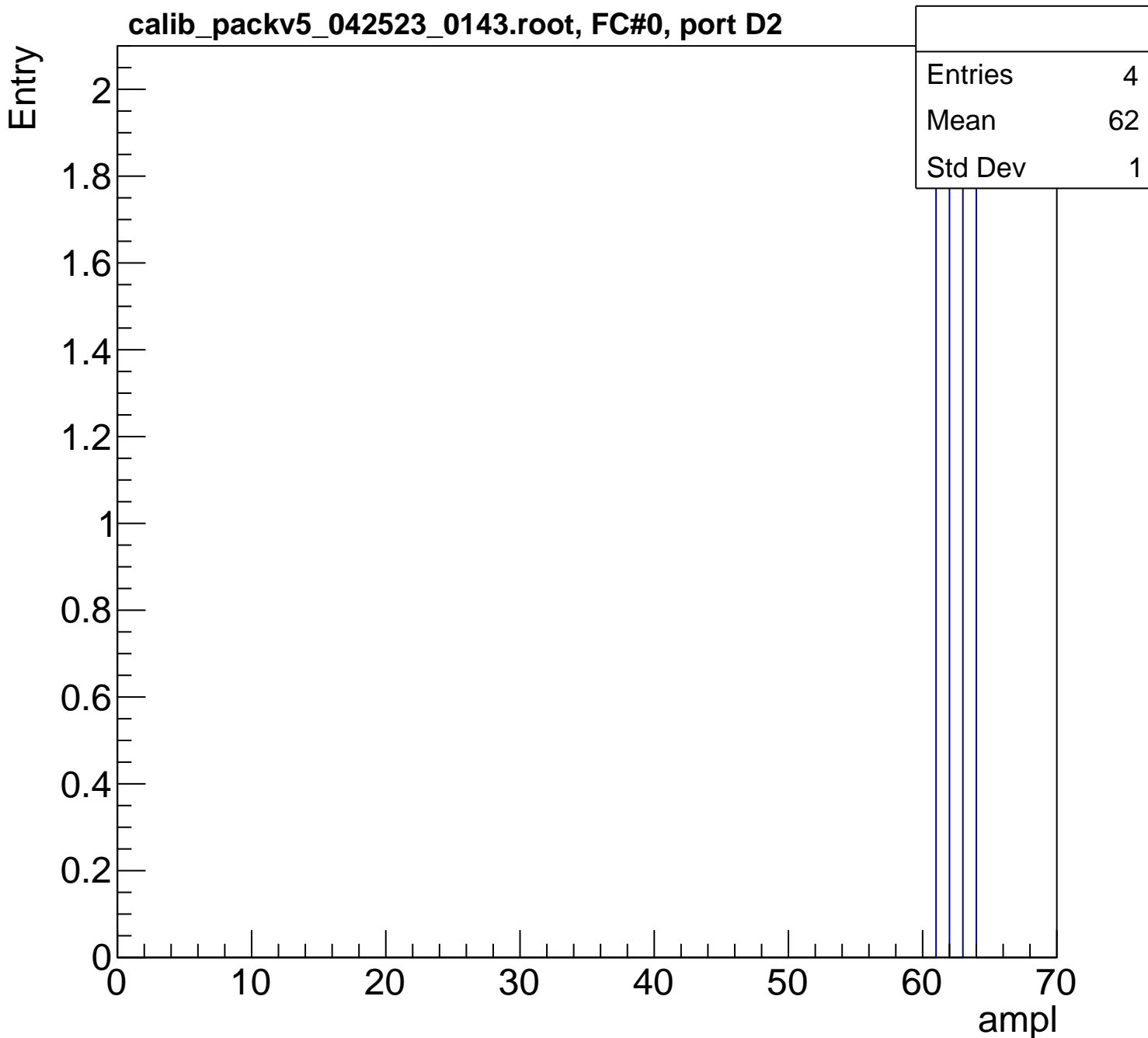
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

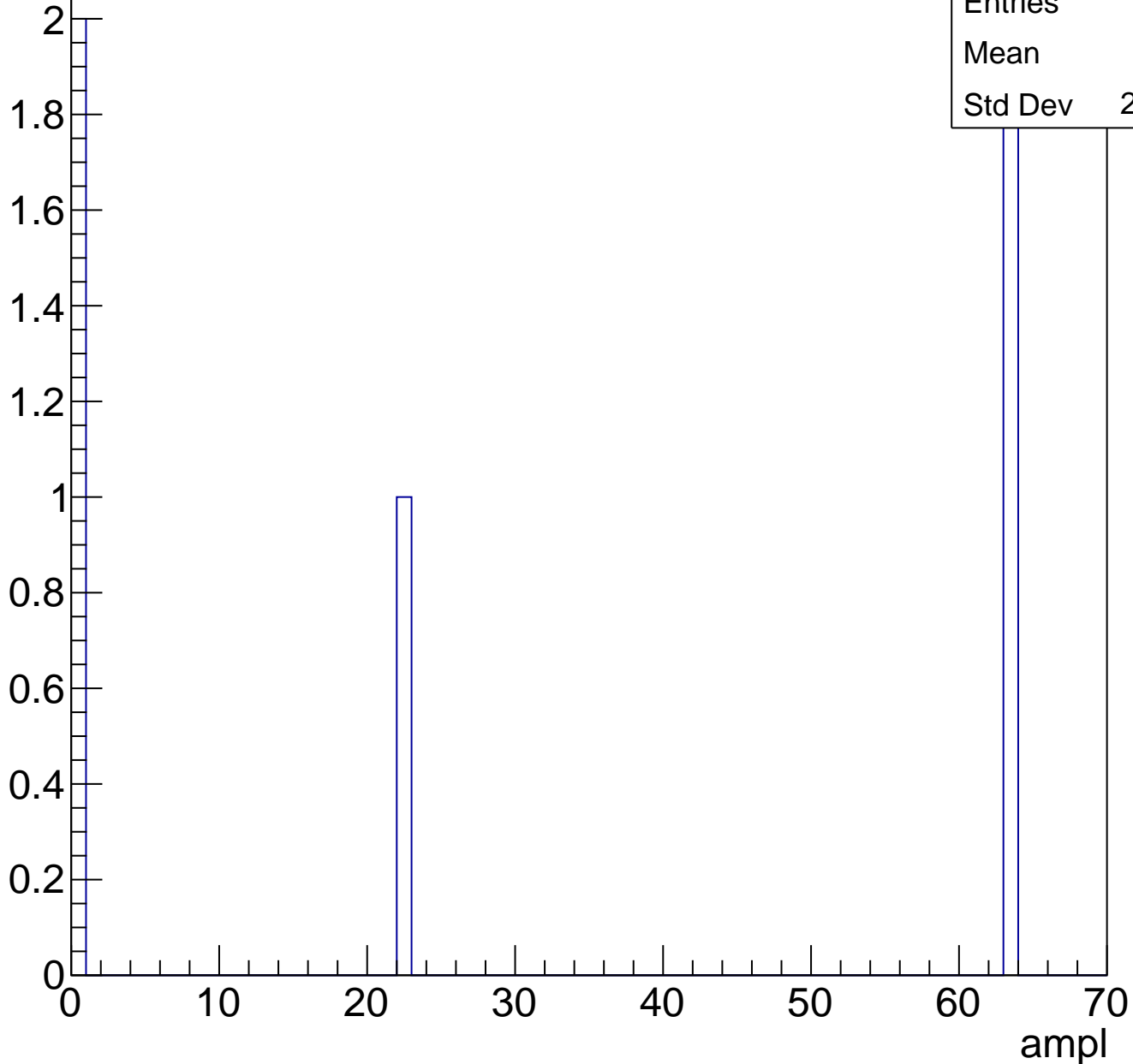
ampl



B1L101S, U6-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch6, adc0

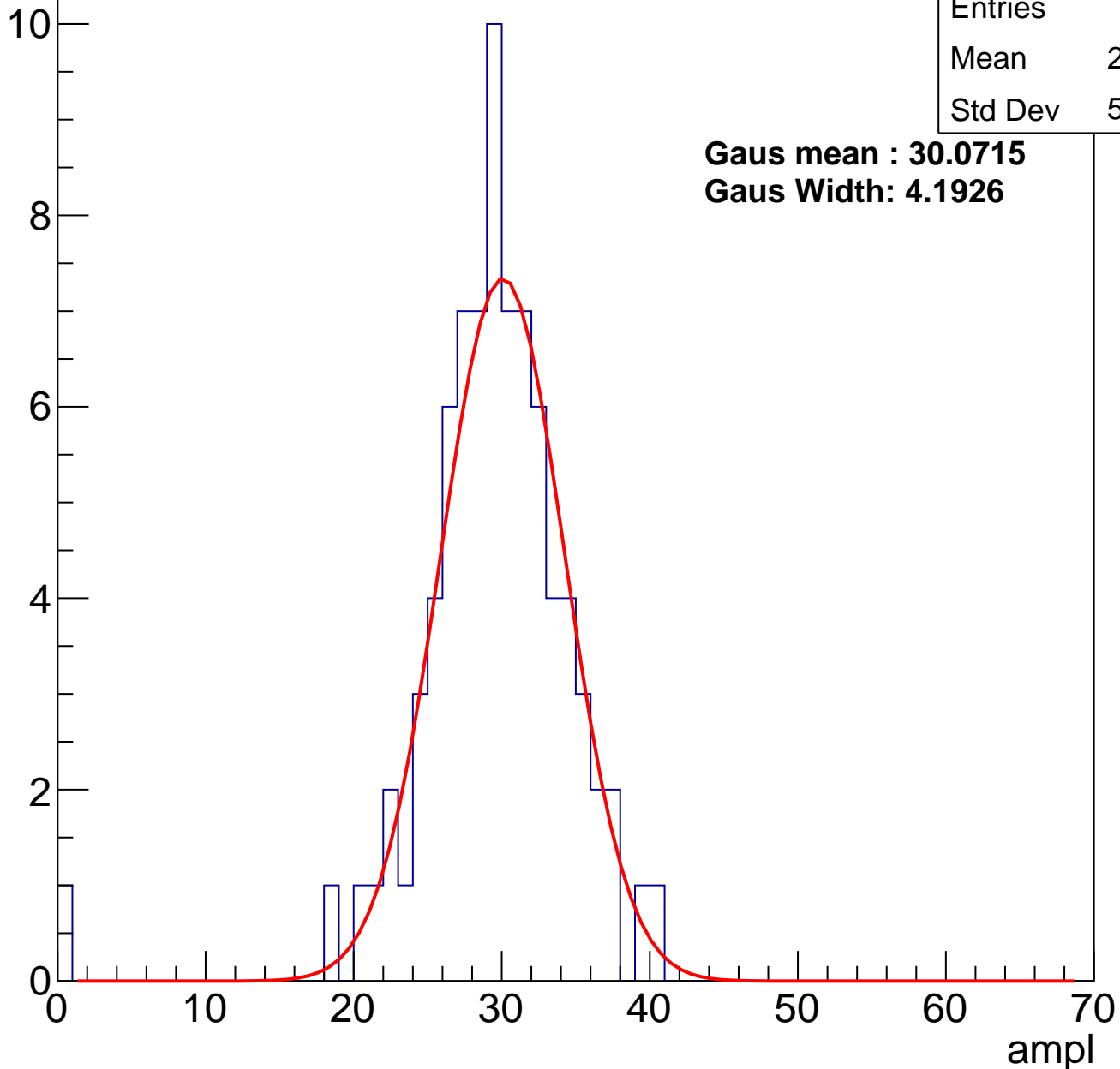
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	28.96
Std Dev	5.302

Gaus mean : 30.0715

Gaus Width: 4.1926

Entry



B1L101S, U6-ch6, adc1

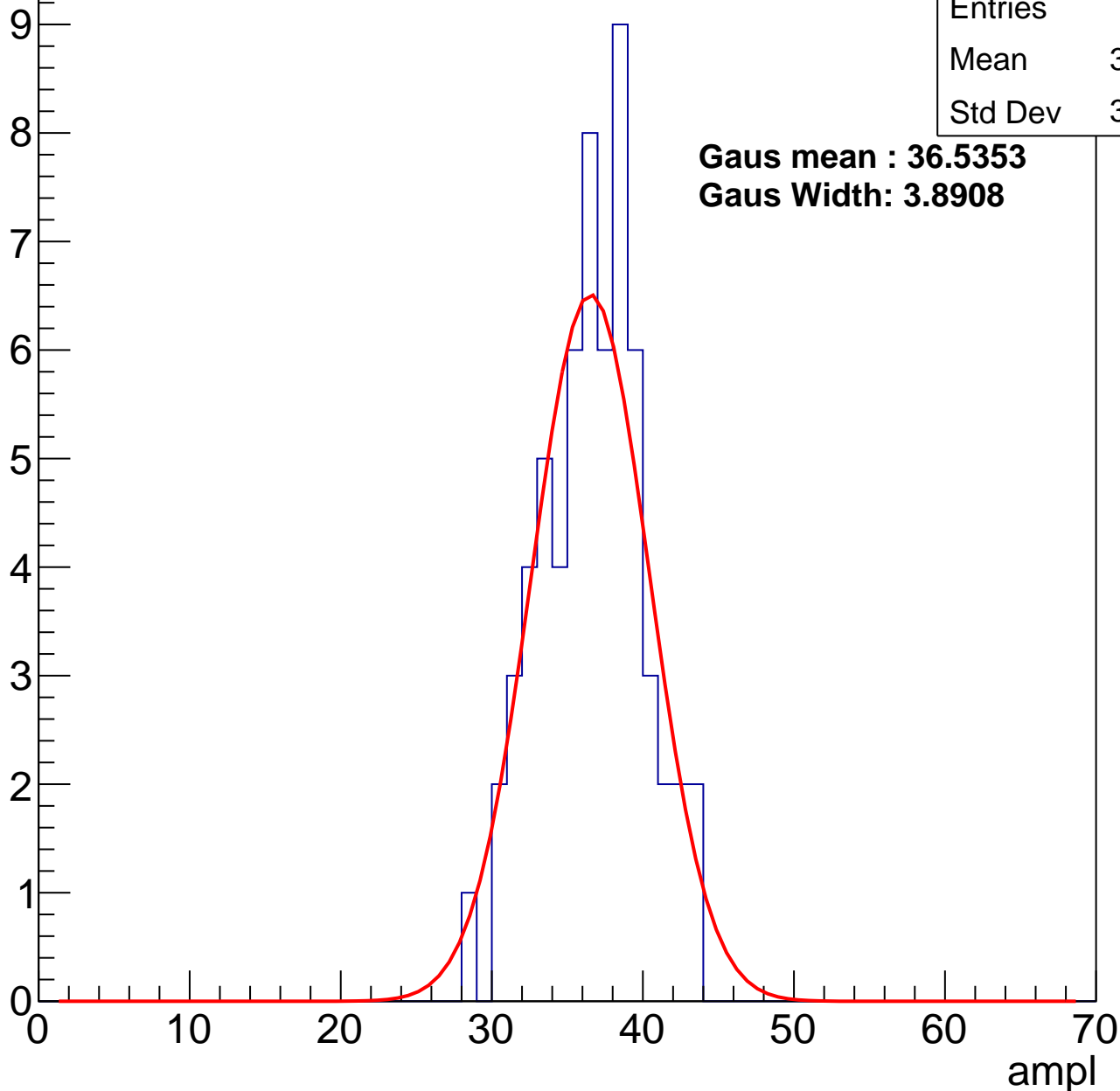
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.16
Std Dev	3.344

Gaus mean : 36.5353

Gaus Width: 3.8908



B1L101S, U6-ch6, adc2

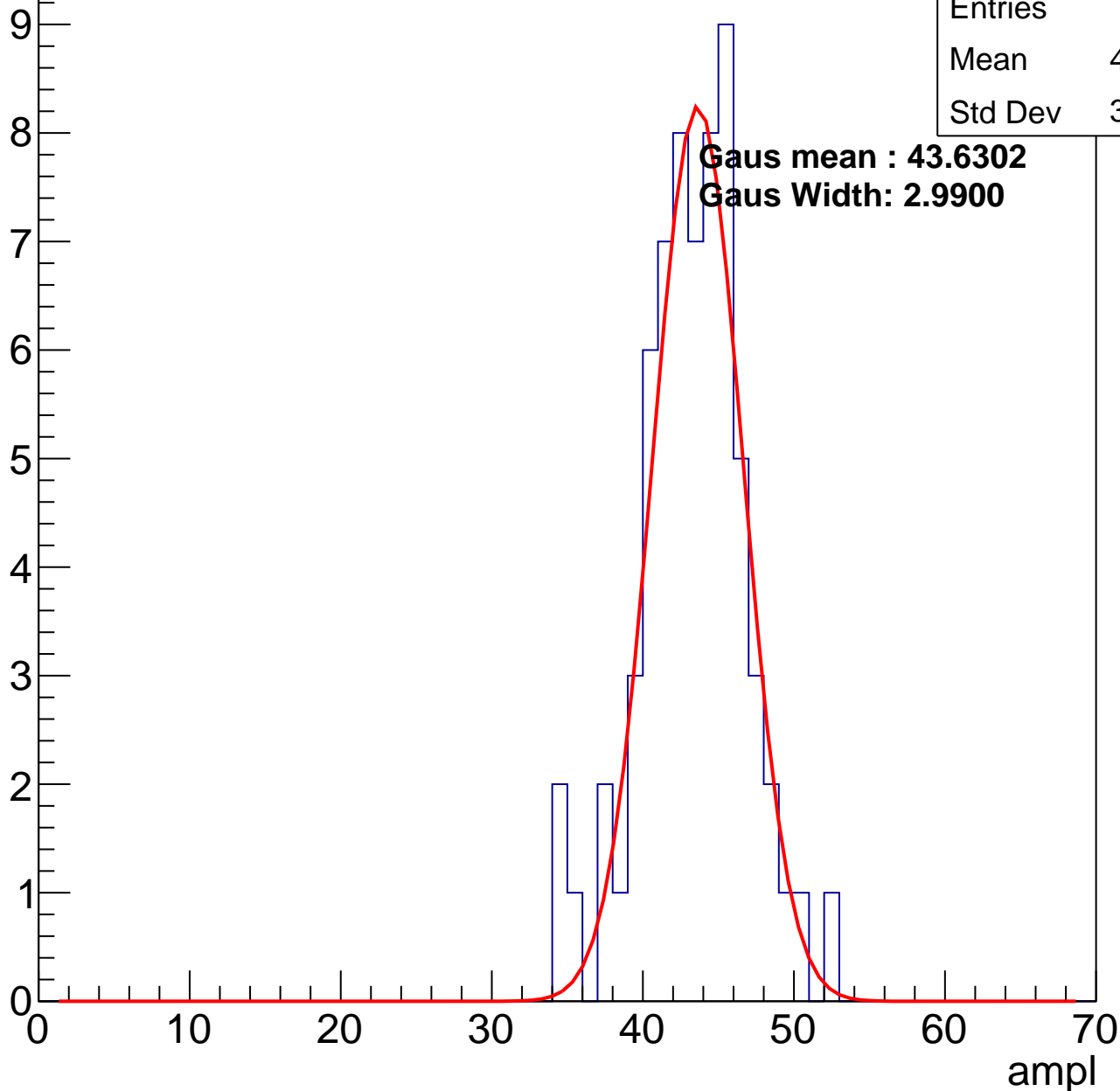
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.85
Std Dev	3.482

Gaus mean : 43.6302

Gaus Width: 2.9900

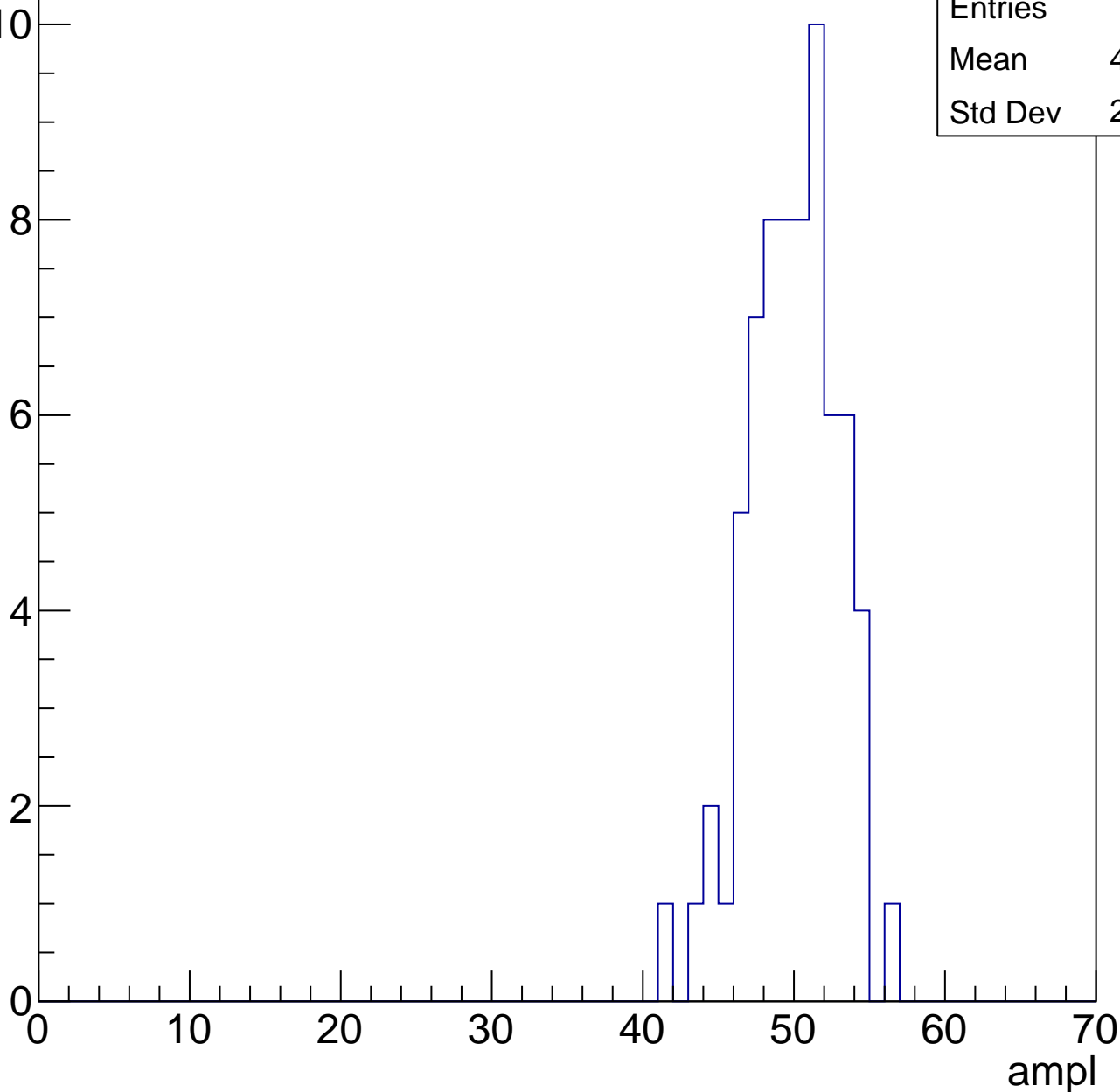


B1L101S, U6-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.47
Std Dev	2.918

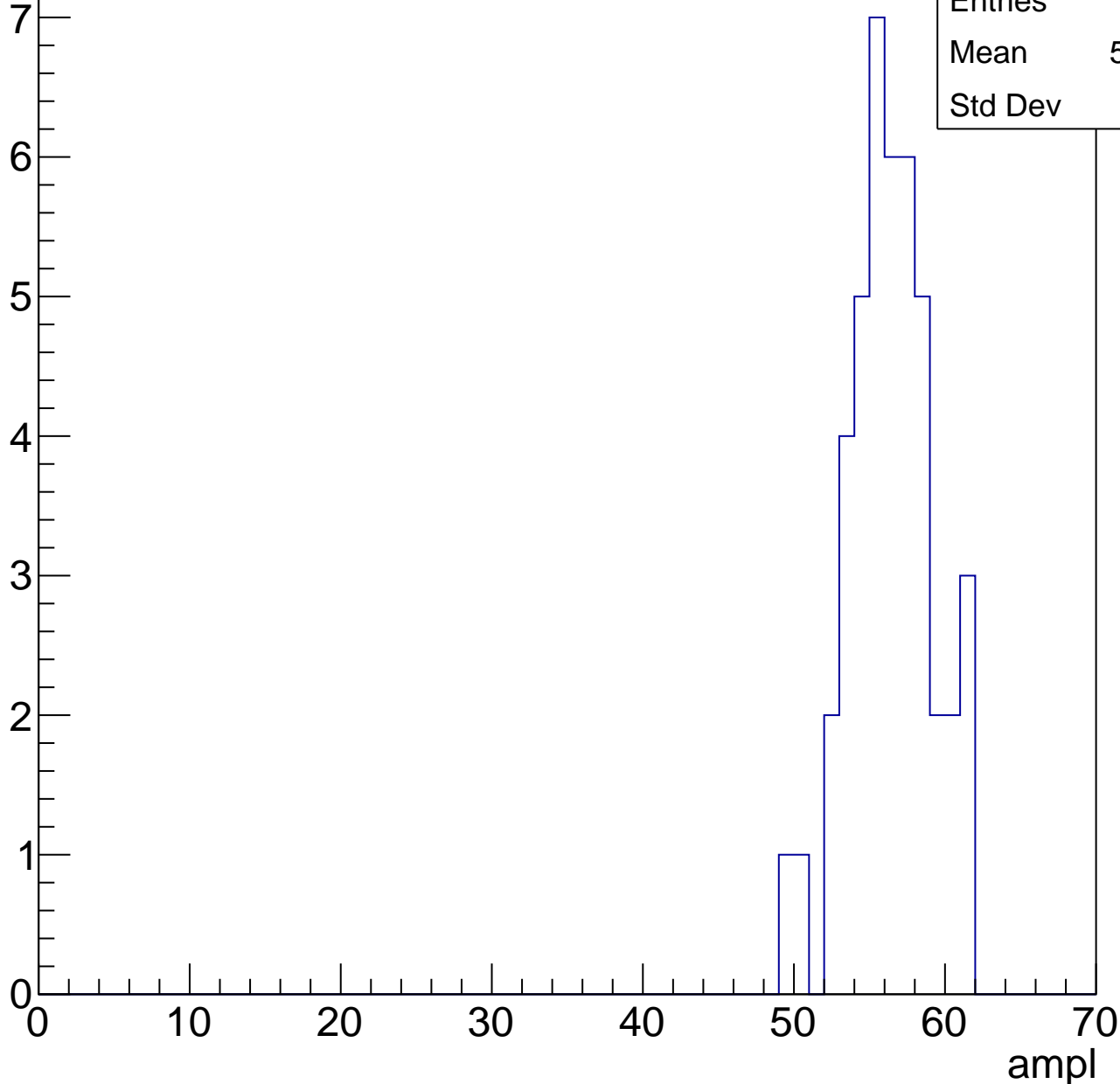


B1L101S, U6-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

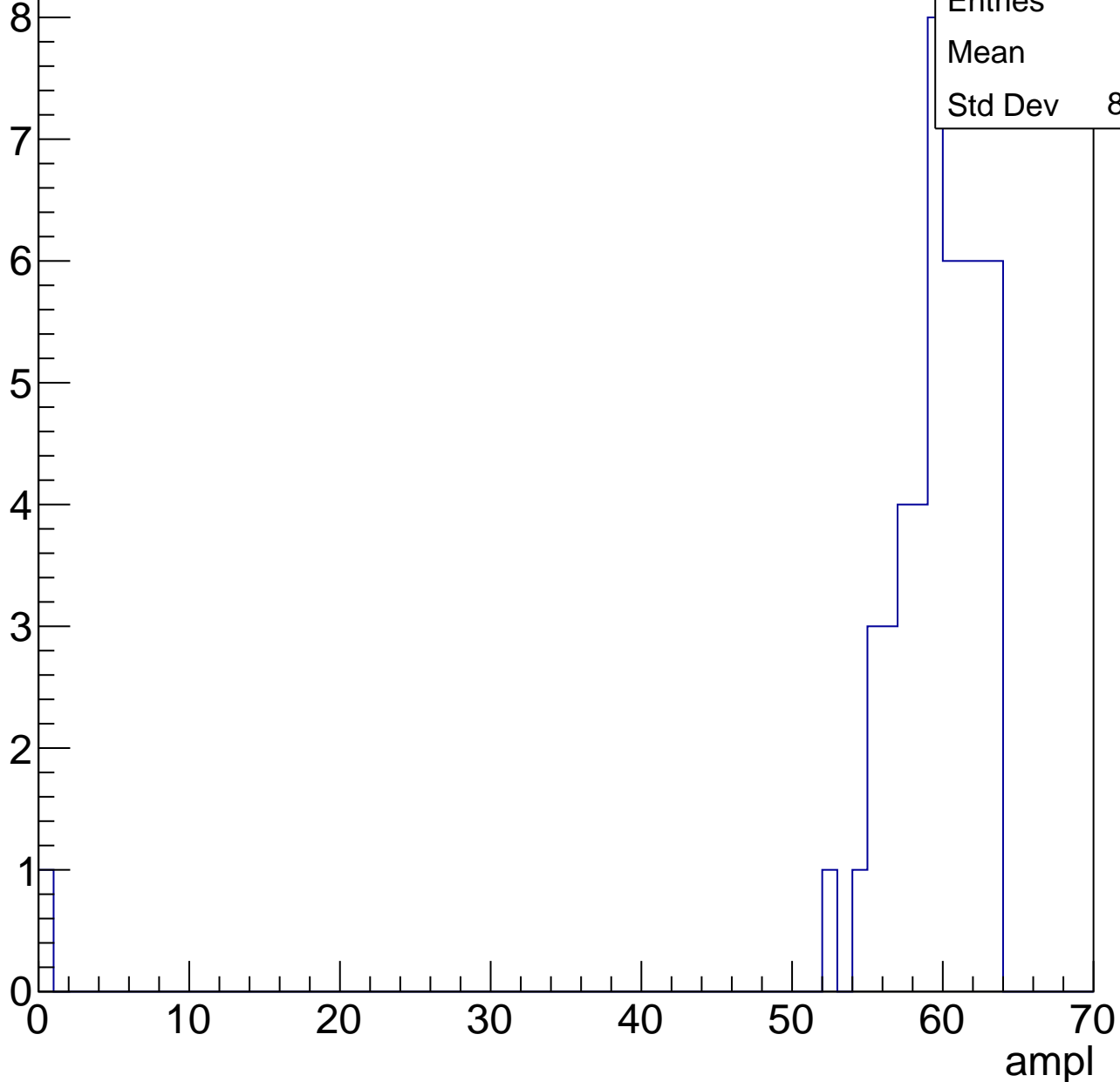
Entries	44
Mean	55.89
Std Dev	2.74



B1L101S, U6-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

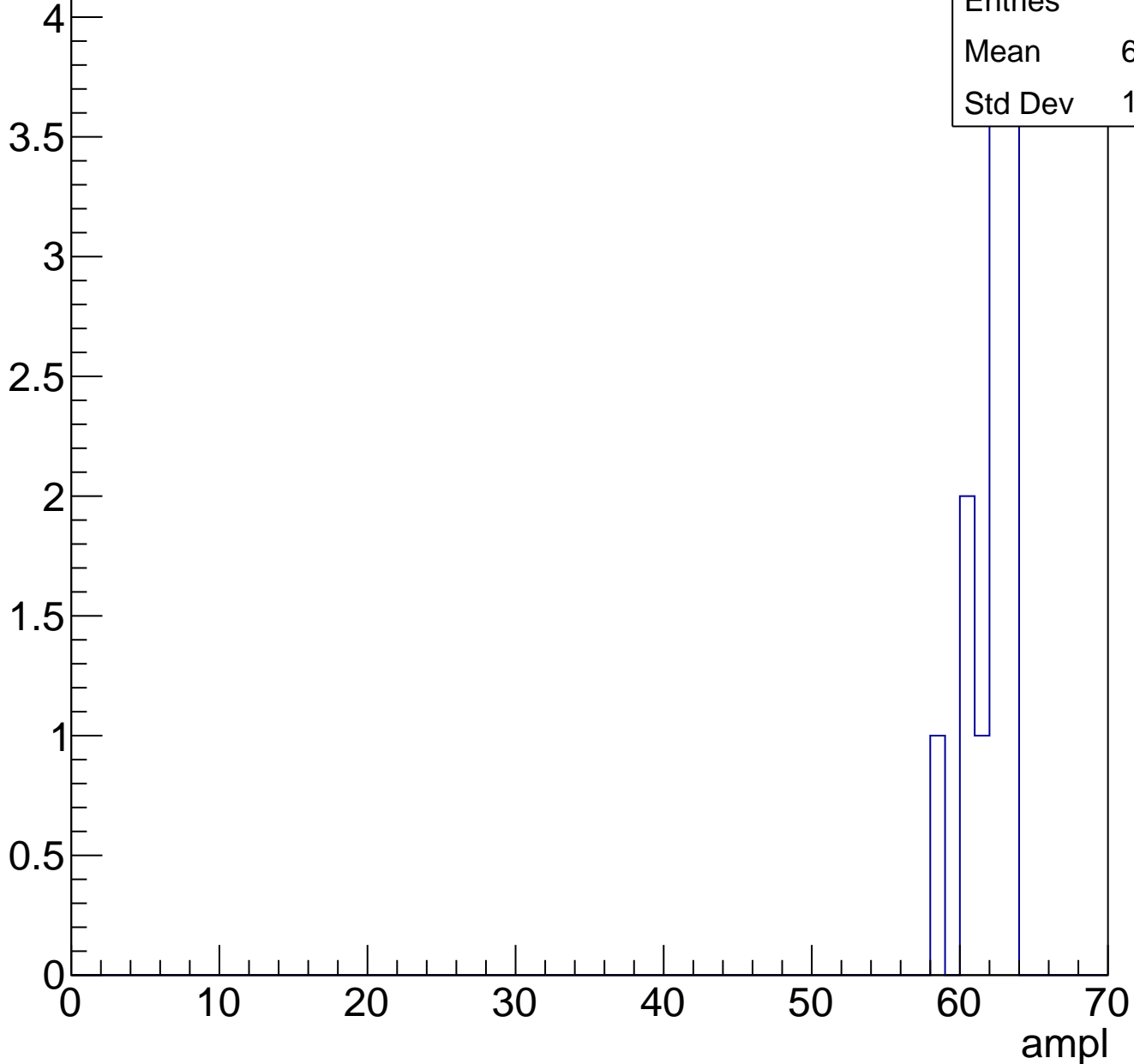
Entry



B1L101S, U6-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch7, adc0

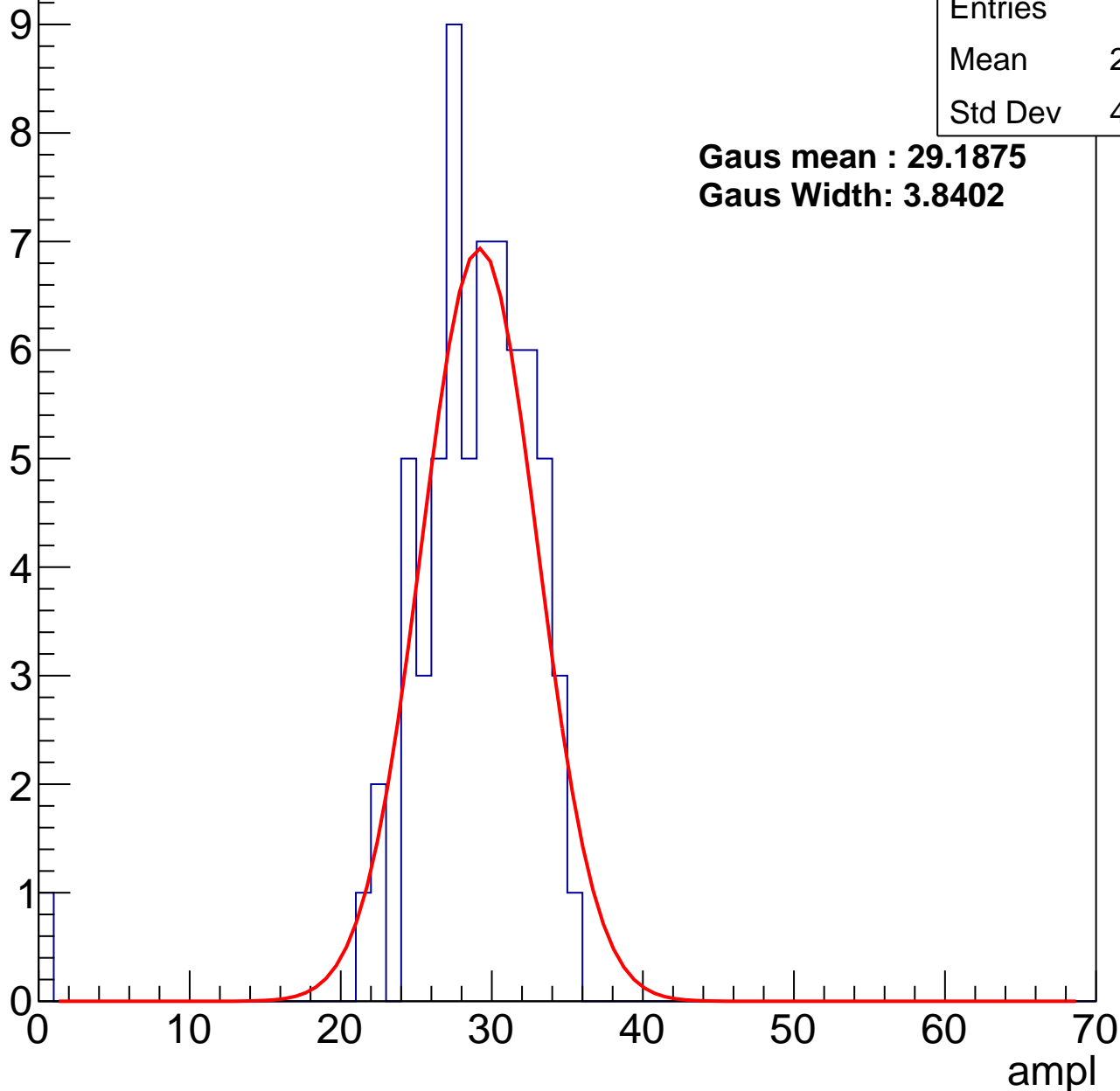
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.27
Std Dev	4.772

Gaus mean : 29.1875

Gaus Width: 3.8402



B1L101S, U6-ch7, adc1

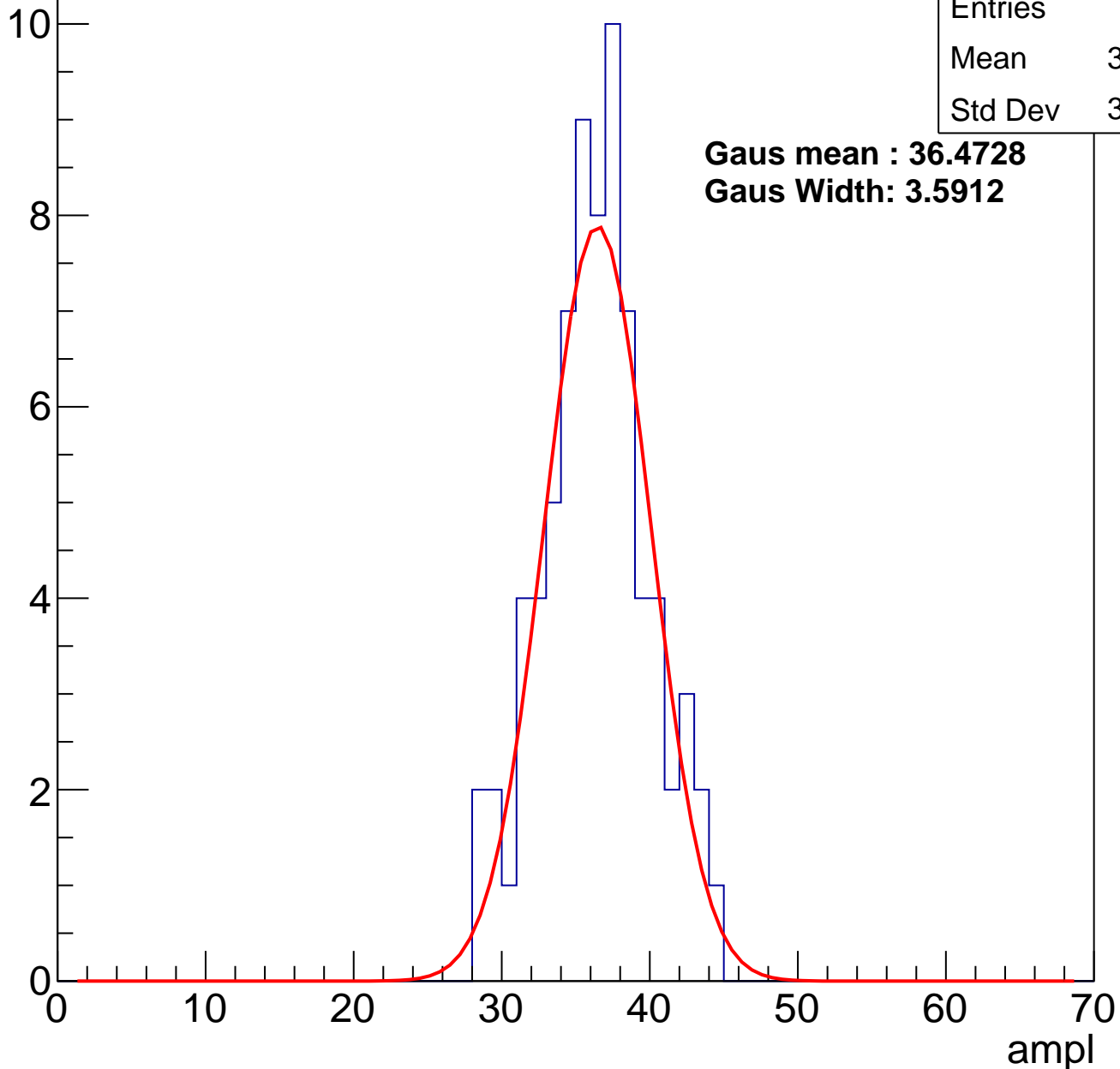
calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	35.89
Std Dev	3.587

Gaus mean : 36.4728

Gaus Width: 3.5912

Entry



B1L101S, U6-ch7, adc2

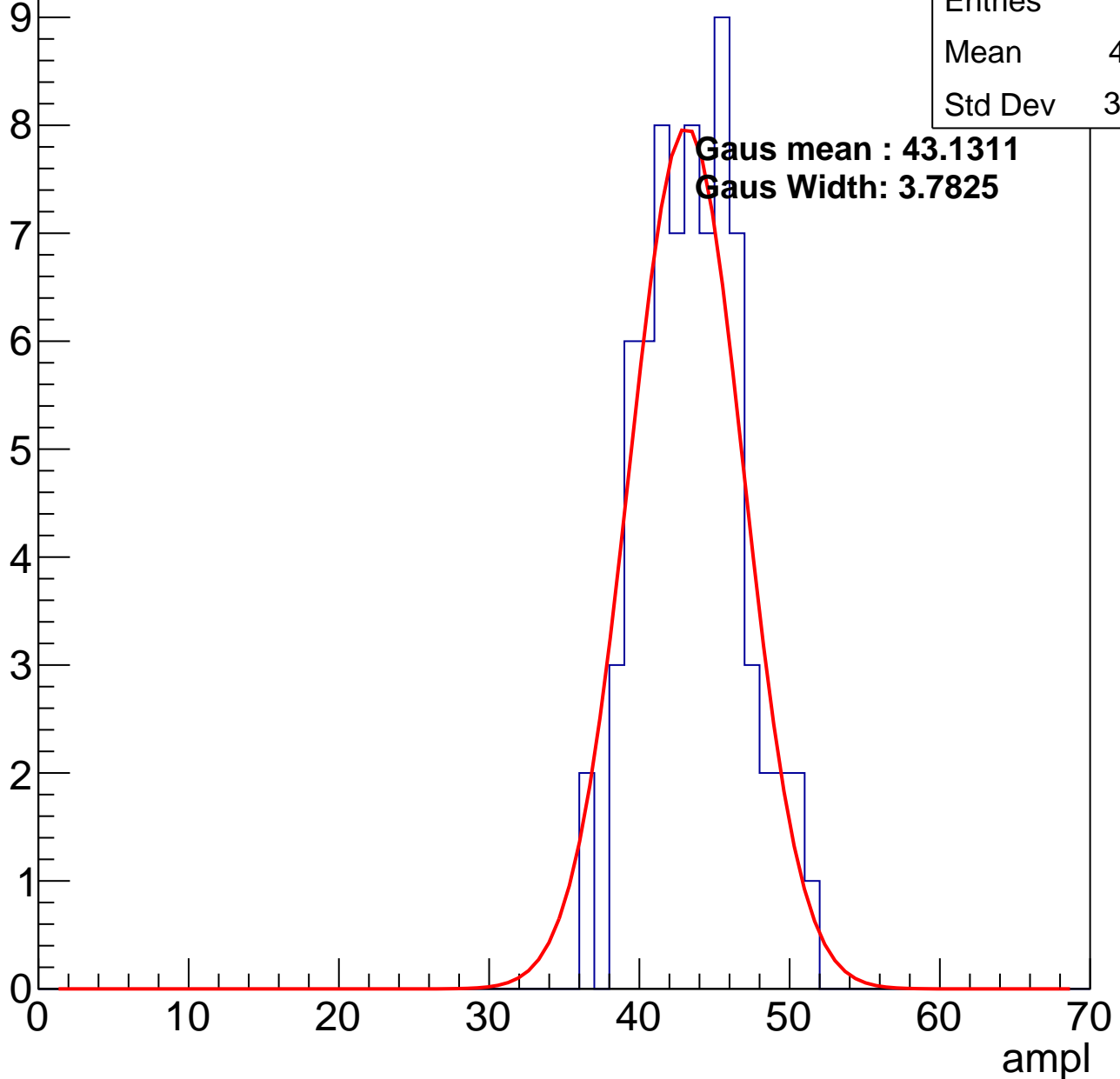
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.11
Std Dev	3.309

Gaus mean : 43.1311

Gaus Width: 3.7825

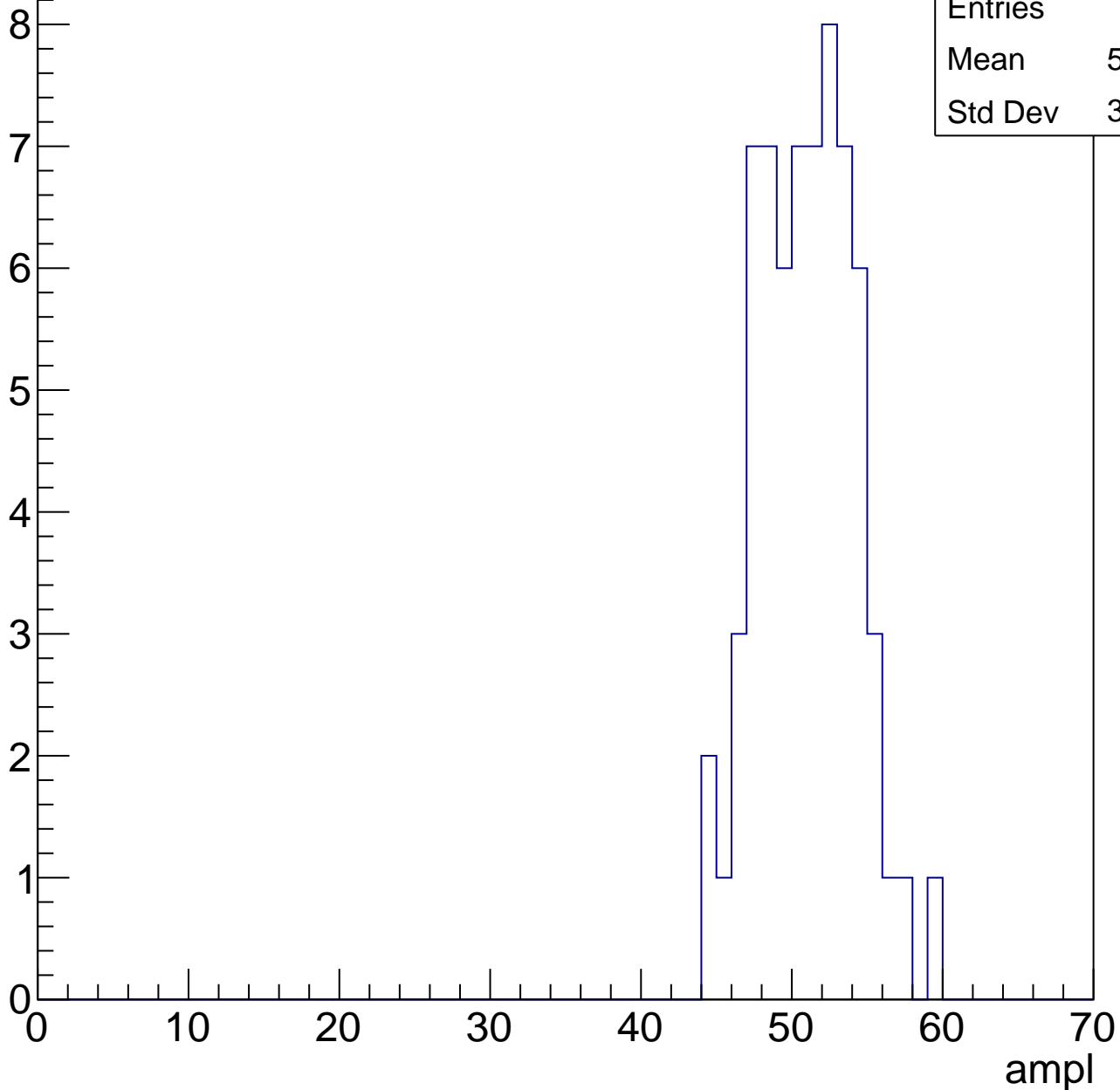


B1L101S, U6-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.52
Std Dev	3.145

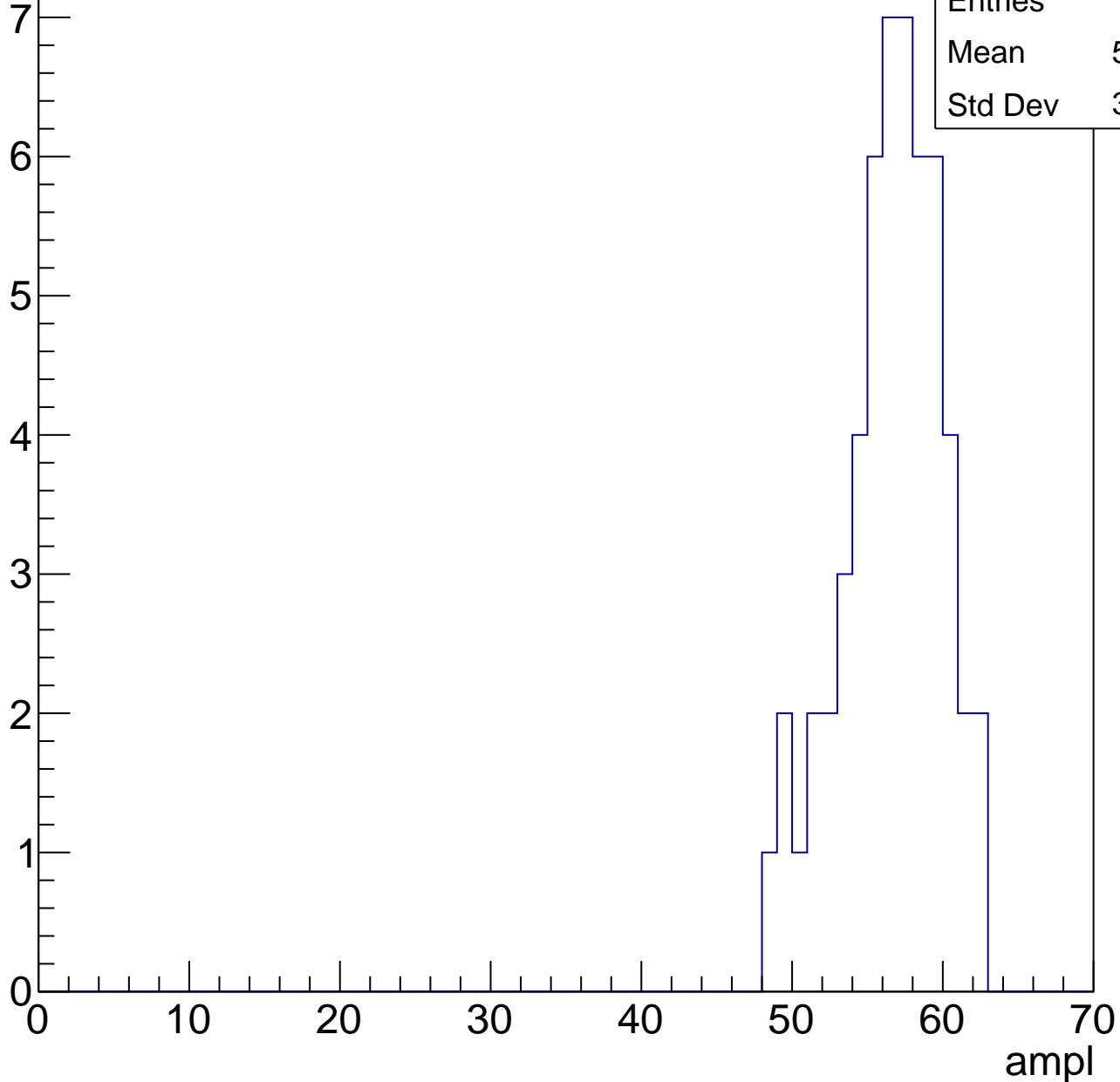


B1L101S, U6-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.11
Std Dev	3.301

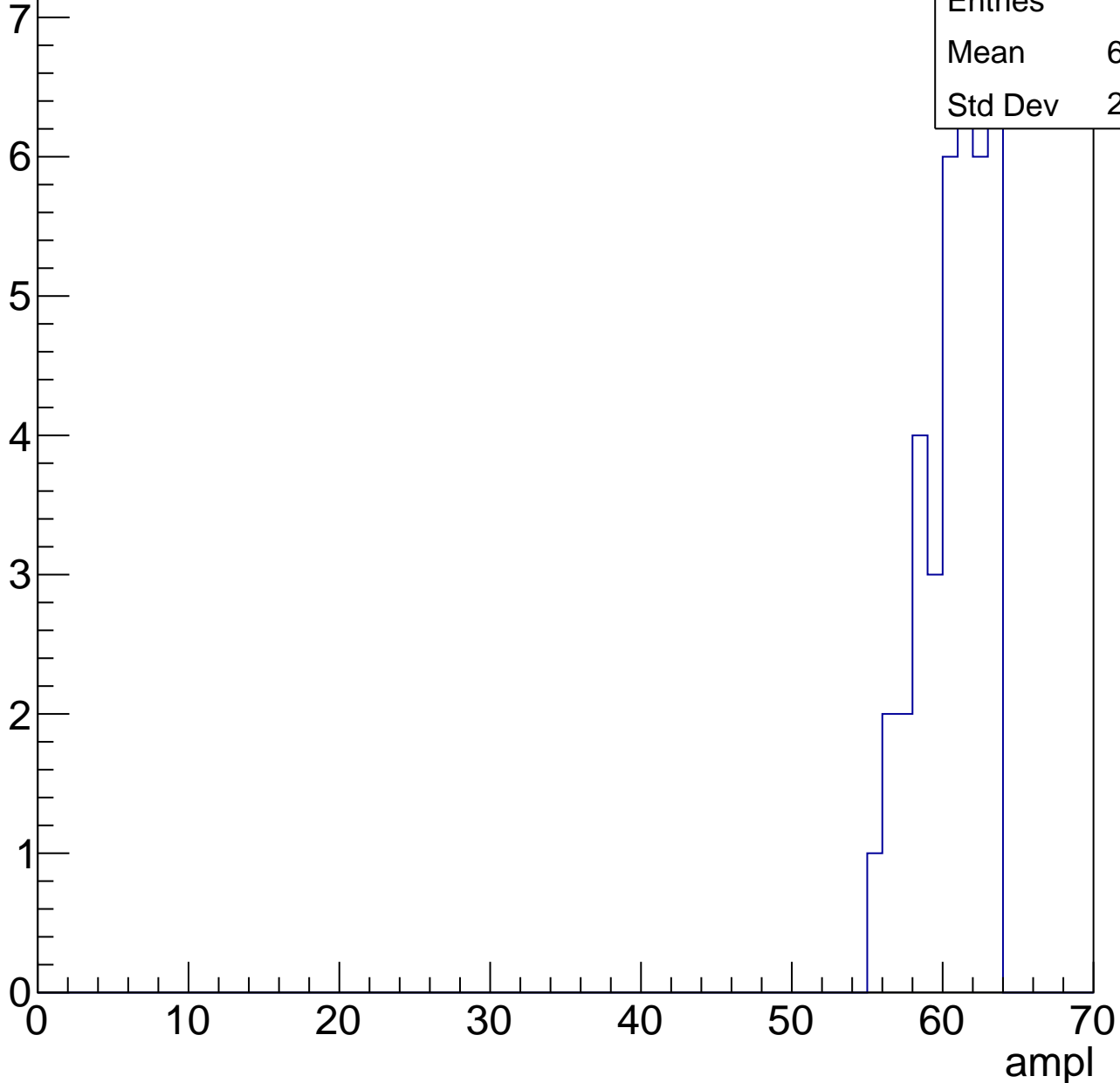


B1L101S, U6-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	60.26
Std Dev	2.209



B1L101S, U6-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch8, adc0

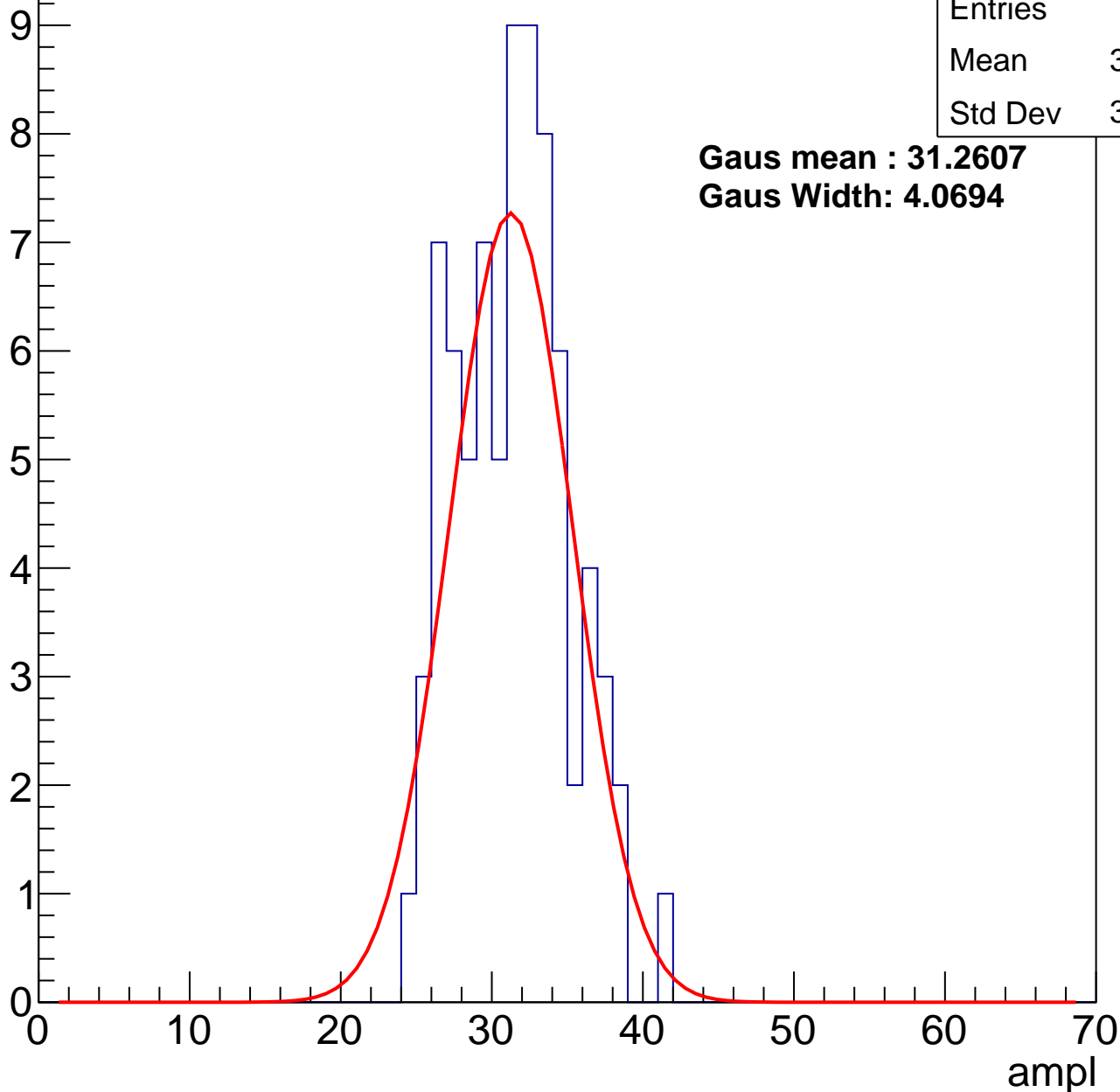
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.94
Std Dev	3.649

Gaus mean : 31.2607

Gaus Width: 4.0694



B1L101S, U6-ch8, adc1

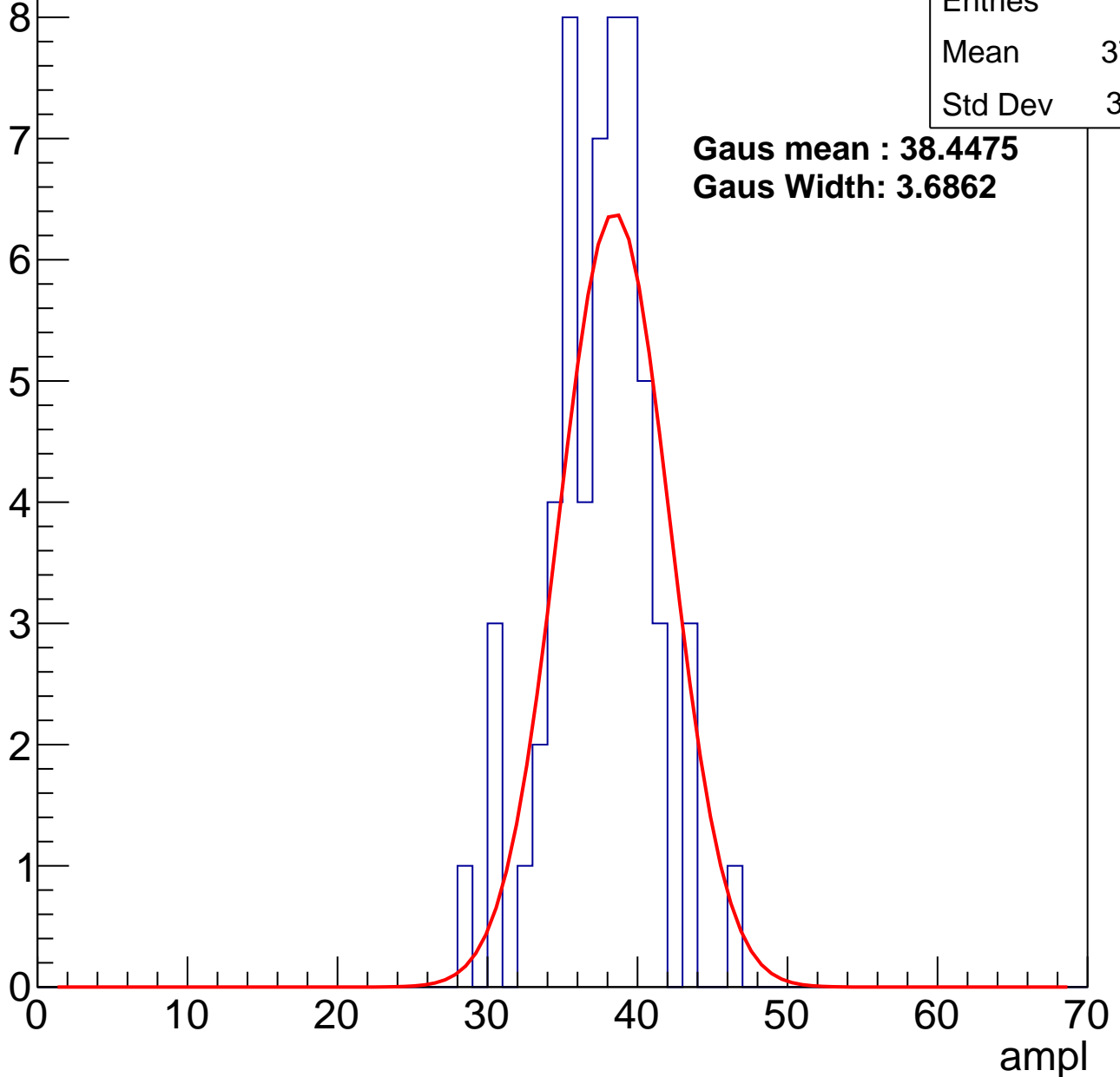
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.05
Std Dev	3.421

Gaus mean : 38.4475

Gaus Width: 3.6862



B1L101S, U6-ch8, adc2

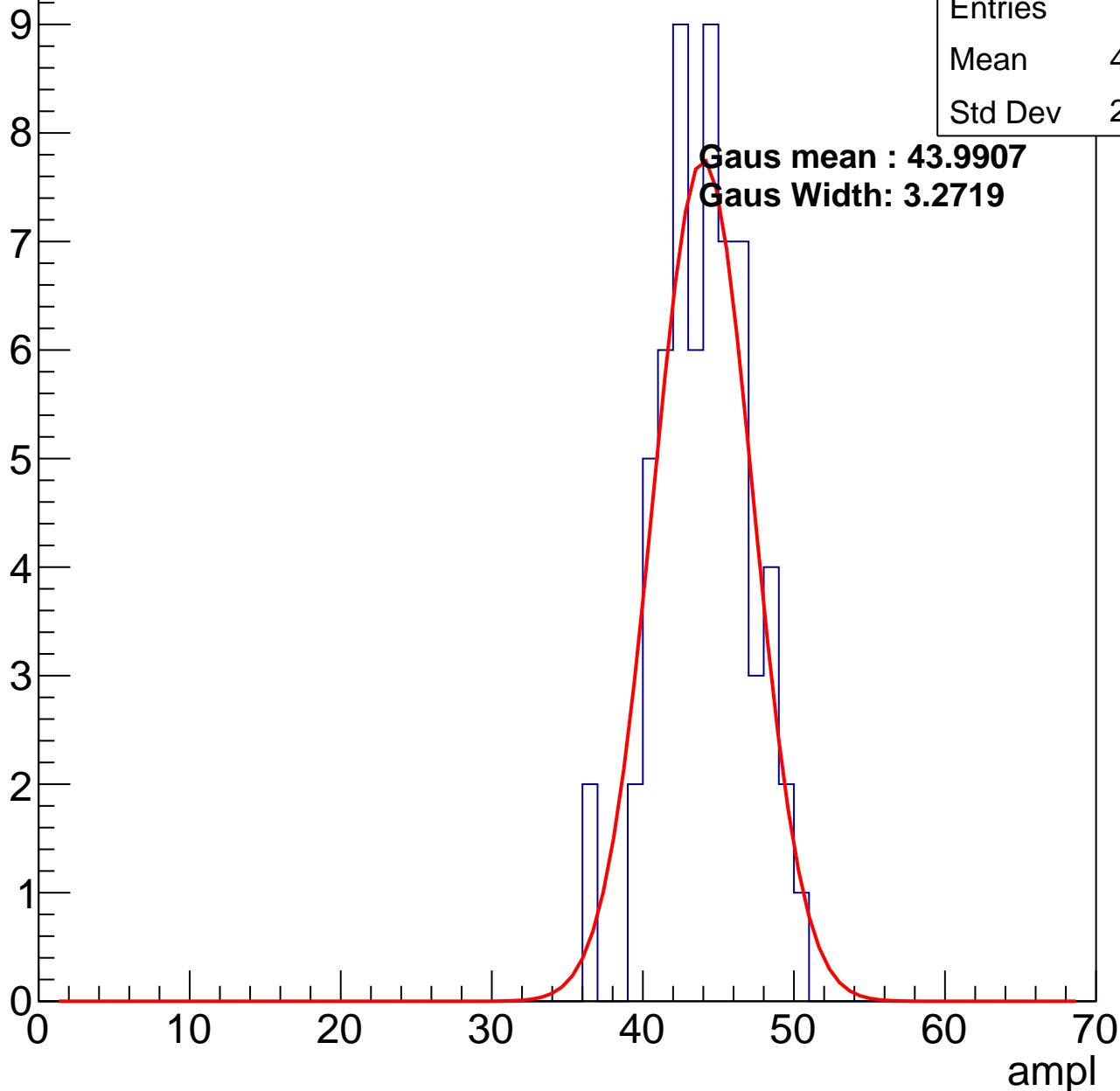
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.59
Std Dev	2.969

Gaus mean : 43.9907

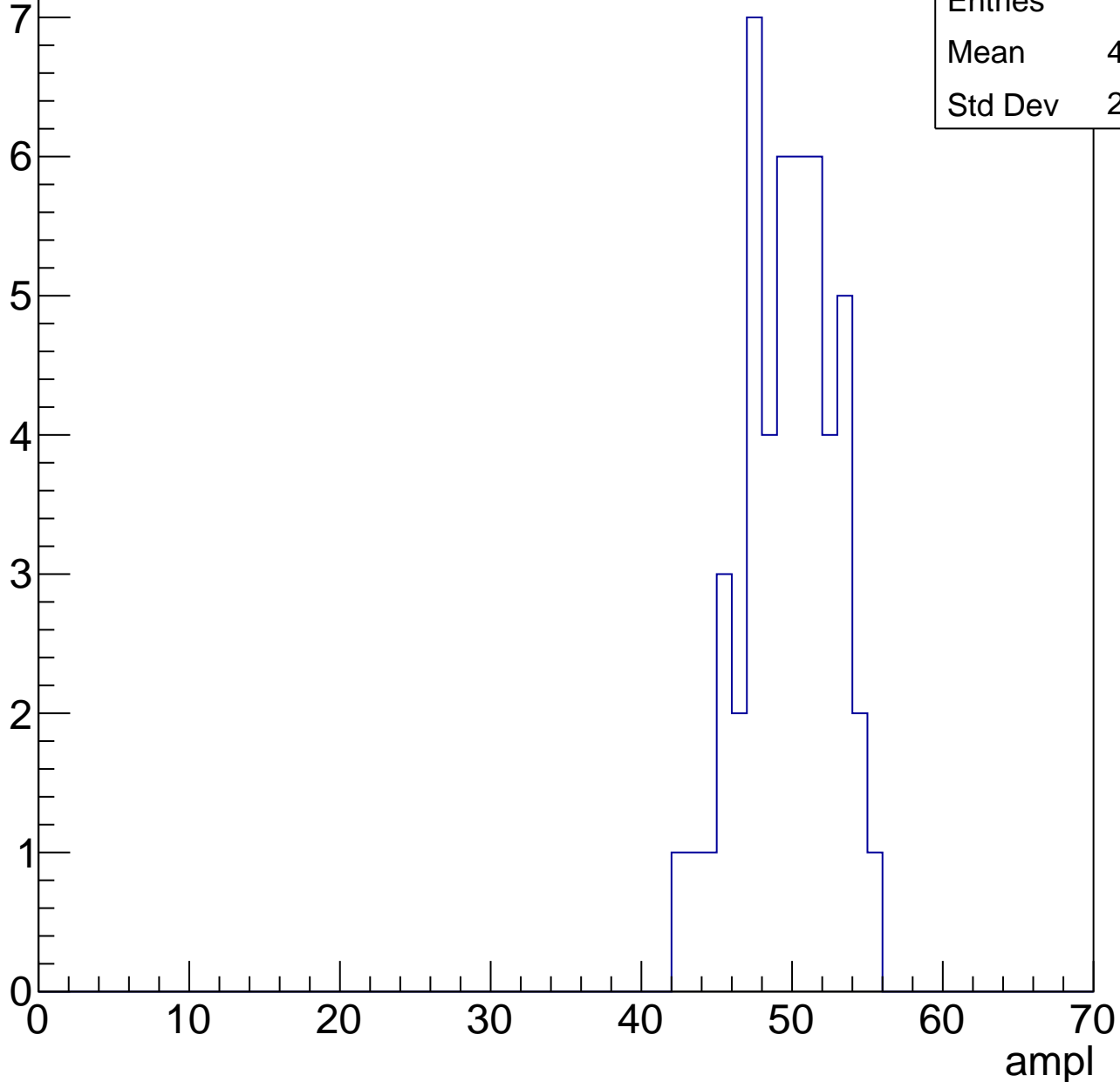
Gaus Width: 3.2719



B1L101S, U6-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

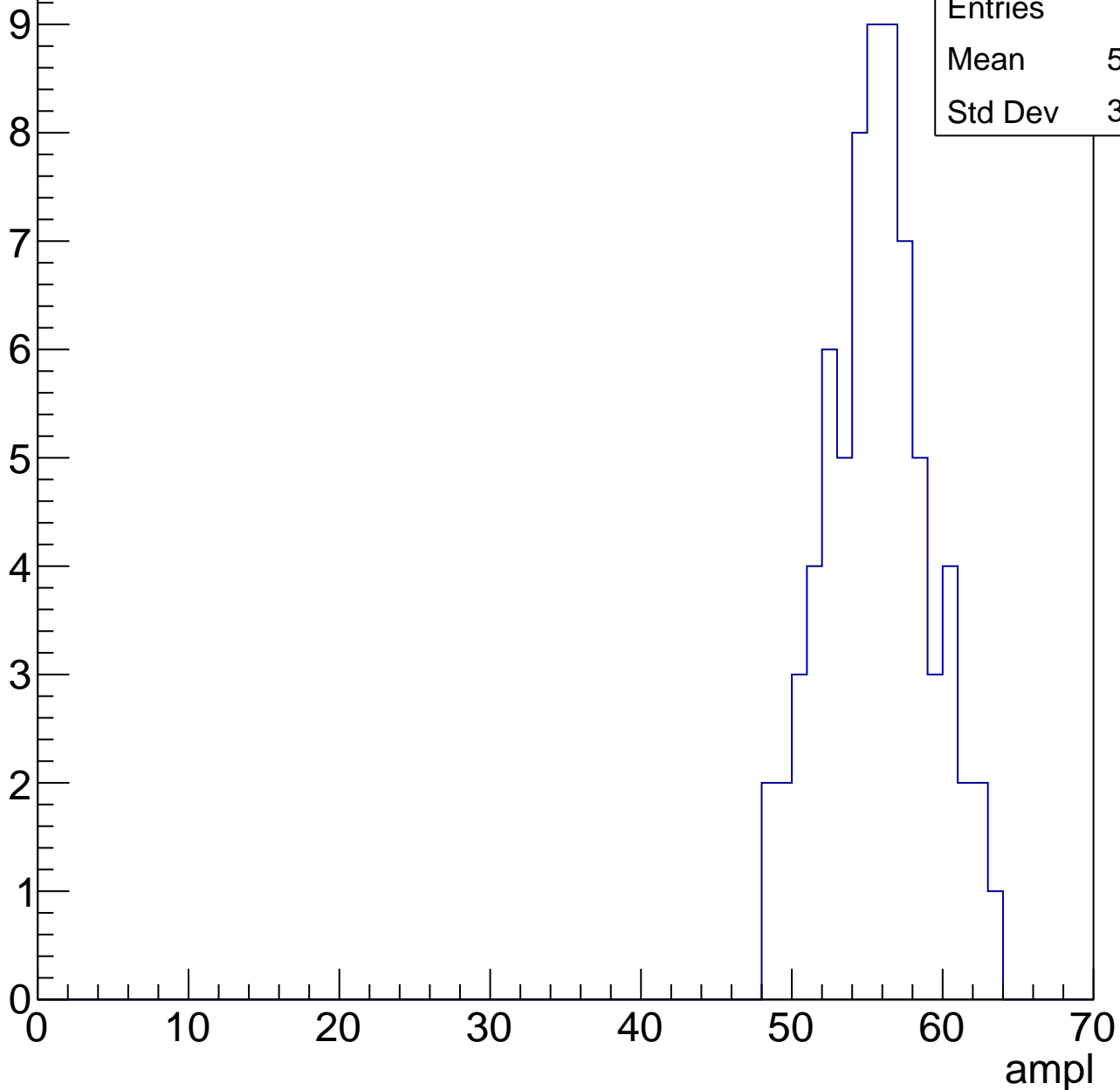


Entries	49
Mean	49.24
Std Dev	2.987

B1L101S, U6-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

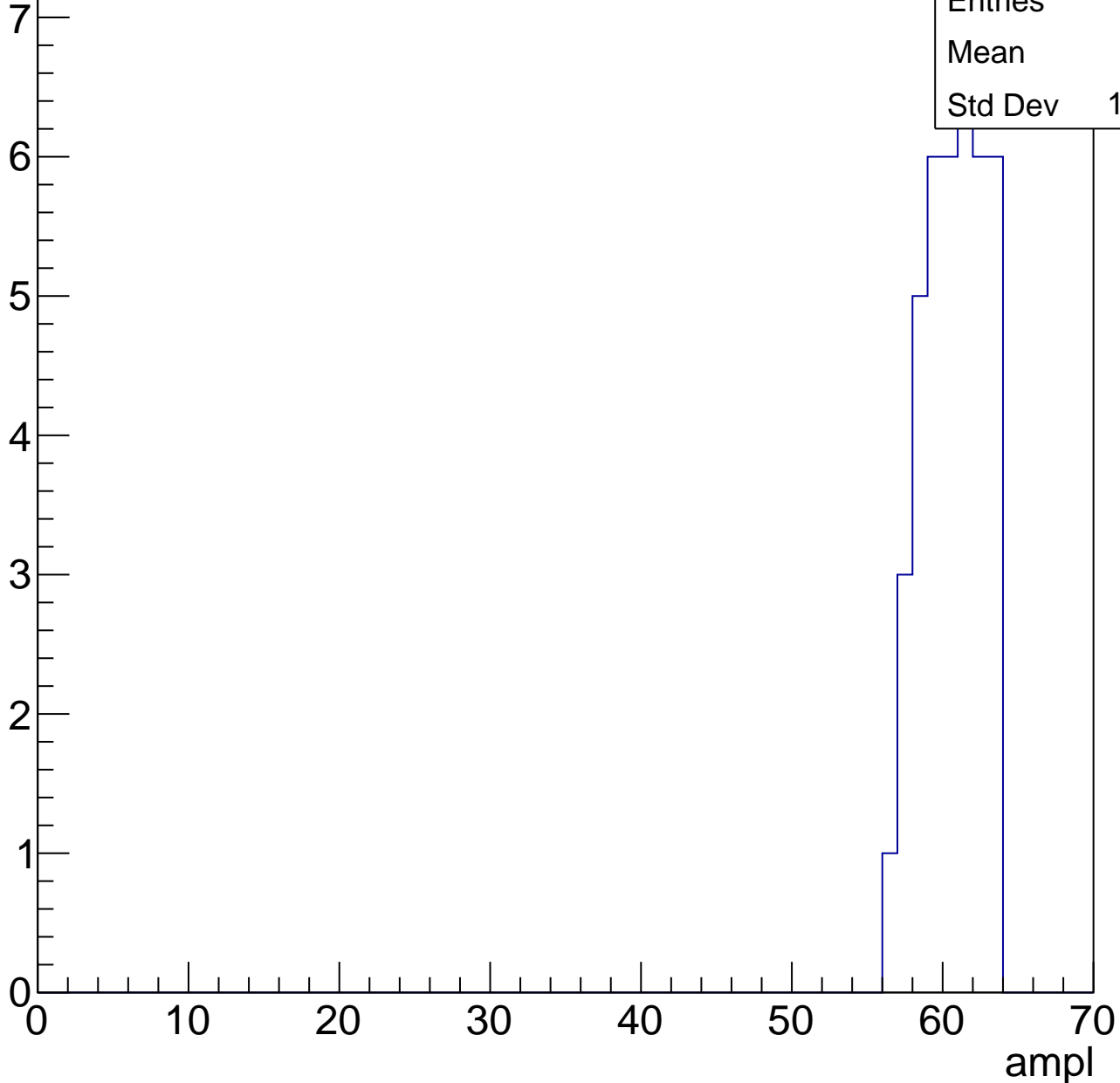


B1L101S, U6-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

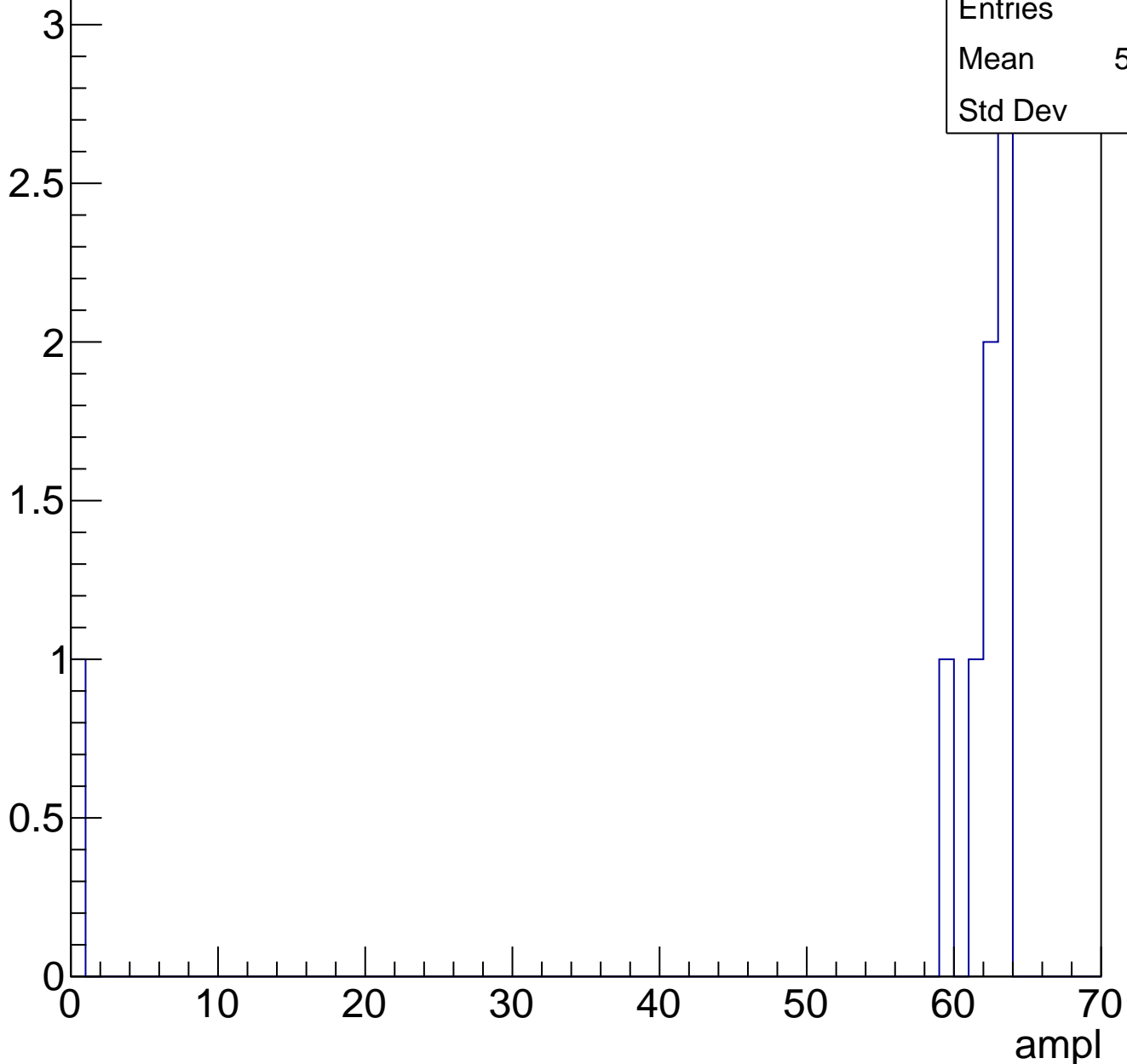
Entries	40
Mean	60.2
Std Dev	1.952



B1L101S, U6-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U6-ch9, adc0

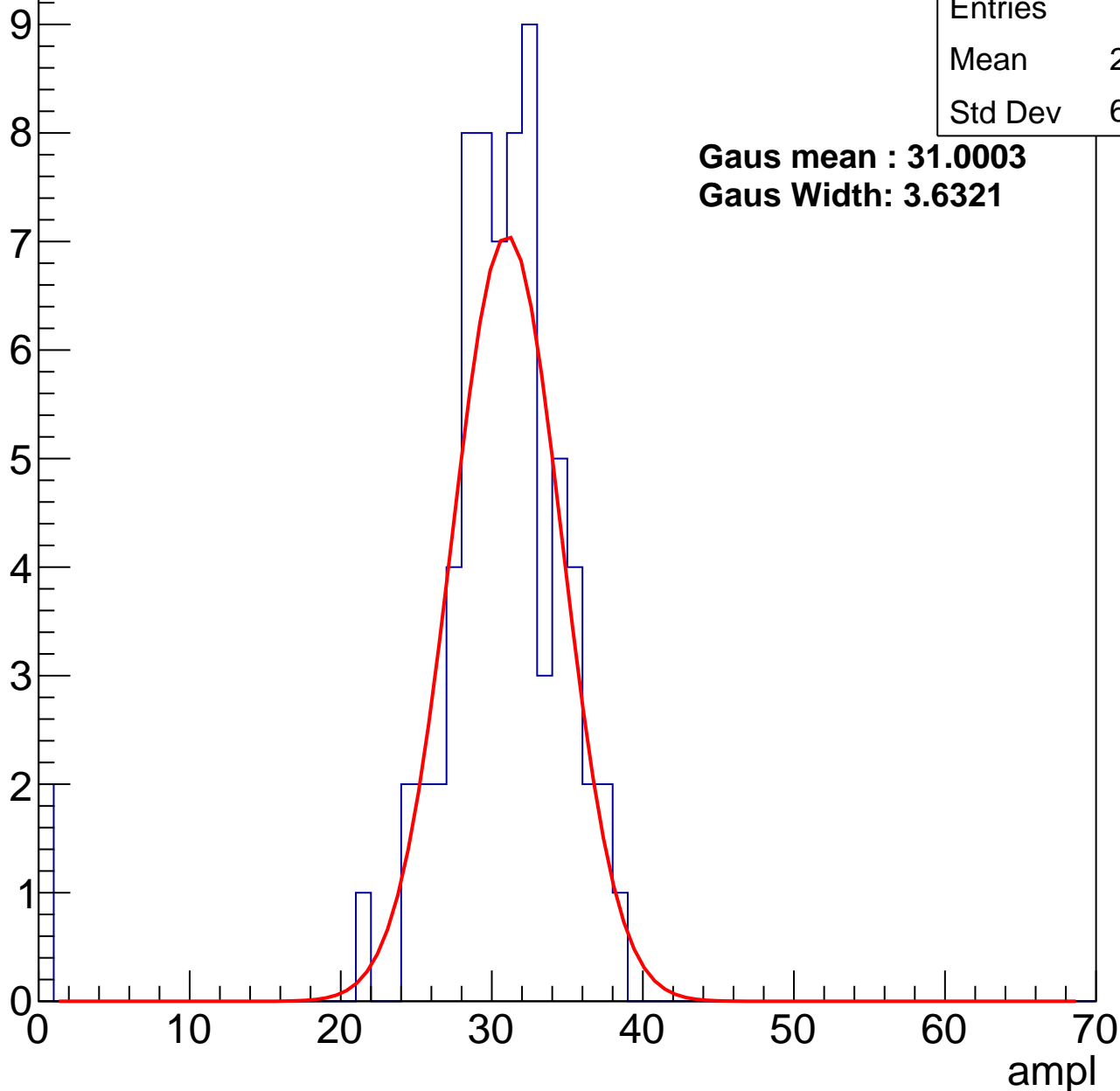
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.63
Std Dev	6.078

Gaus mean : 31.0003

Gaus Width: 3.6321



B1L101S, U6-ch9, adc1

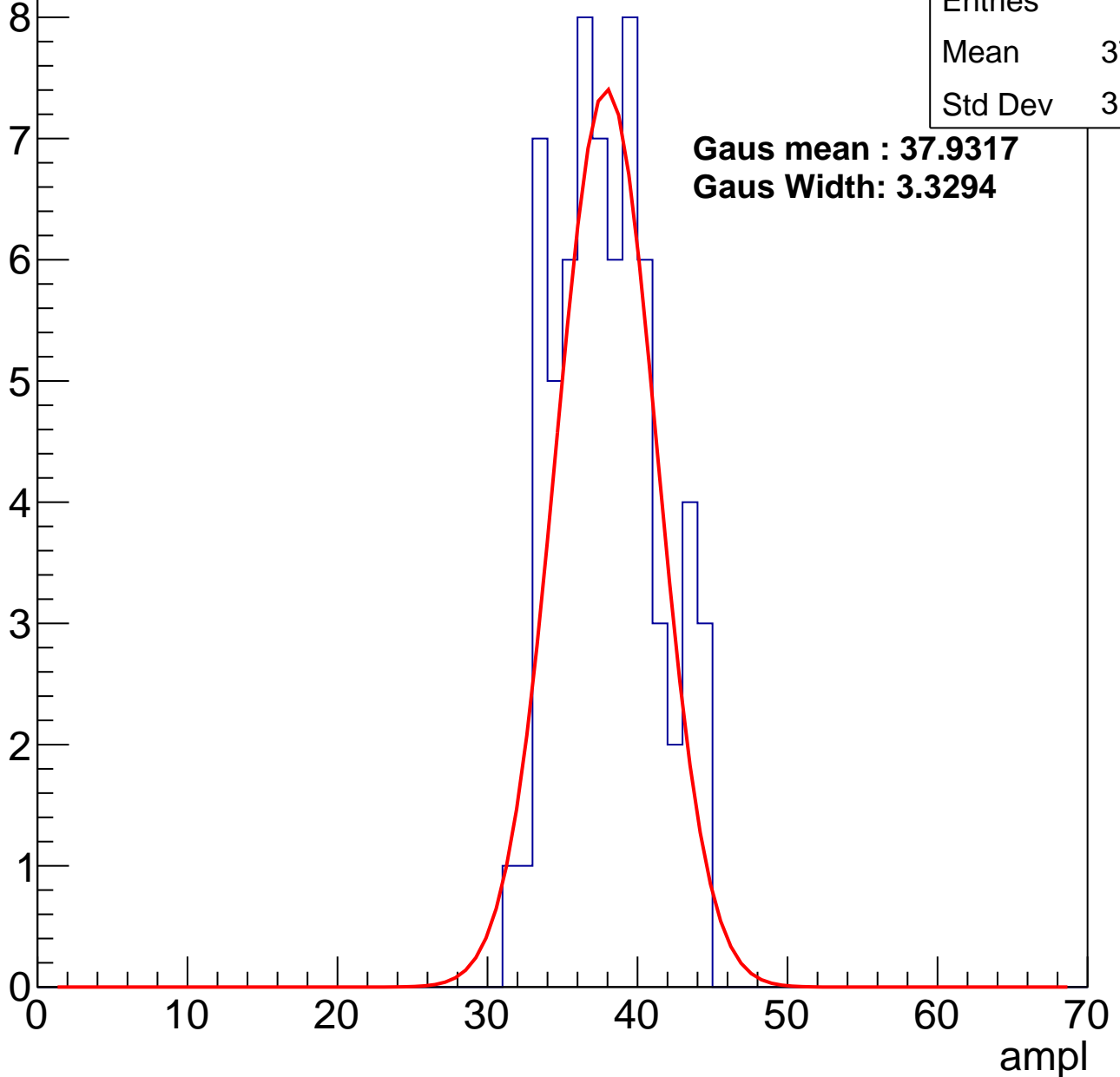
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.49
Std Dev	3.266

Gaus mean : 37.9317

Gaus Width: 3.3294



B1L101S, U6-ch9, adc2

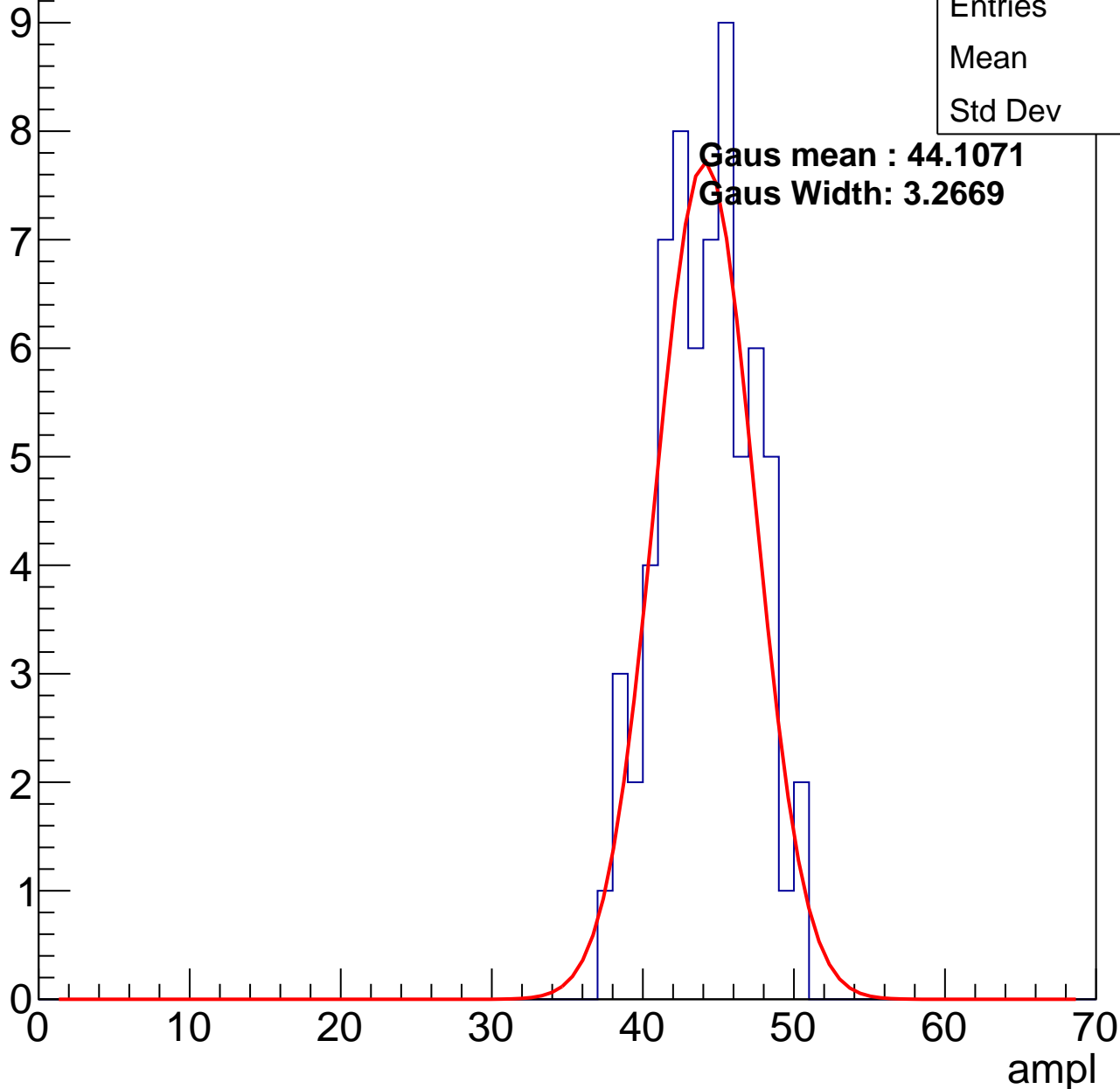
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.7
Std Dev	3.08

Gaus mean : 44.1071

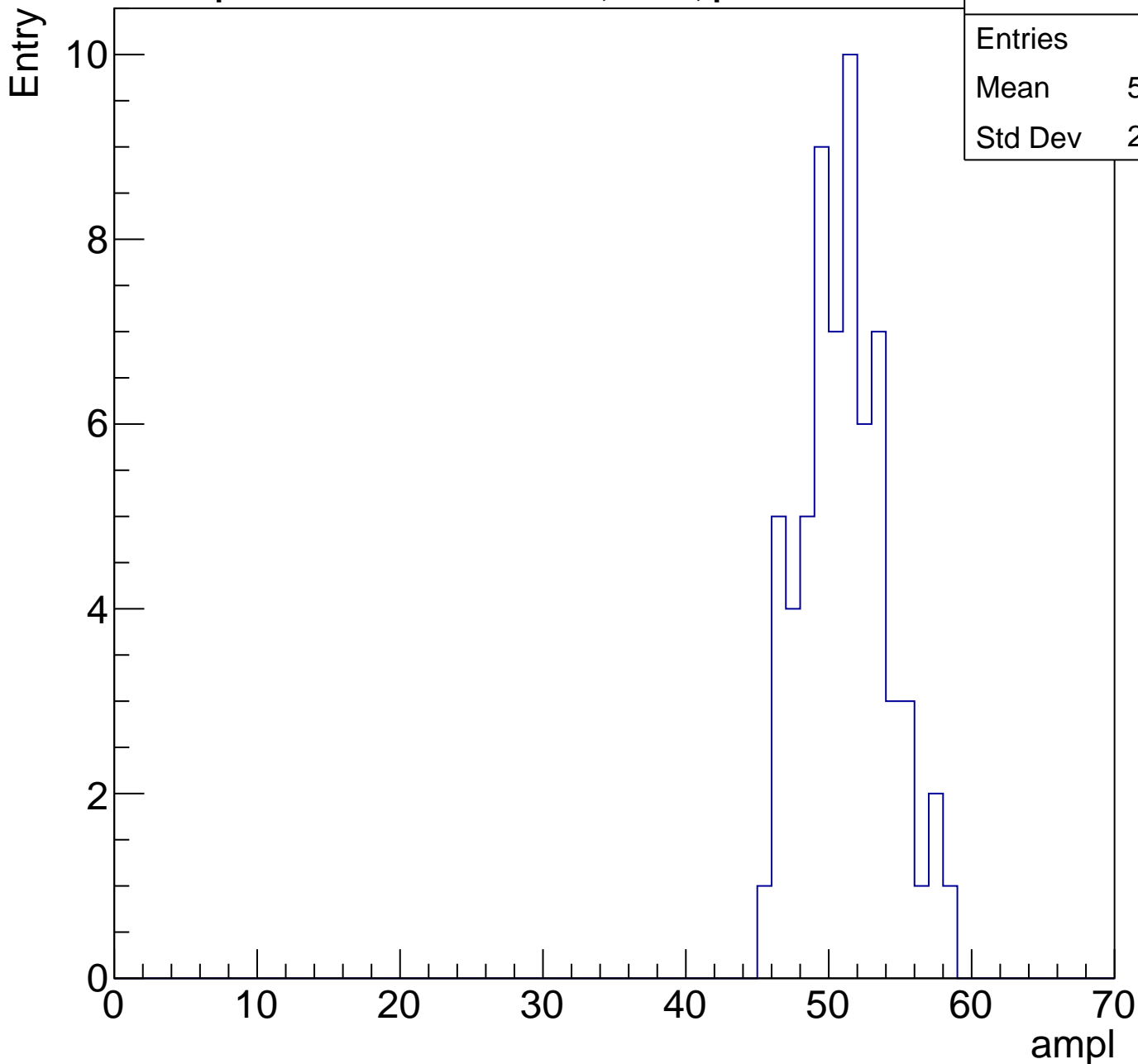
Gaus Width: 3.2669



B1L101S, U6-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

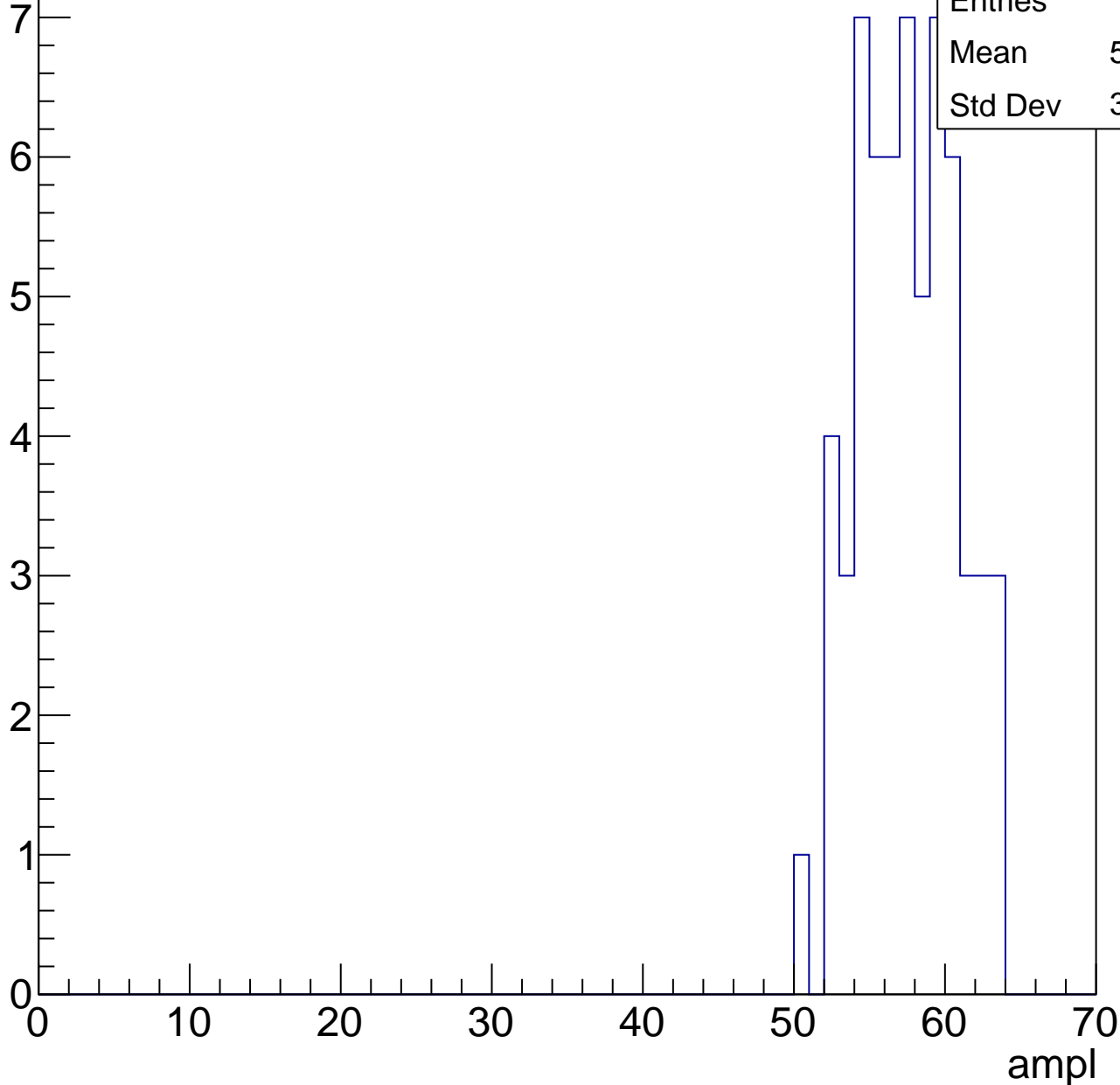
Entries	64
Mean	50.66
Std Dev	2.959



B1L101S, U6-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

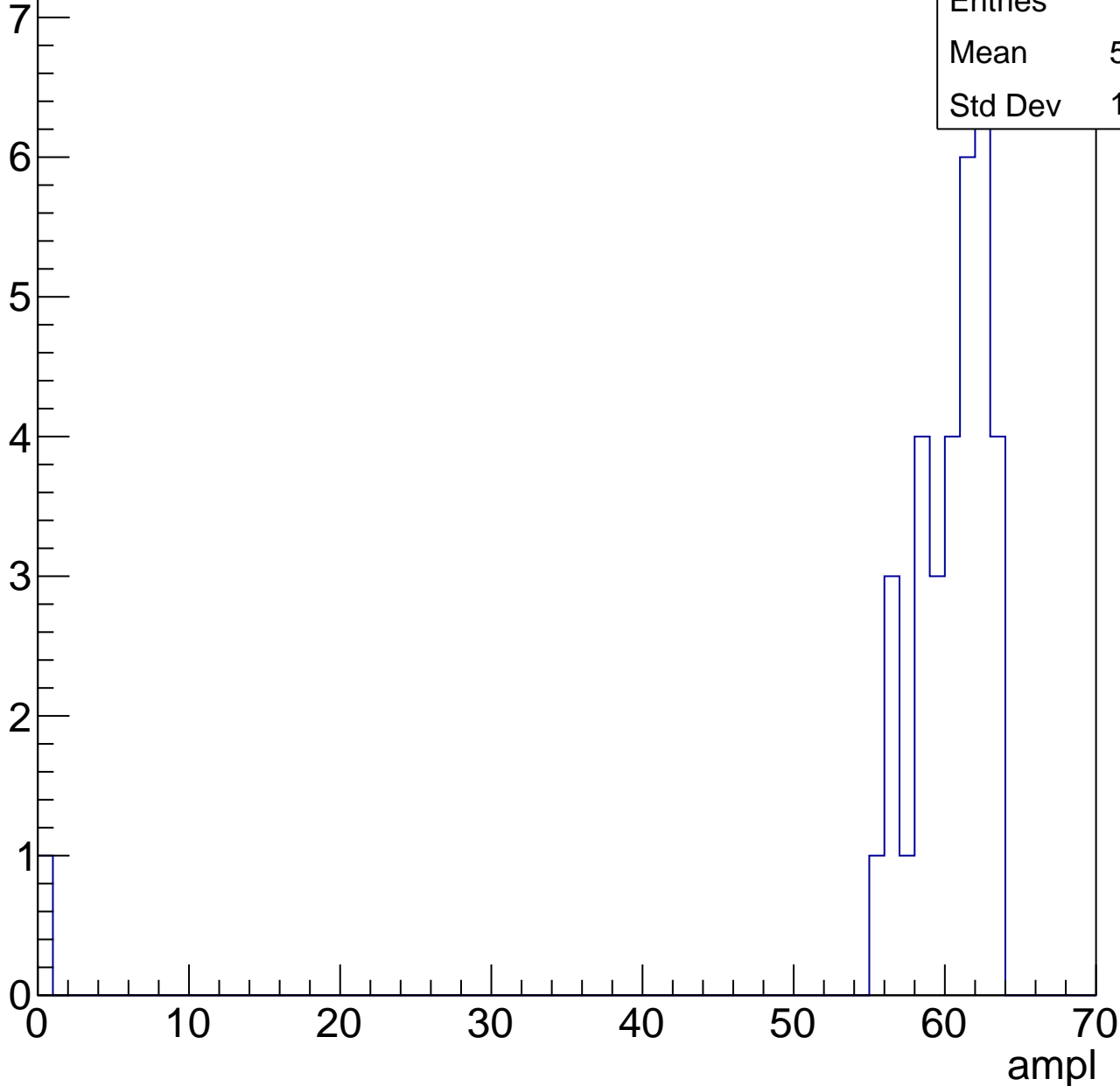


B1L101S, U6-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.26
Std Dev	10.39



B1L101S, U6-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch10, adc0

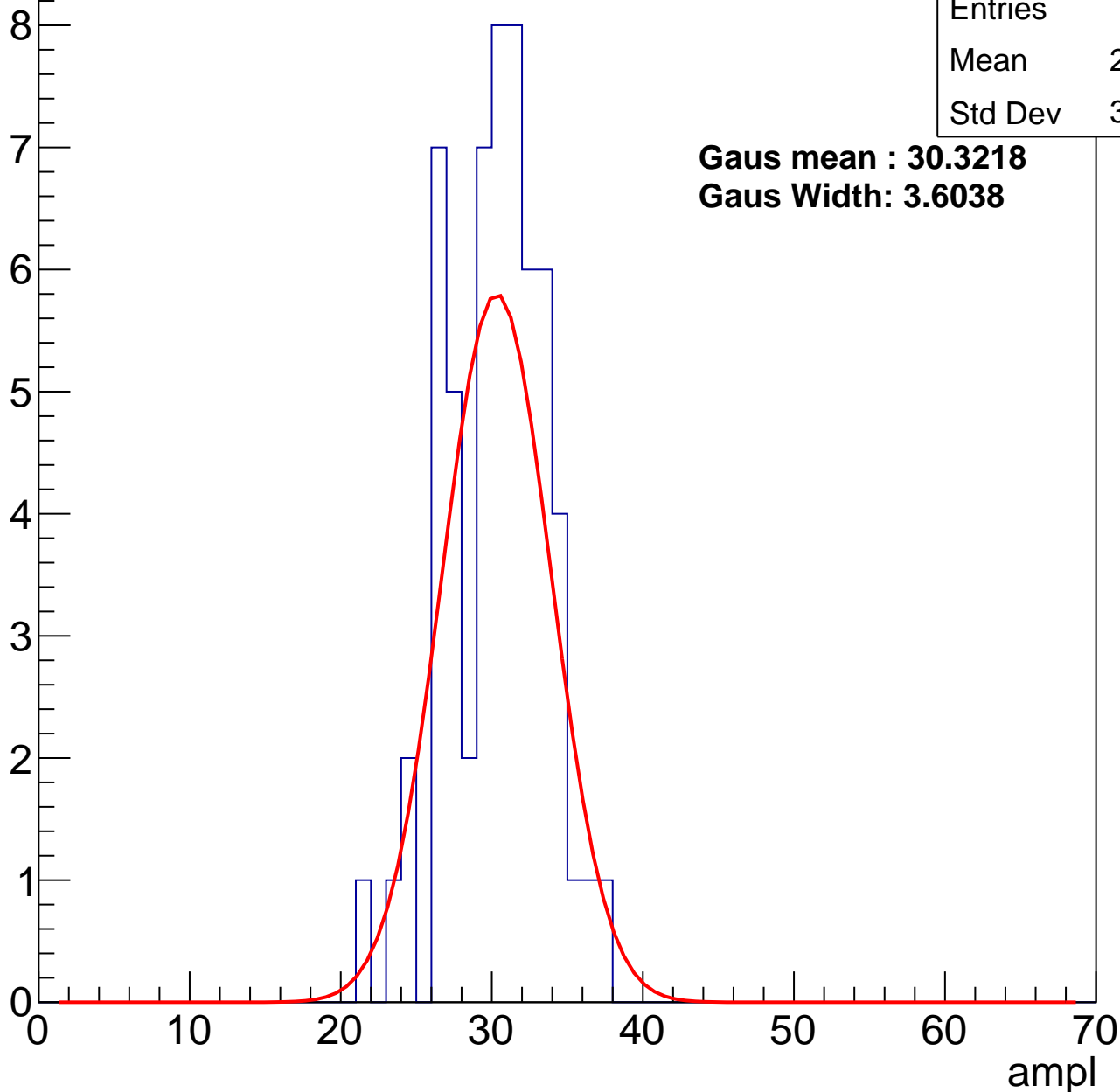
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.83
Std Dev	3.246

Gaus mean : 30.3218

Gaus Width: 3.6038



B1L101S, U6-ch10, adc1

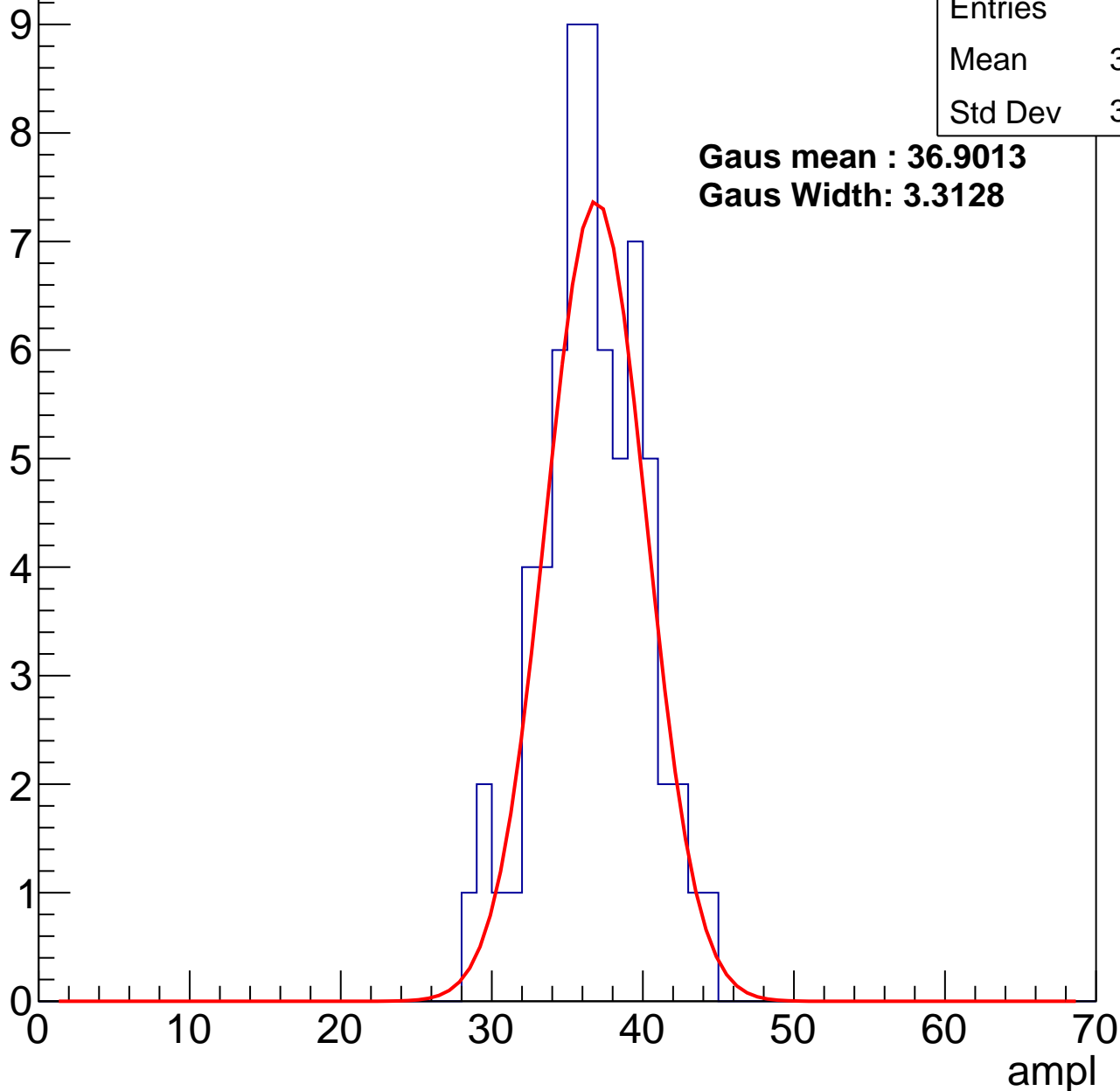
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.18
Std Dev	3.389

Gaus mean : 36.9013

Gaus Width: 3.3128



B1L101S, U6-ch10, adc2

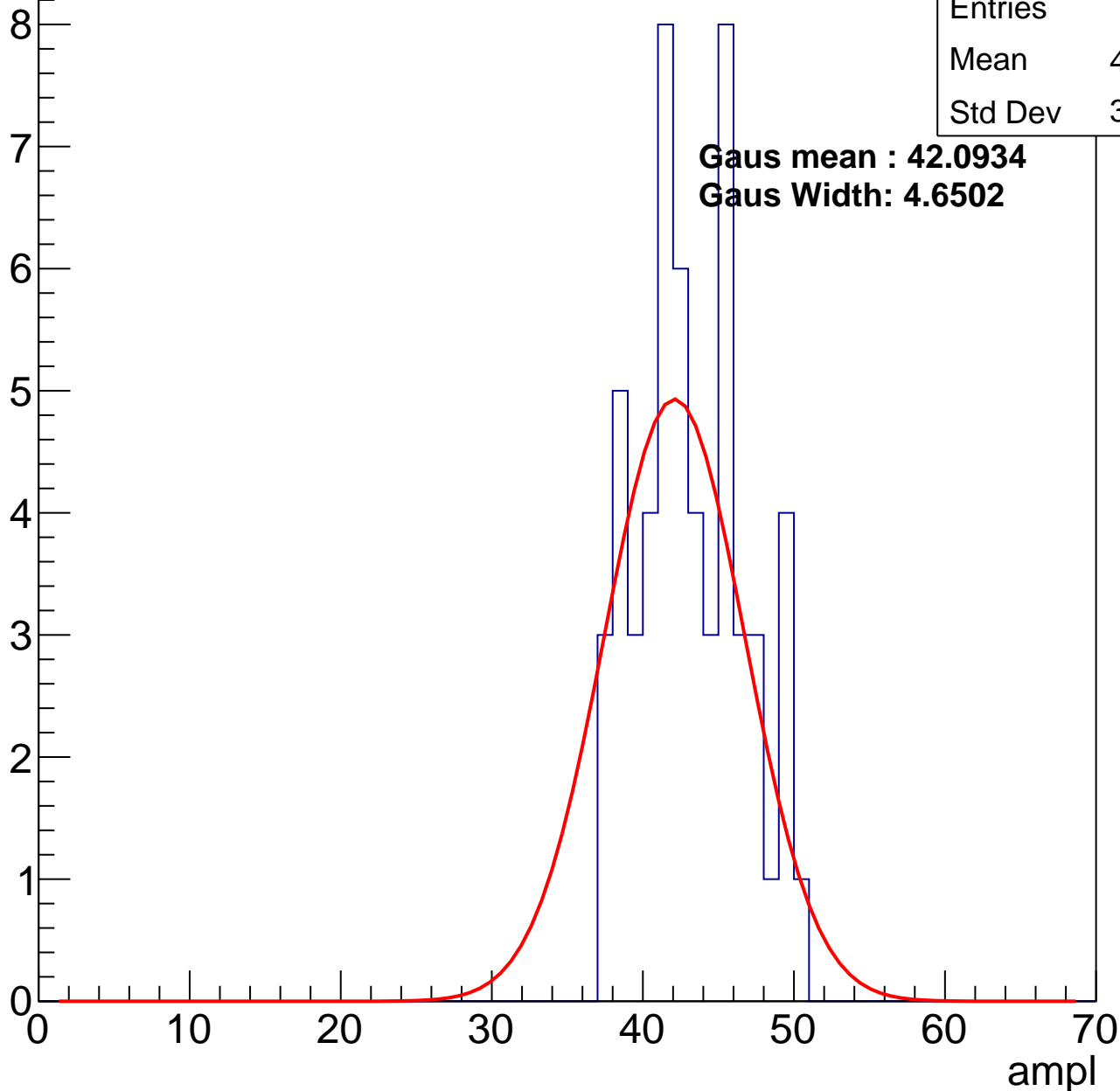
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.77
Std Dev	3.485

Gaus mean : 42.0934

Gaus Width: 4.6502

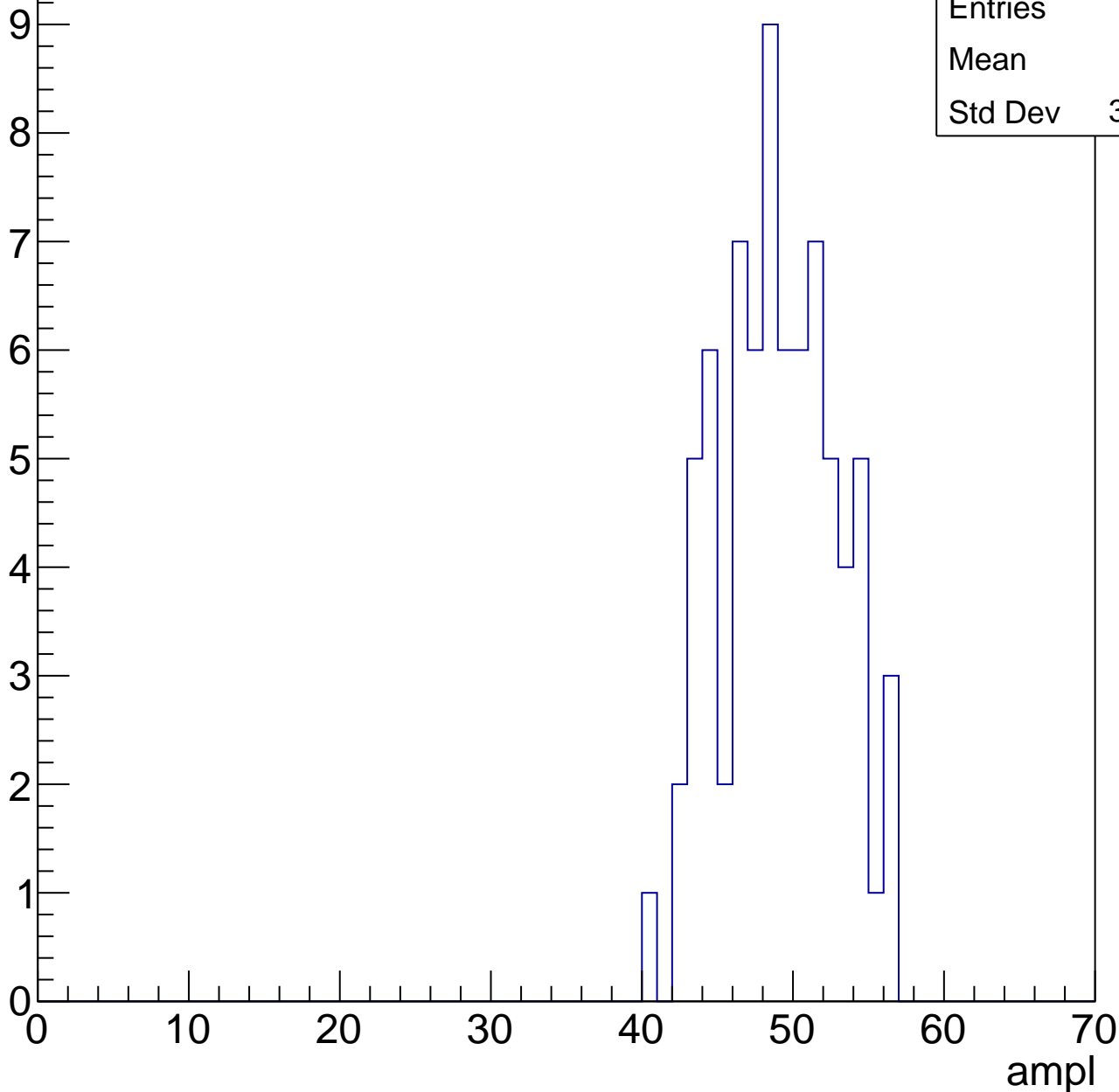


B1L101S, U6-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	48.6
Std Dev	3.788

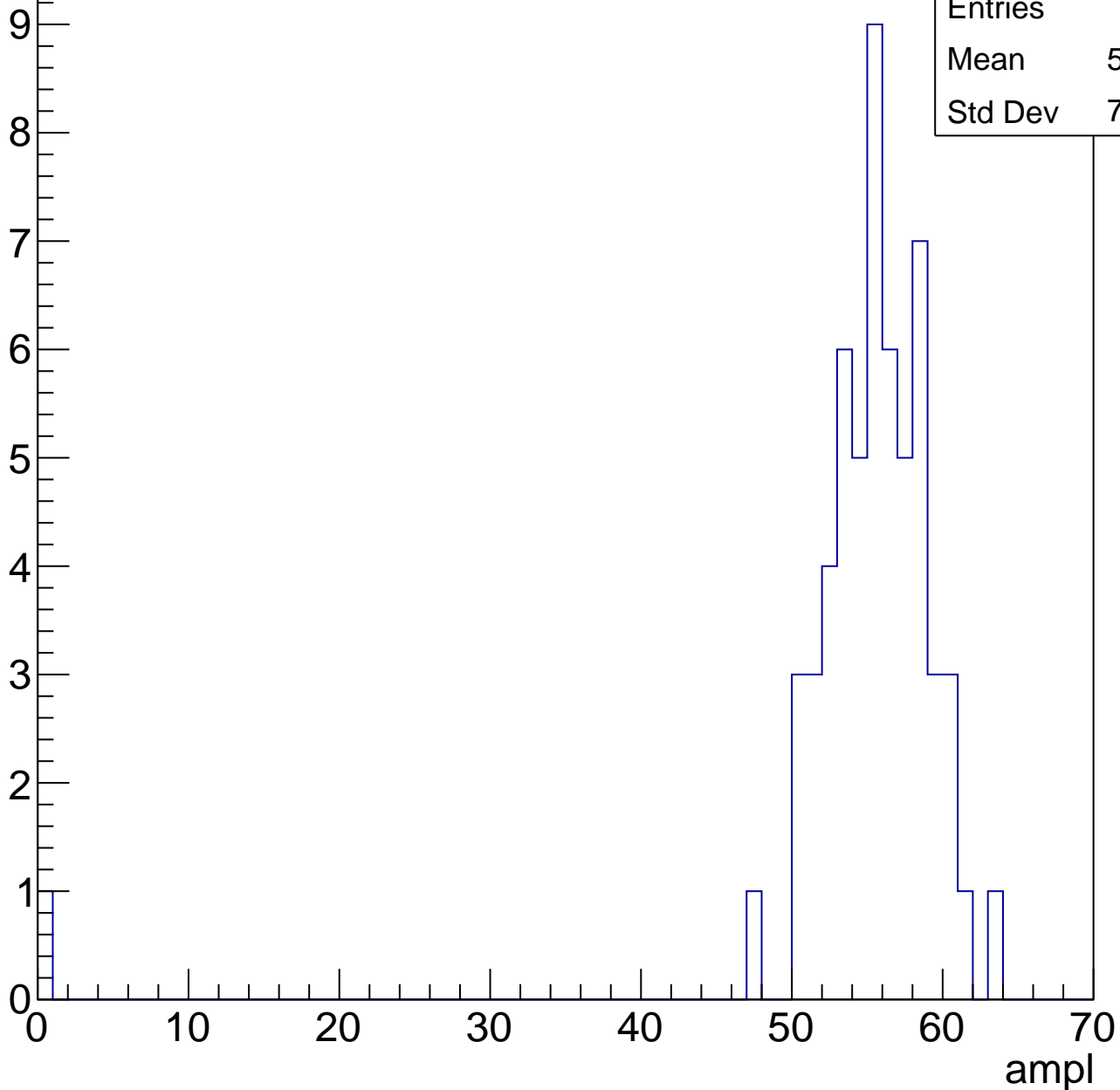


B1L101S, U6-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

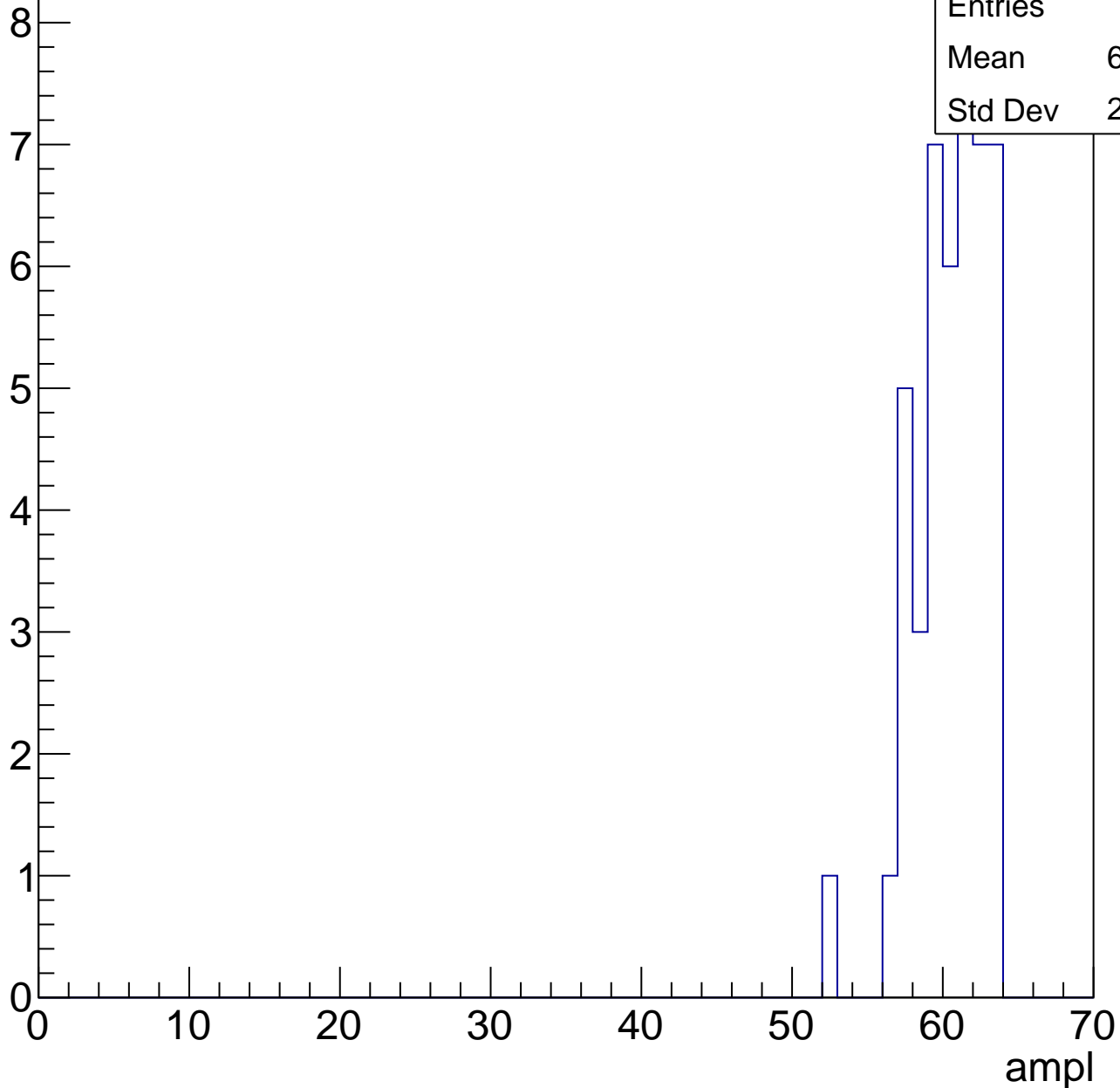
Entries	58
Mean	54.29
Std Dev	7.835



B1L101S, U6-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

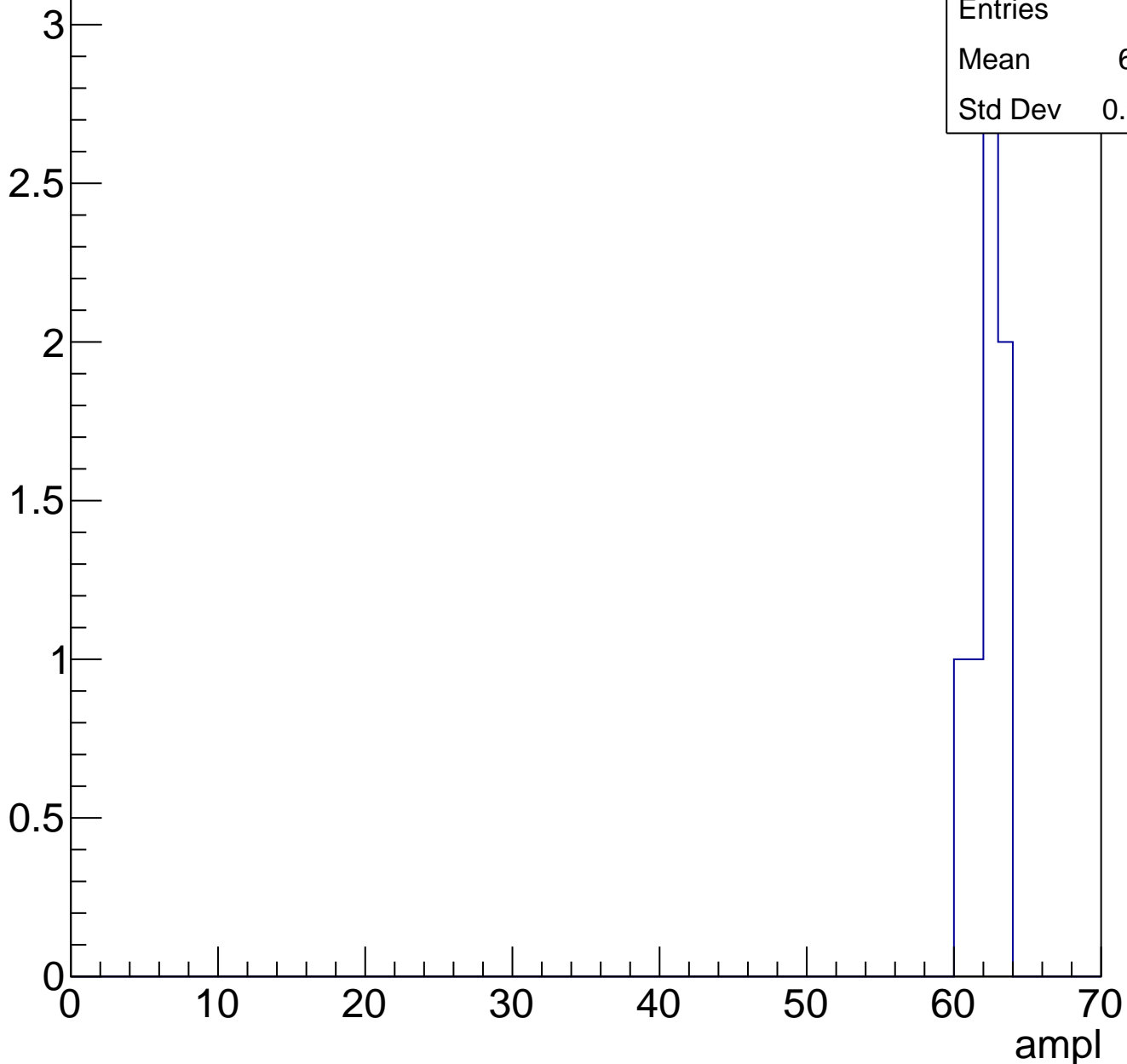
Entry



B1L101S, U6-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch11, adc0

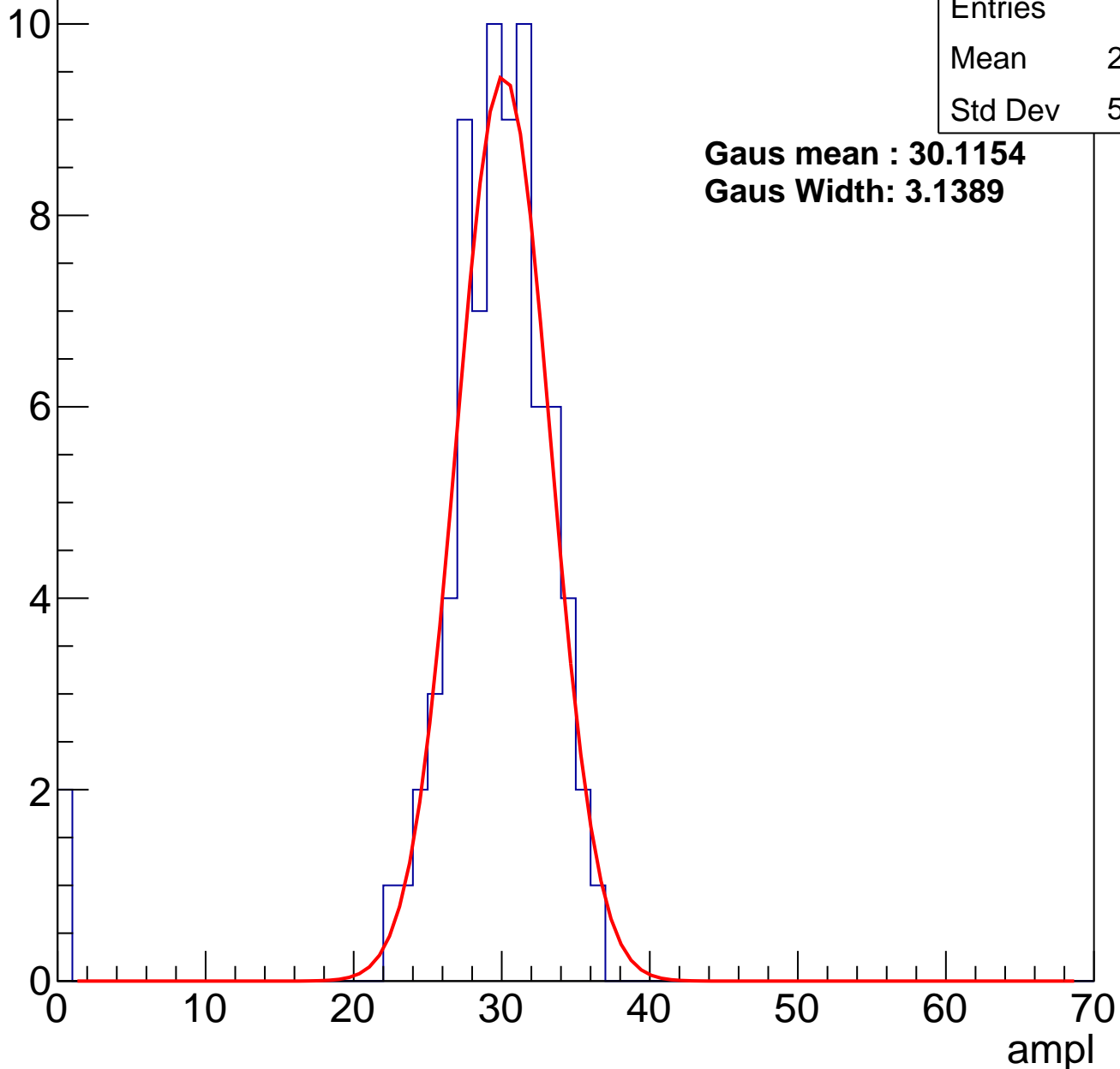
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	28.74
Std Dev	5.535

Gaus mean : 30.1154

Gaus Width: 3.1389

Entry



B1L101S, U6-ch11, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.44
Std Dev	3.407

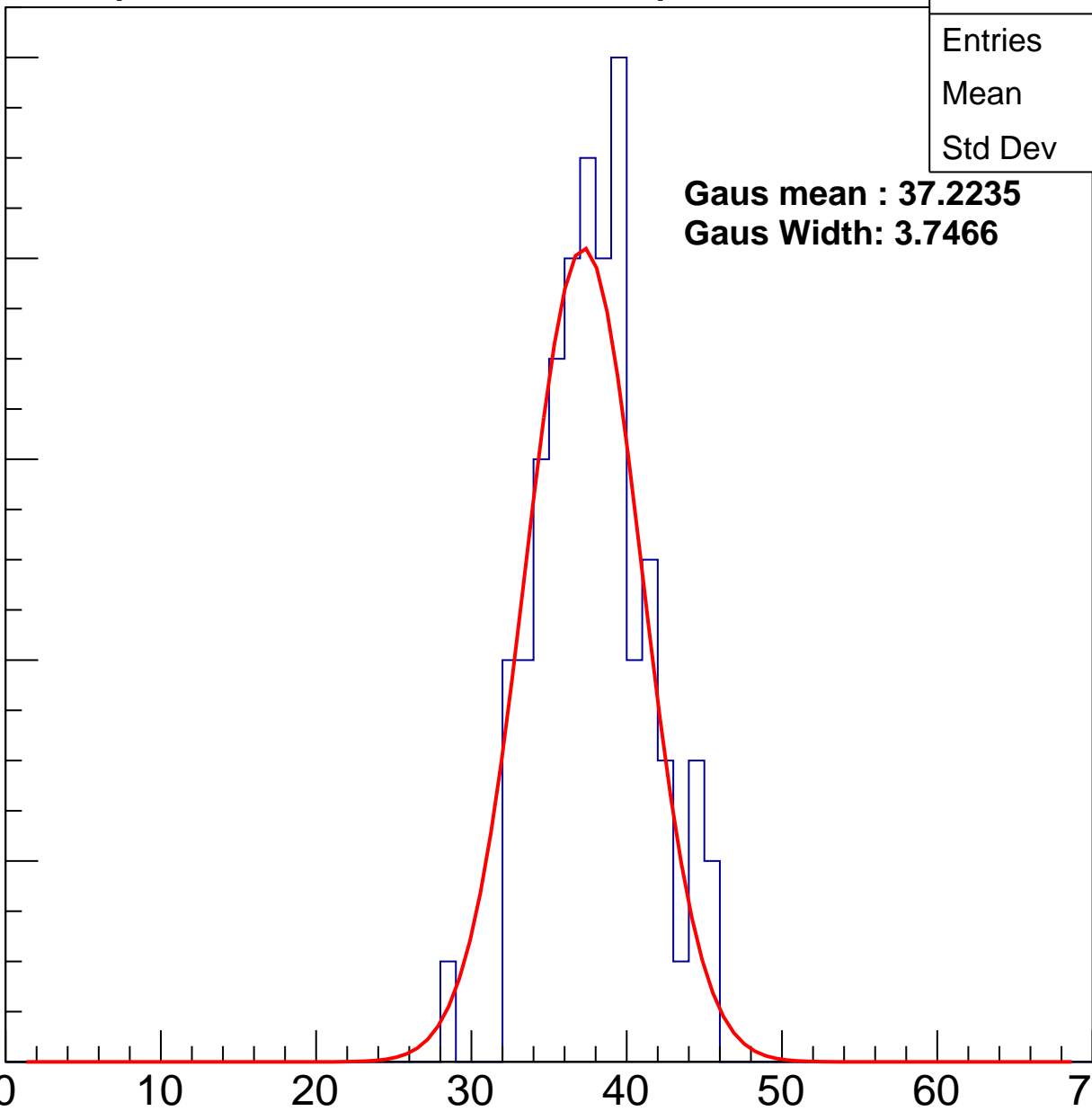
Gaus mean : 37.2235

Gaus Width: 3.7466

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch11, adc2

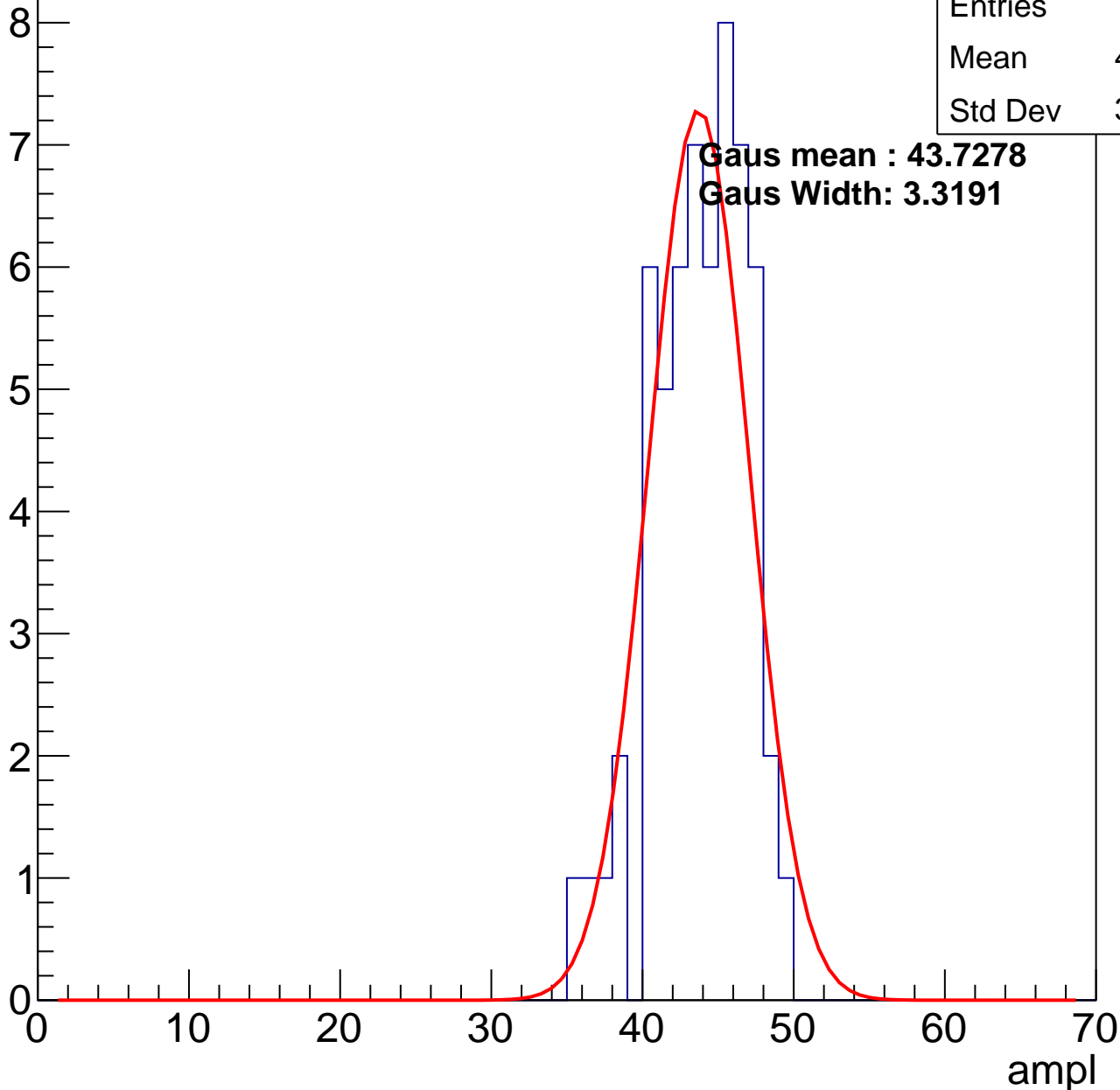
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.31
Std Dev	3.071

Gaus mean : 43.7278

Gaus Width: 3.3191

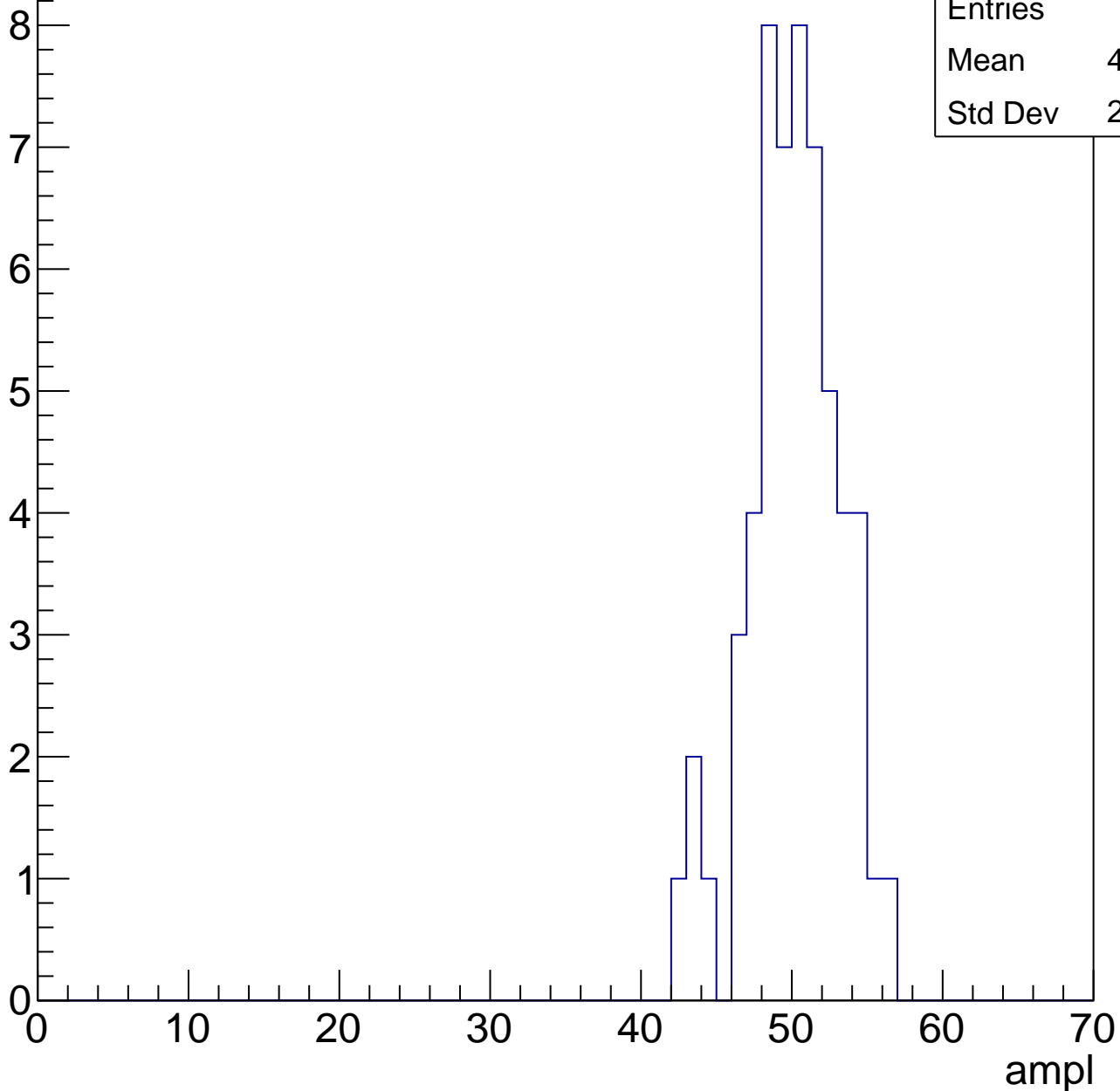


B1L101S, U6-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

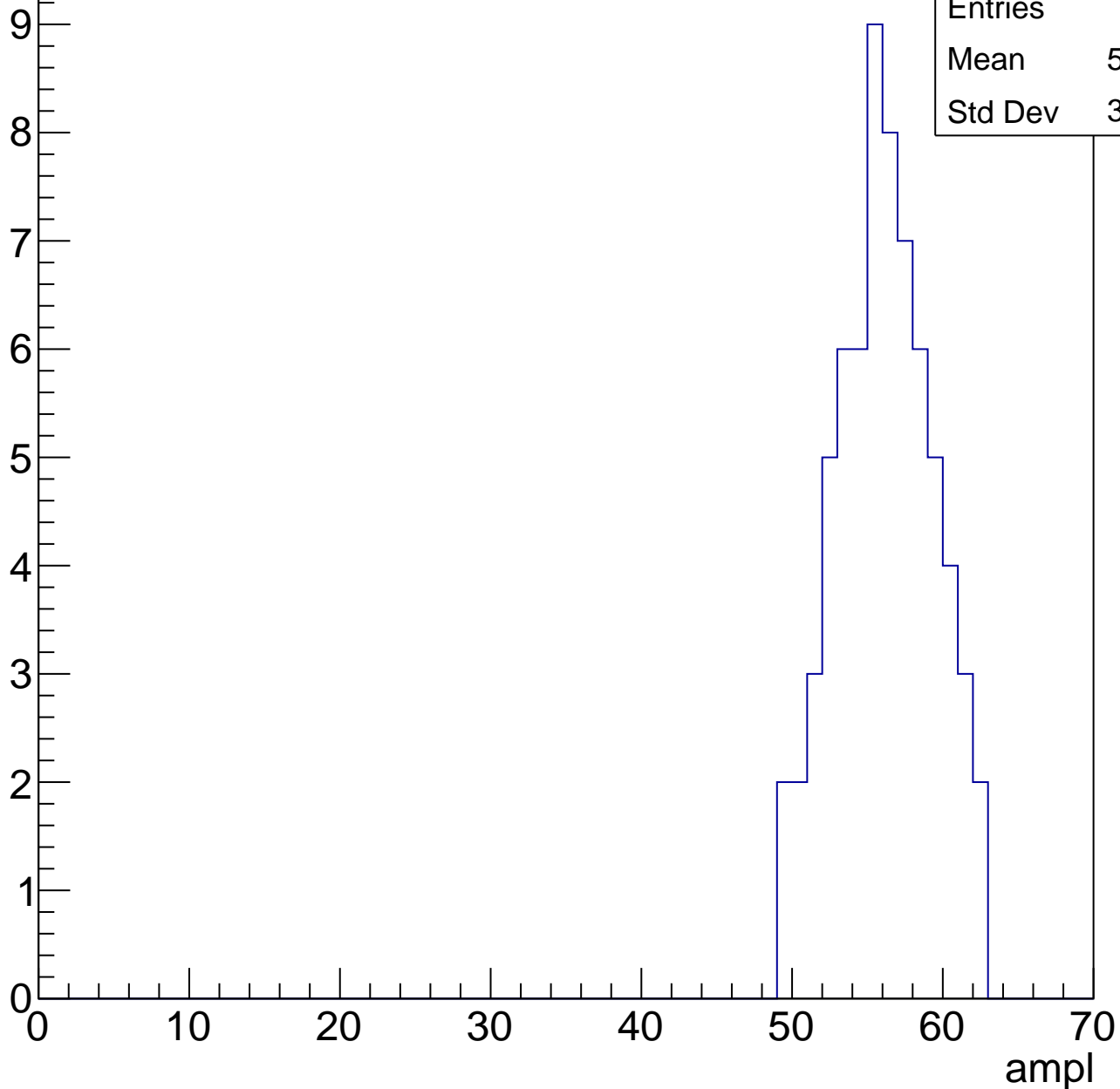
Entries	56
Mean	49.66
Std Dev	2.996



B1L101S, U6-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



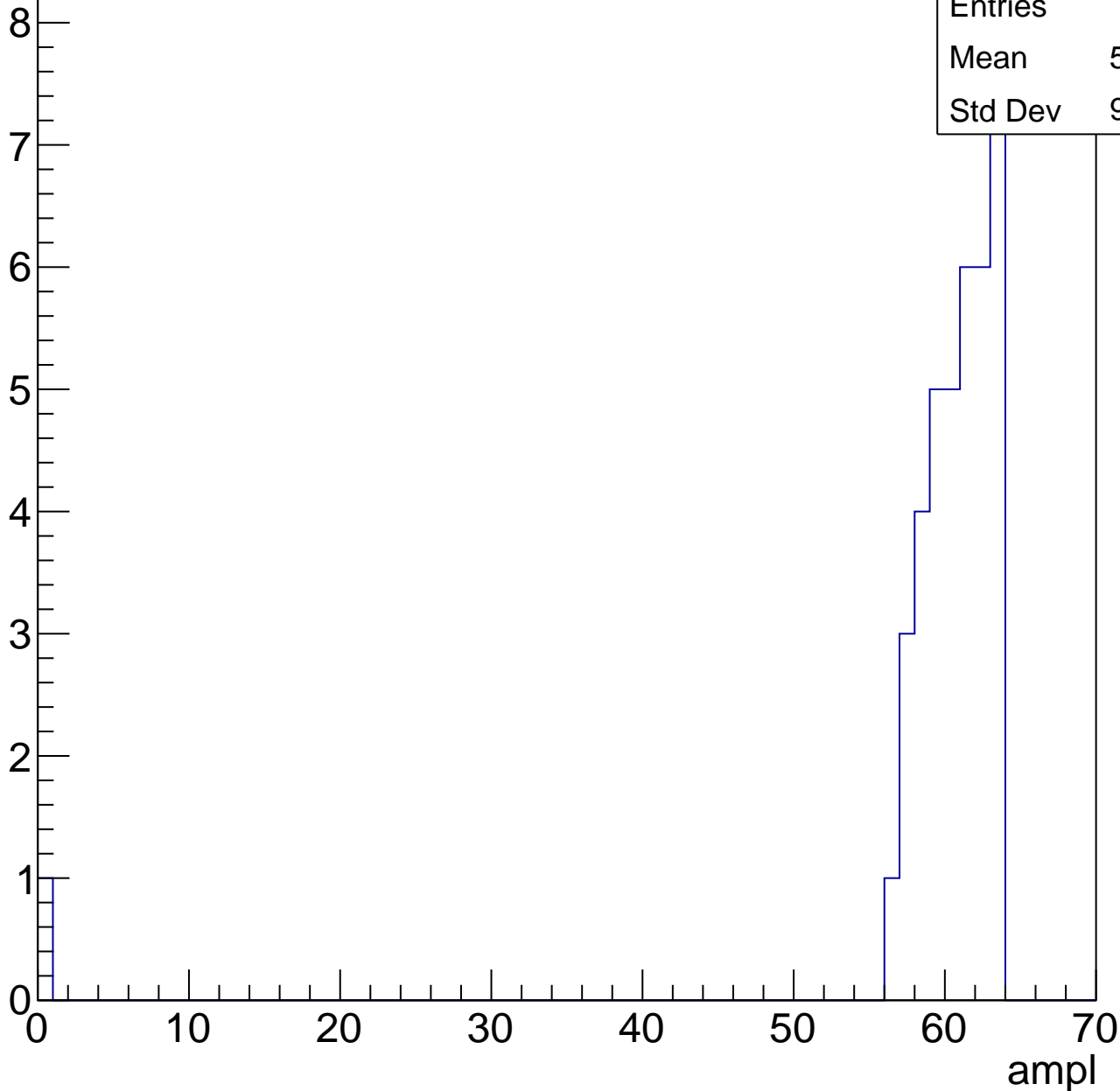
Entries	68
Mean	55.66
Std Dev	3.188

B1L101S, U6-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.87
Std Dev	9.762



B1L101S, U6-ch11, adc6

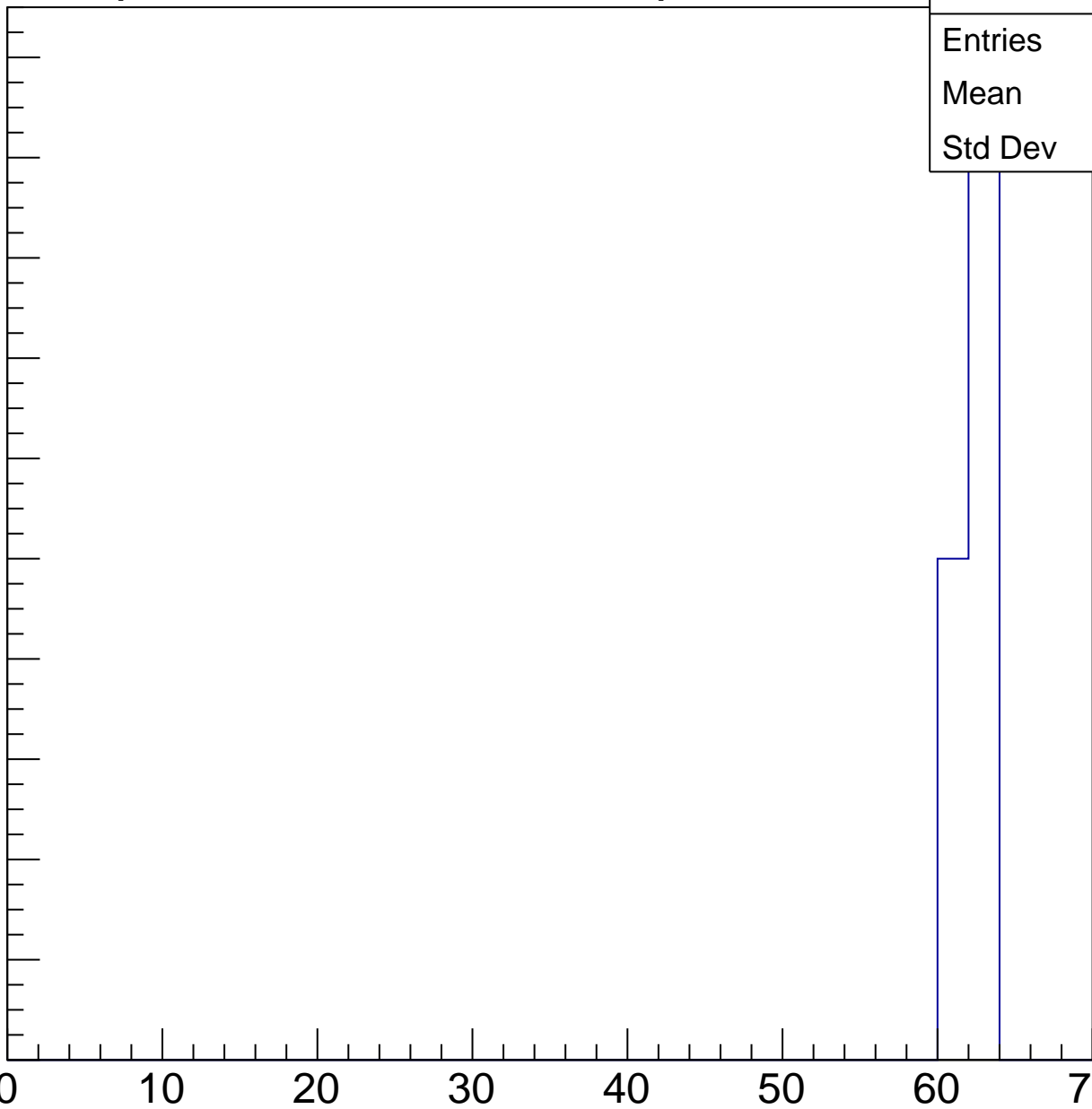
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl



B1L101S, U6-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch12, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	98
Mean	29.3
Std Dev	6.663

Gaus mean : 30.6361

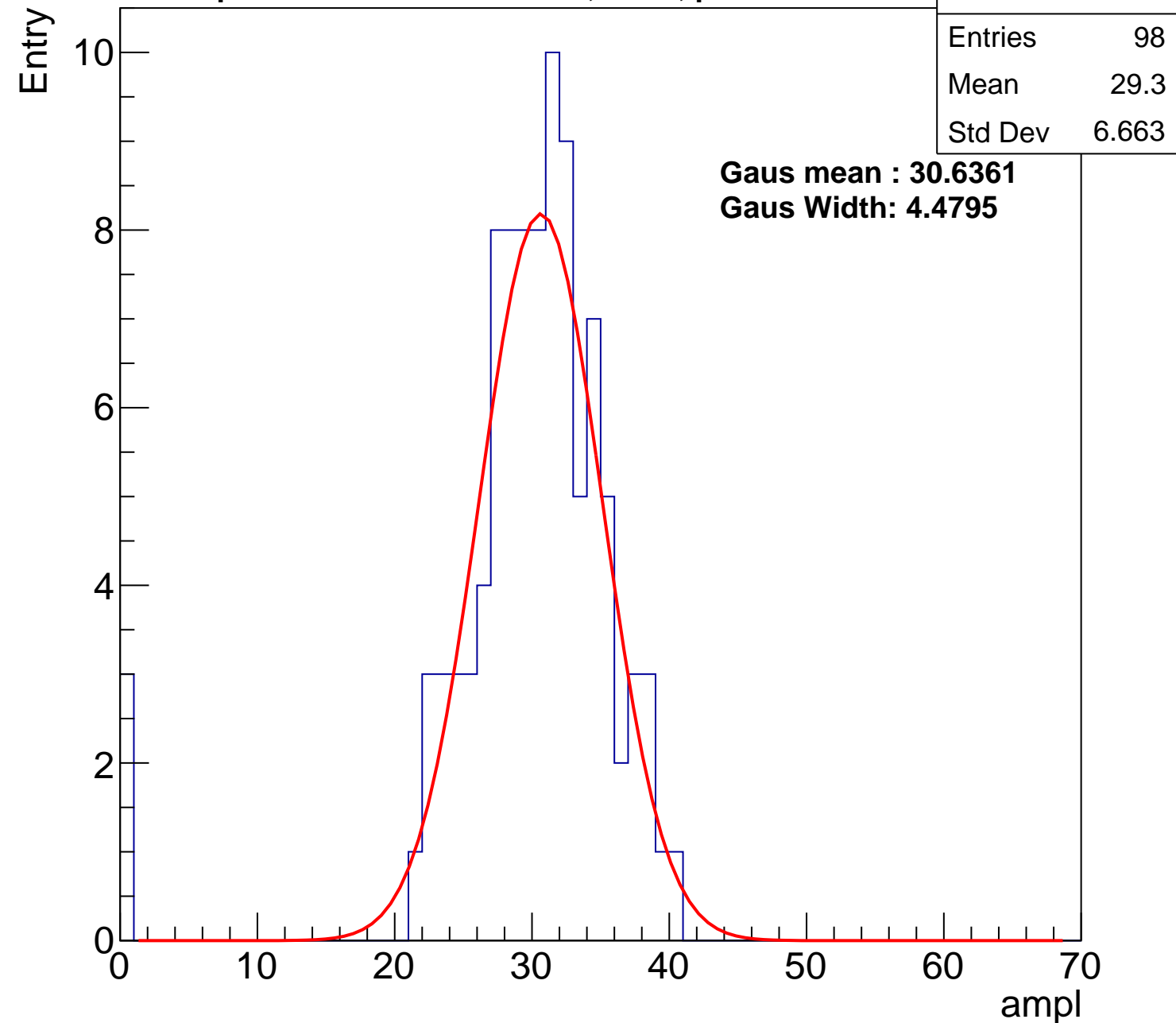
Gaus Width: 4.4795

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch12, adc1

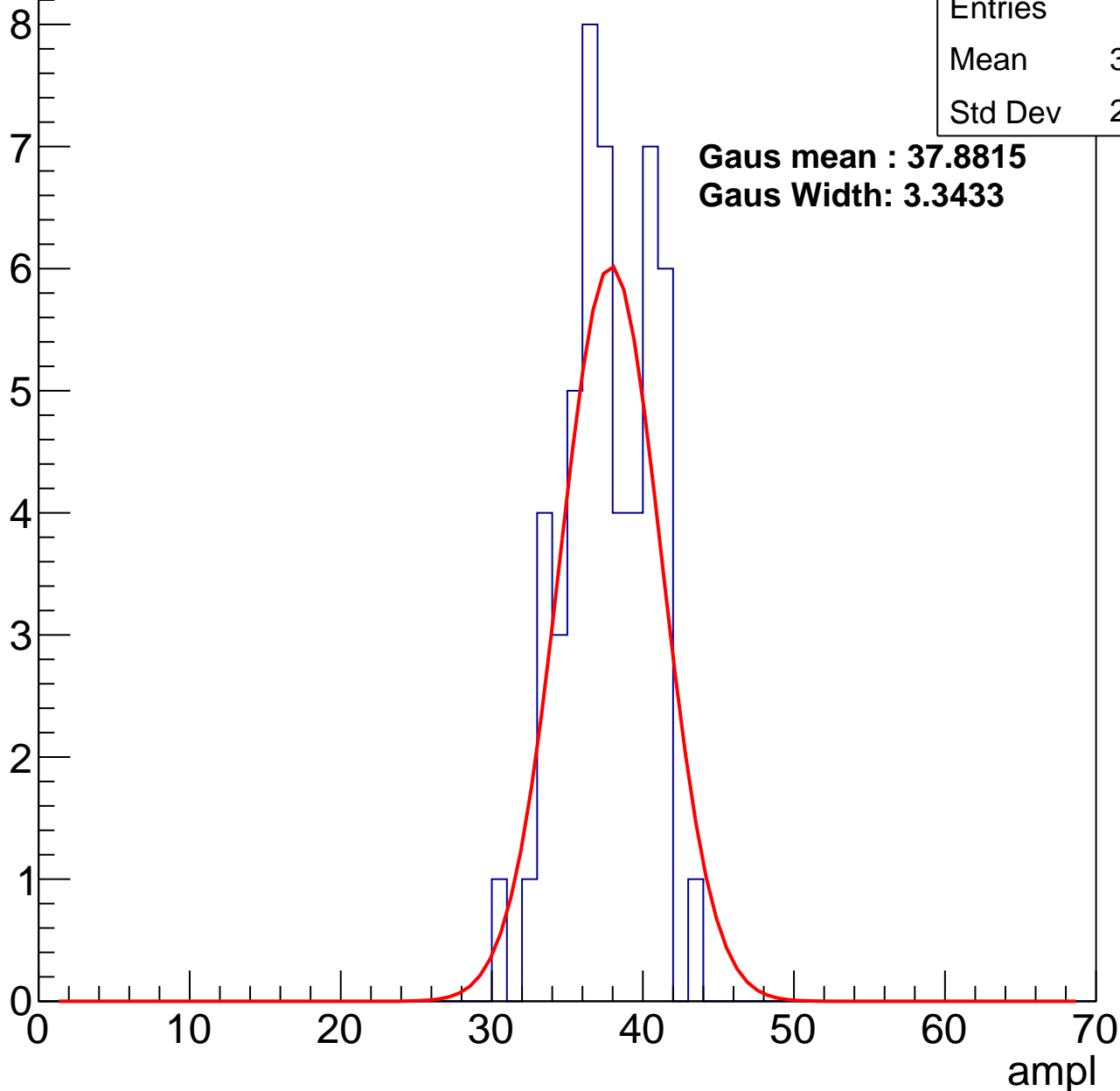
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	37.16
Std Dev	2.824

Gaus mean : 37.8815

Gaus Width: 3.3433



B1L101S, U6-ch12, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	43.88
Std Dev	3.555

Gaus mean : 44.5900

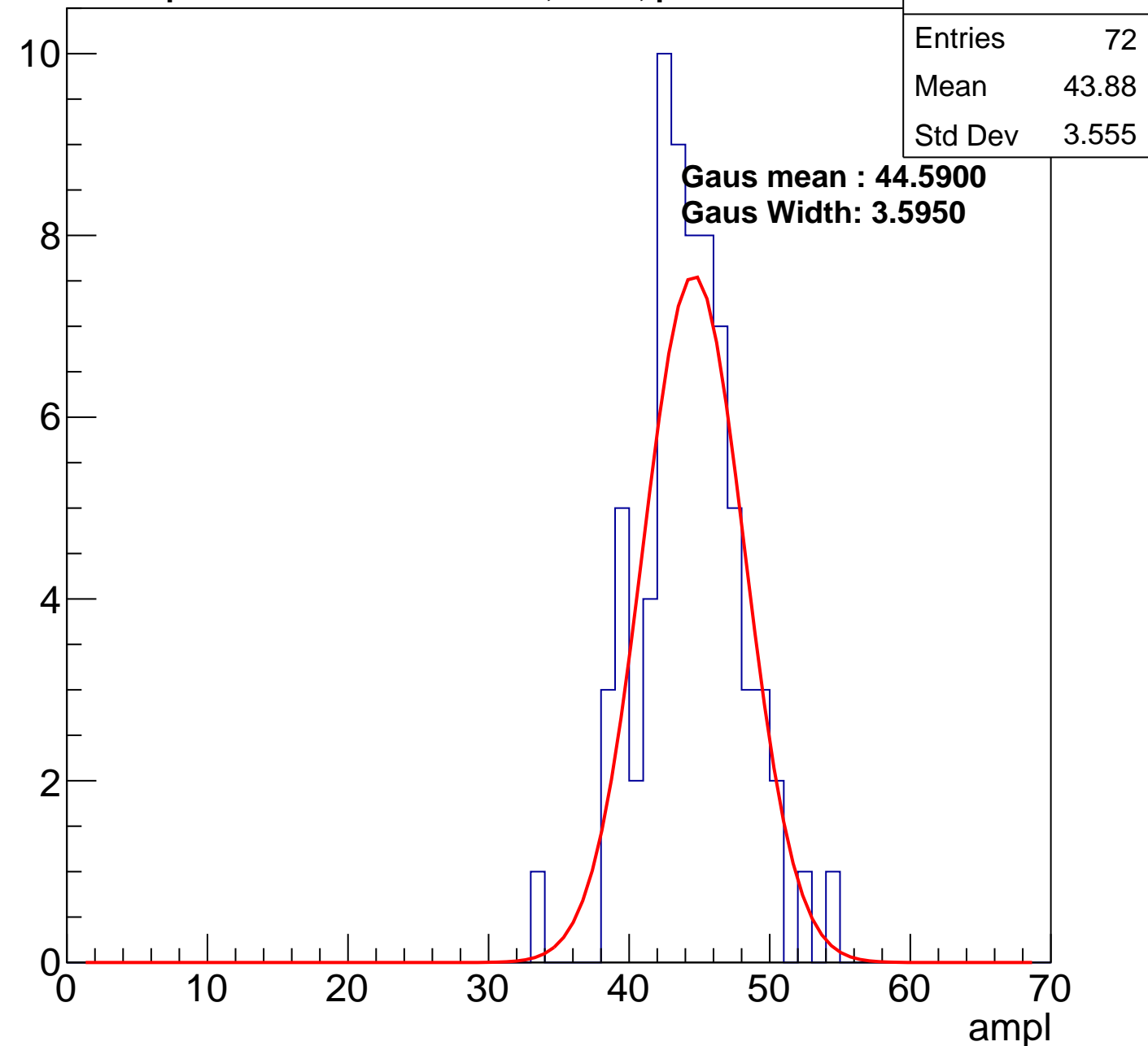
Gaus Width: 3.5950

Entry

10
8
6
4
2
0

ampl

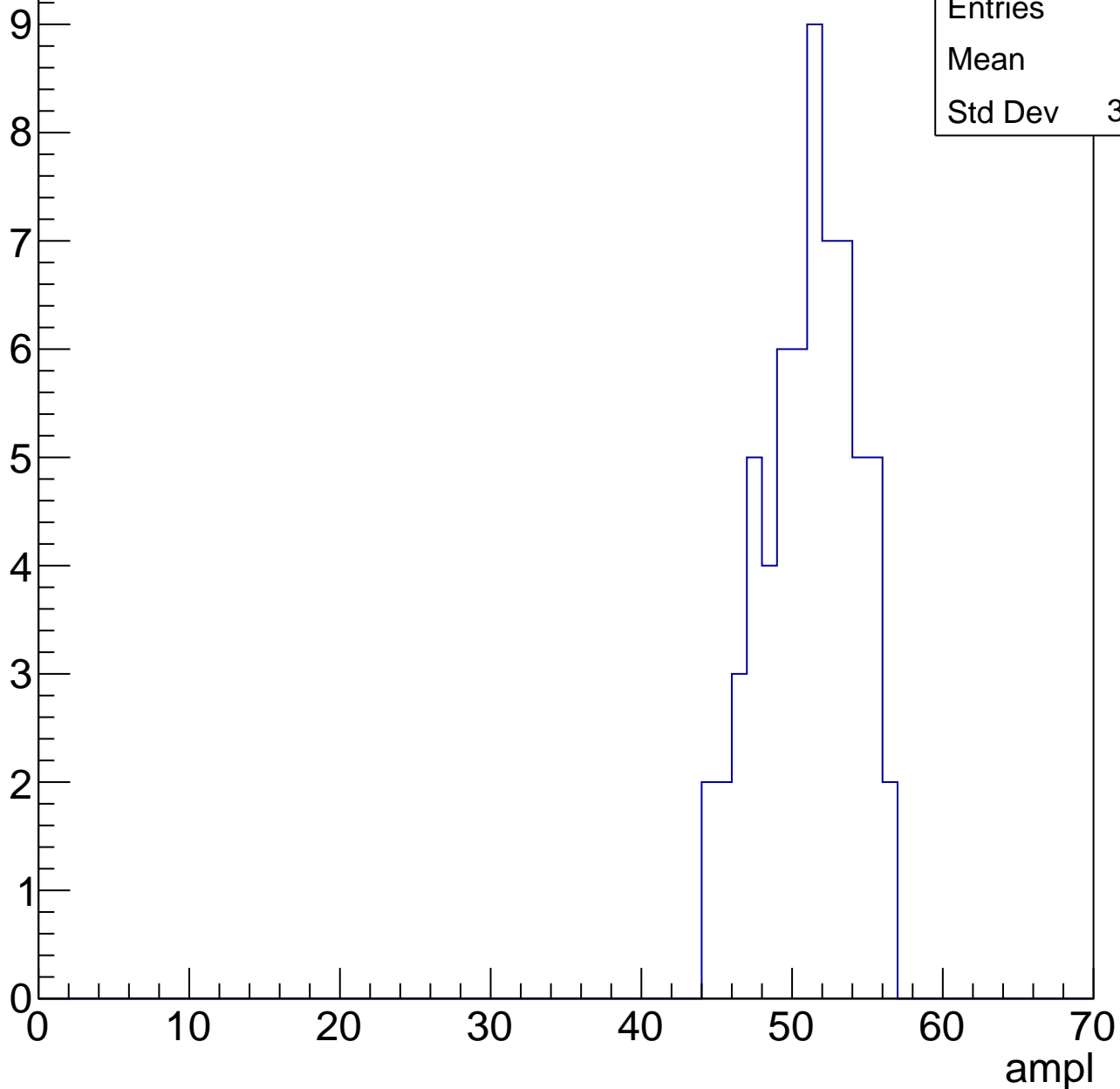
0 10 20 30 40 50 60 70



B1L101S, U6-ch12, adc3

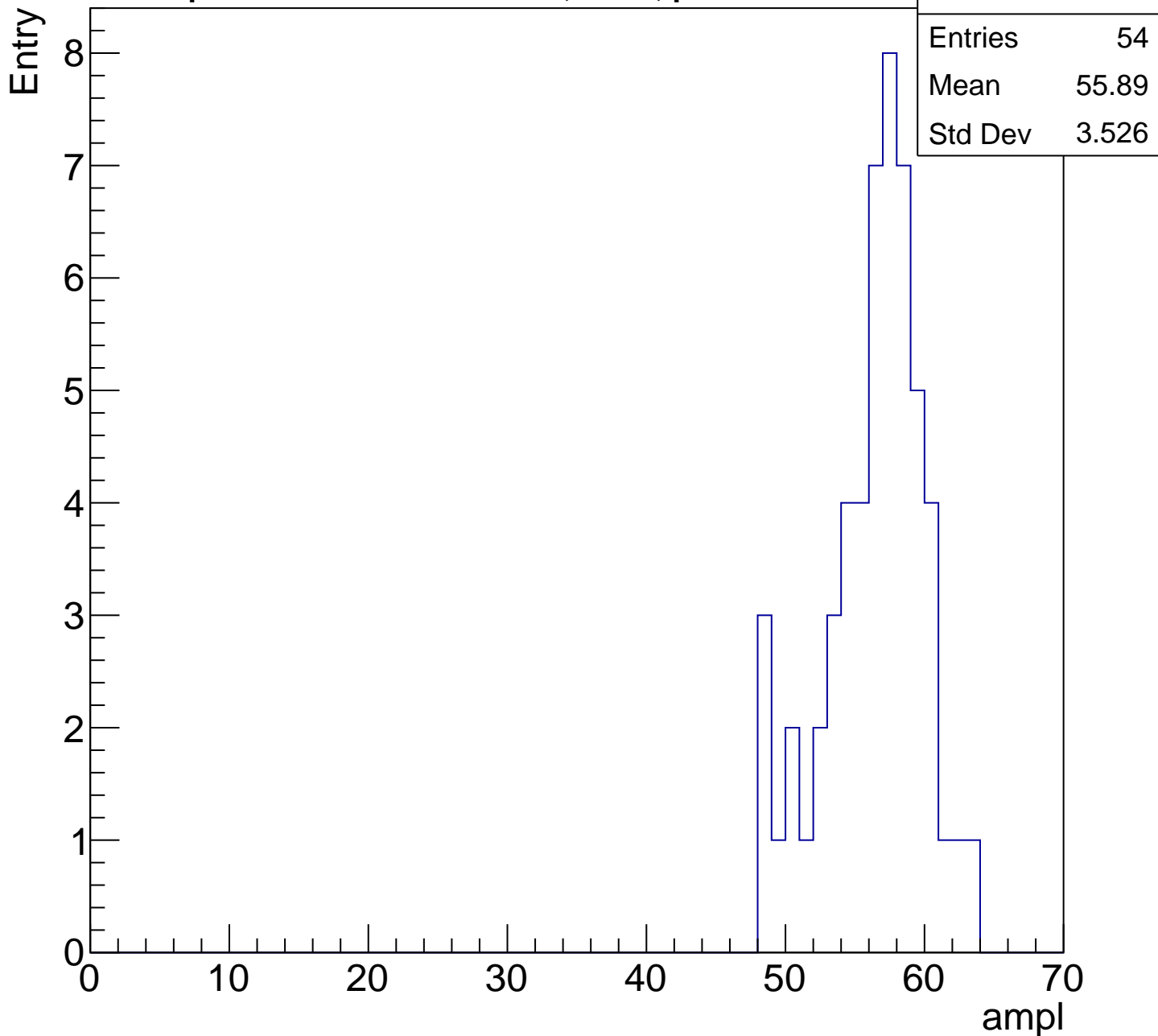
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch12, adc5

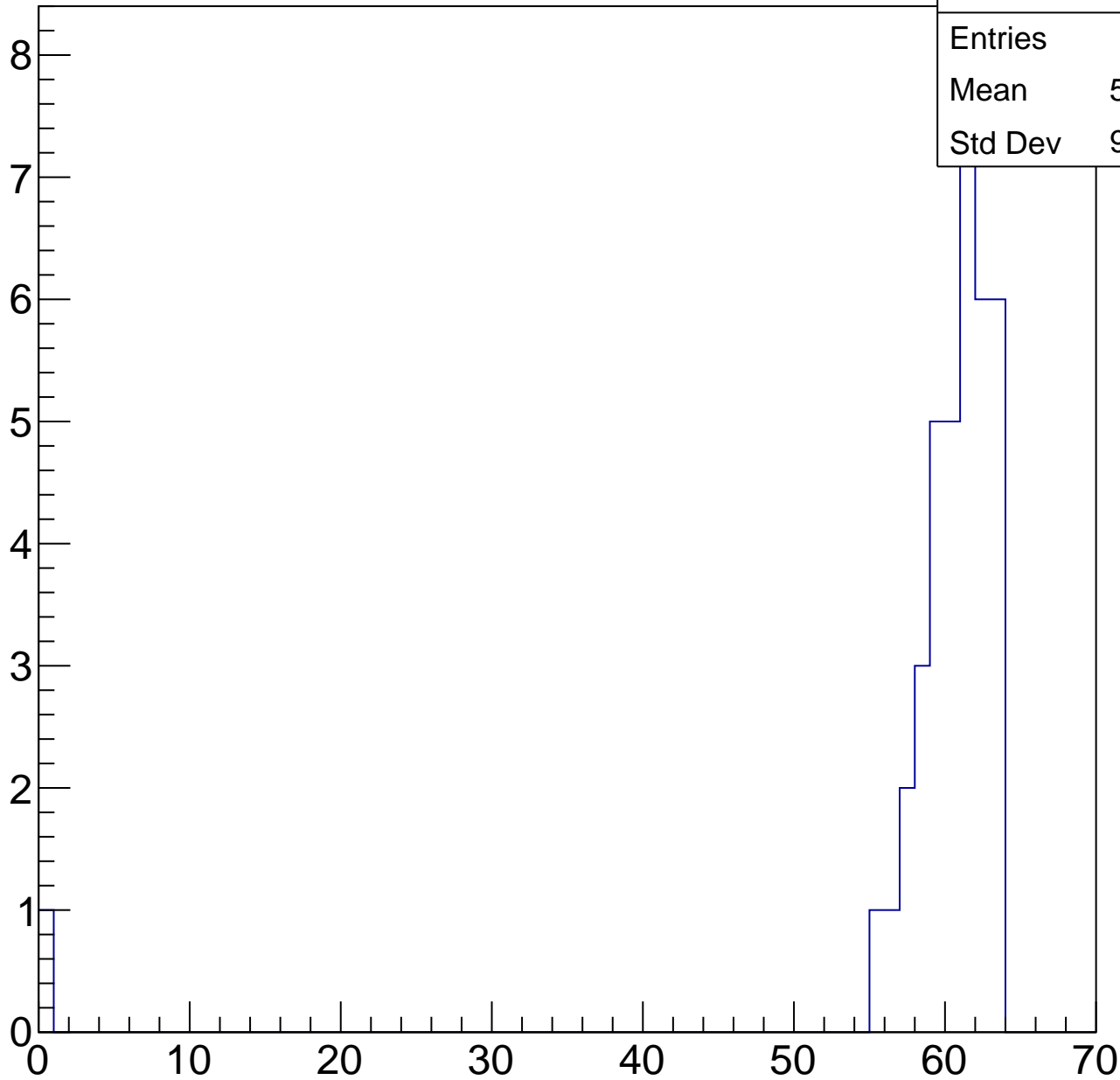
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	58.74
Std Dev	9.869

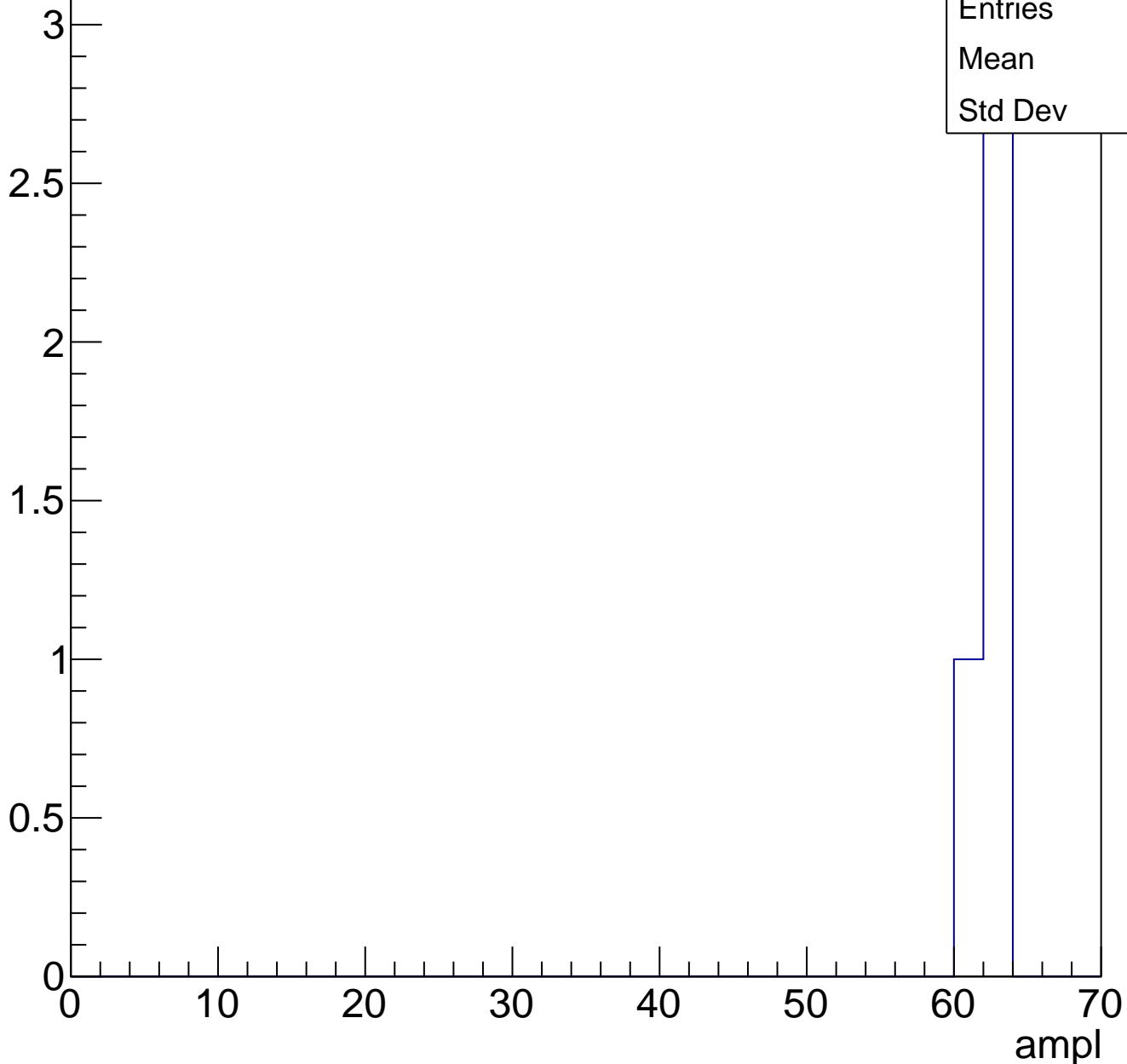
ampl



B1L101S, U6-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

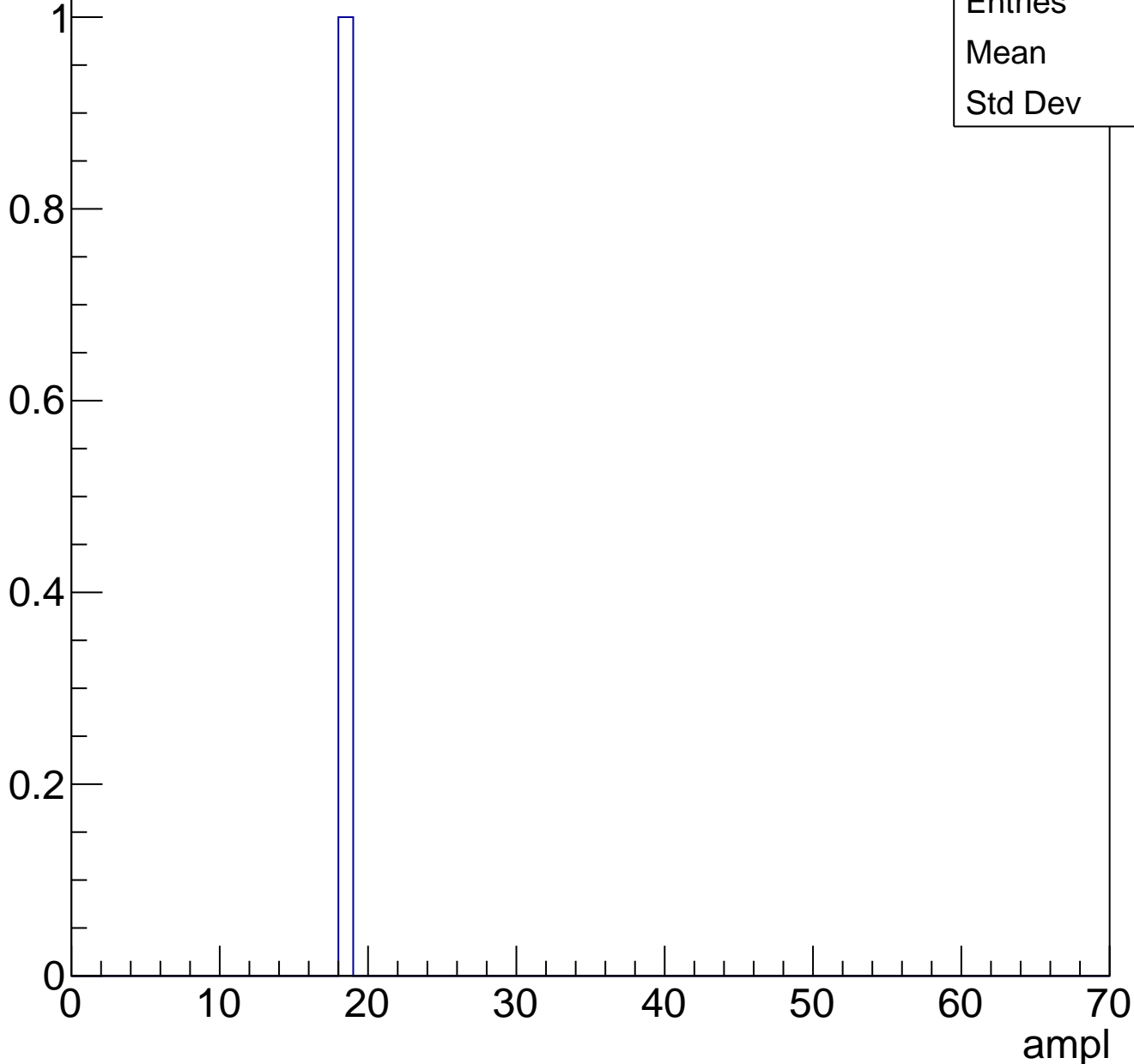
Entry



B1L101S, U6-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch13, adc0

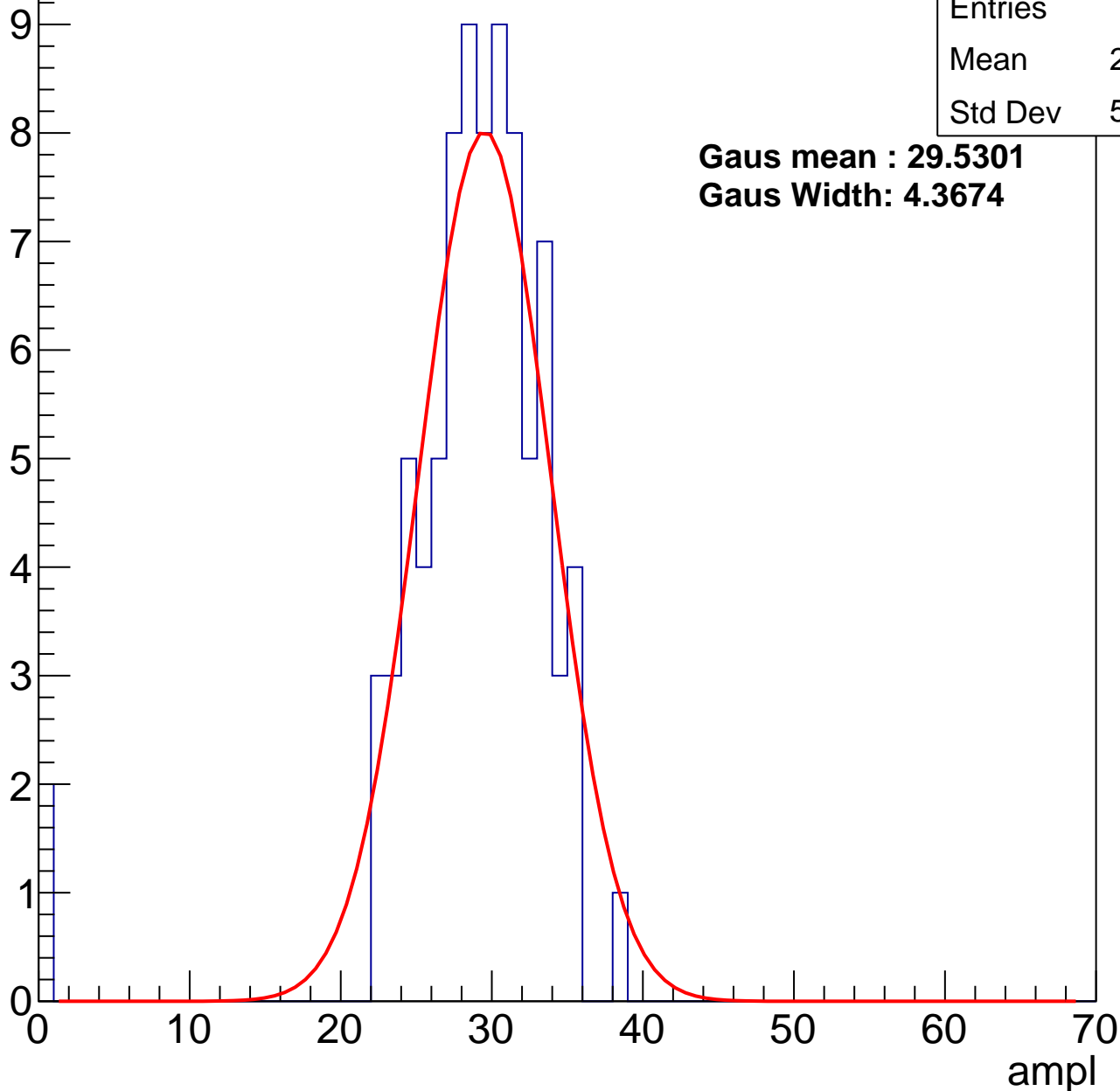
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	28.26
Std Dev	5.632

Gaus mean : 29.5301

Gaus Width: 4.3674



B1L101S, U6-ch13, adc1

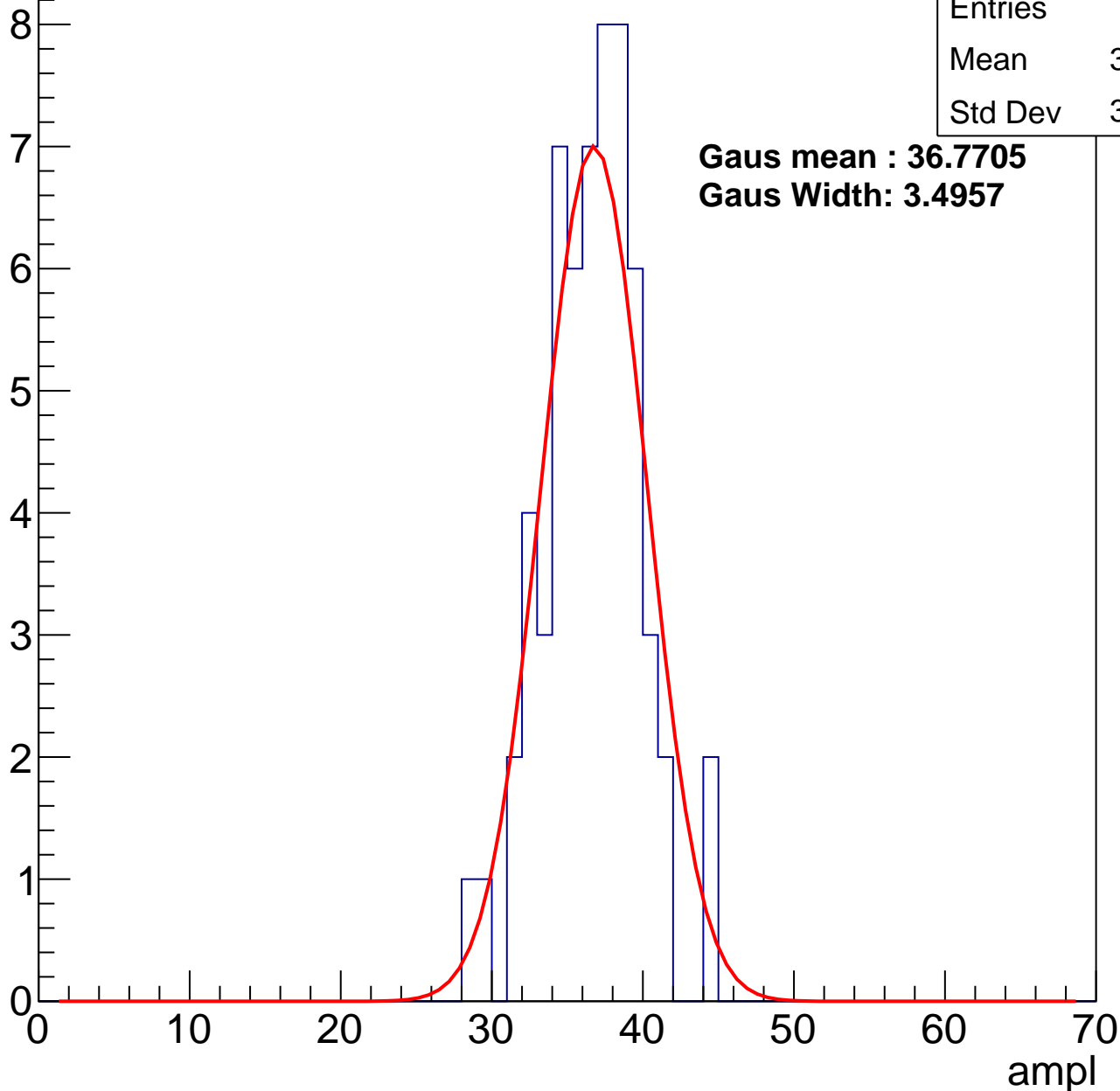
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.17
Std Dev	3.179

Gaus mean : 36.7705

Gaus Width: 3.4957



B1L101S, U6-ch13, adc2

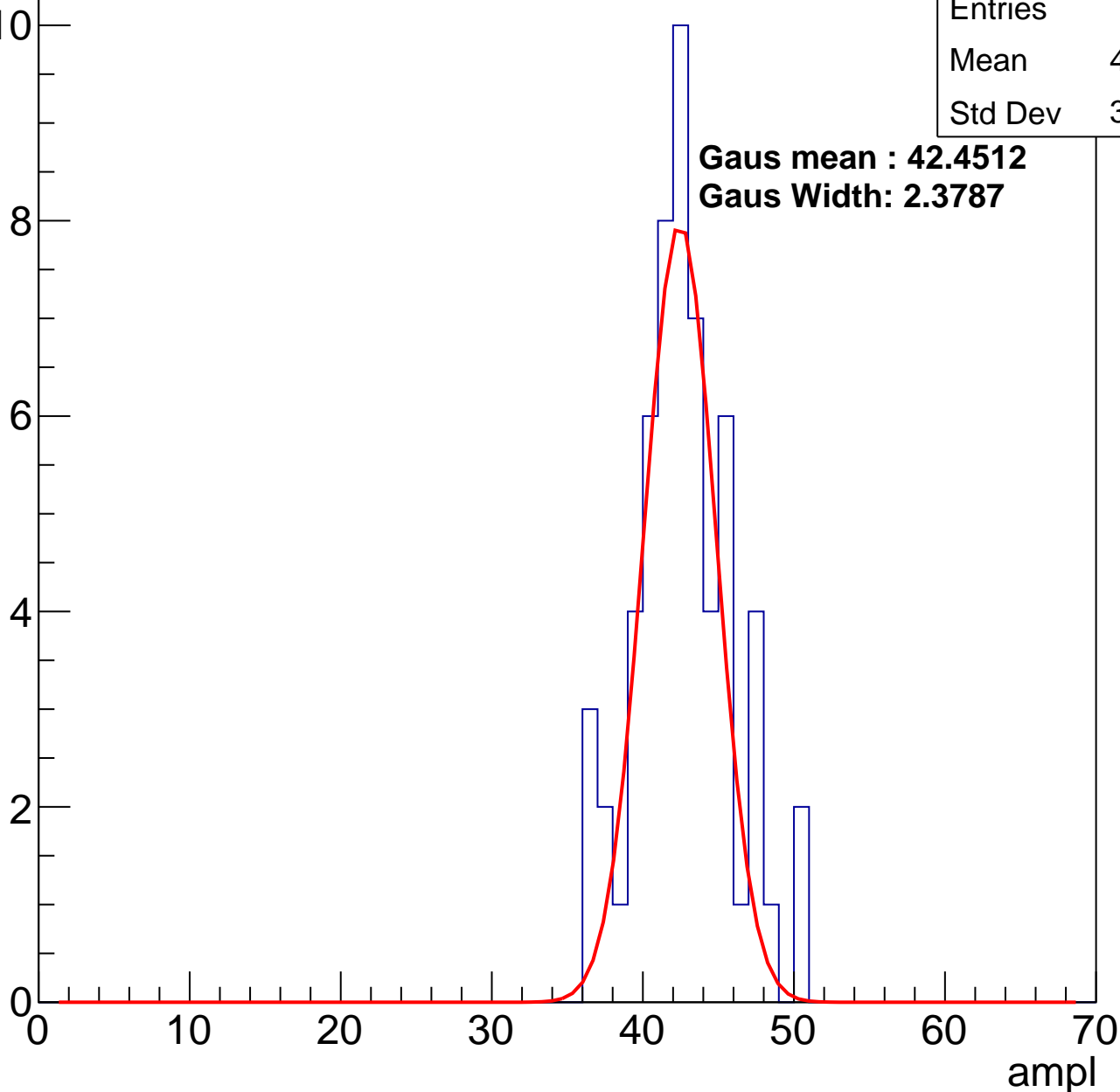
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.25
Std Dev	3.176

Gaus mean : 42.4512

Gaus Width: 2.3787



B1L101S, U6-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

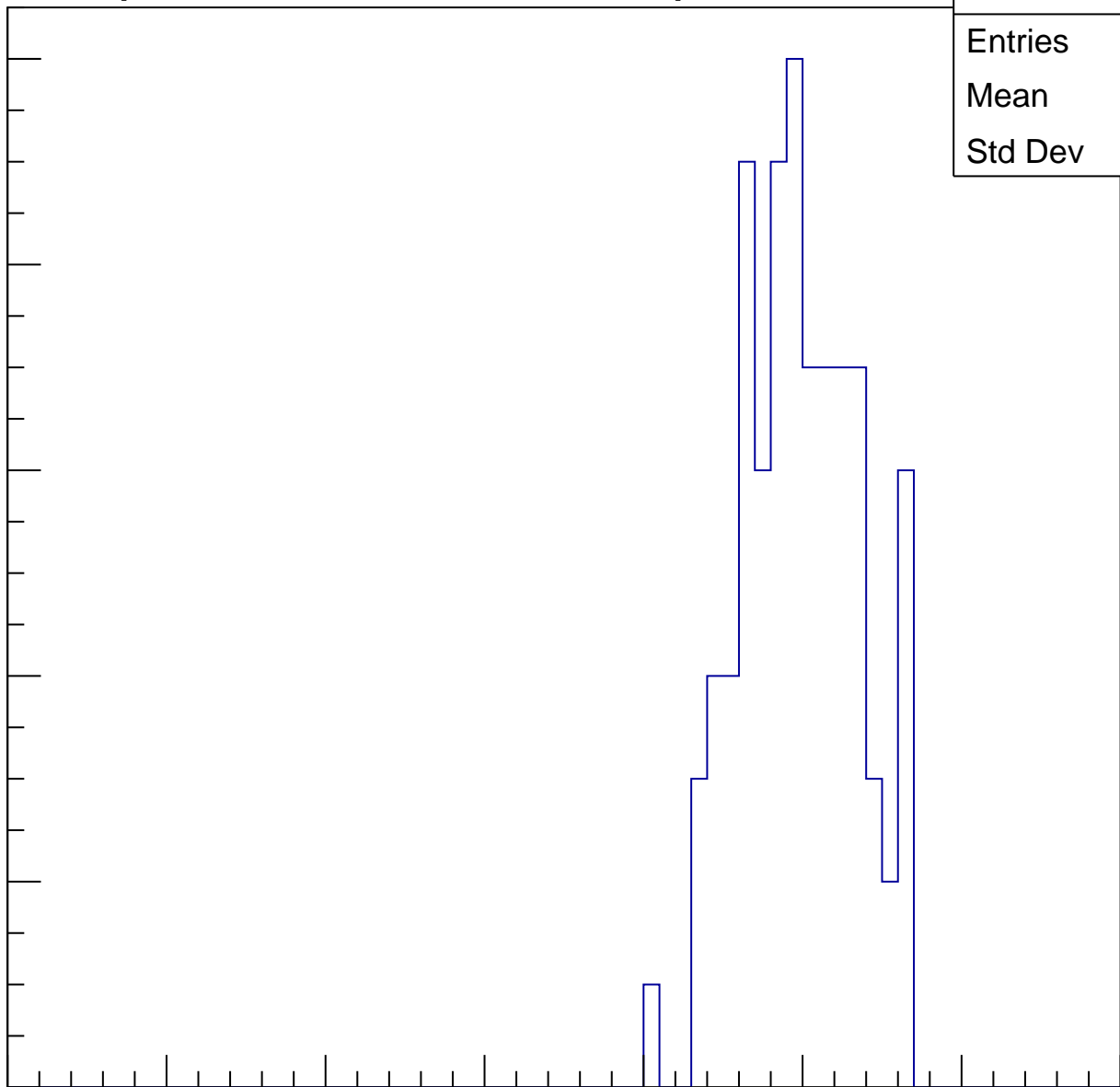
Entries	85
Mean	49.33
Std Dev	3.612

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

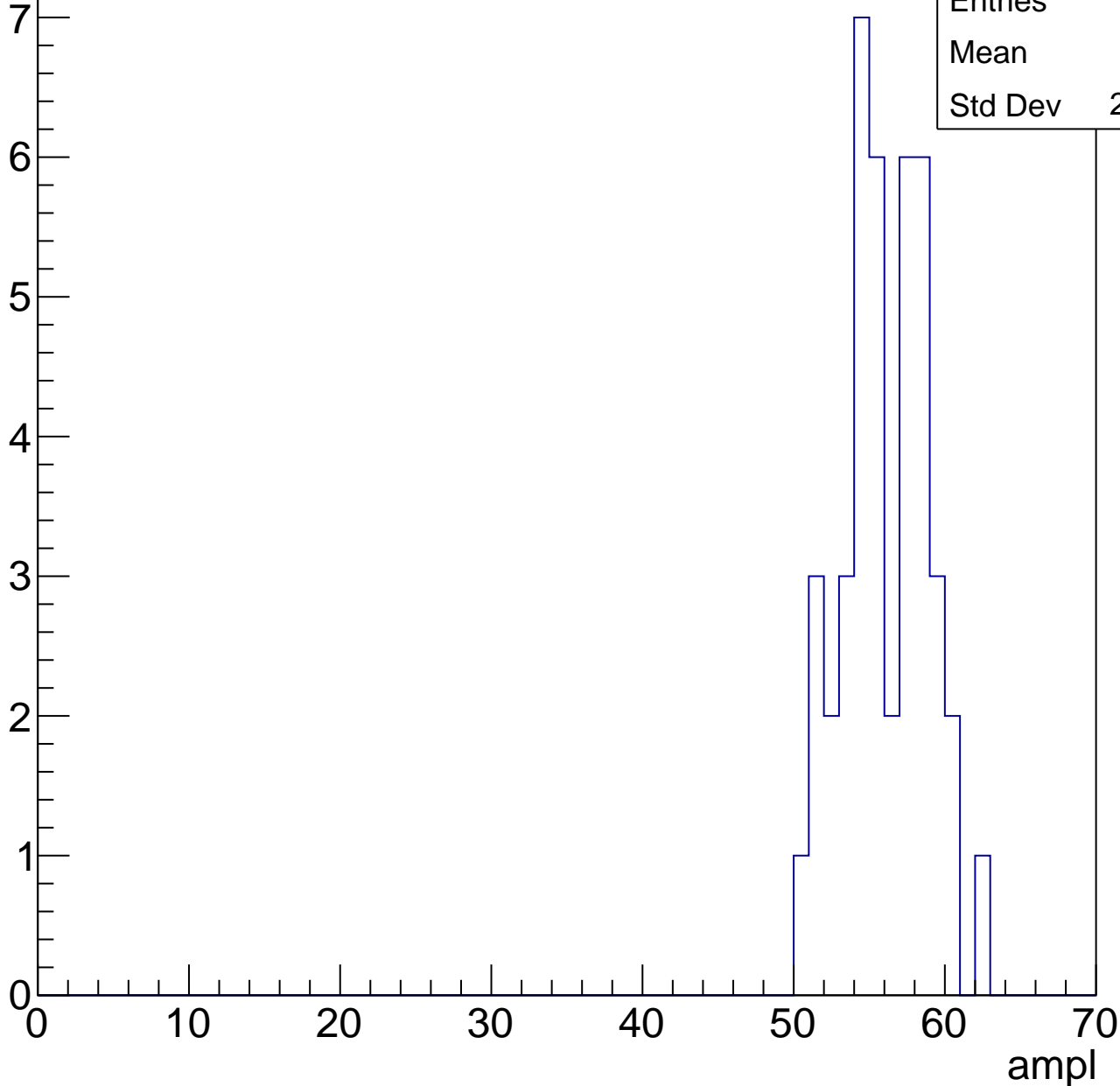


B1L101S, U6-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	55.6
Std Dev	2.769

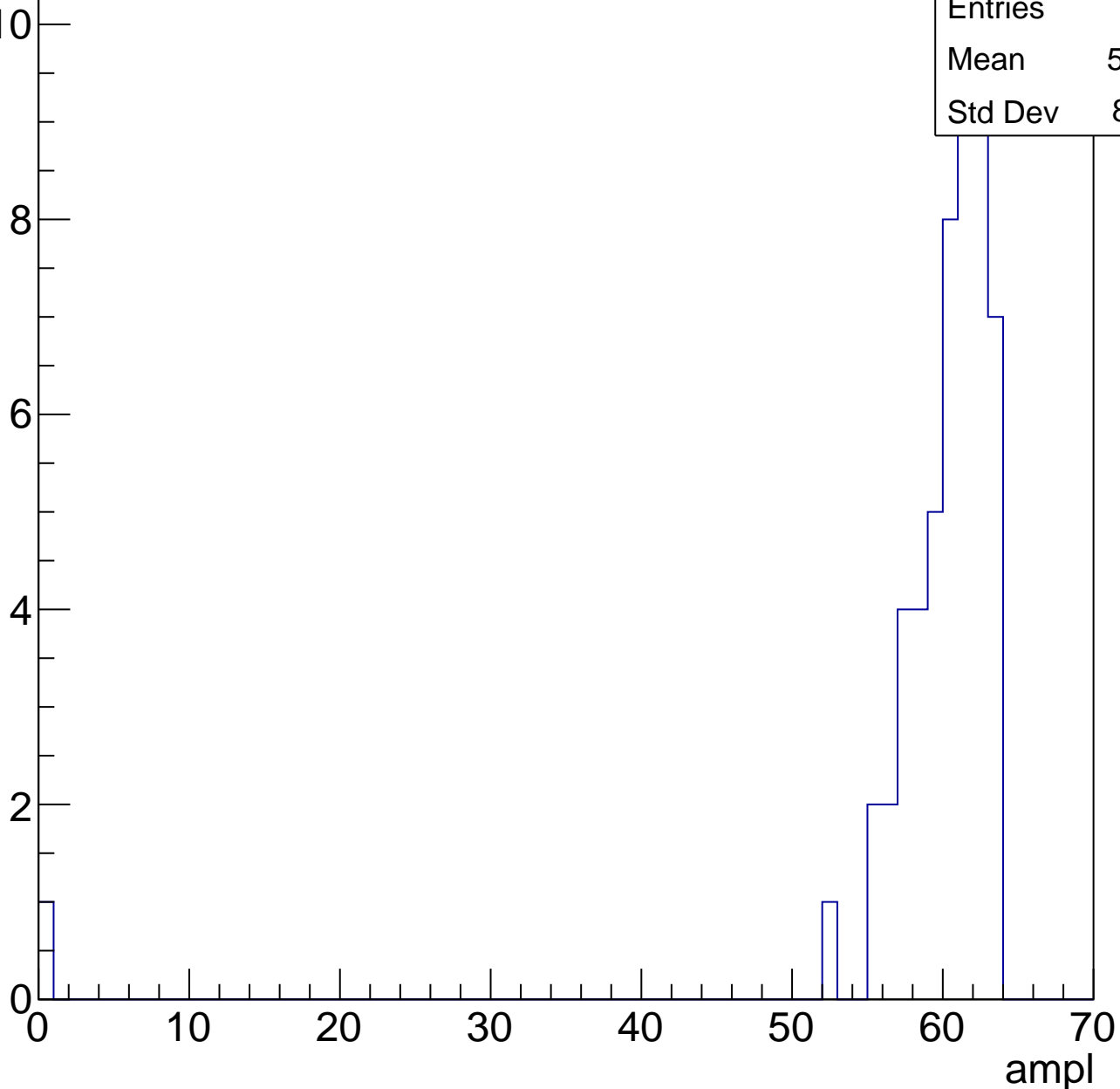


B1L101S, U6-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

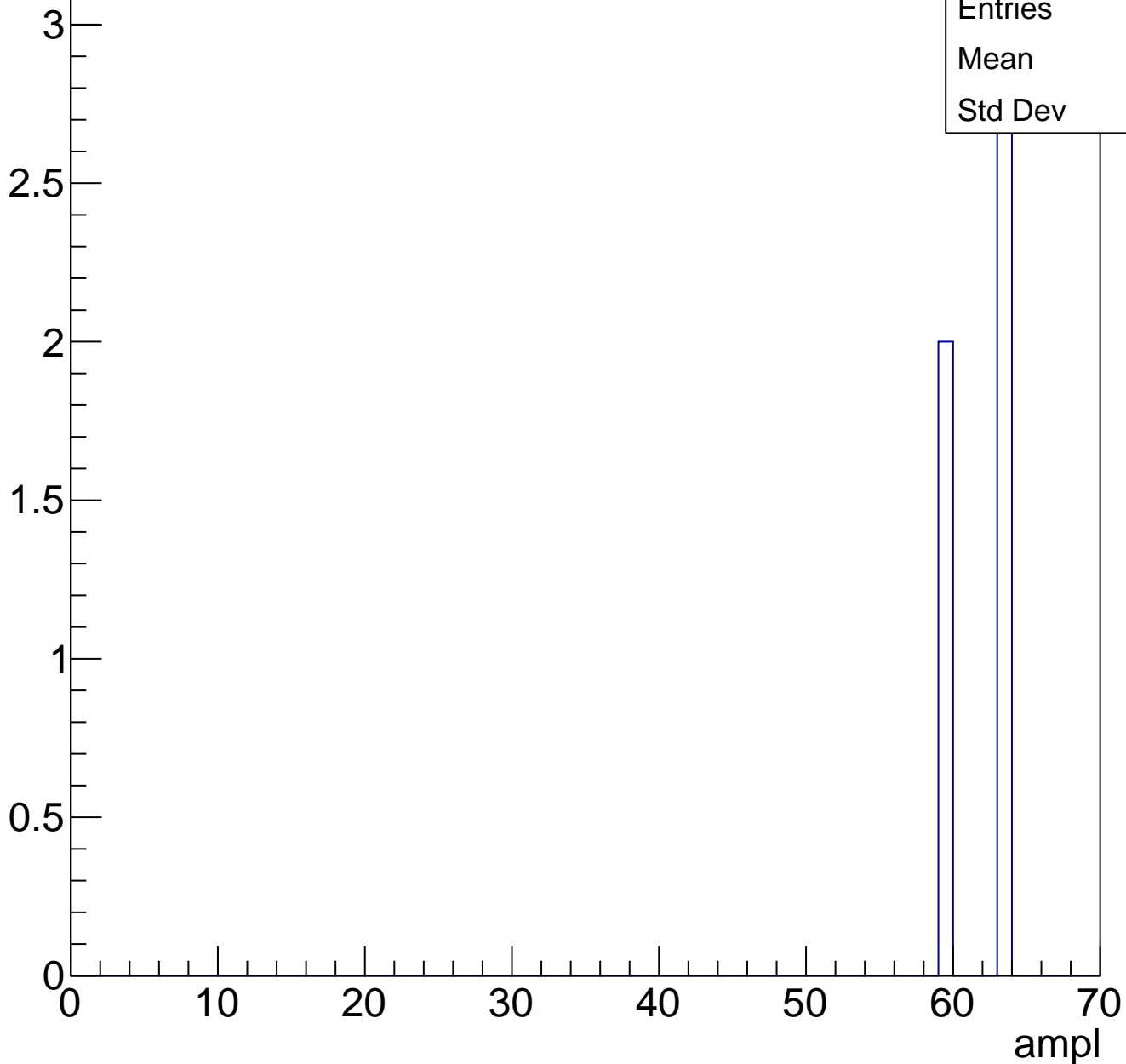
Entries	53
Mean	58.83
Std Dev	8.511



B1L101S, U6-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch14, adc0

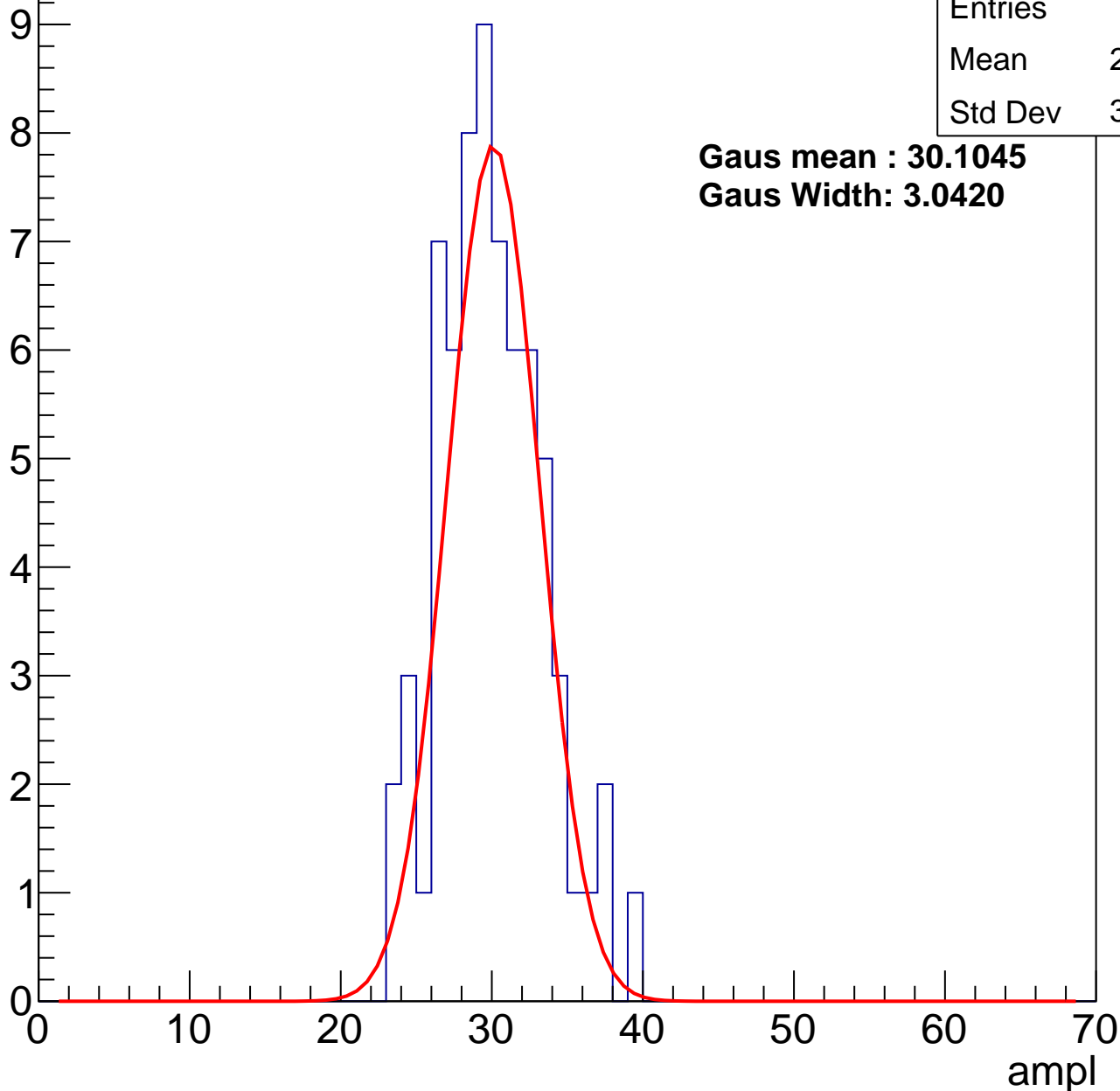
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.57
Std Dev	3.406

Gaus mean : 30.1045

Gaus Width: 3.0420



B1L101S, U6-ch14, adc1

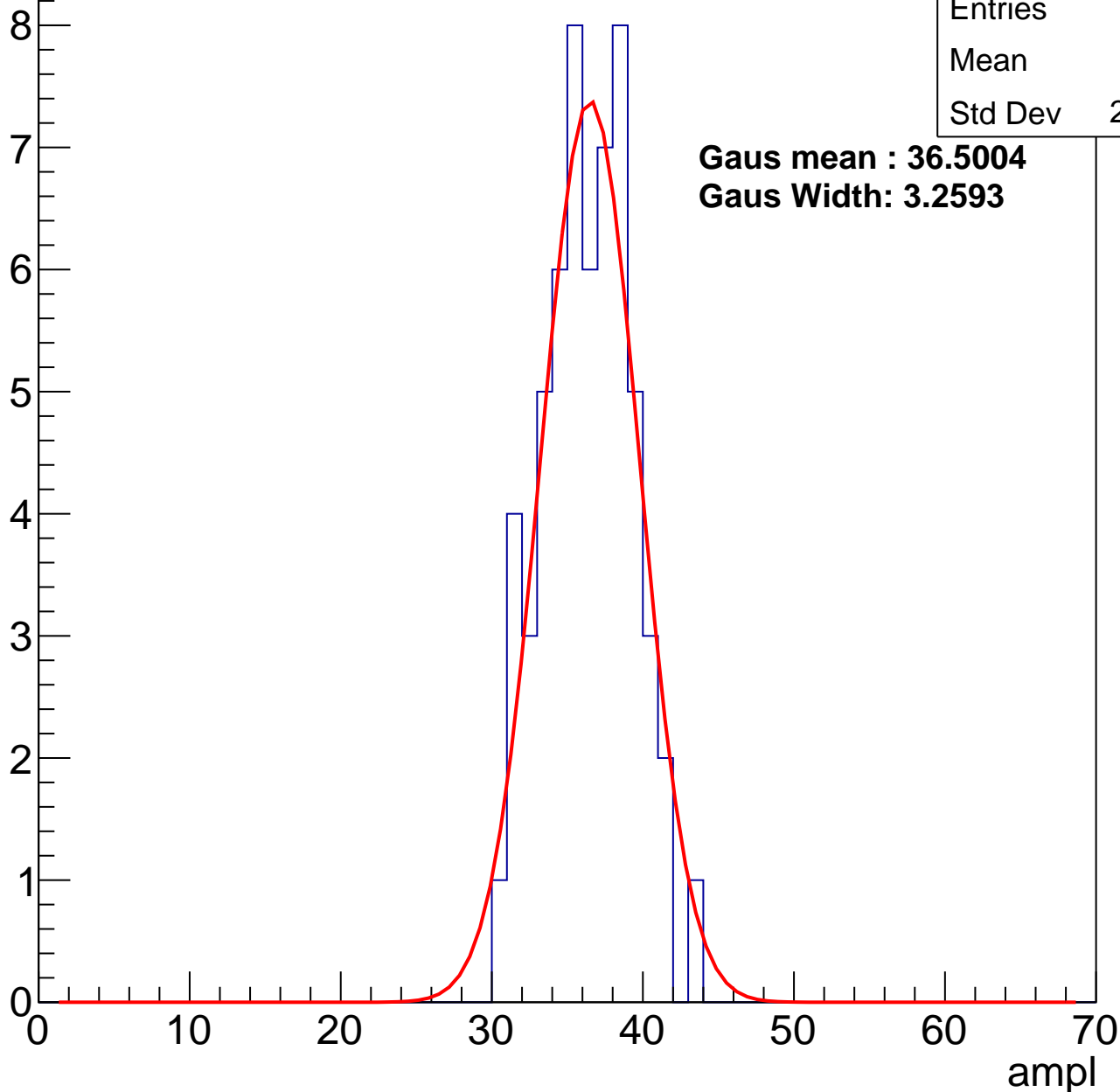
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.9
Std Dev	2.886

Gaus mean : 36.5004

Gaus Width: 3.2593



B1L101S, U6-ch14, adc2

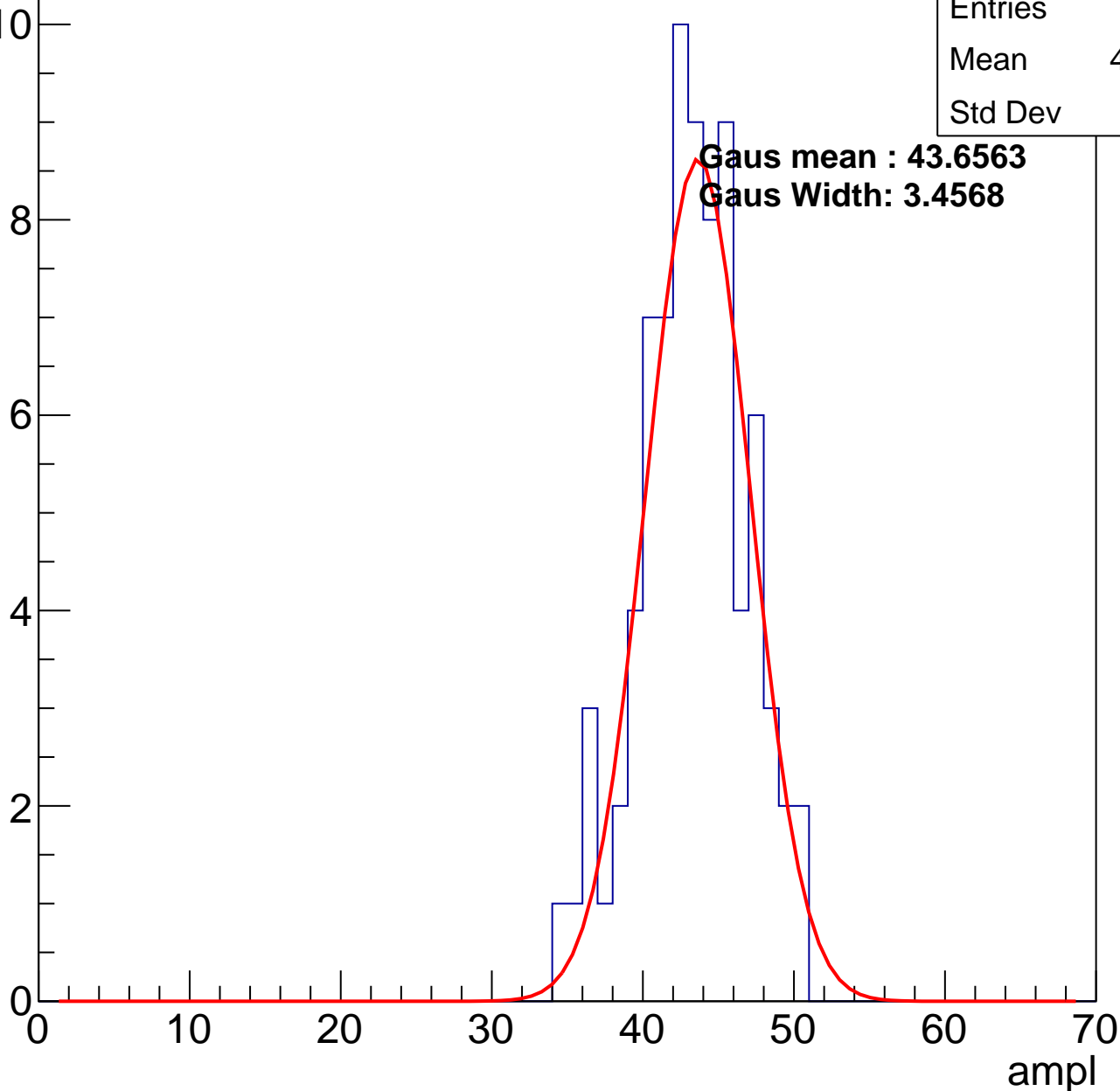
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	42.85
Std Dev	3.49

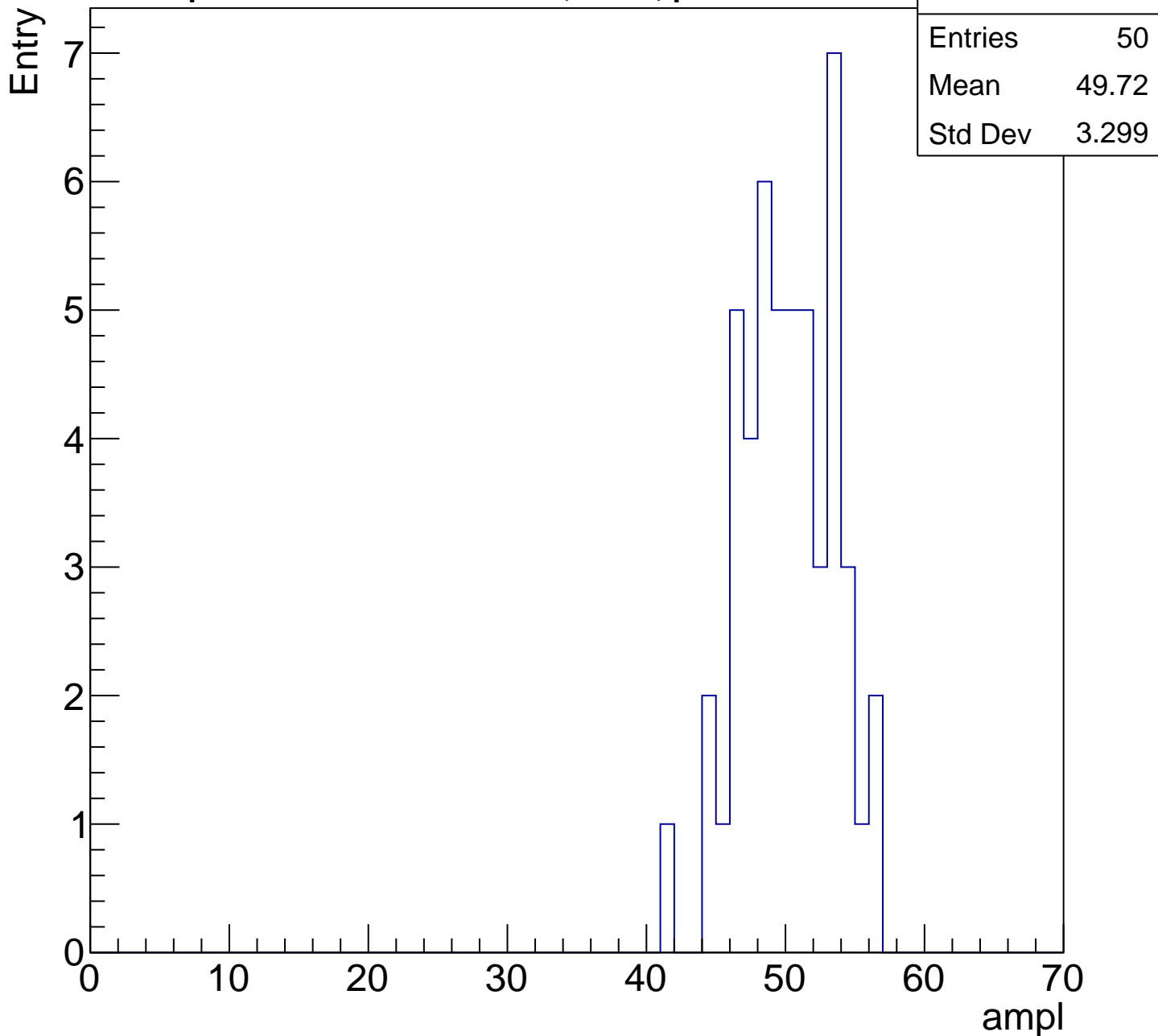
Gaus mean : 43.6563

Gaus Width: 3.4568



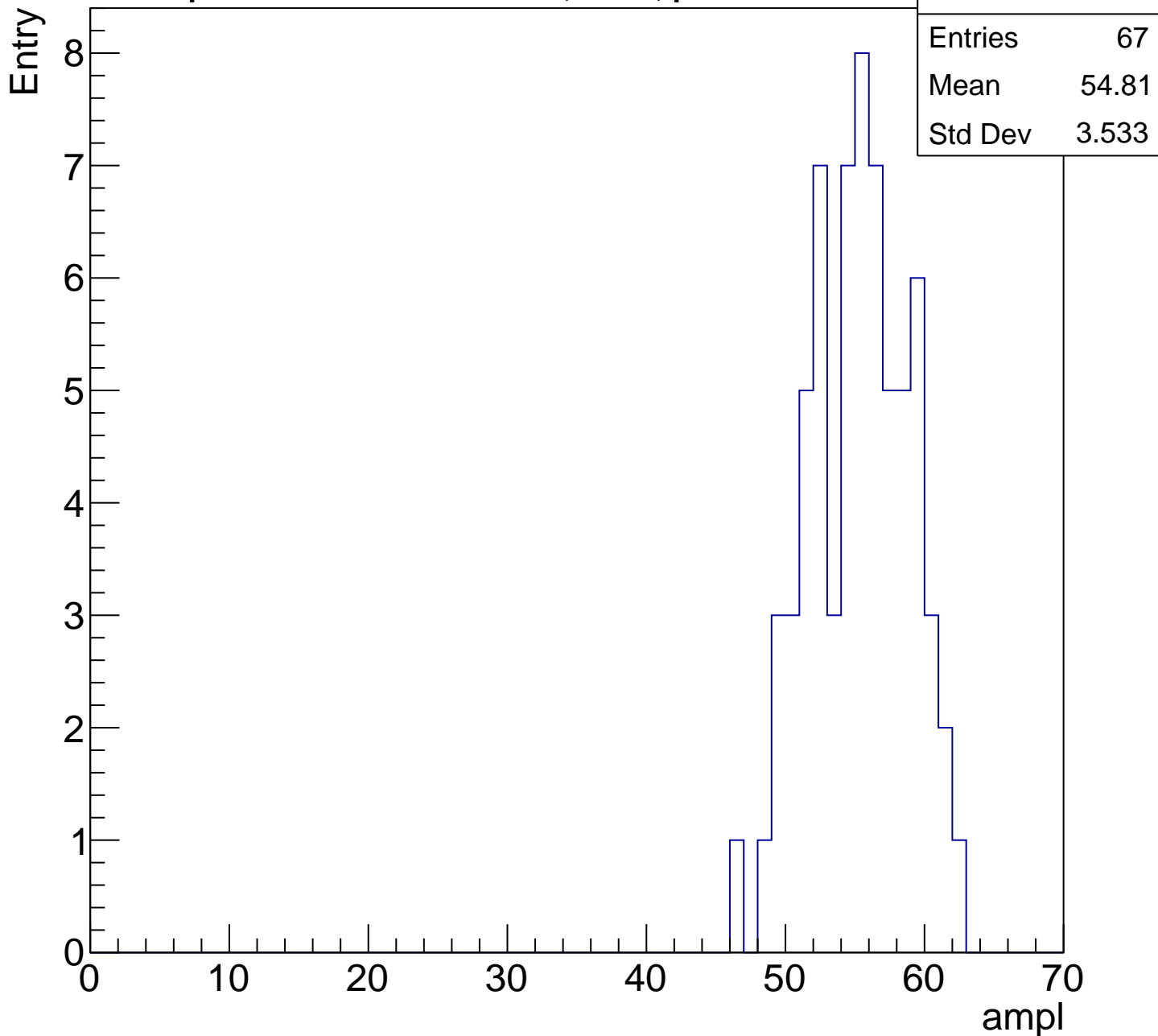
B1L101S, U6-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

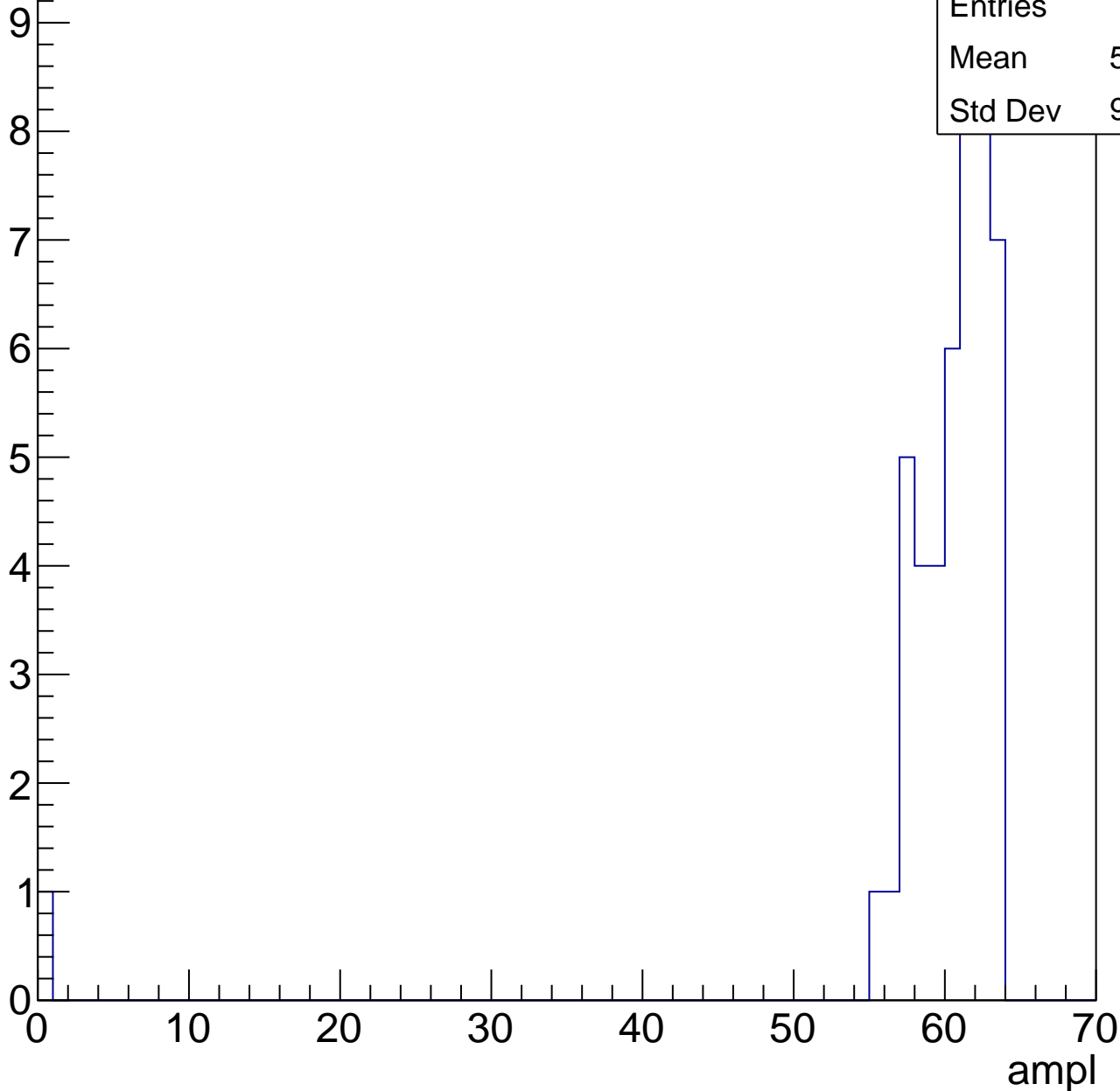


B1L101S, U6-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

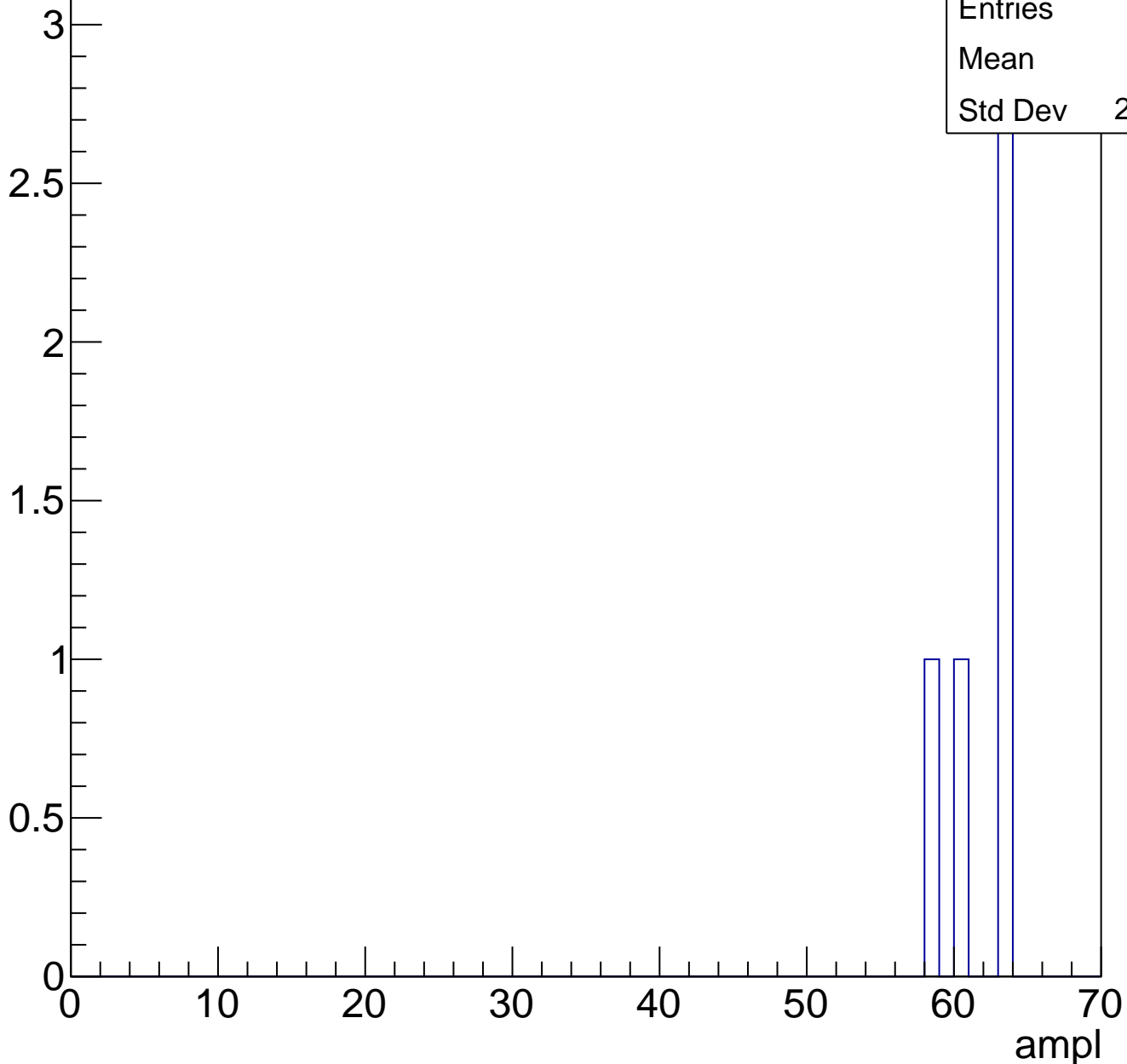
Entries	46
Mean	58.93
Std Dev	9.042



B1L101S, U6-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch15, adc0

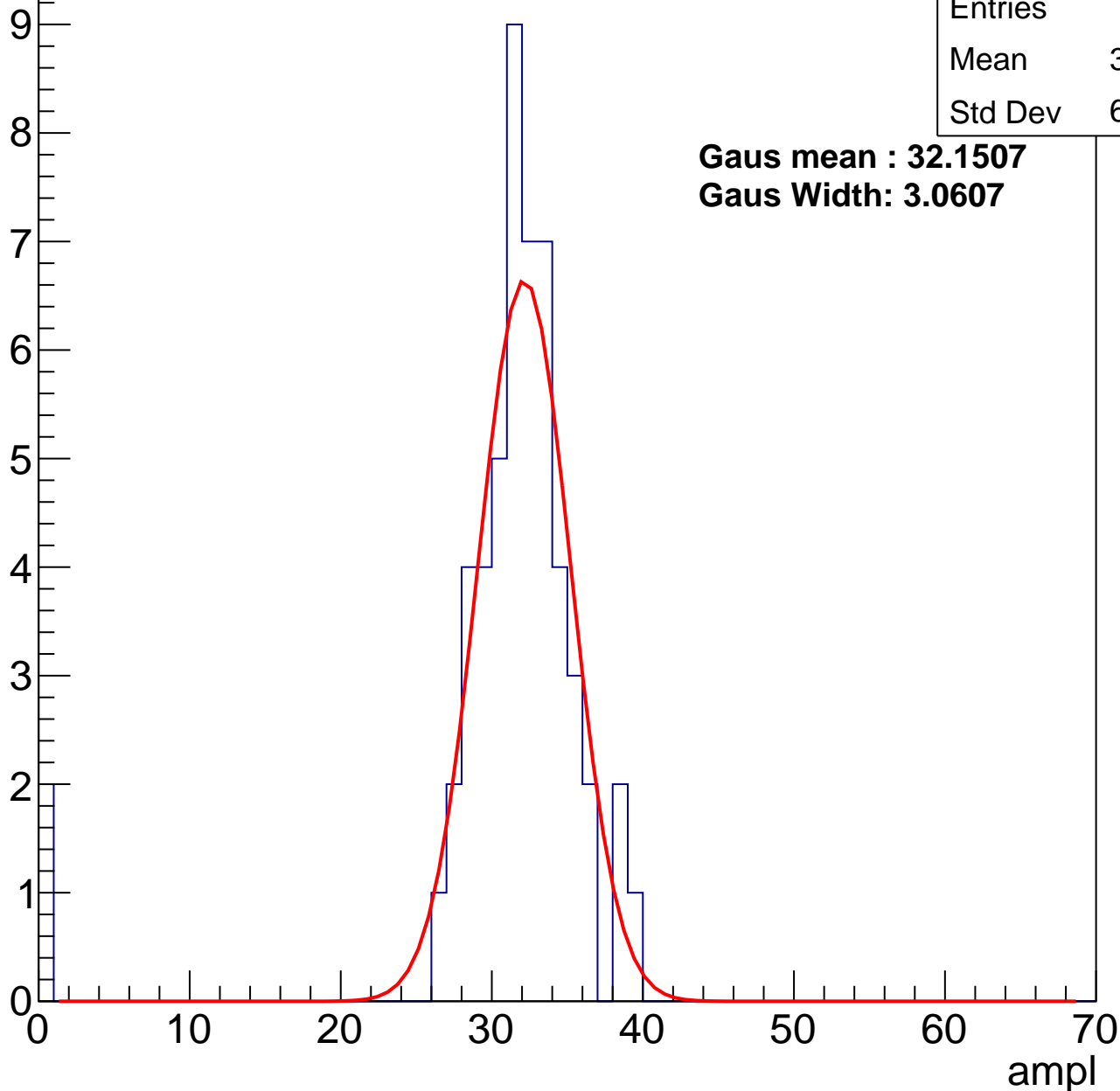
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	30.57
Std Dev	6.669

Gaus mean : 32.1507

Gaus Width: 3.0607



B1L101S, U6-ch15, adc1

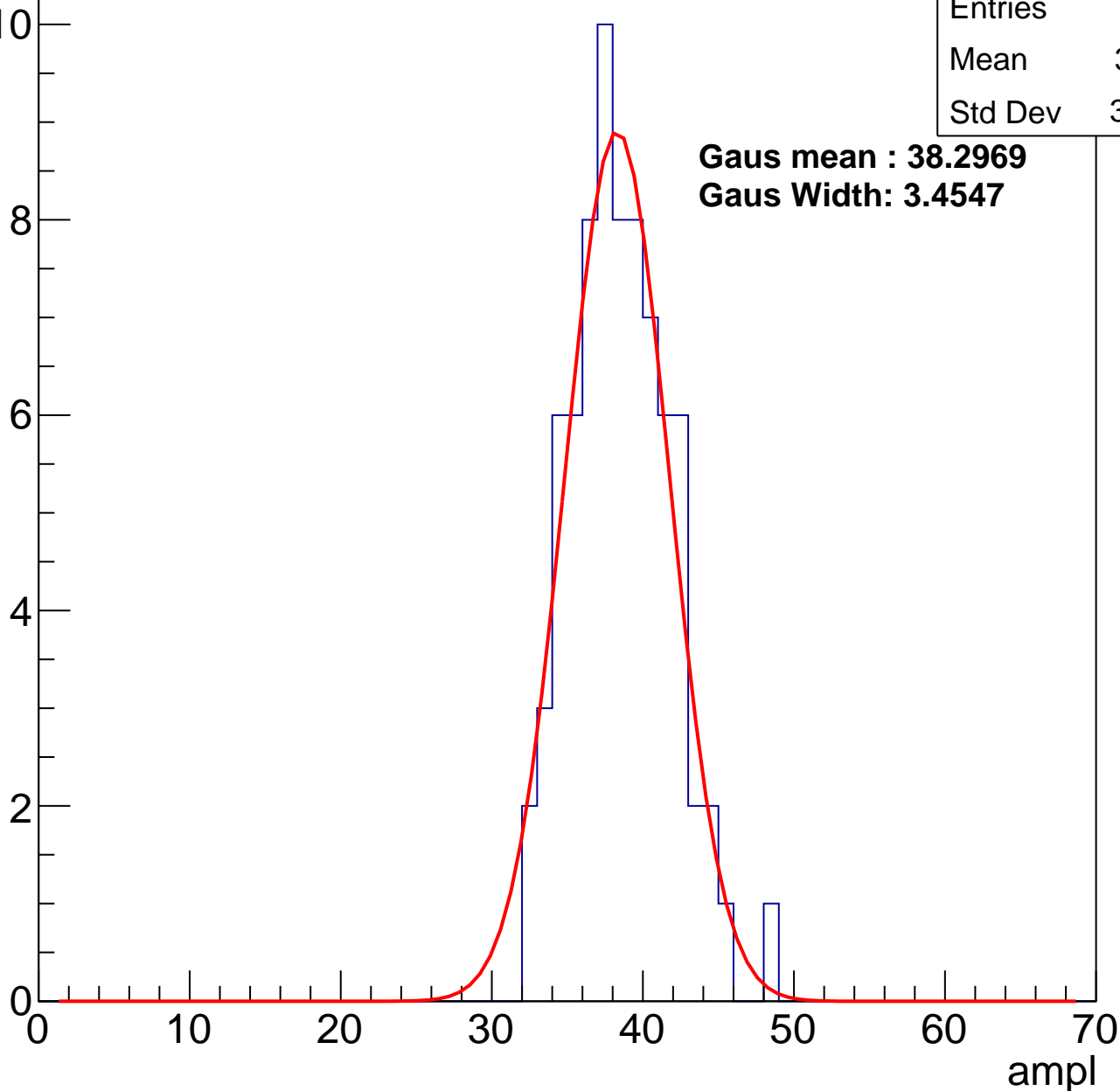
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	38.11
Std Dev	3.235

Gaus mean : 38.2969

Gaus Width: 3.4547



B1L101S, U6-ch15, adc2

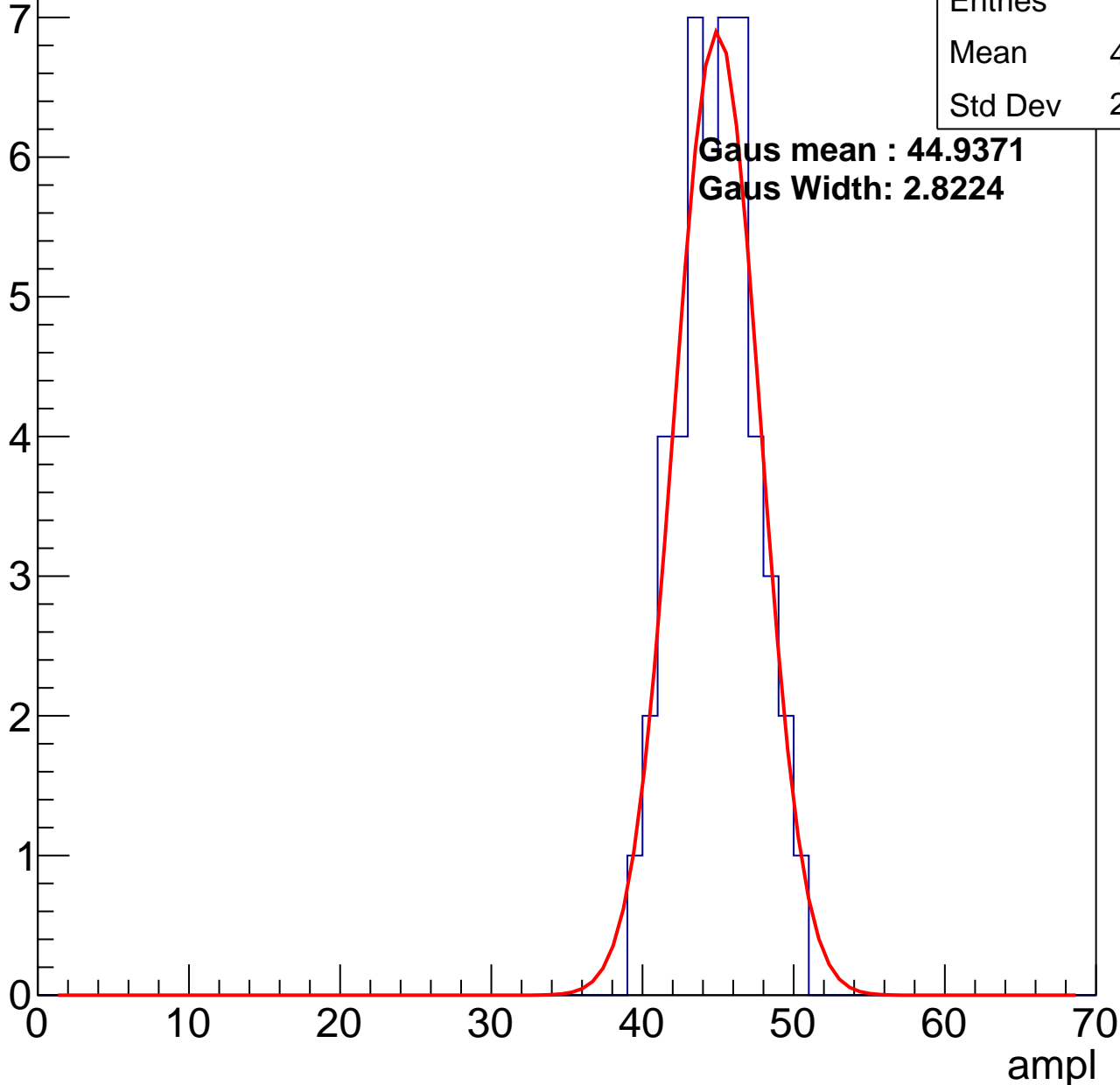
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	44.44
Std Dev	2.549

Gaus mean : 44.9371

Gaus Width: 2.8224



B1L101S, U6-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

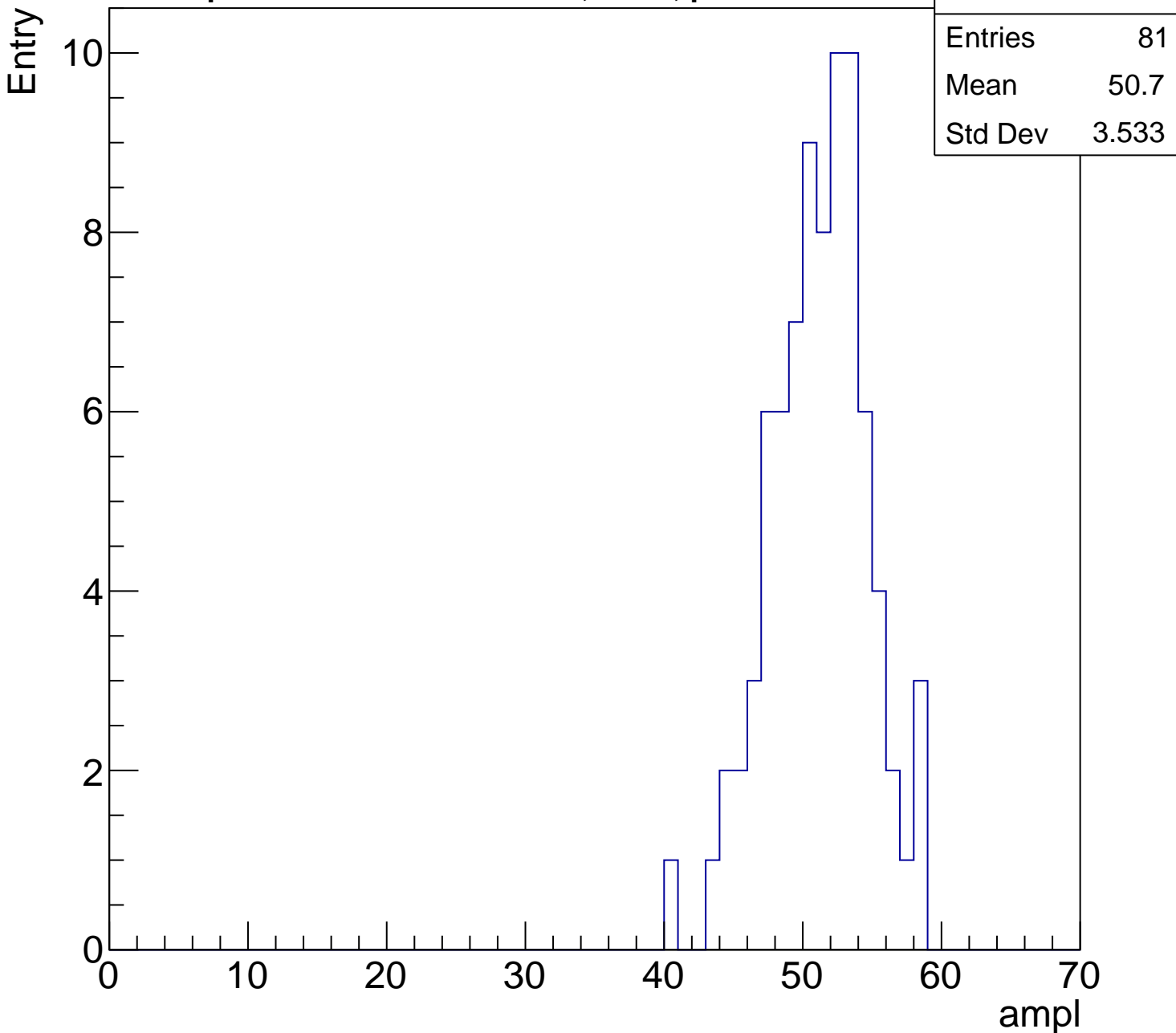
Entries	81
Mean	50.7
Std Dev	3.533

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

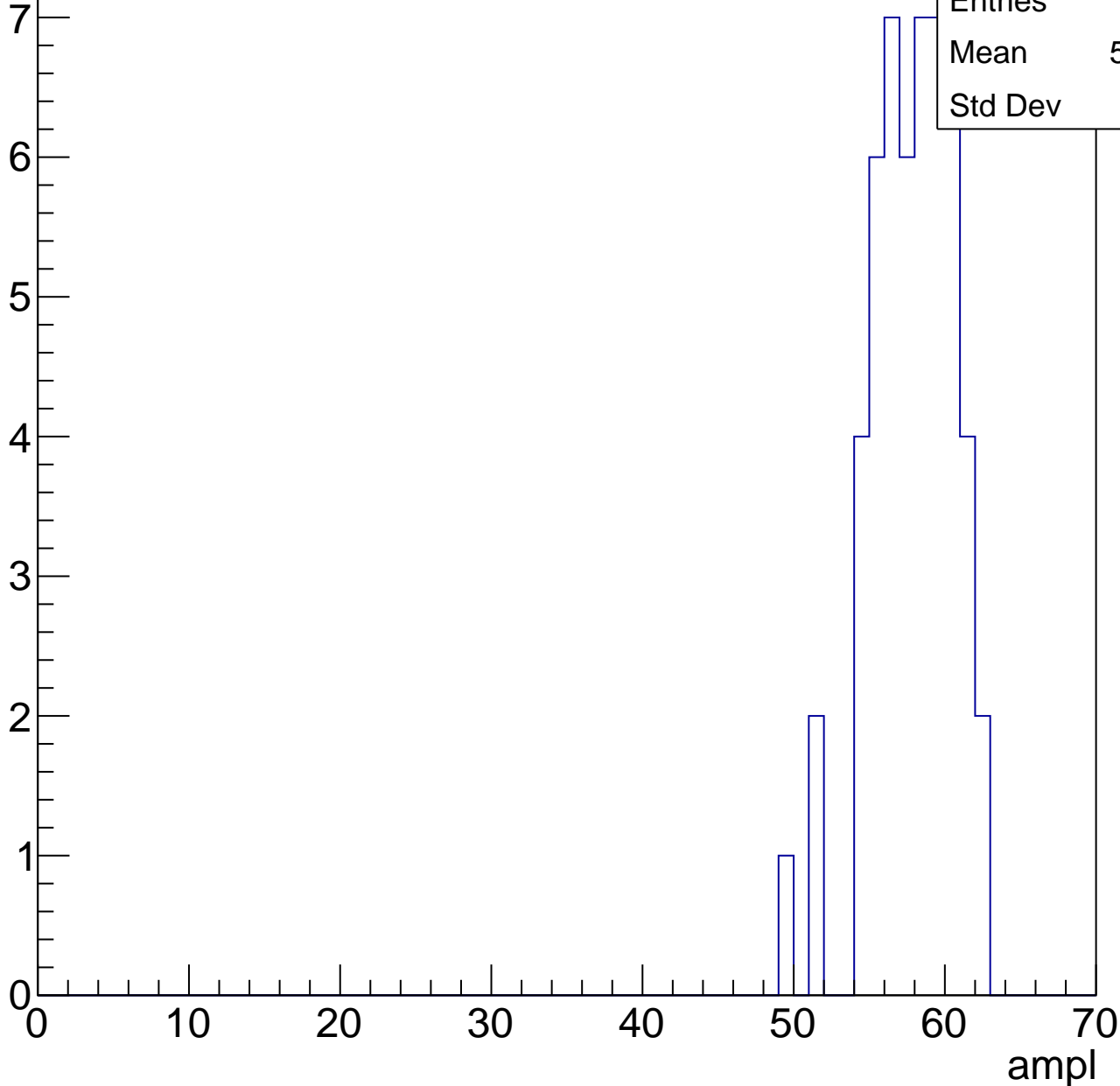


B1L101S, U6-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.32
Std Dev	2.78

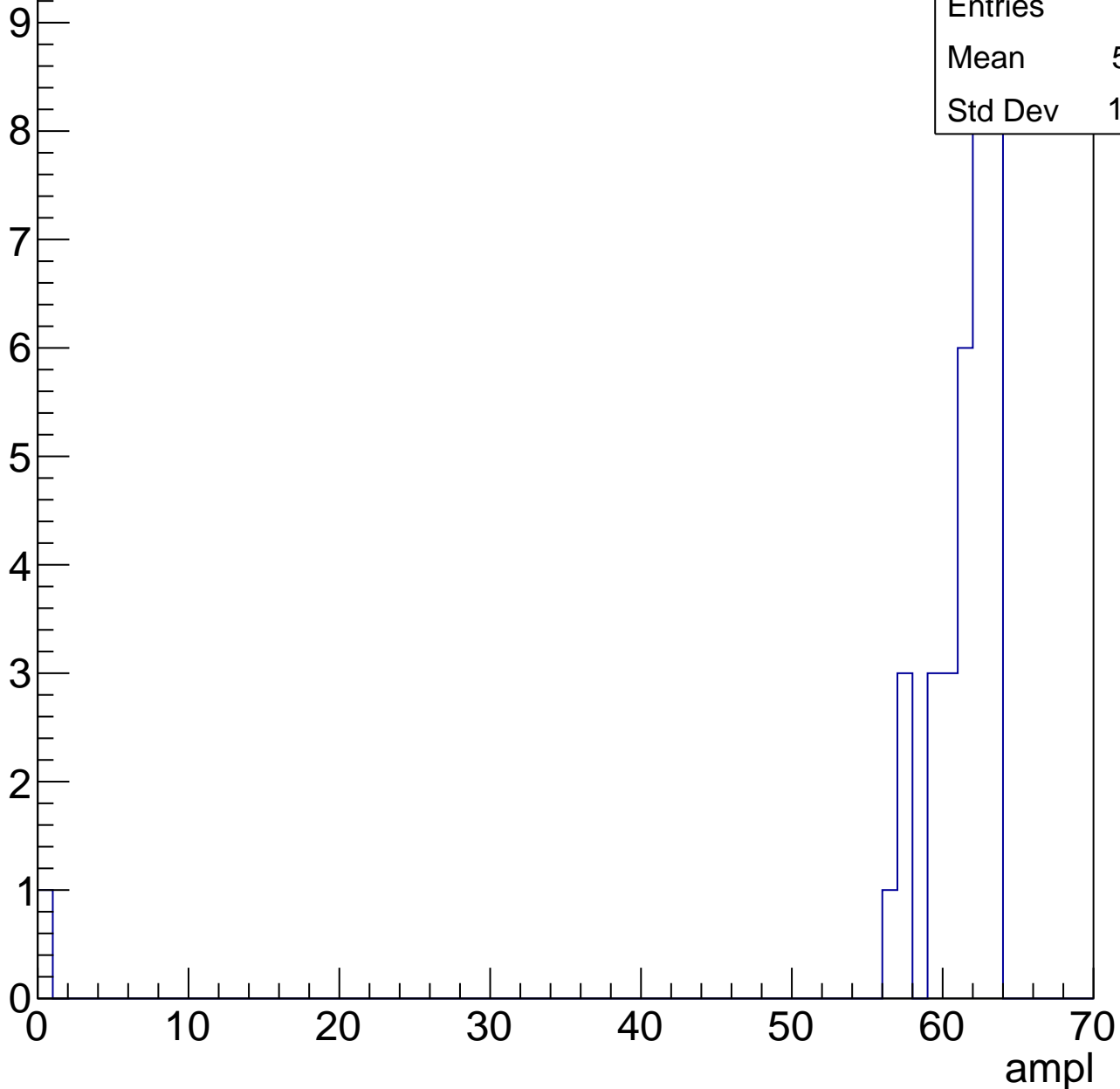


B1L101S, U6-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.21
Std Dev	10.49



B1L101S, U6-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

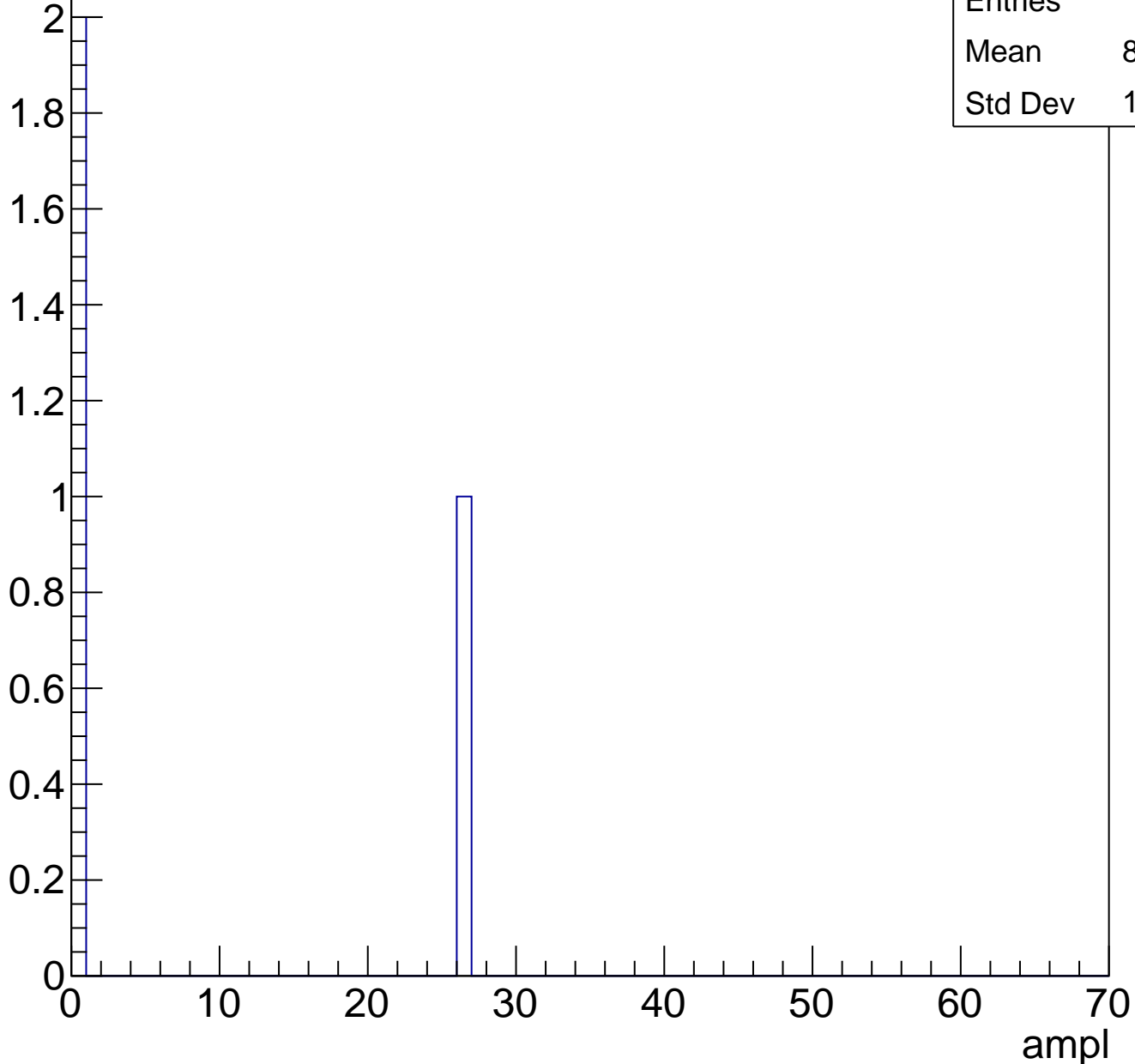
Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U6-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8.667
Std Dev	12.26

B1L101S, U6-ch16, adc0

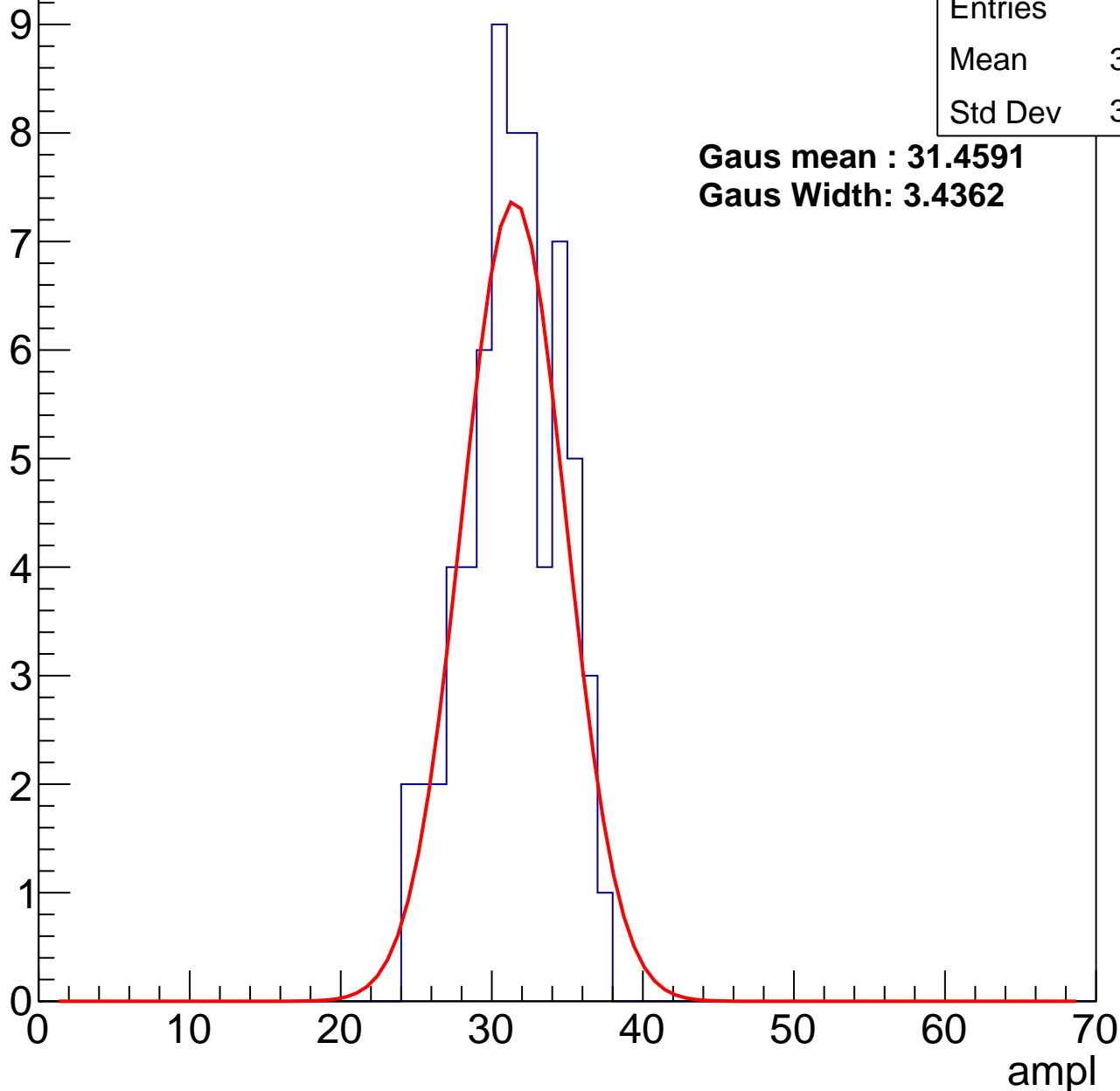
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.89
Std Dev	3.114

Gaus mean : 31.4591

Gaus Width: 3.4362



B1L101S, U6-ch16, adc1

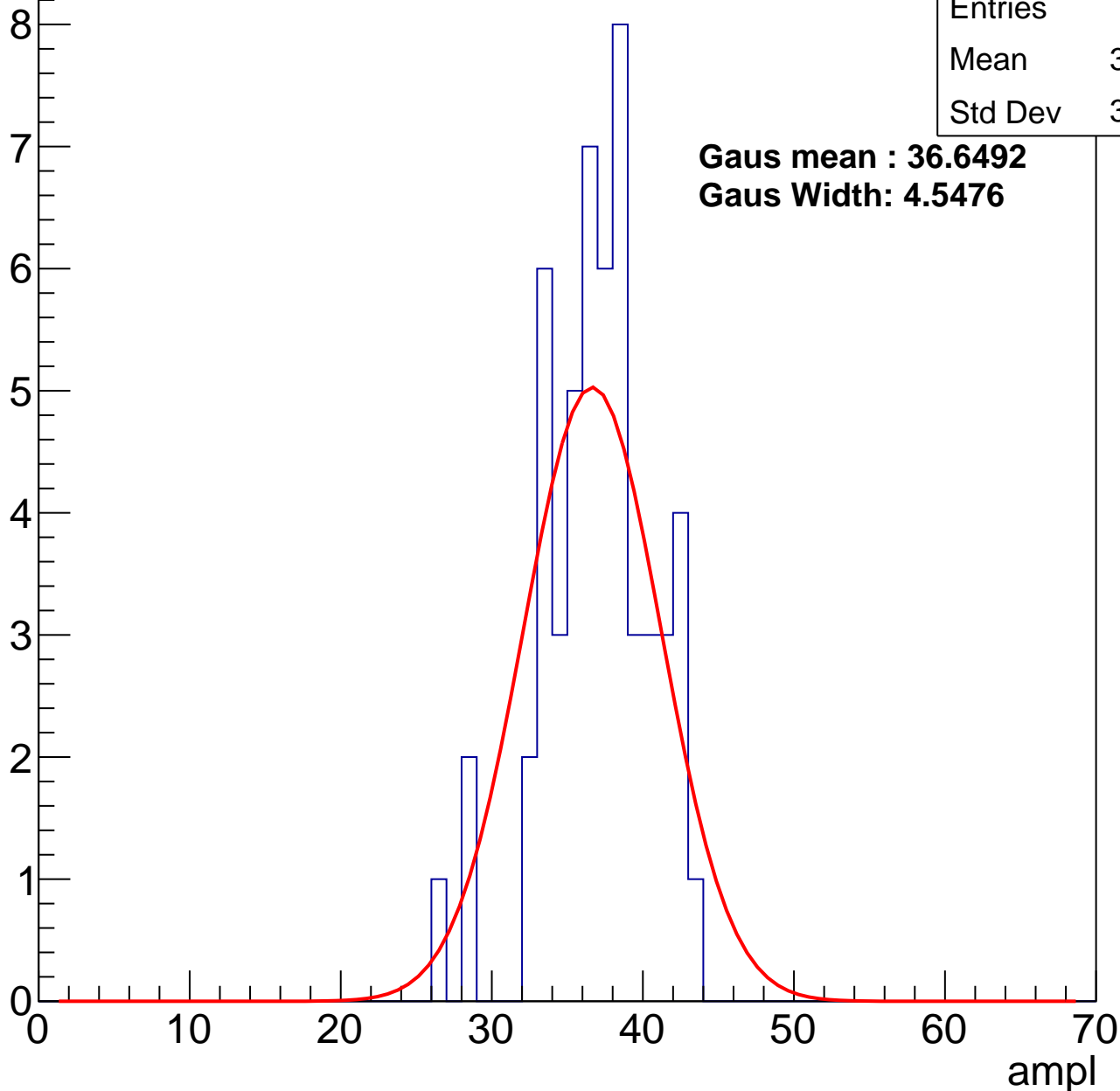
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.48
Std Dev	3.599

Gaus mean : 36.6492

Gaus Width: 4.5476



B1L101S, U6-ch16, adc2

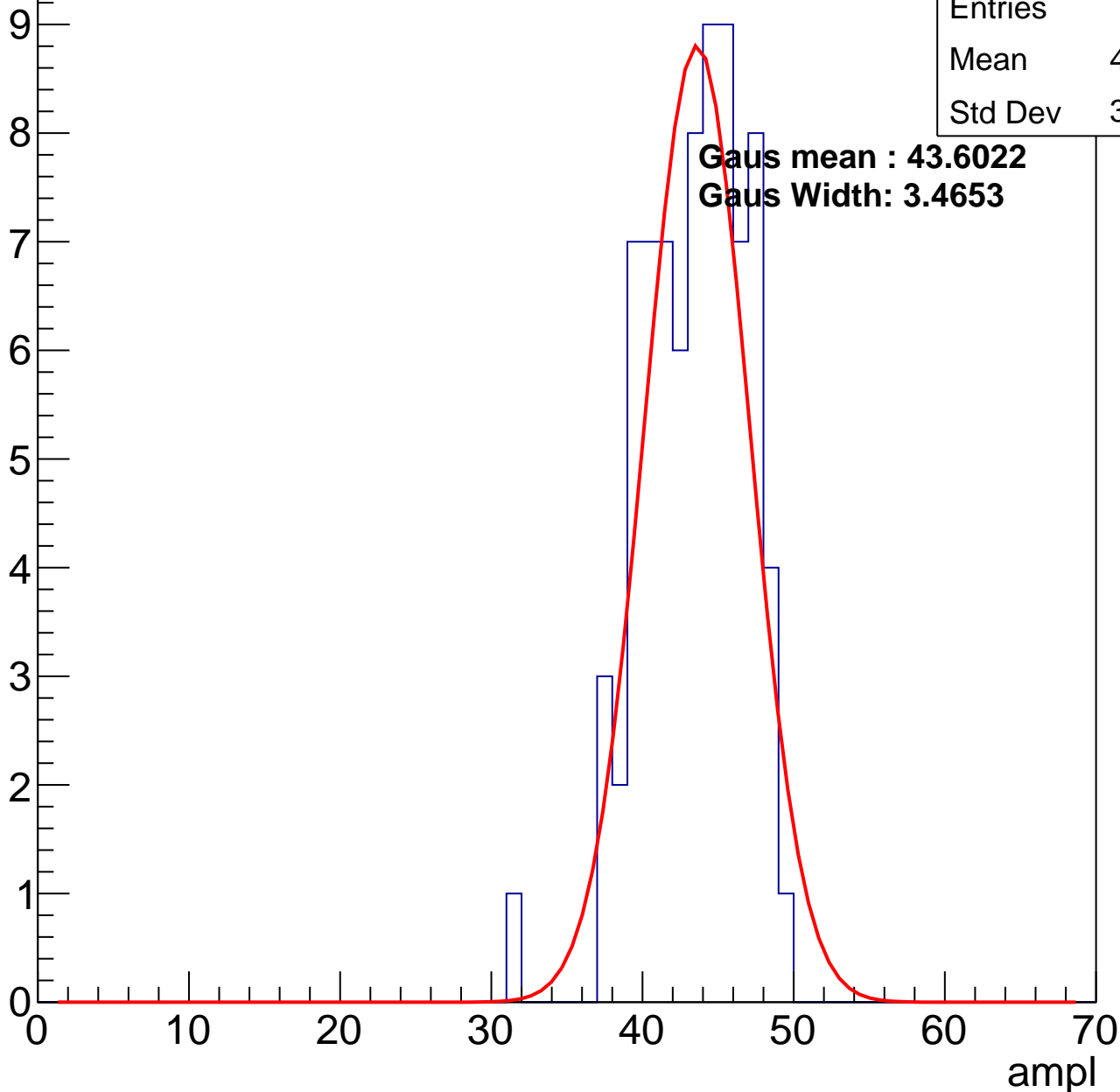
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	42.96
Std Dev	3.343

Gaus mean : 43.6022

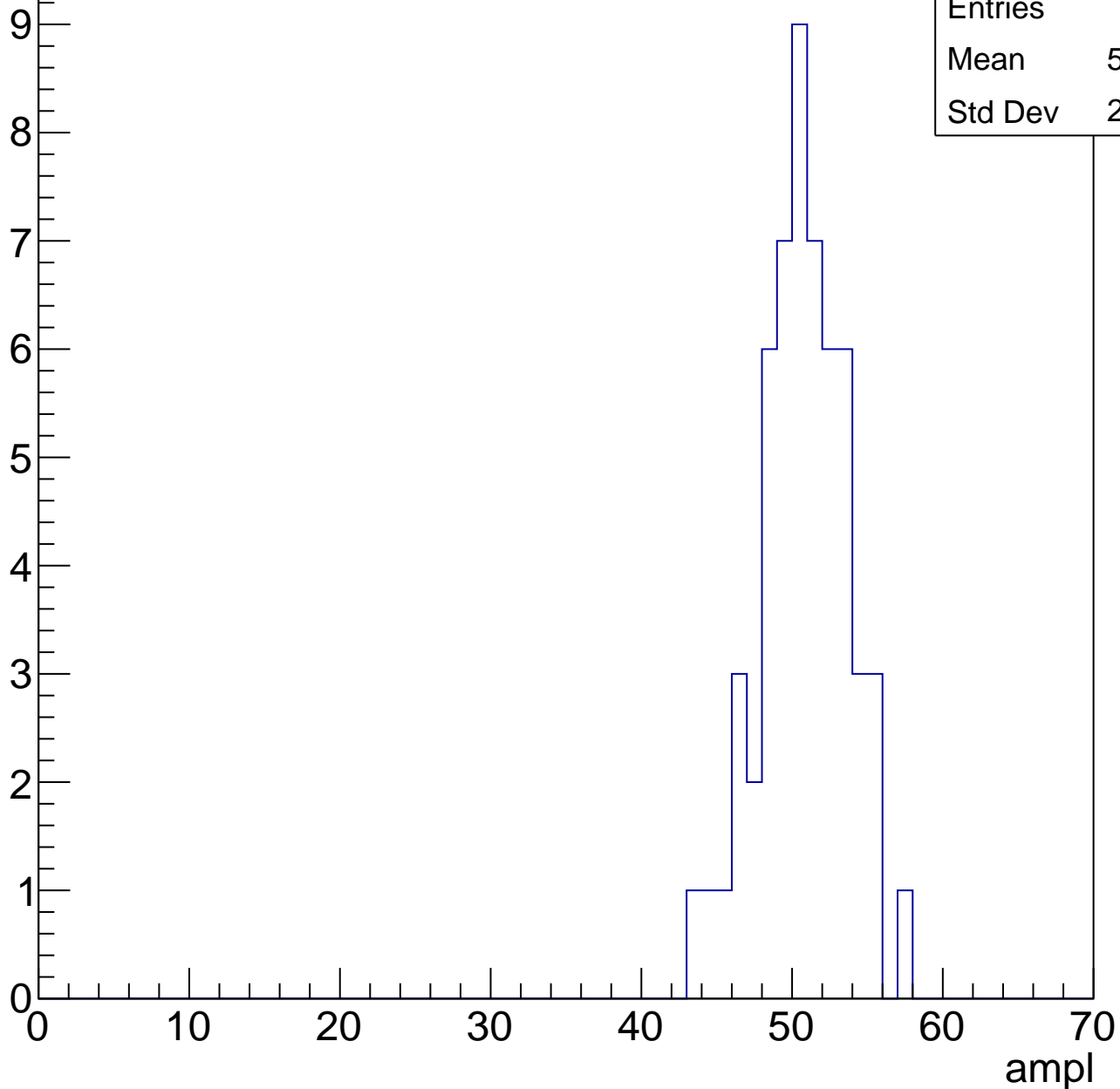
Gaus Width: 3.4653



B1L101S, U6-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

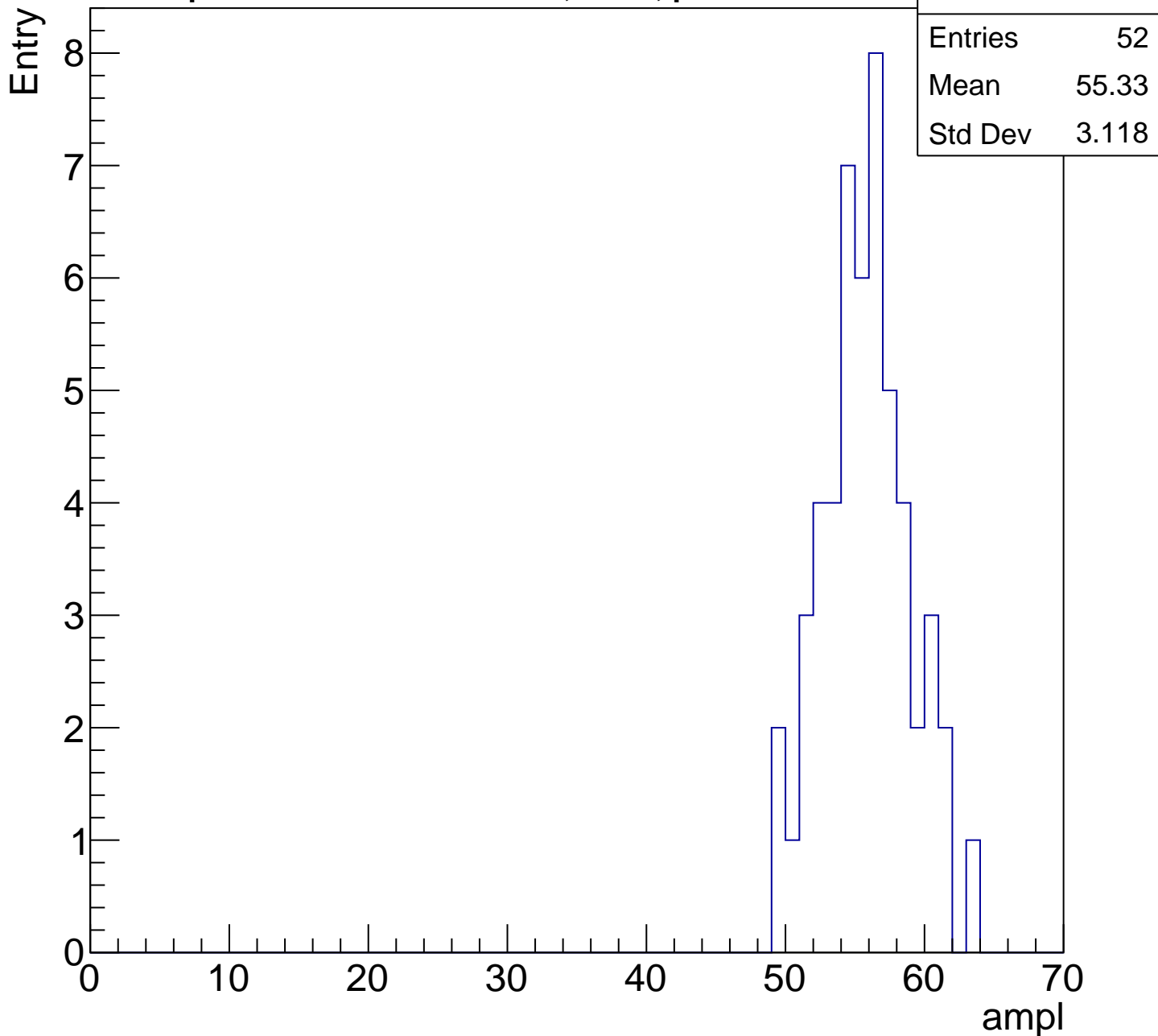
Entry



Entries	56
Mean	50.29
Std Dev	2.864

B1L101S, U6-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

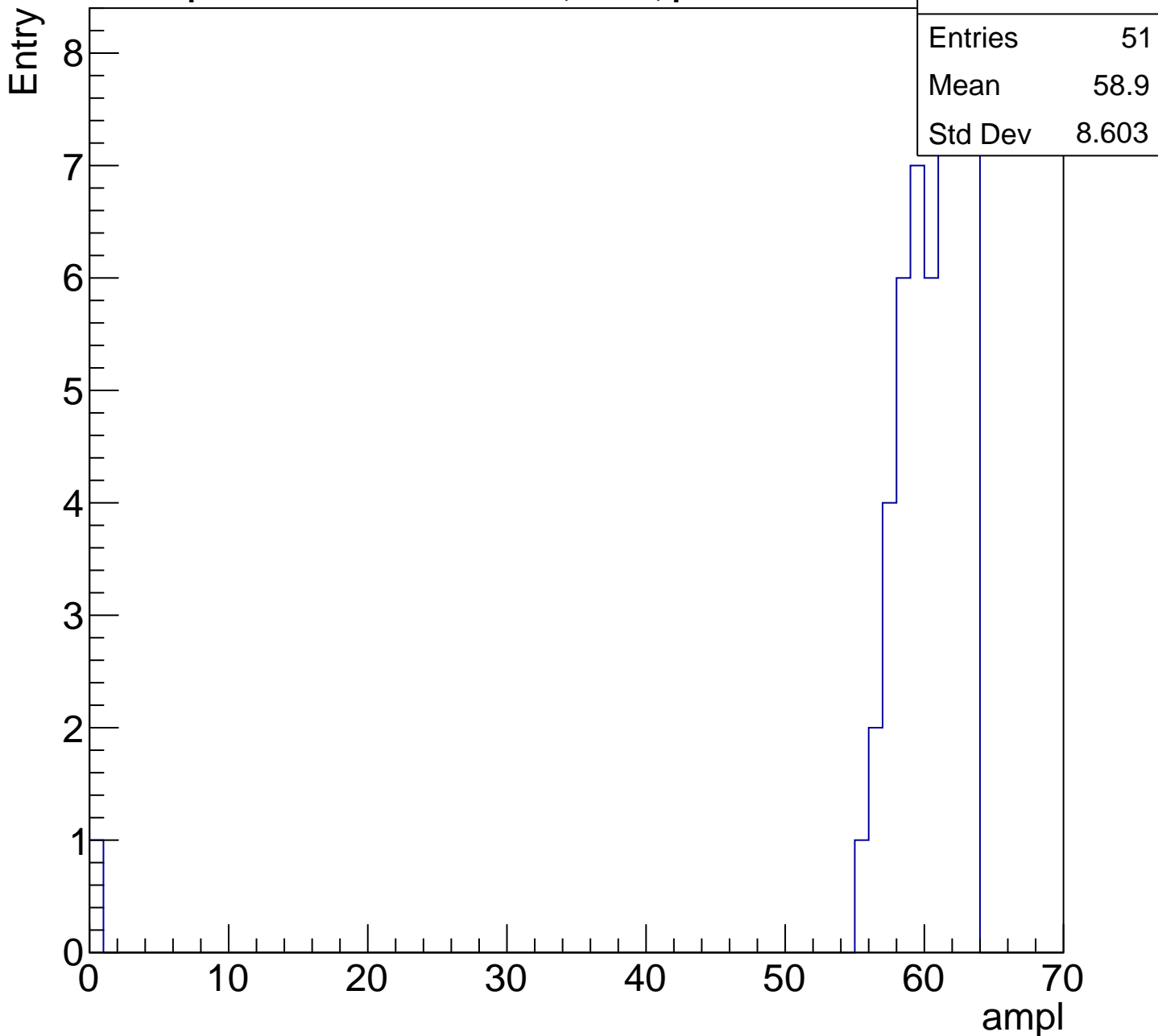
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.9
Std Dev	8.603

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

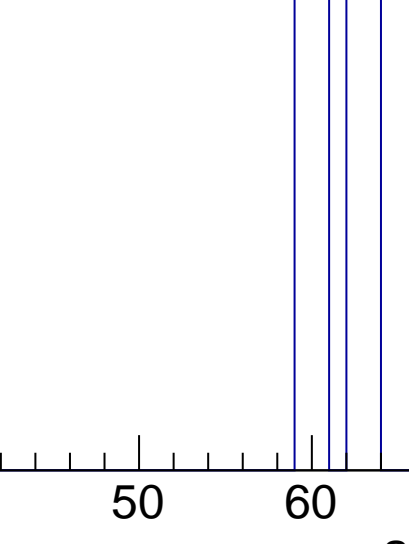
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.47

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch17, adc0

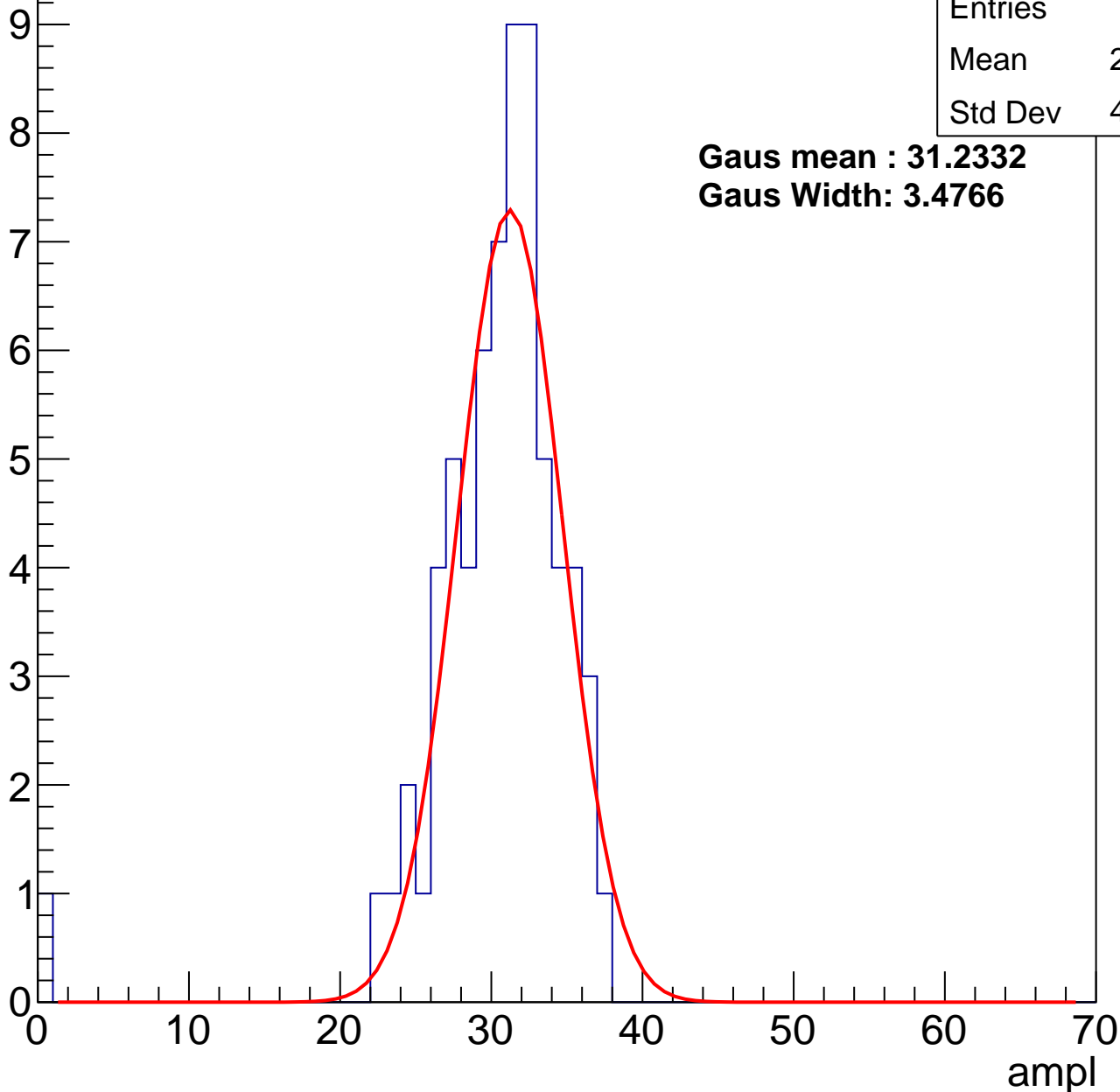
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.94
Std Dev	4.965

Gaus mean : 31.2332

Gaus Width: 3.4766



B1L101S, U6-ch17, adc1

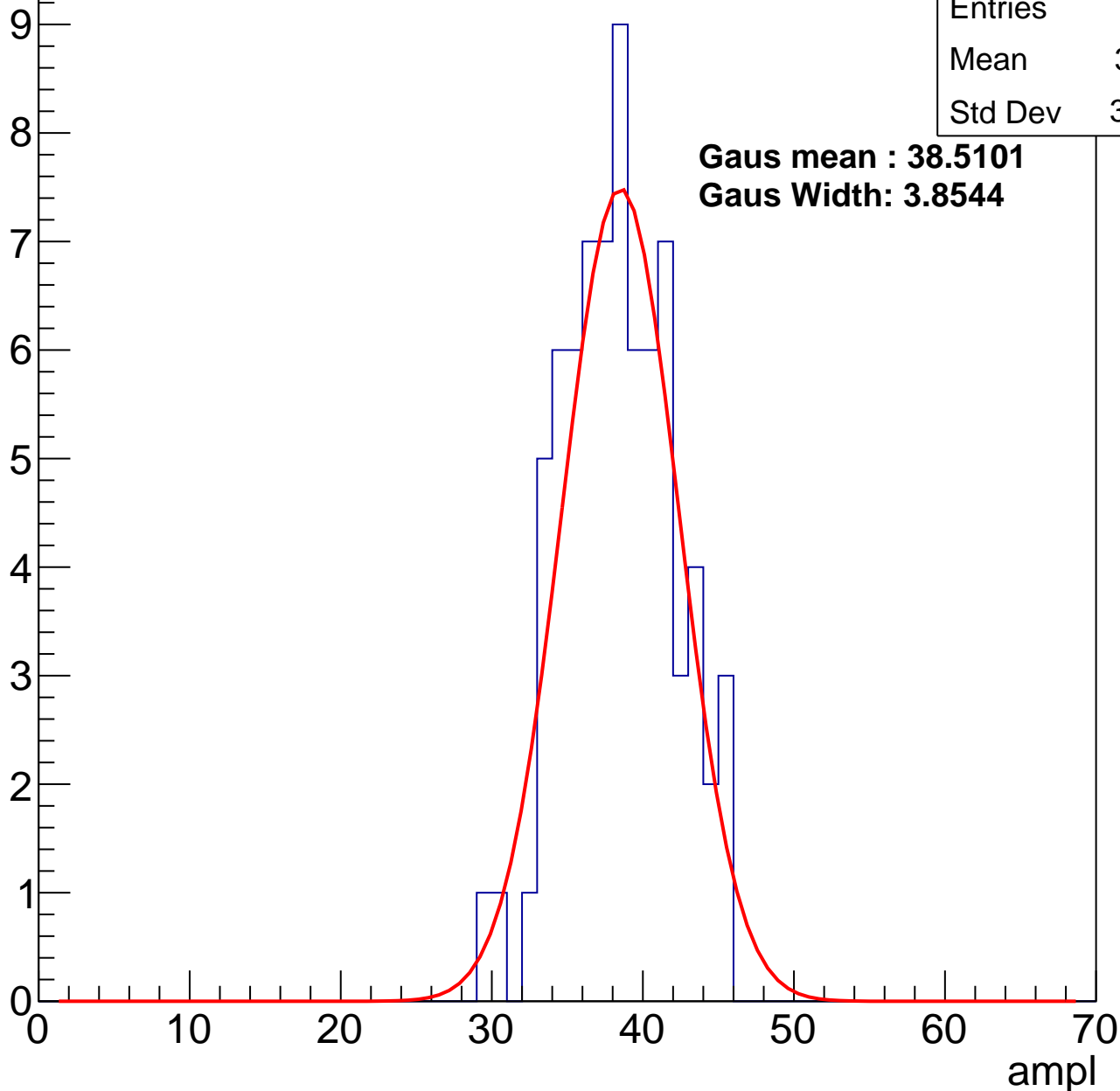
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.91
Std Dev	3.584

Gaus mean : 38.5101

Gaus Width: 3.8544



B1L101S, U6-ch17, adc2

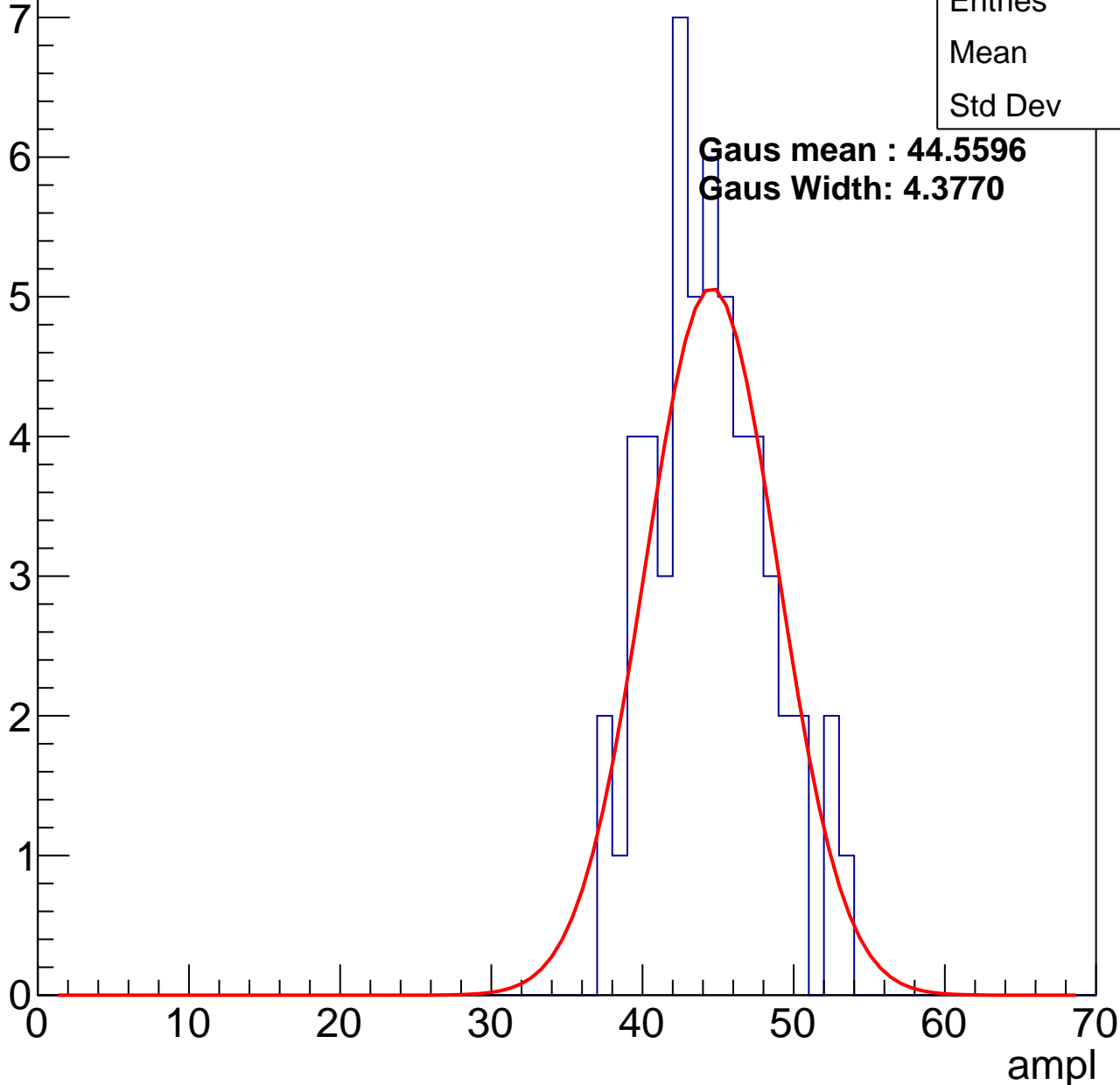
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	44
Std Dev	3.8

Gaus mean : 44.5596

Gaus Width: 4.3770

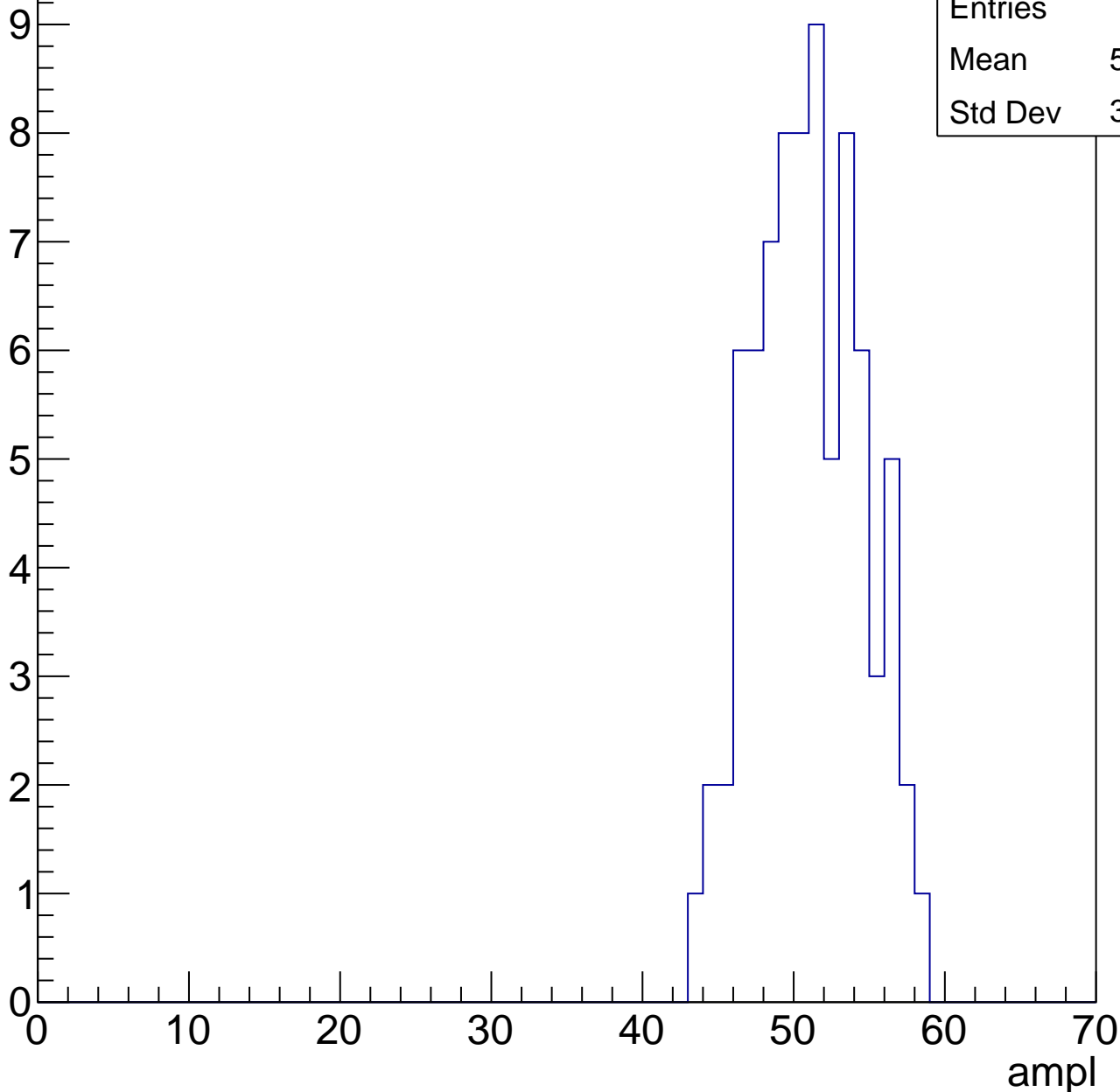


B1L101S, U6-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

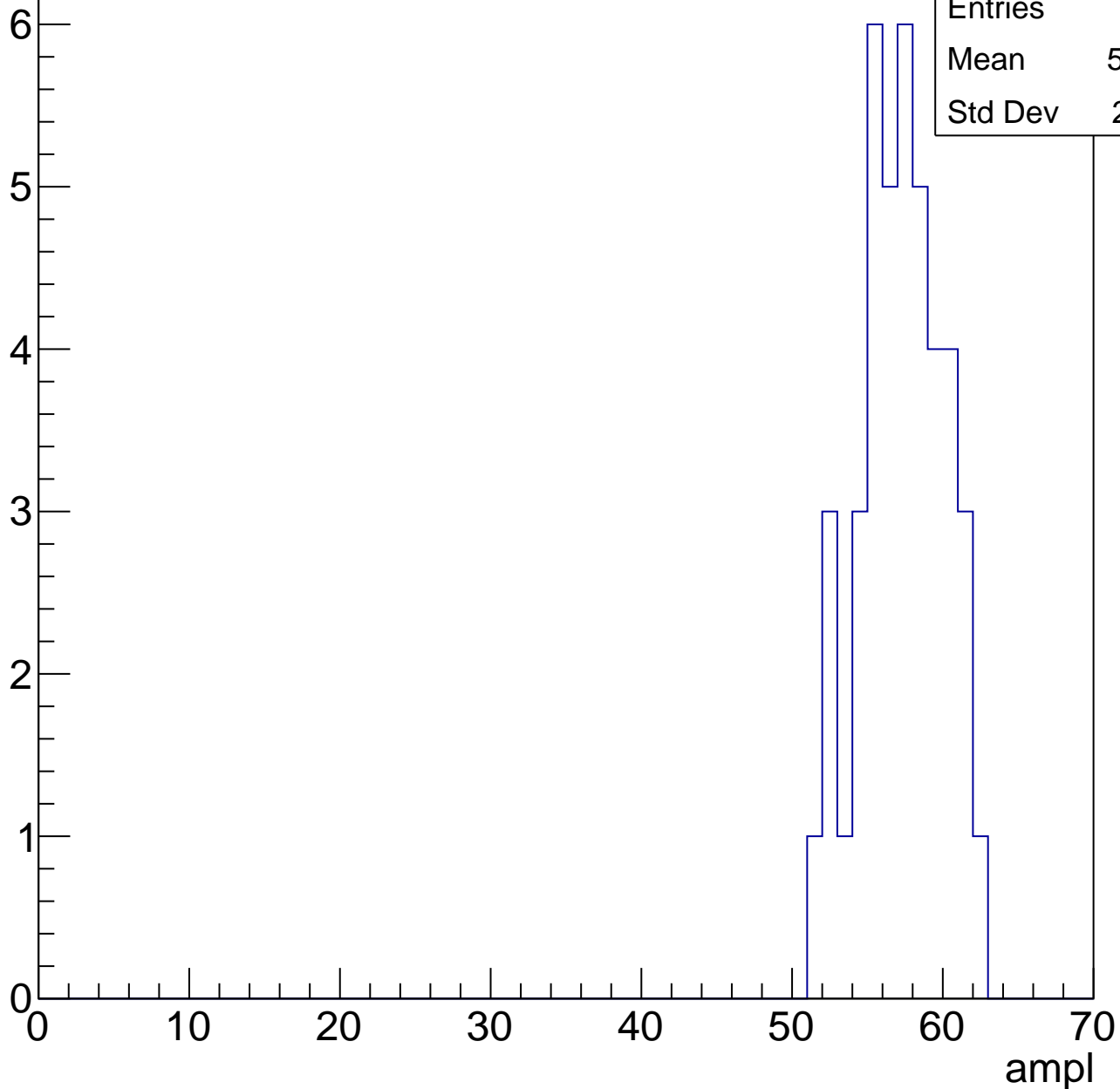
Entries	79
Mean	50.52
Std Dev	3.467



B1L101S, U6-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



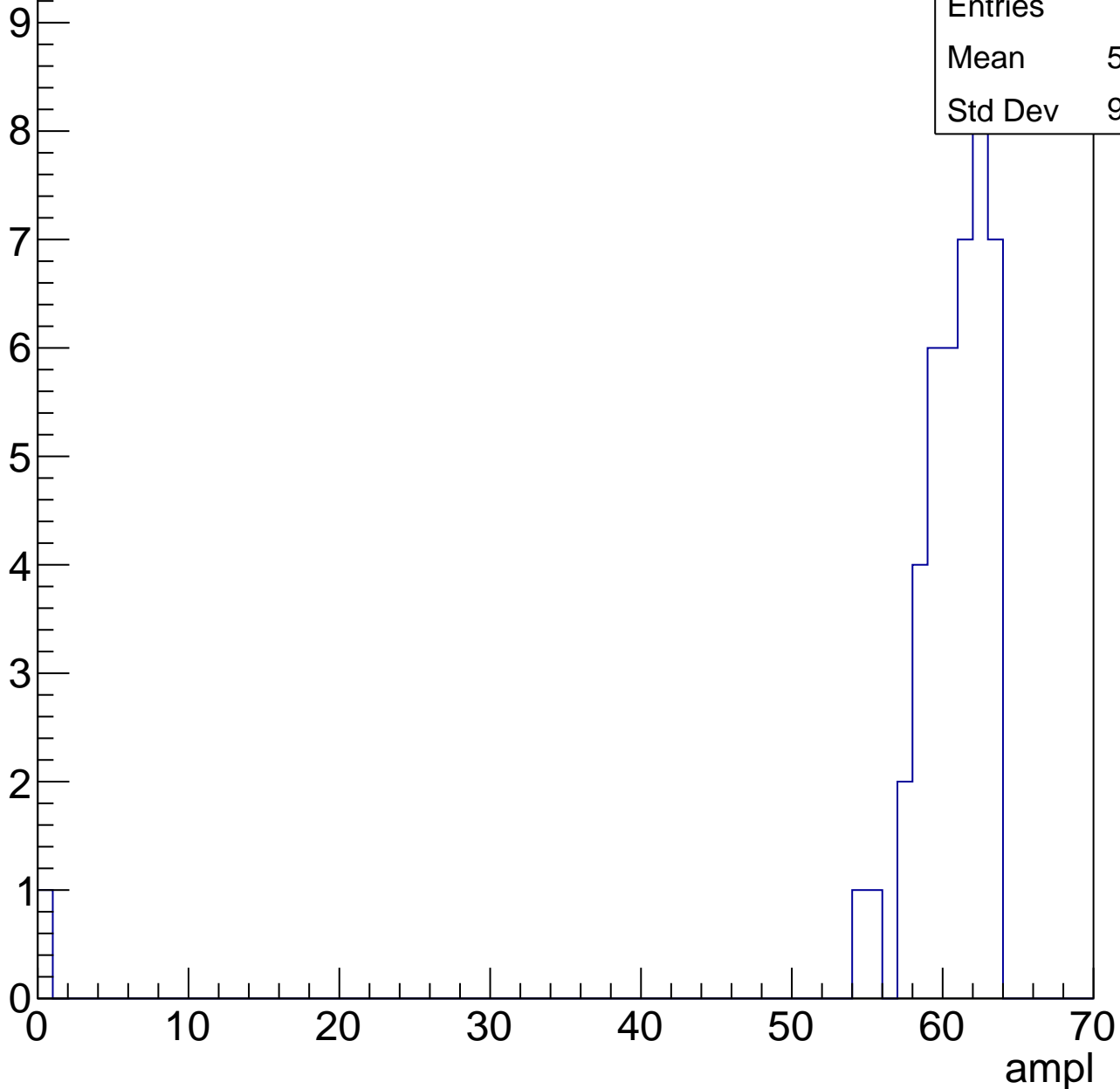
Entries	42
Mean	56.79
Std Dev	2.721

B1L101S, U6-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.98
Std Dev	9.245



B1L101S, U6-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch18, adc0

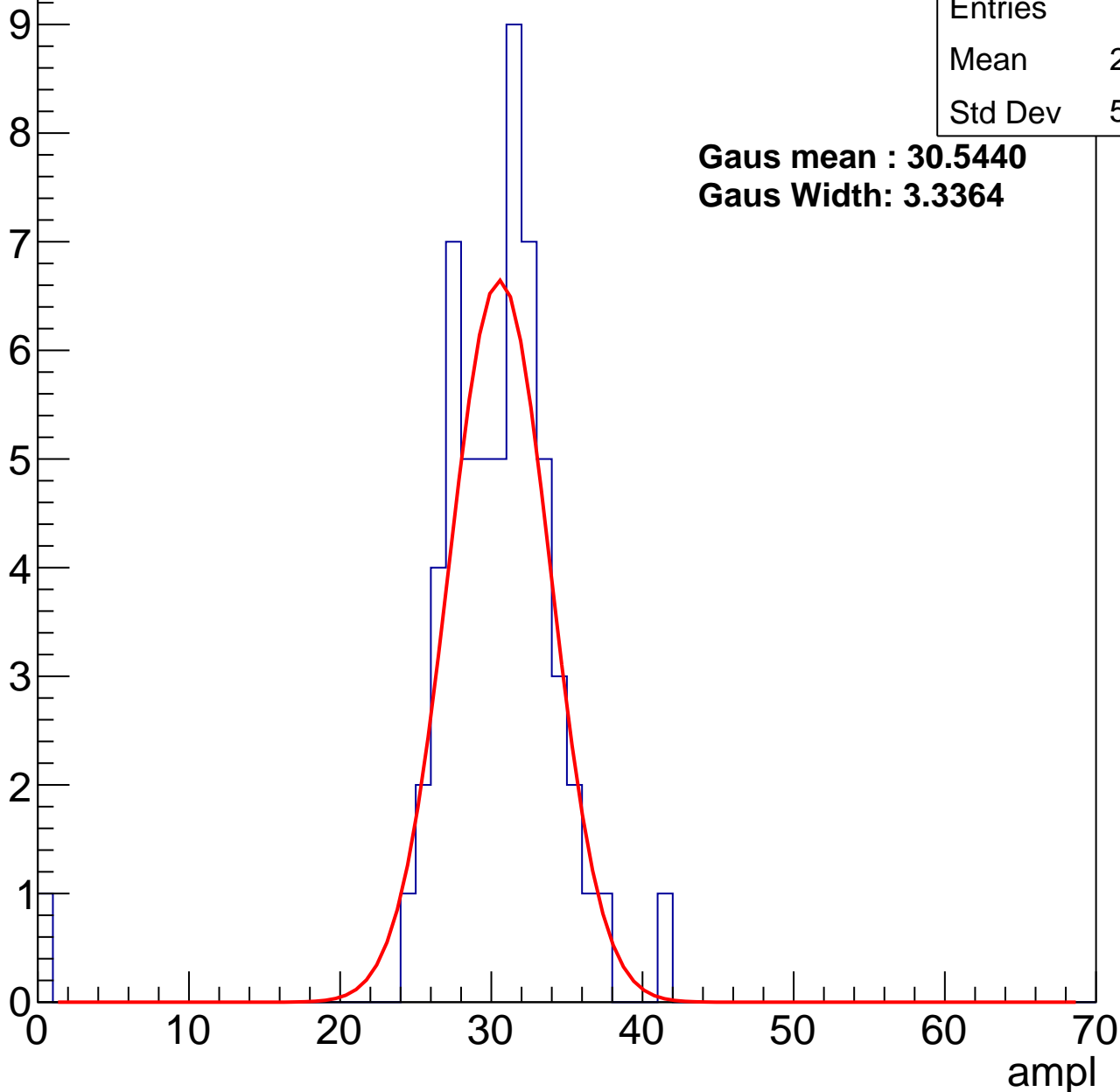
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.76
Std Dev	5.077

Gaus mean : 30.5440

Gaus Width: 3.3364



B1L101S, U6-ch18, adc1

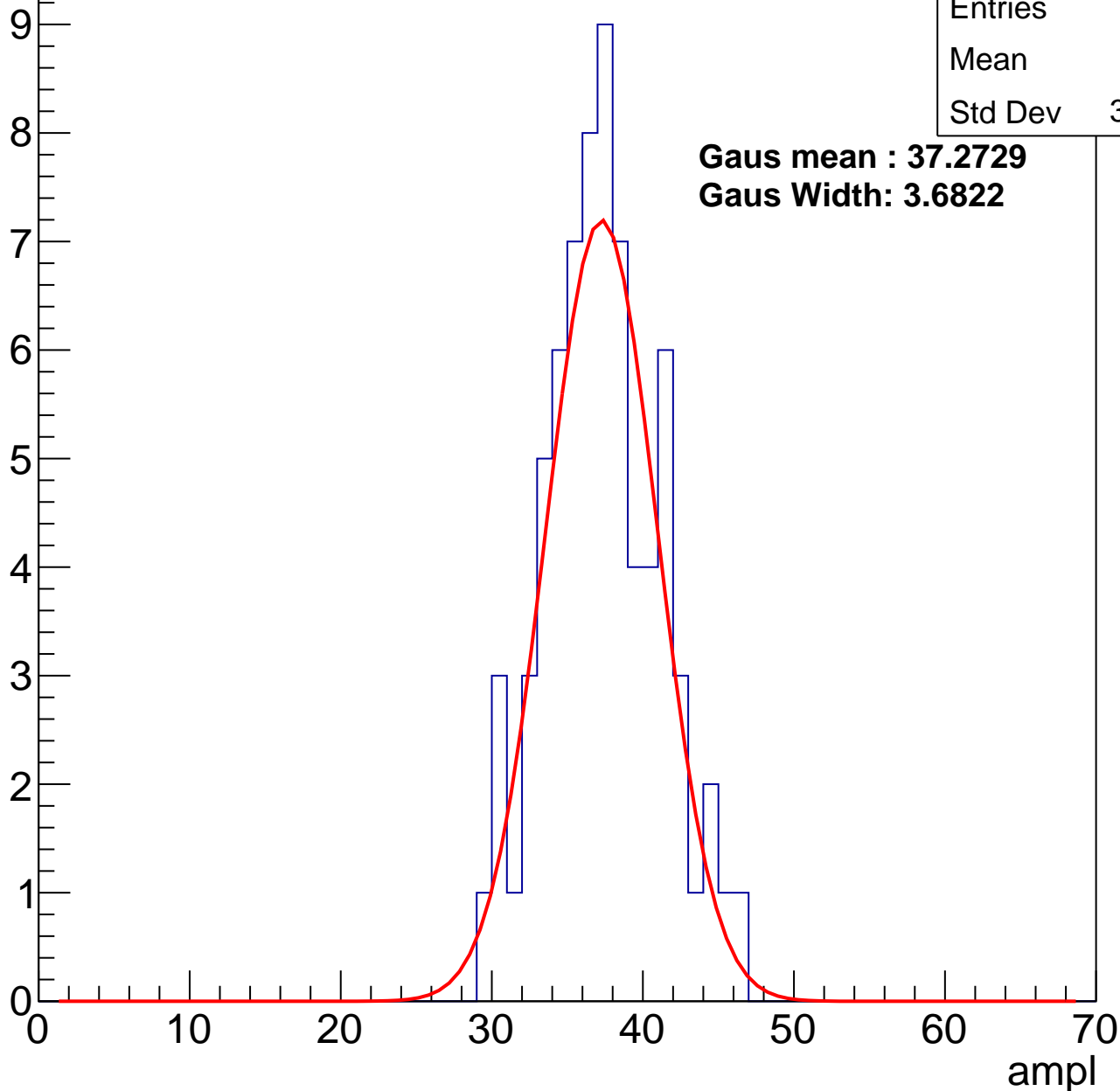
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.9
Std Dev	3.727

Gaus mean : 37.2729

Gaus Width: 3.6822



B1L101S, U6-ch18, adc2

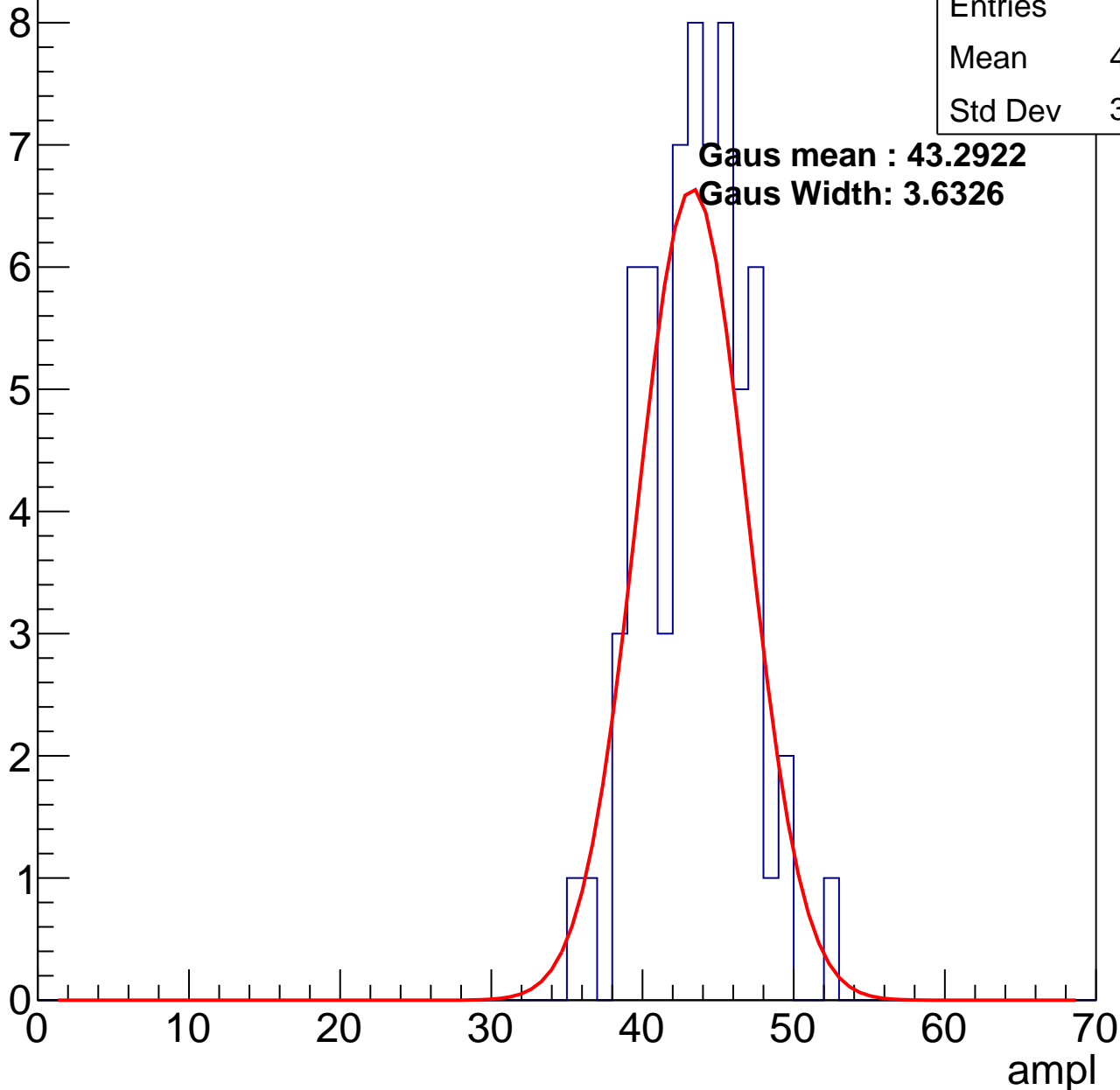
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.05
Std Dev	3.316

Gaus mean : 43.2922

Gaus Width: 3.6326



B1L101S, U6-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

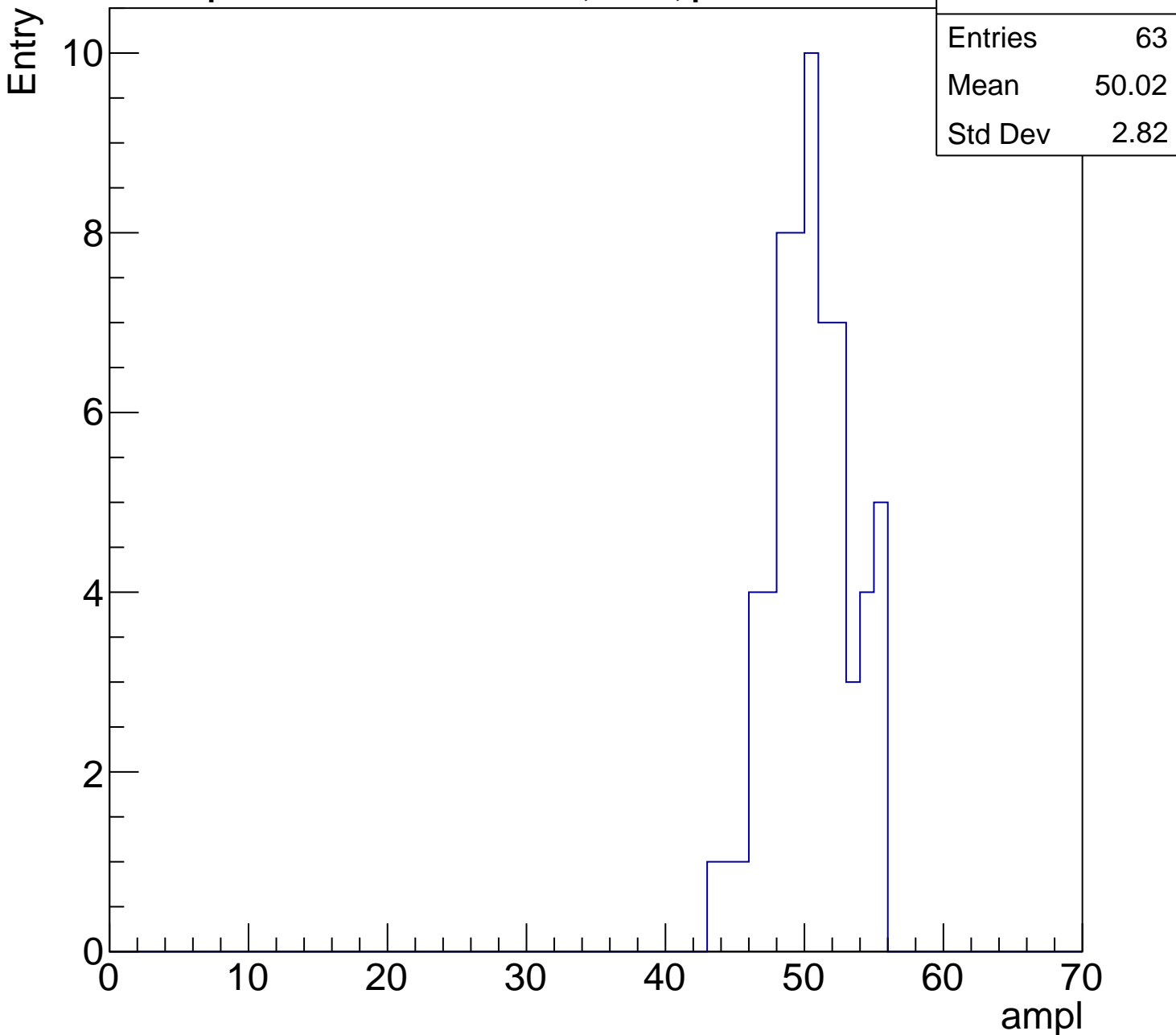
Entries	63
Mean	50.02
Std Dev	2.82

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

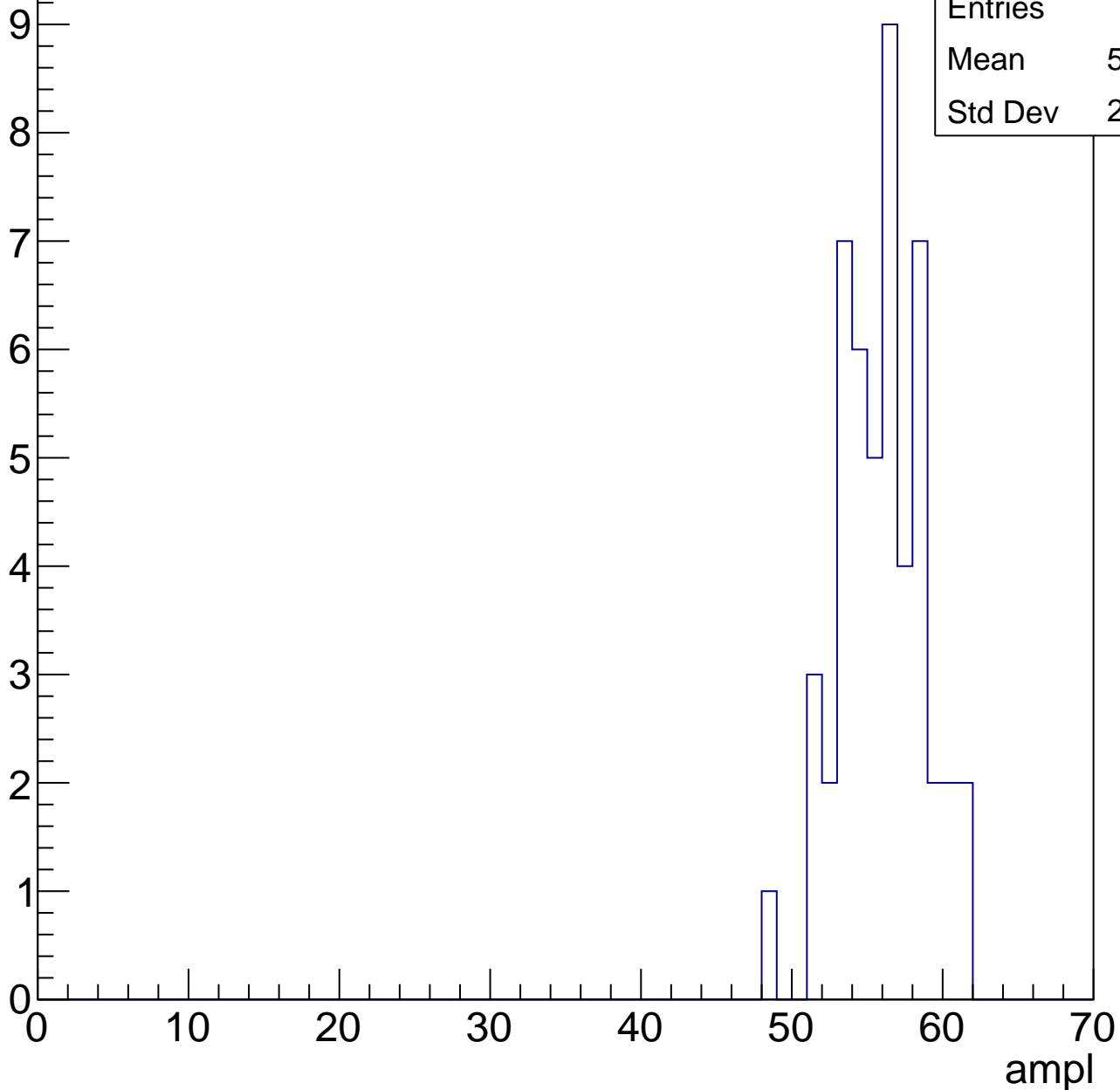


B1L101S, U6-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.46
Std Dev	2.758



B1L101S, U6-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.1
Std Dev	8.592

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L101S, U6-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B1L101S, U6-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch19, adc0

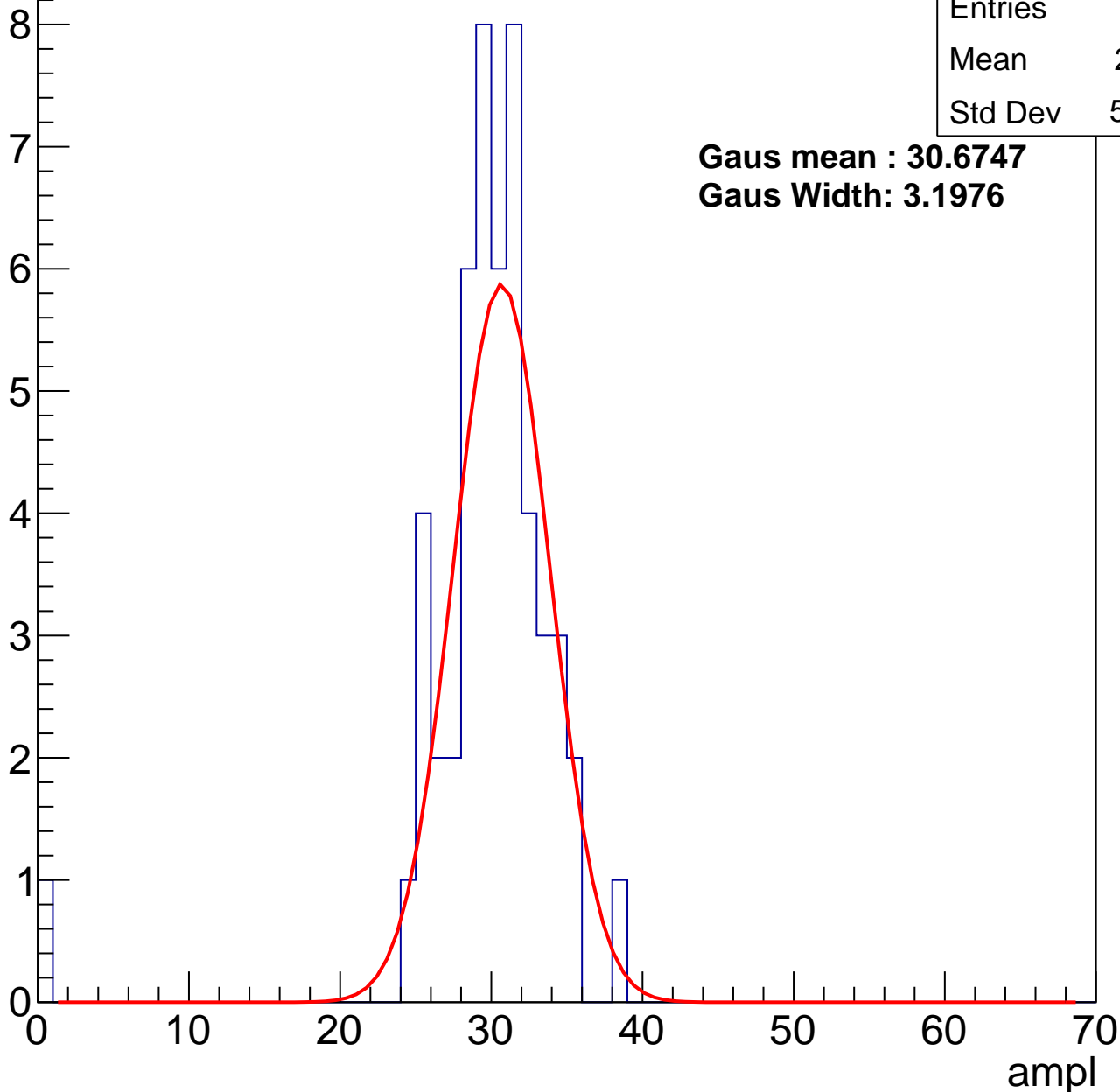
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	29.31
Std Dev	5.062

Gaus mean : 30.6747

Gaus Width: 3.1976



B1L101S, U6-ch19, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	36.43
Std Dev	3.369

Gaus mean : 36.7378

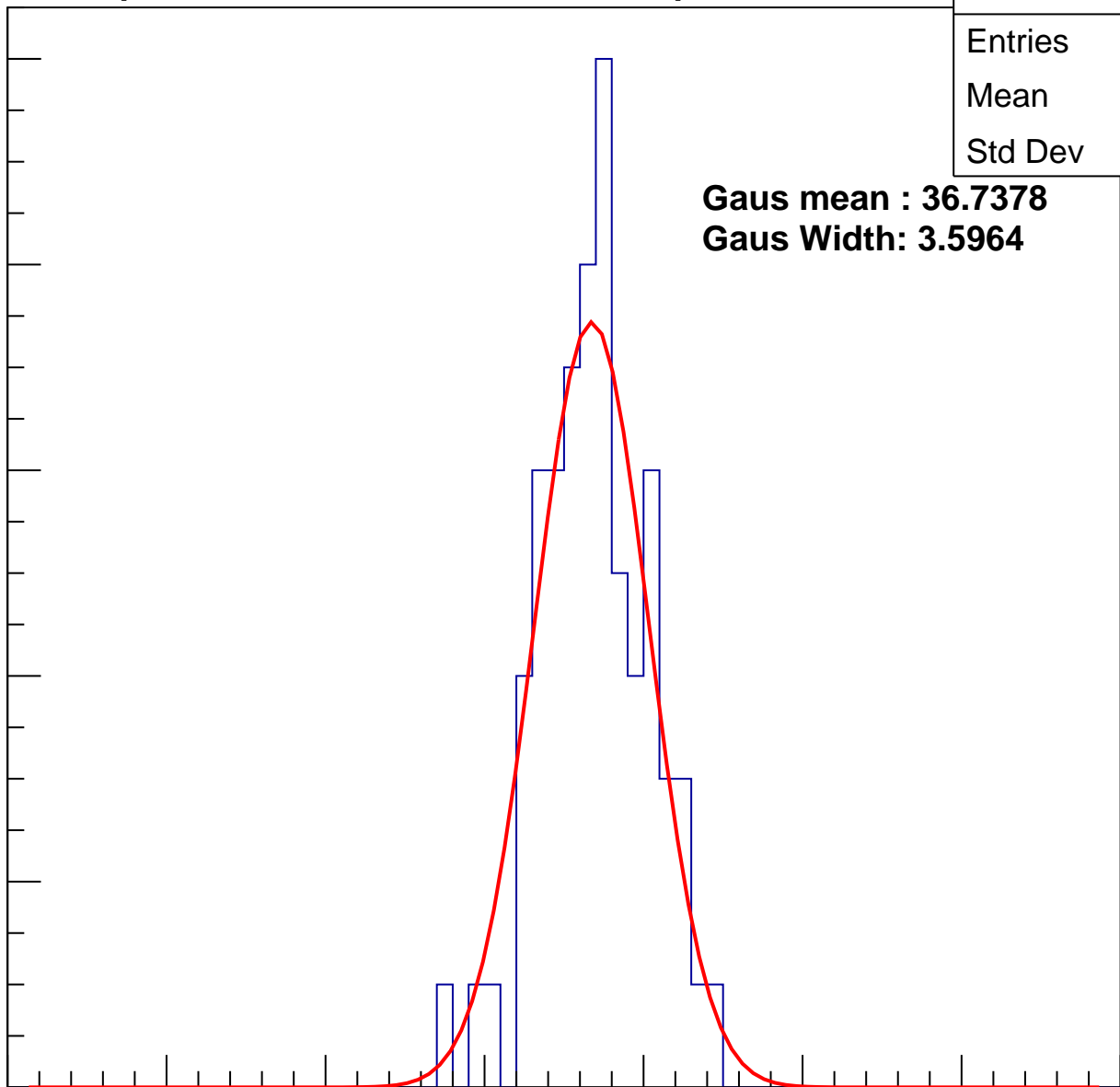
Gaus Width: 3.5964

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch19, adc2

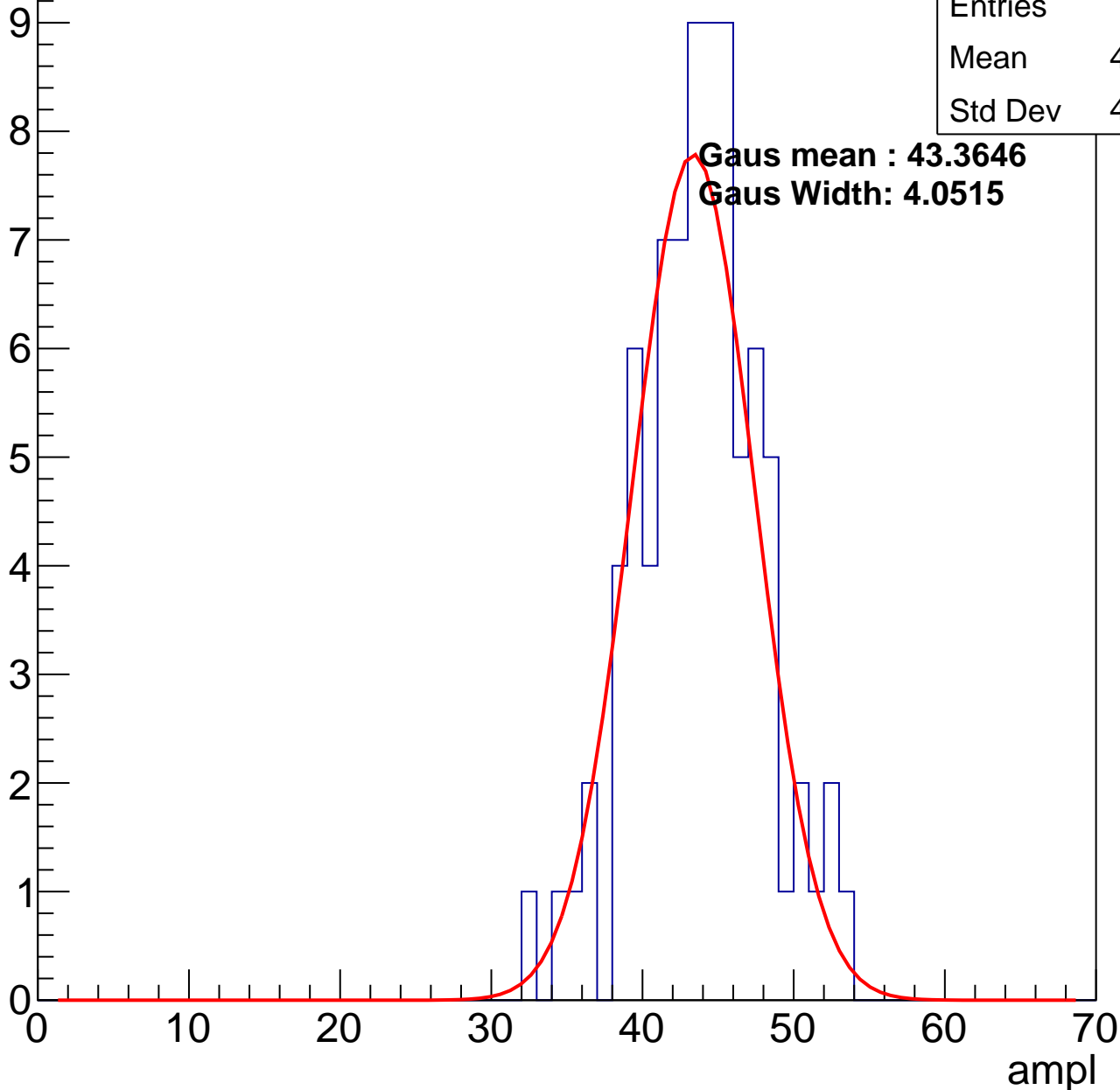
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	43.34
Std Dev	4.096

Gaus mean : 43.3646

Gaus Width: 4.0515

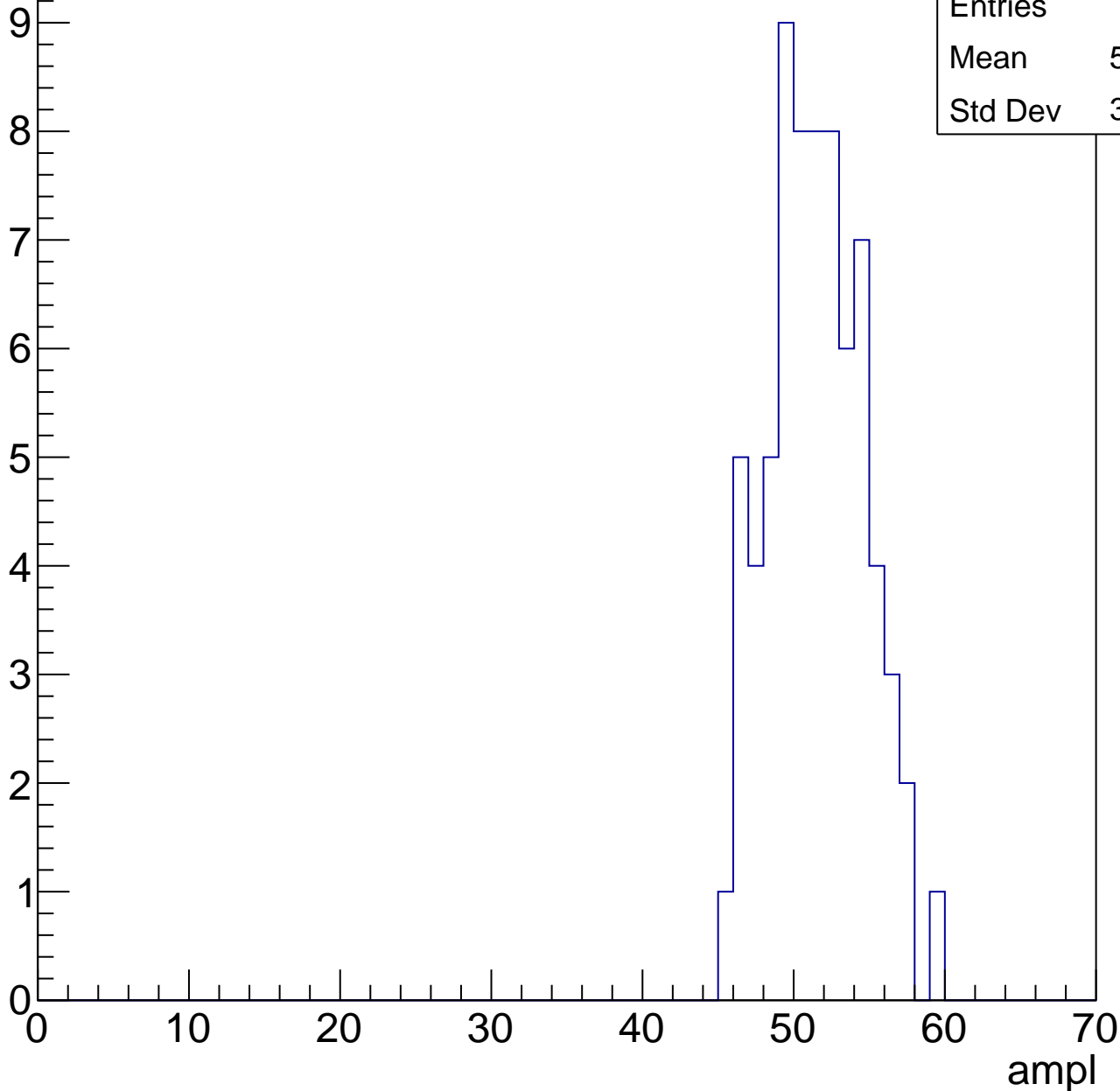


B1L101S, U6-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.06
Std Dev	3.103

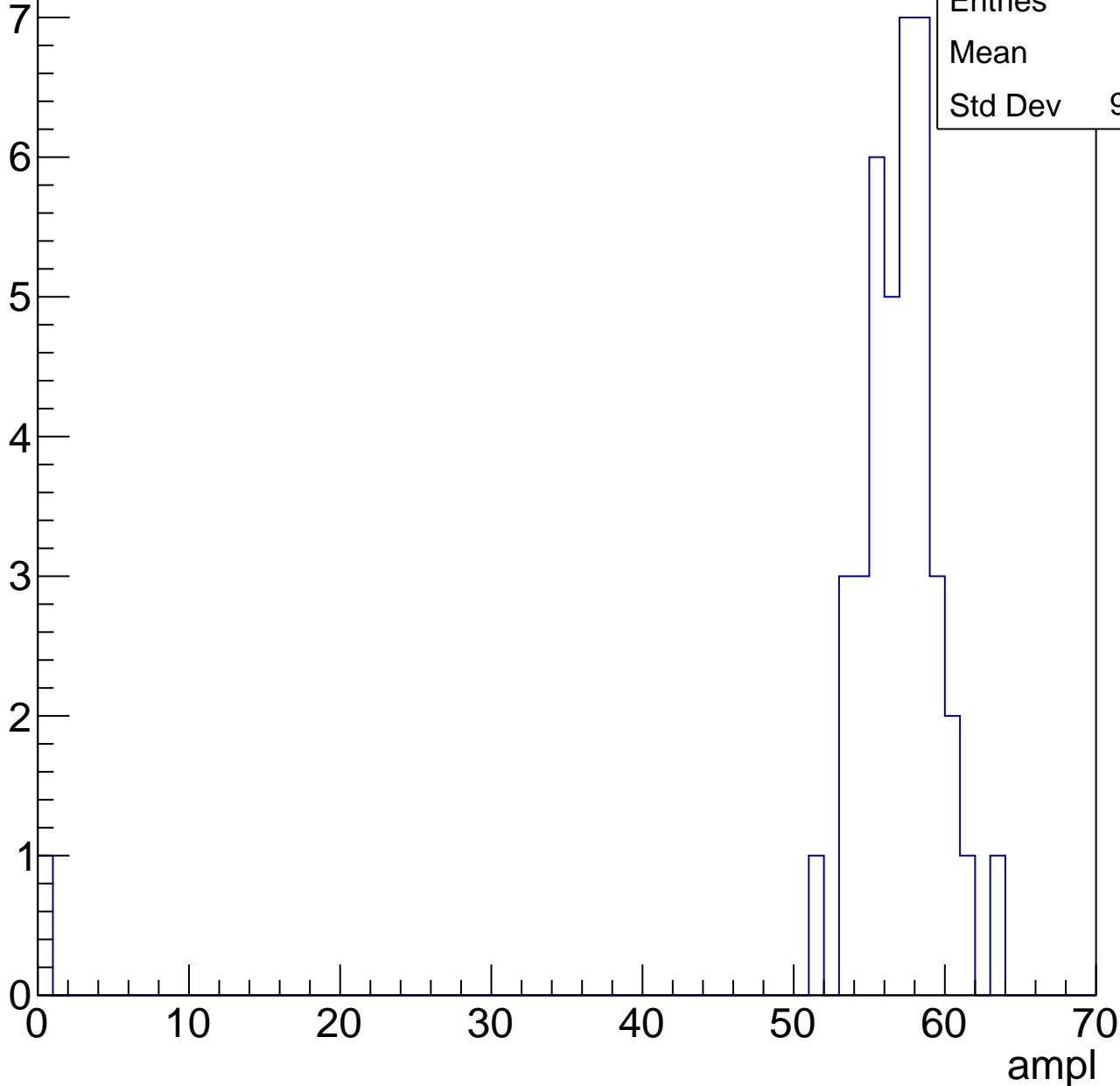


B1L101S, U6-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

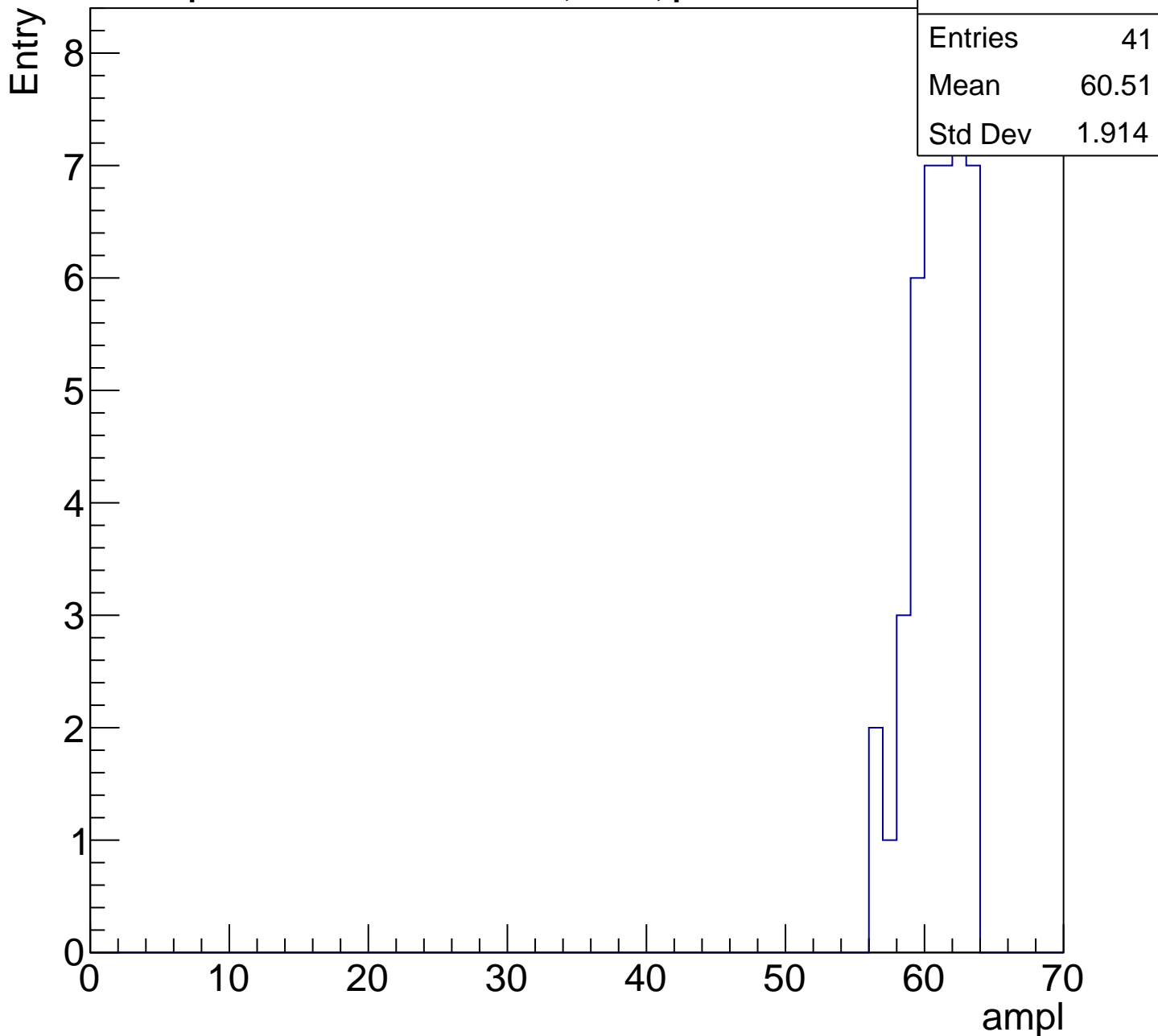
Entry

Entries	40
Mean	55.2
Std Dev	9.149



B1L101S, U6-ch19, adc5

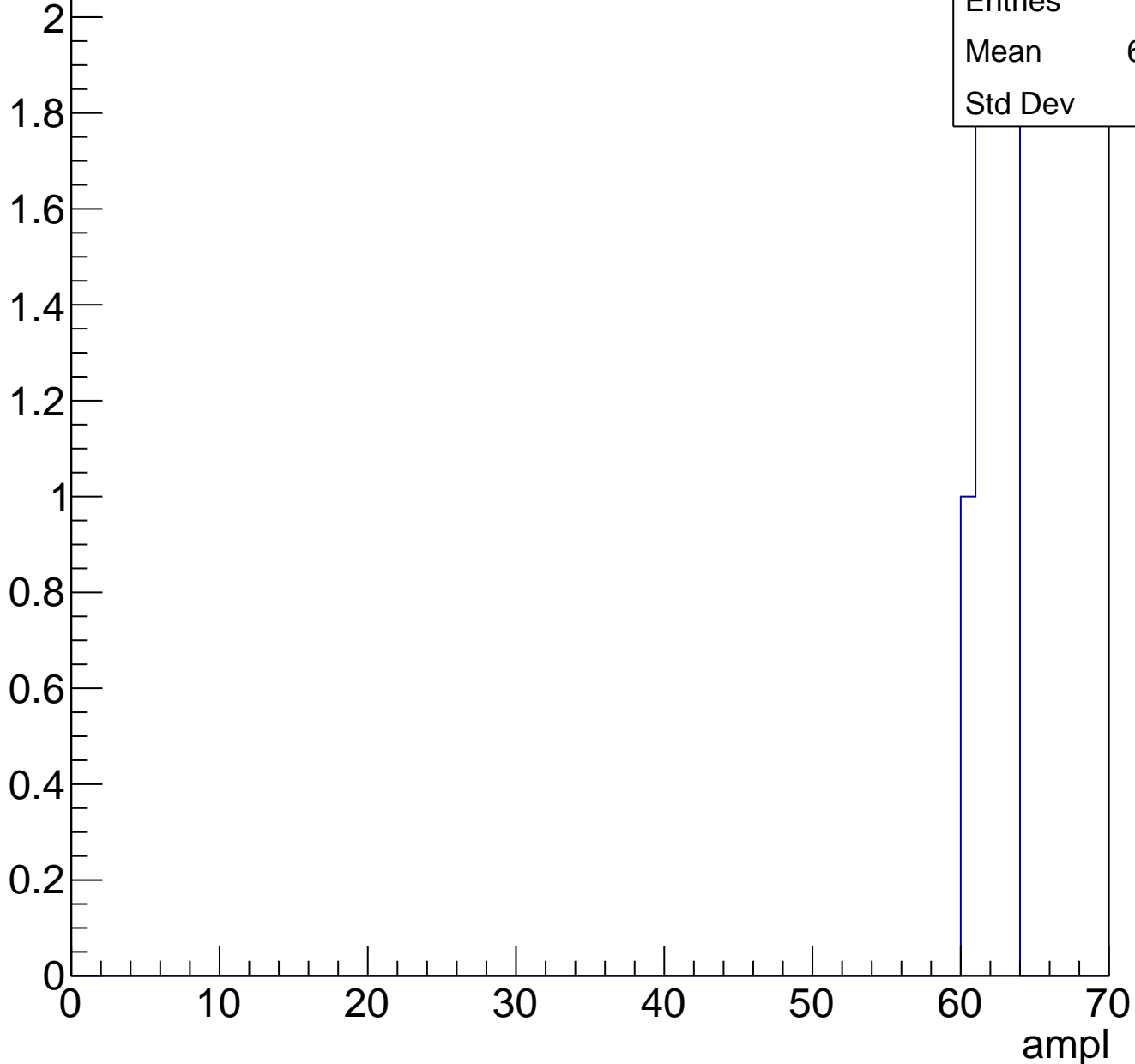
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L101S, U6-ch20, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	31.38
Std Dev	3.036

Gaus mean : 31.6598

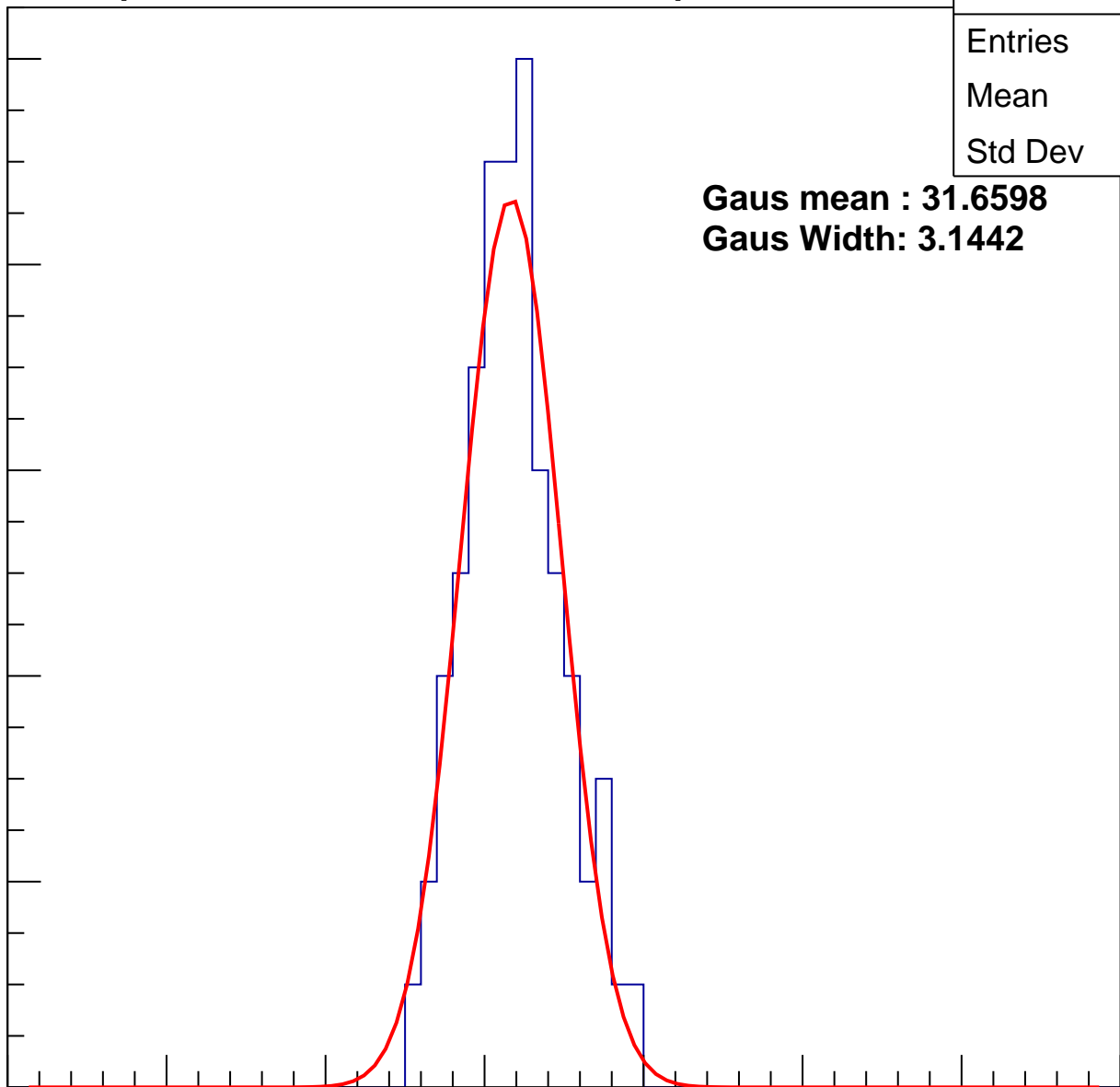
Gaus Width: 3.1442

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch20, adc1

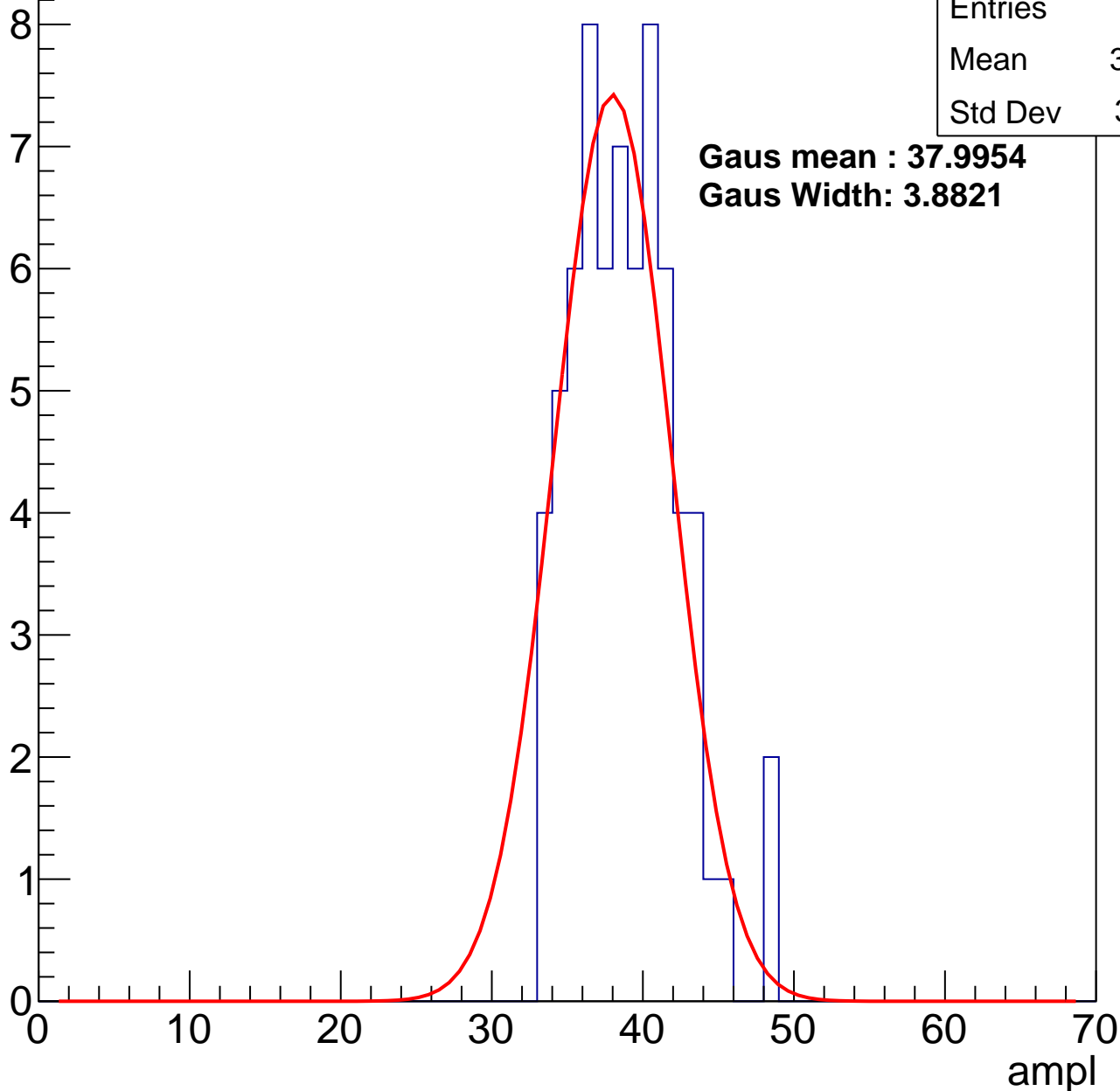
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	38.43
Std Dev	3.431

Gaus mean : 37.9954

Gaus Width: 3.8821



B1L101S, U6-ch20, adc2

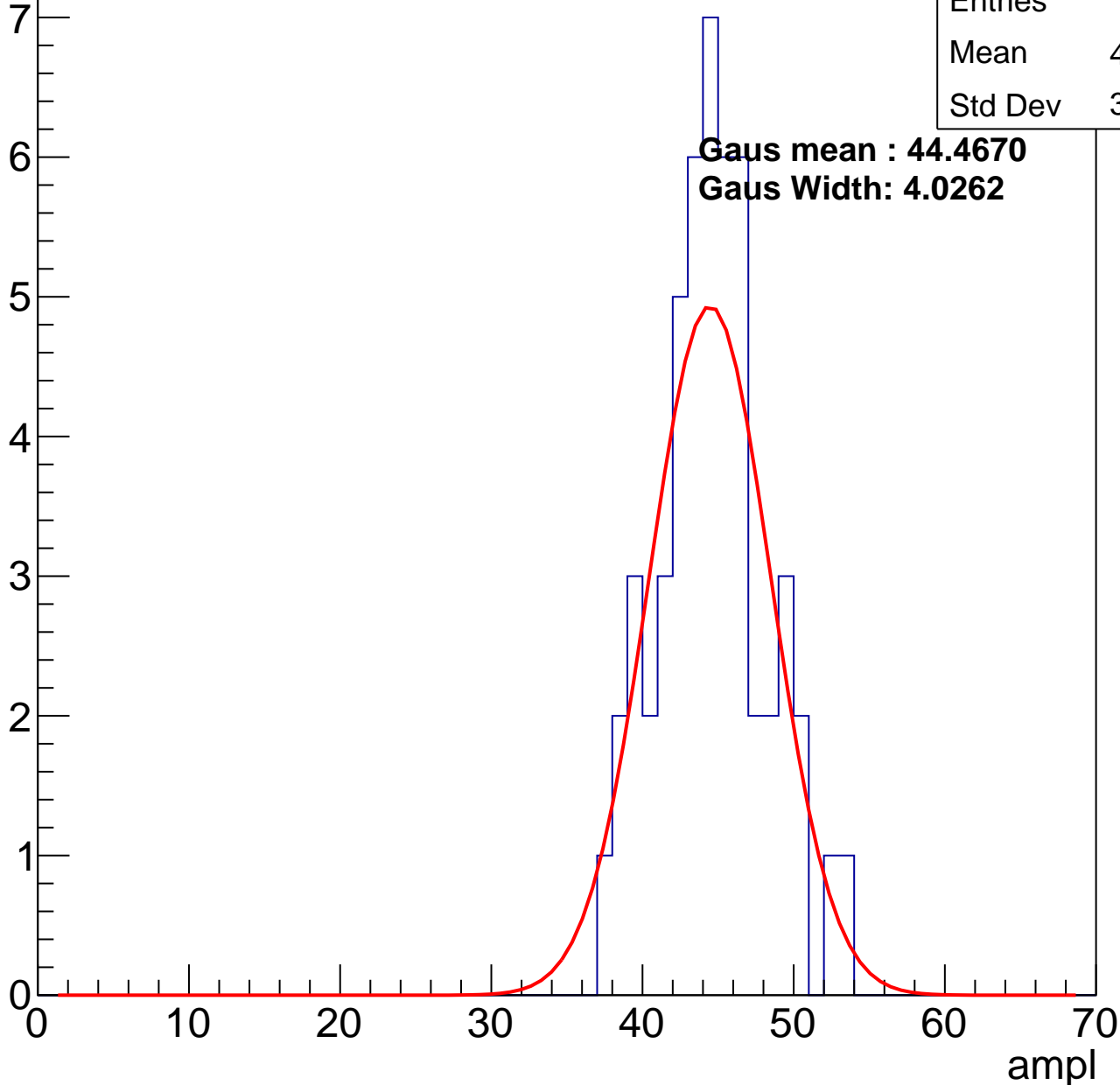
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.17
Std Dev	3.539

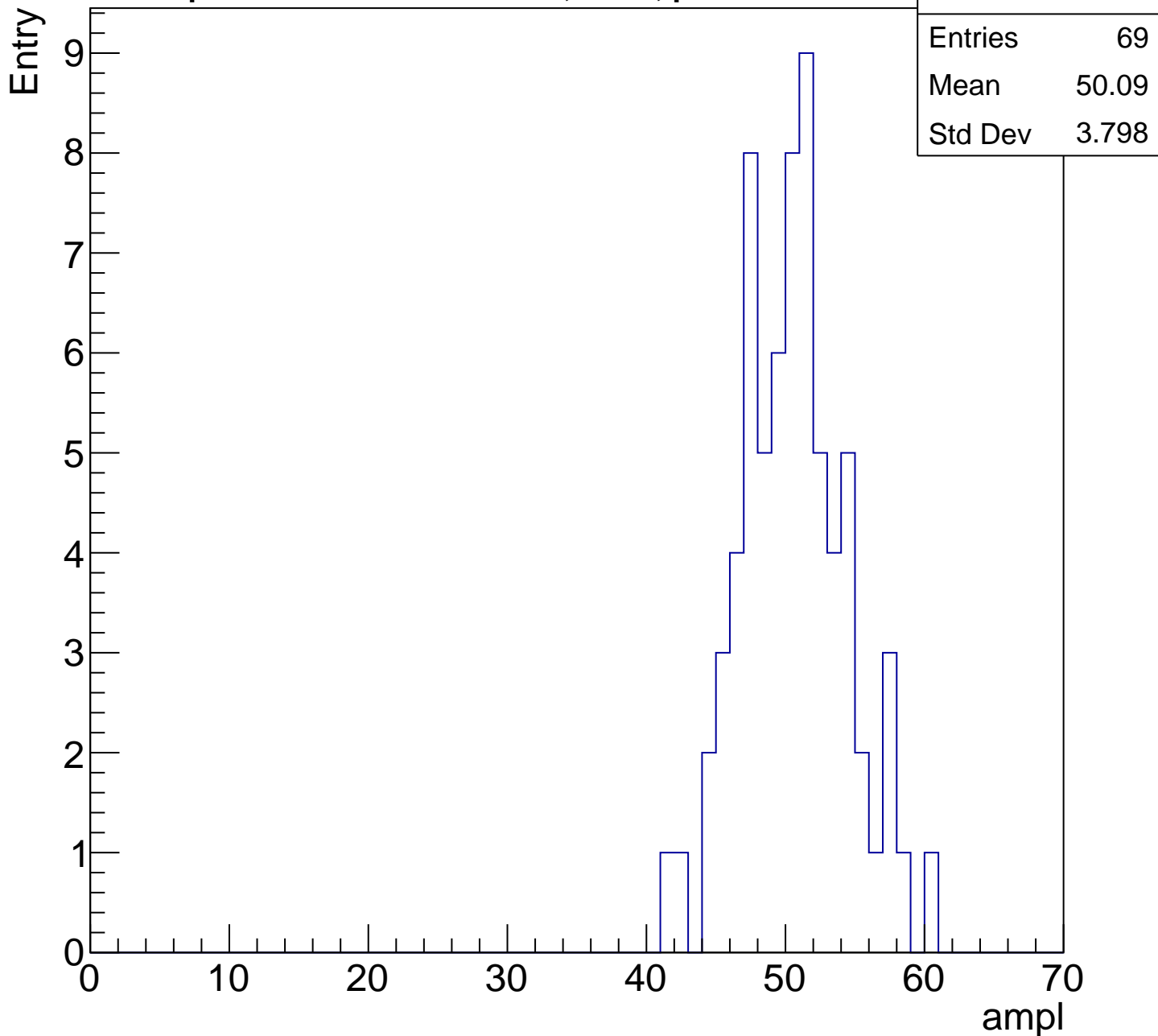
Gaus mean : 44.4670

Gaus Width: 4.0262



B1L101S, U6-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

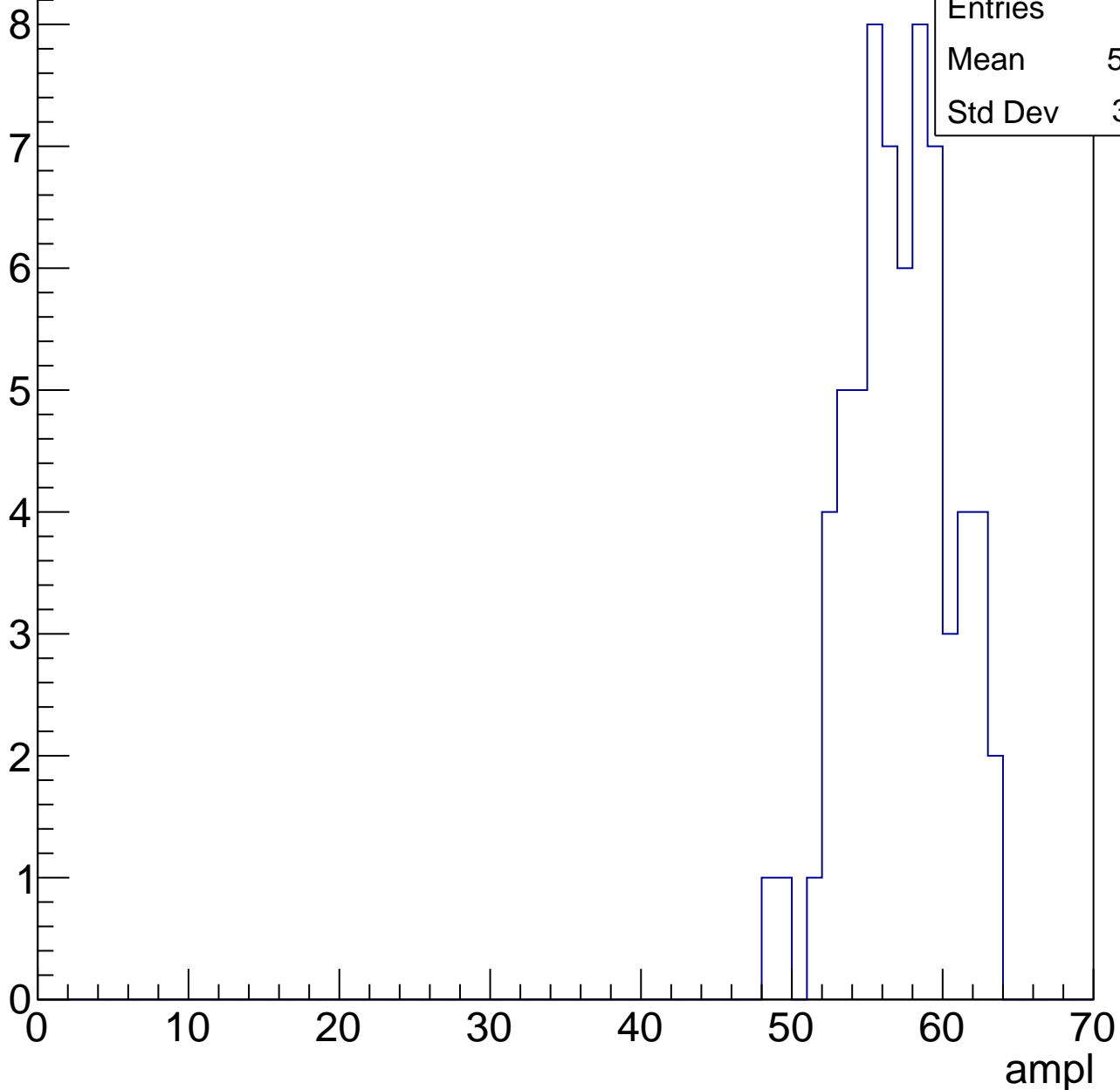


B1L101S, U6-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	56.67
Std Dev	3.341

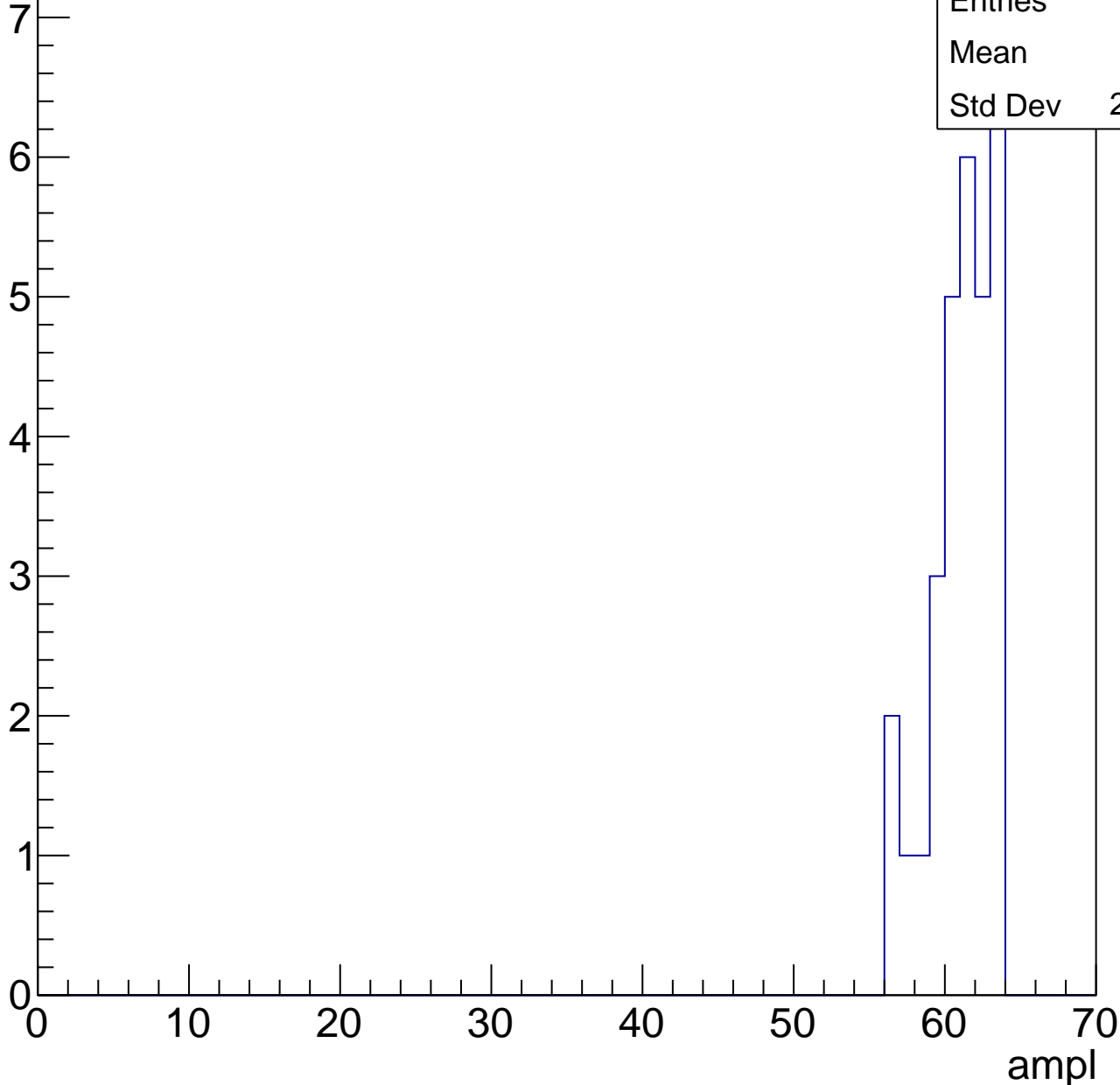


B1L101S, U6-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

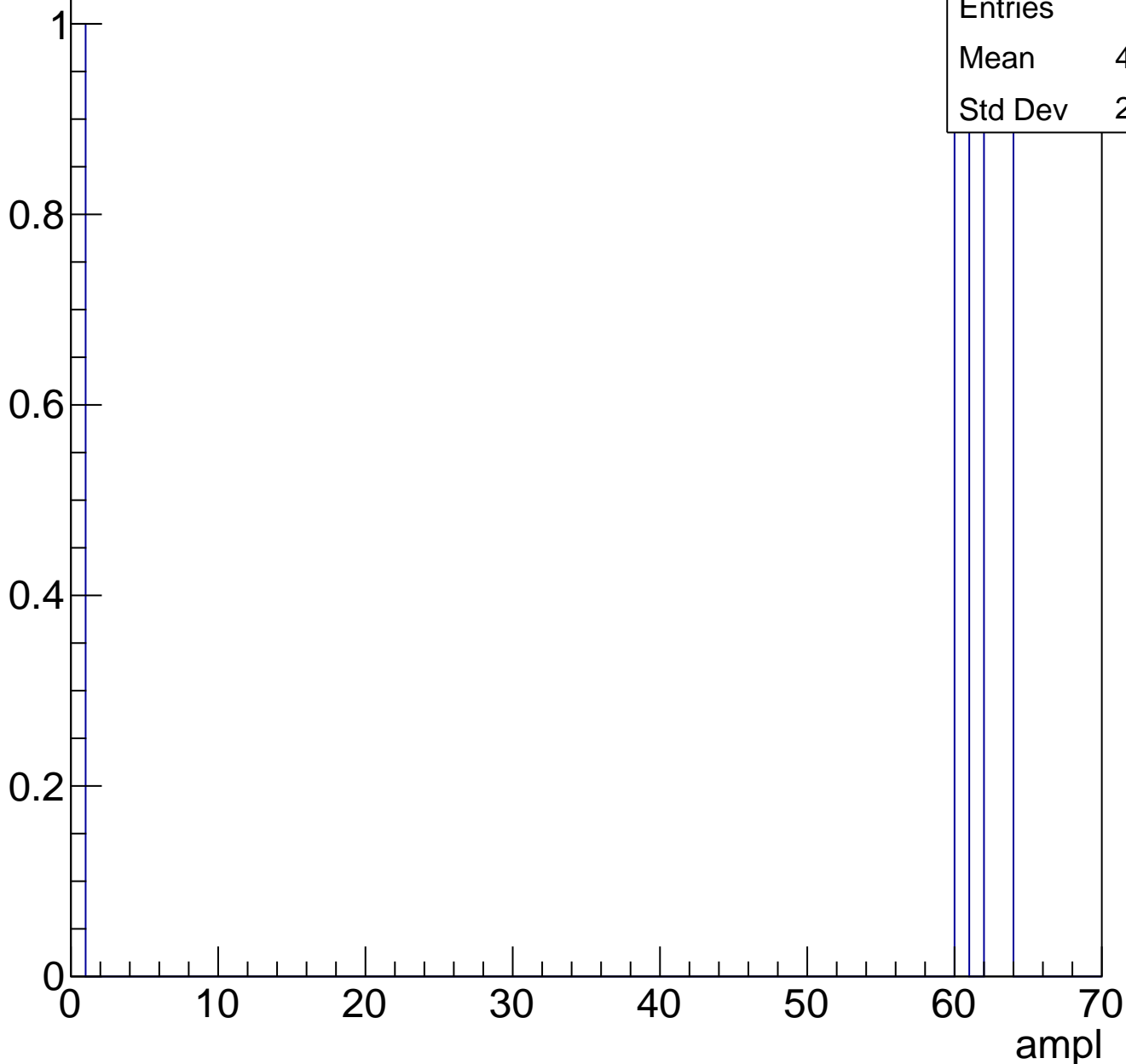
Entries	30
Mean	60.7
Std Dev	2.019



B1L101S, U6-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch21, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	30.41
Std Dev	3.701

Gaus mean : 30.8600

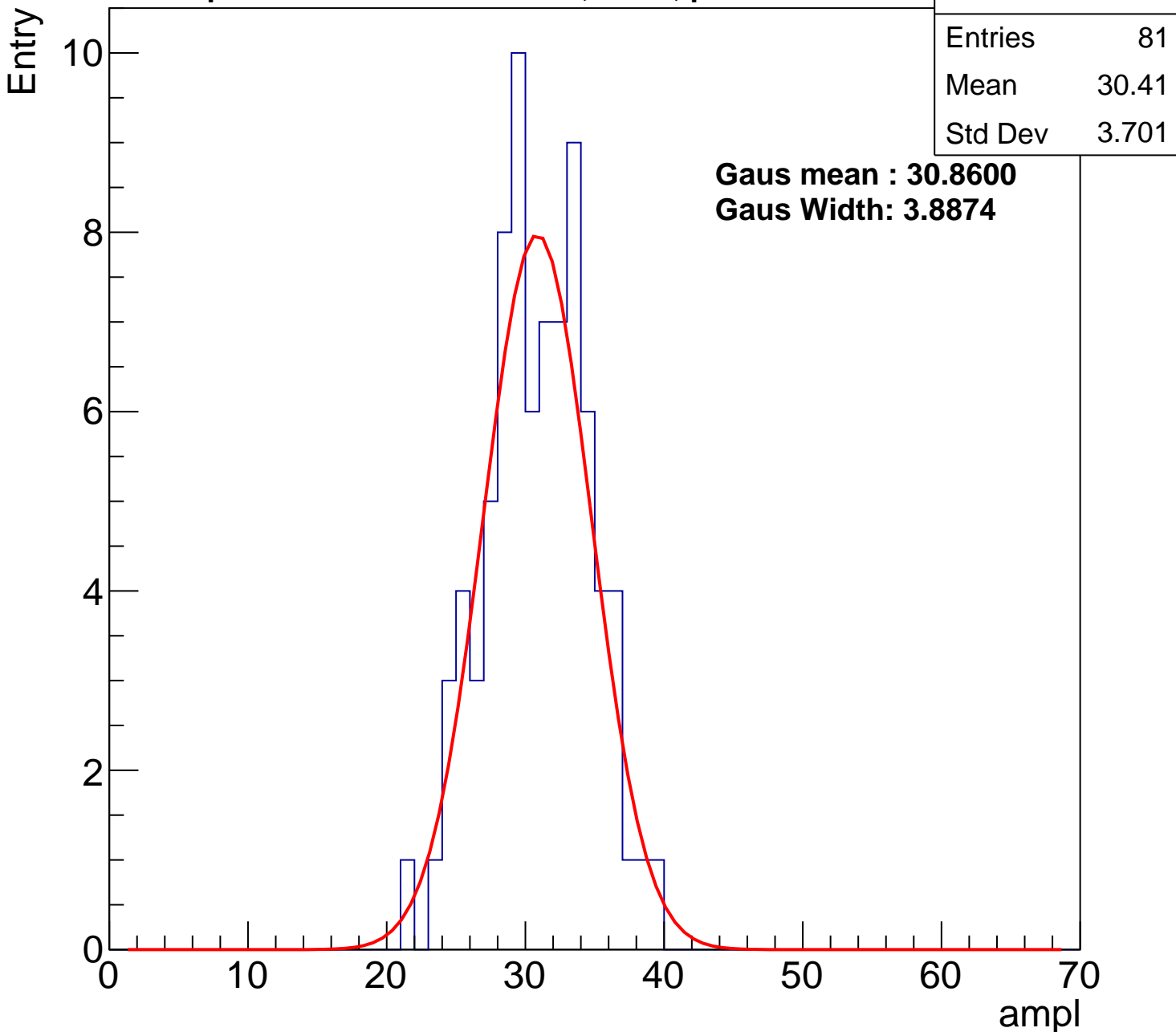
Gaus Width: 3.8874

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch21, adc1

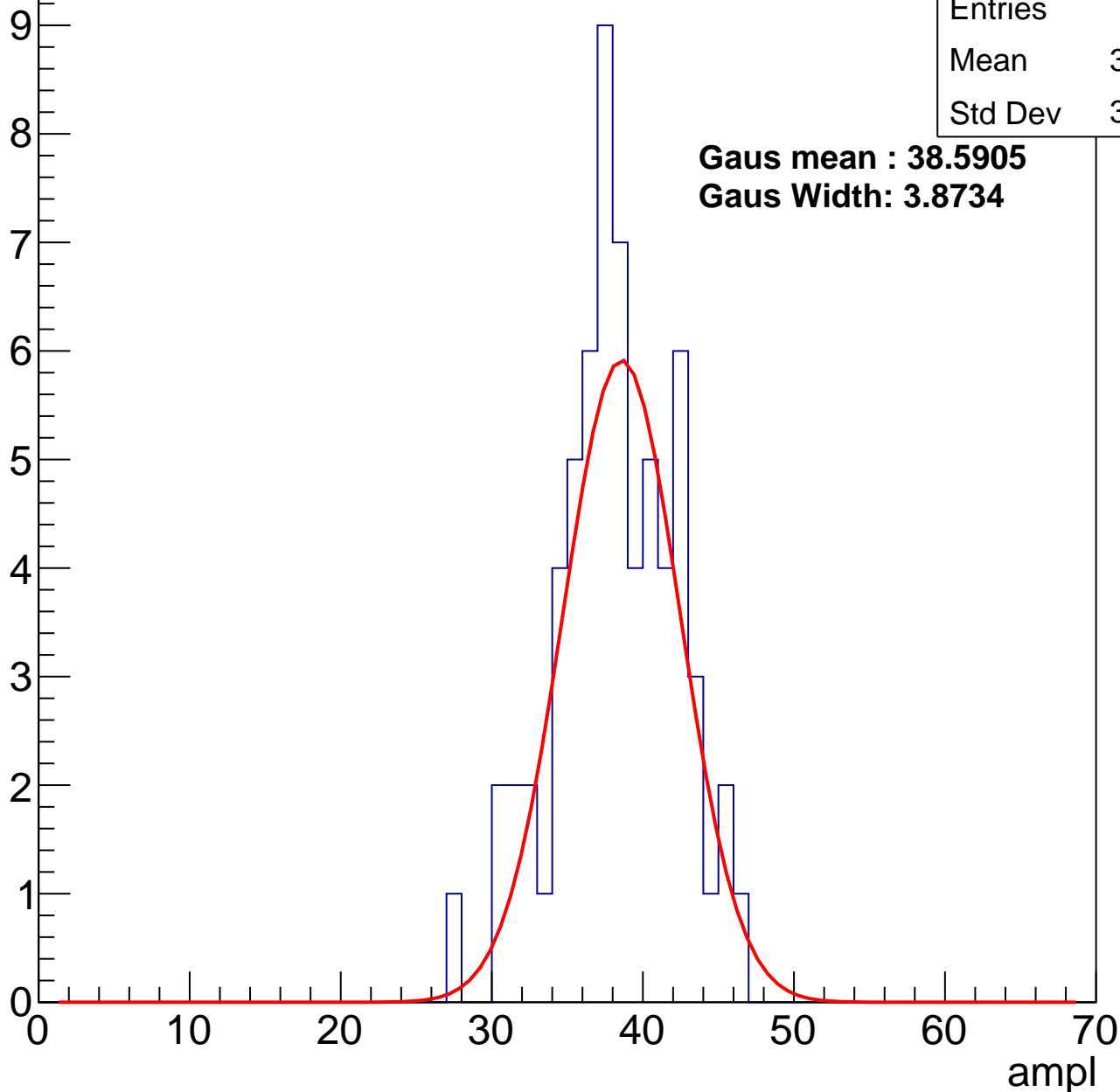
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.74
Std Dev	3.943

Gaus mean : 38.5905

Gaus Width: 3.8734



B1L101S, U6-ch21, adc2

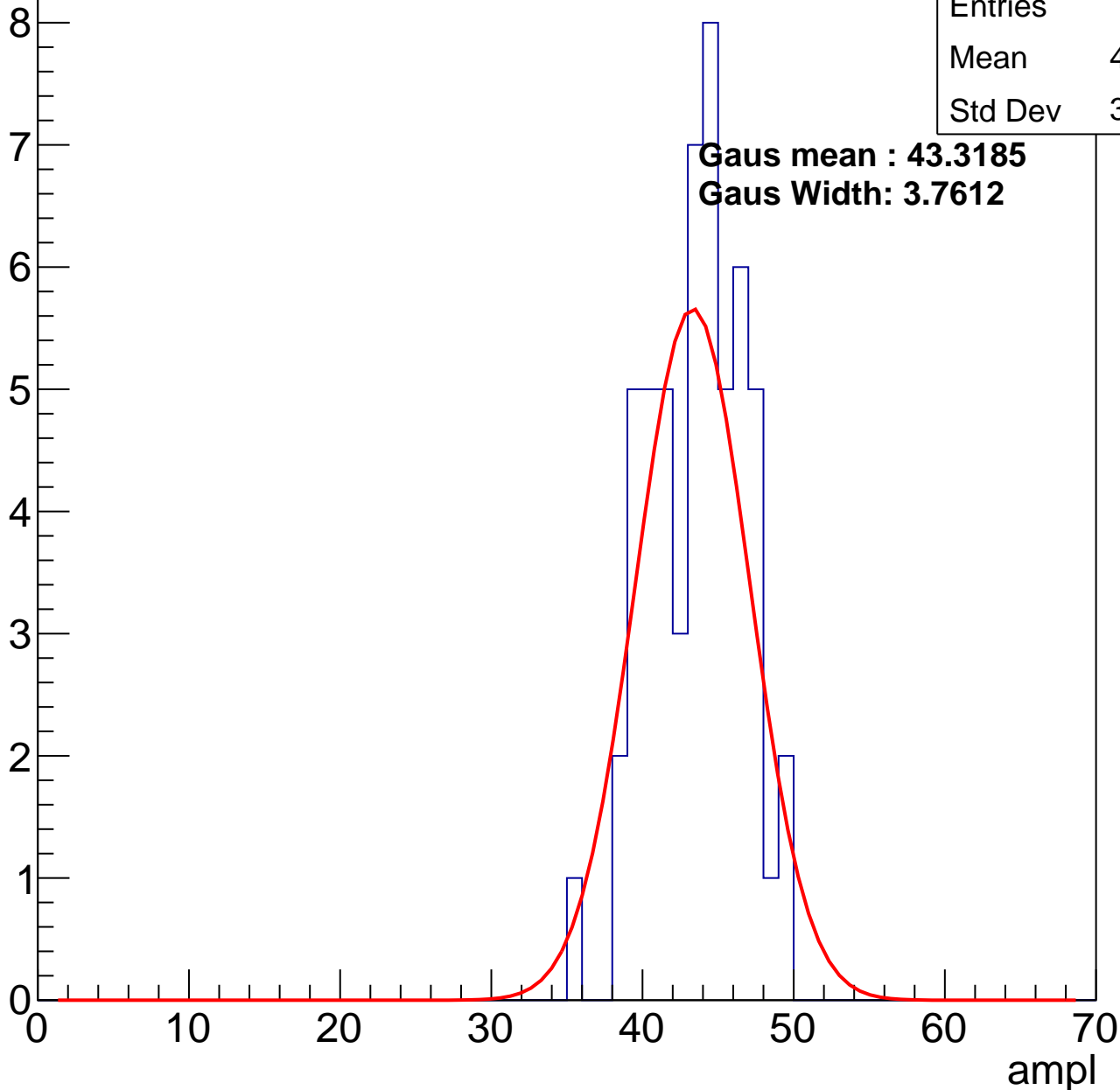
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.13
Std Dev	3.075

Gaus mean : 43.3185

Gaus Width: 3.7612

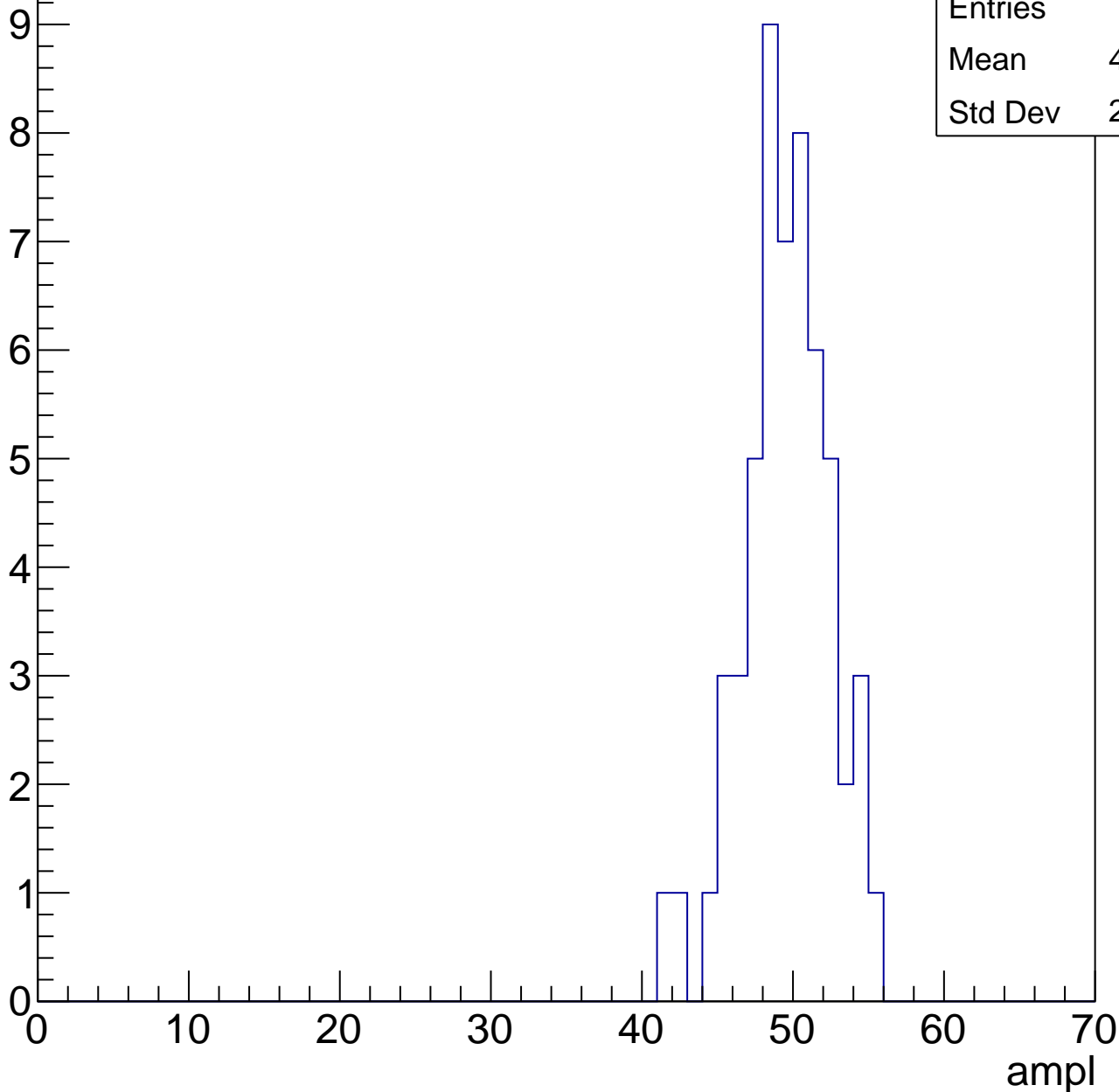


B1L101S, U6-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

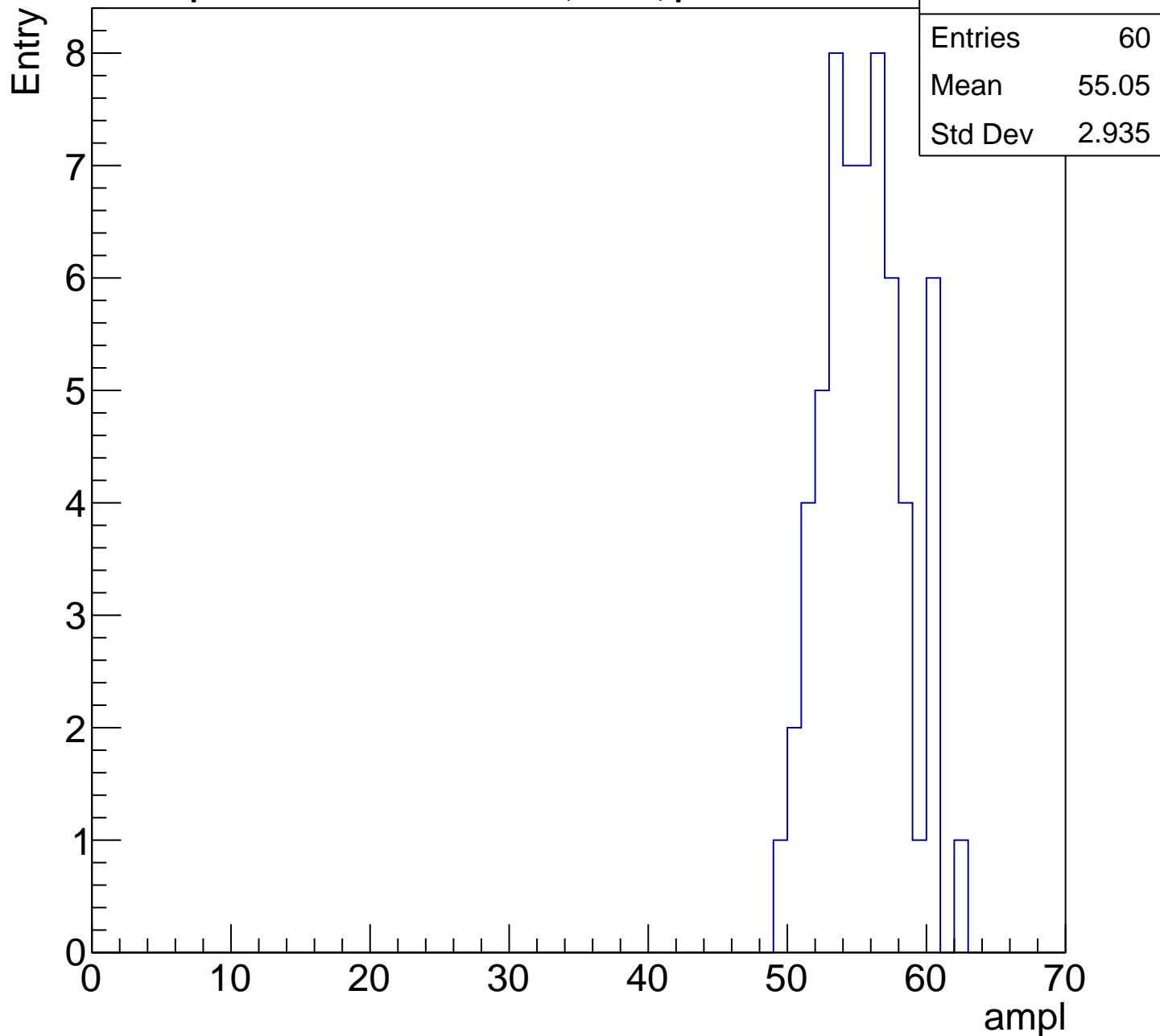
Entry

Entries	55
Mean	49.07
Std Dev	2.897



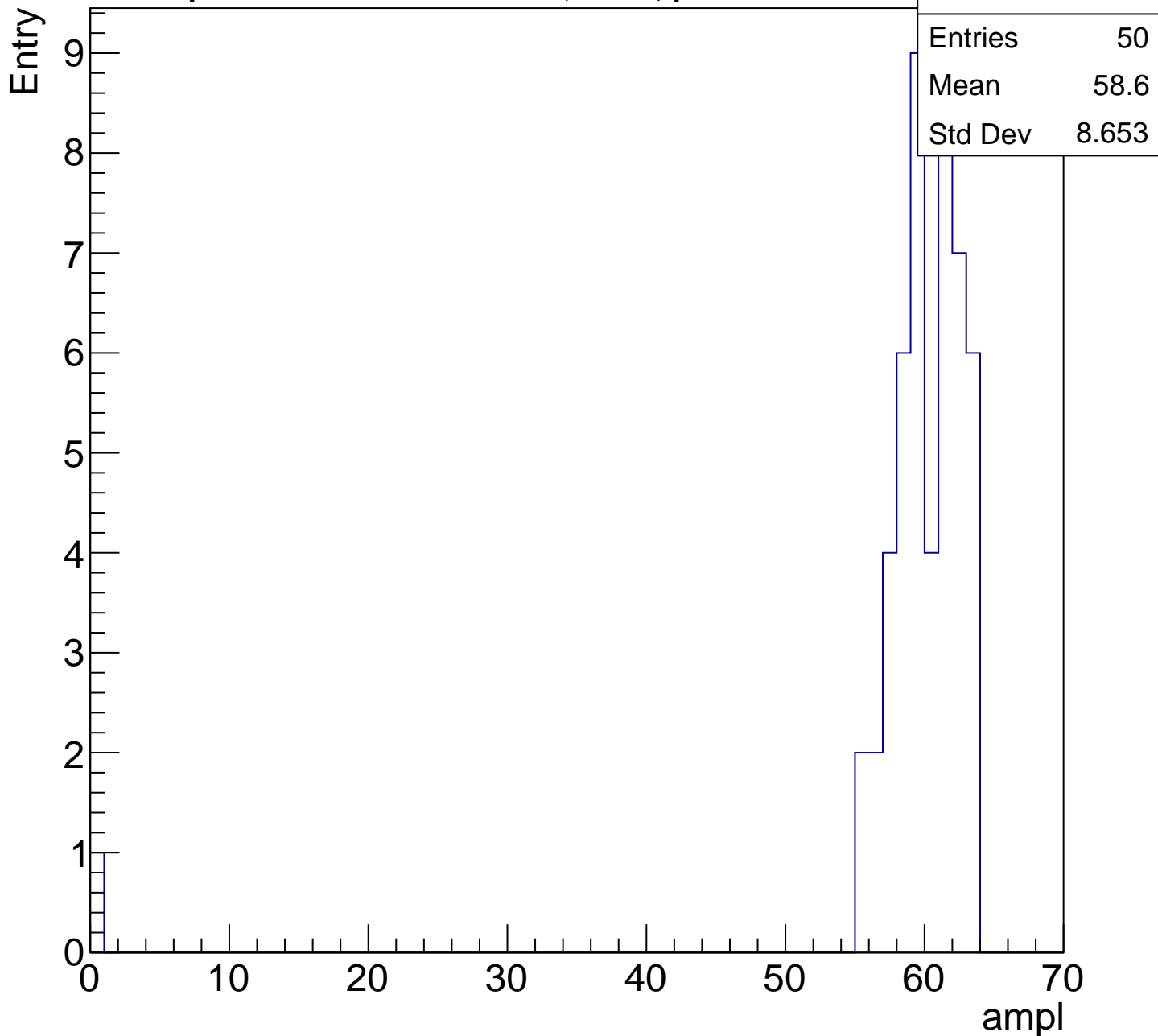
B1L101S, U6-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch21, adc5

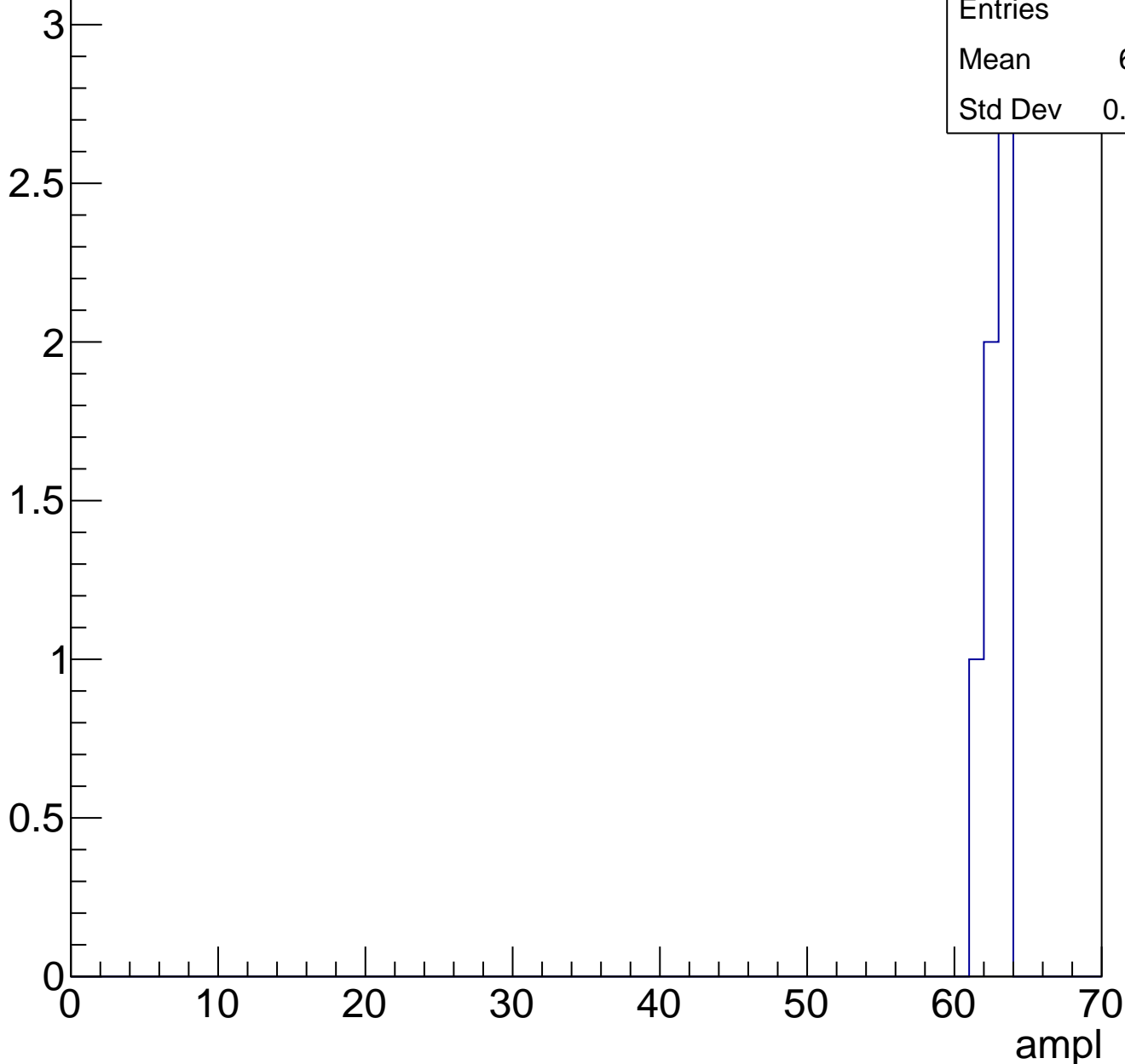
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

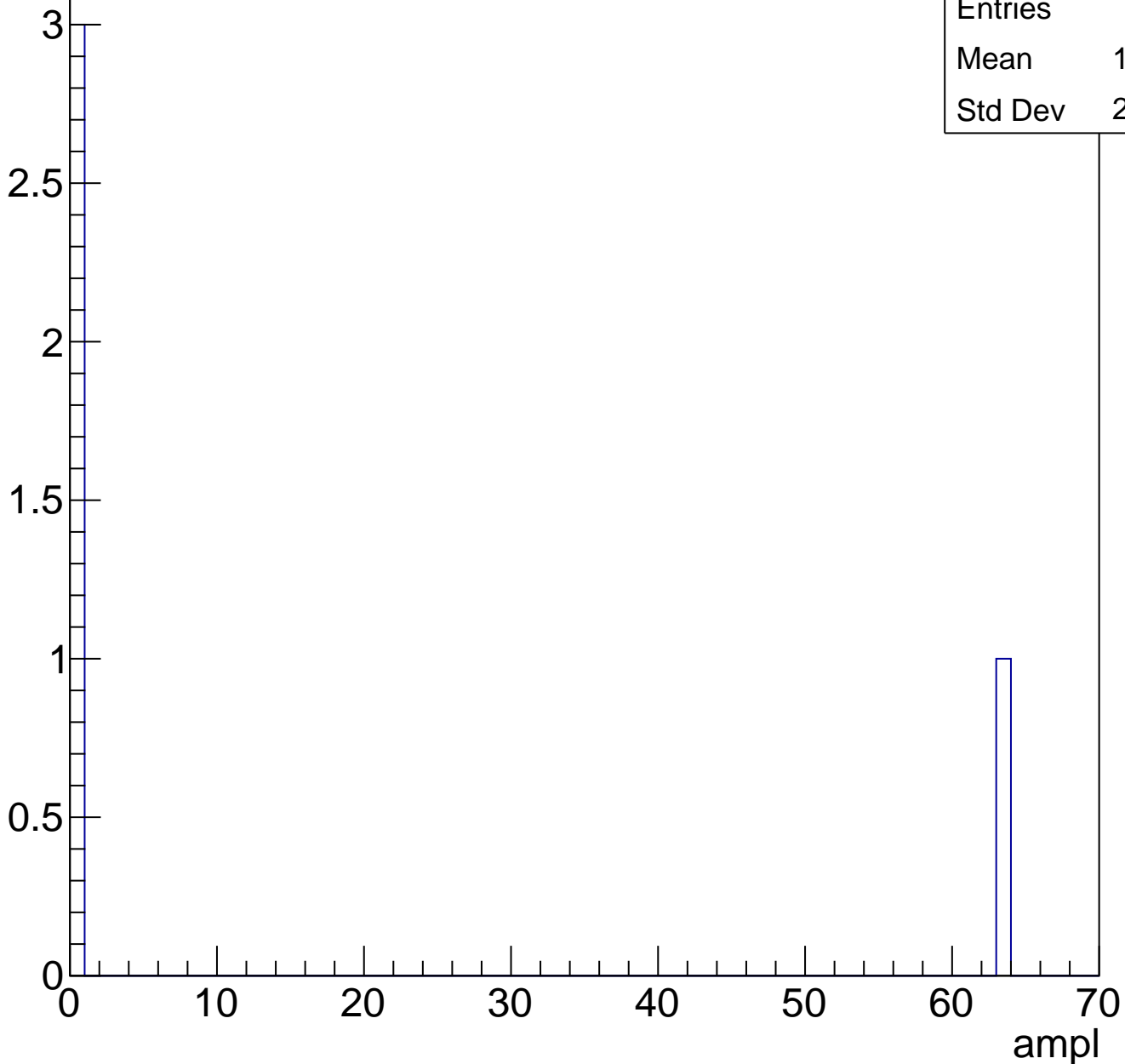
Entry



B1L101S, U6-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch22, adc0

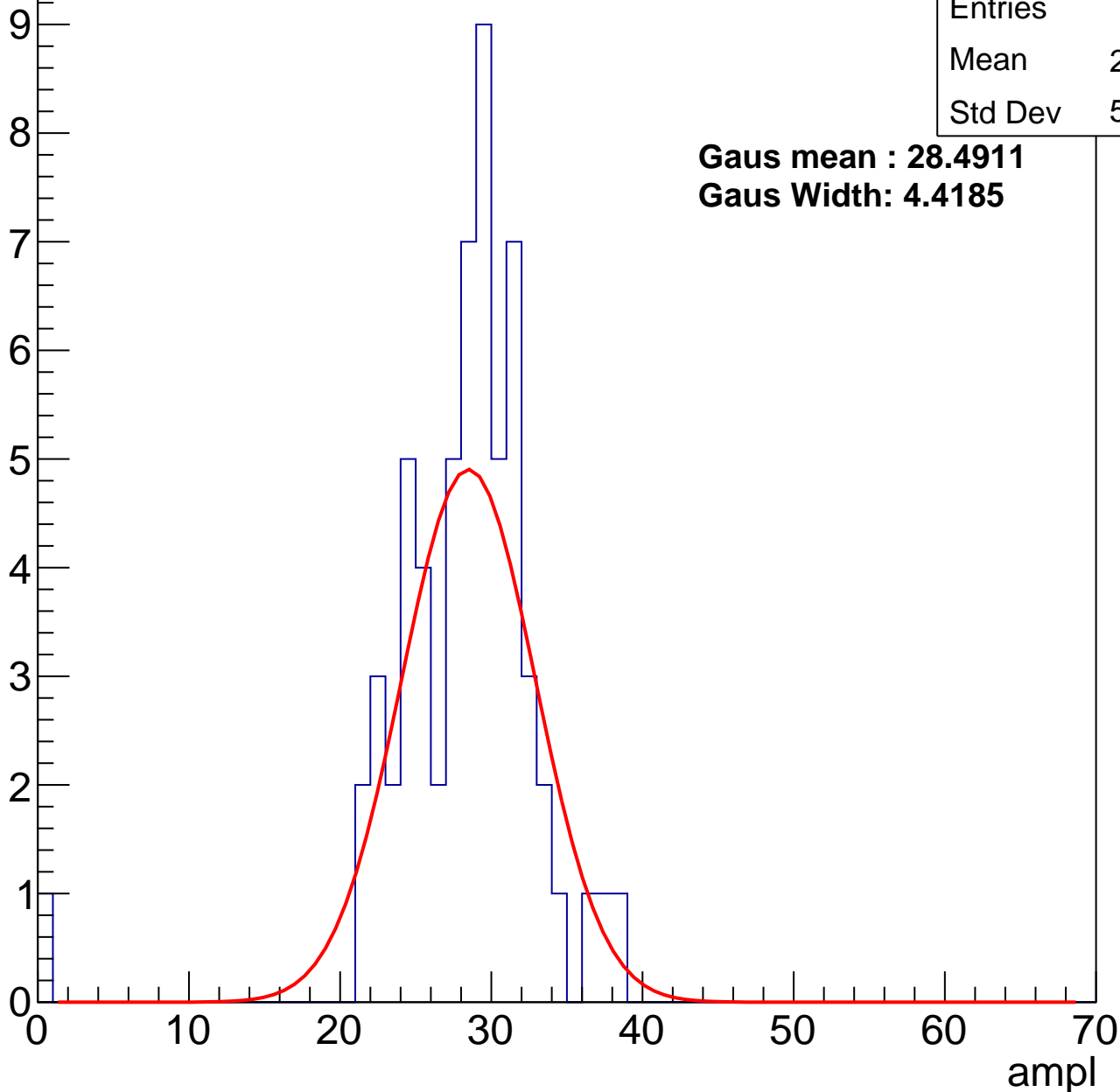
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	27.74
Std Dev	5.178

Gaus mean : 28.4911

Gaus Width: 4.4185



B1L101S, U6-ch22, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	36.09
Std Dev	3.379

Gaus mean : 36.3732

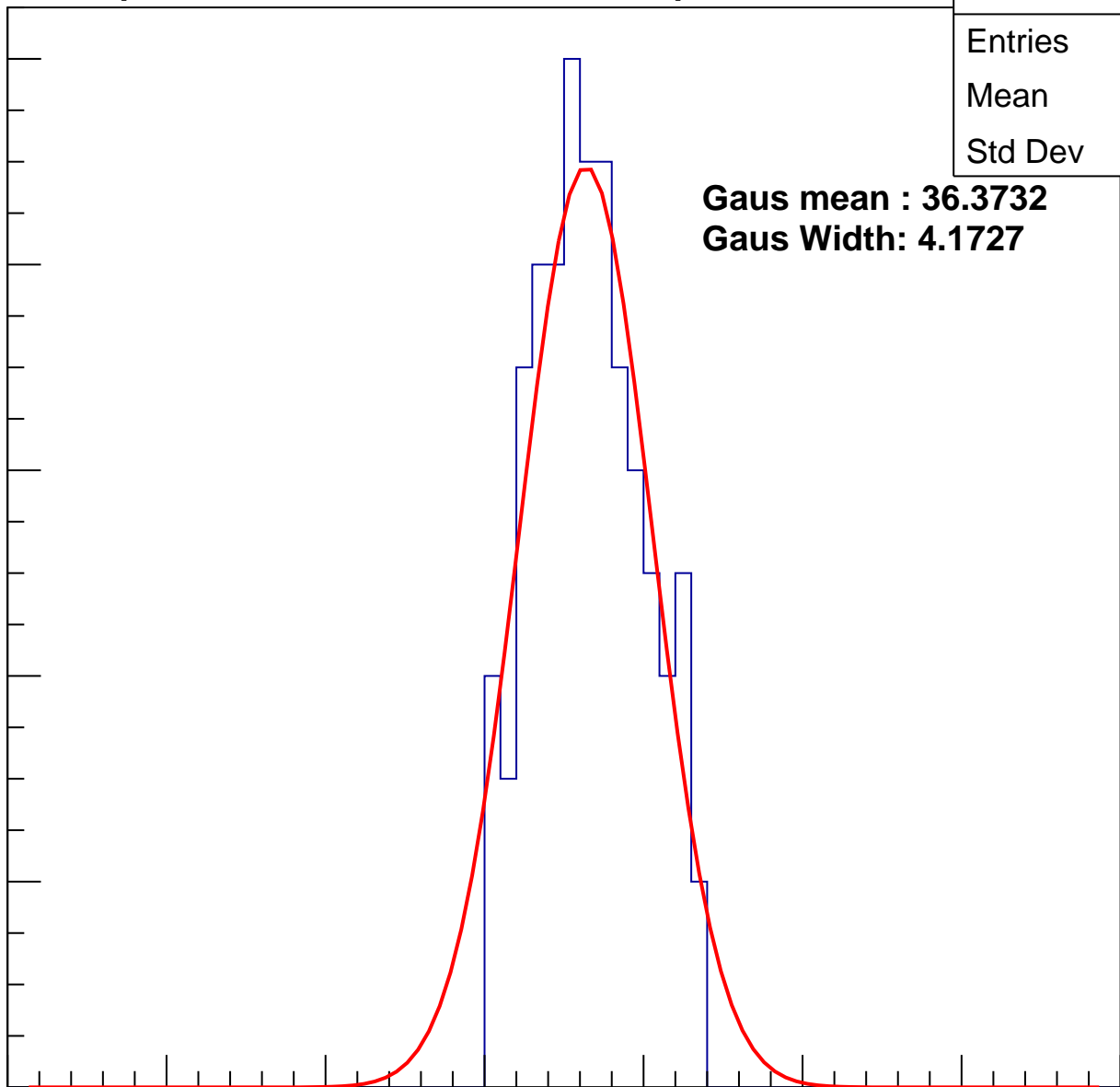
Gaus Width: 4.1727

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch22, adc2

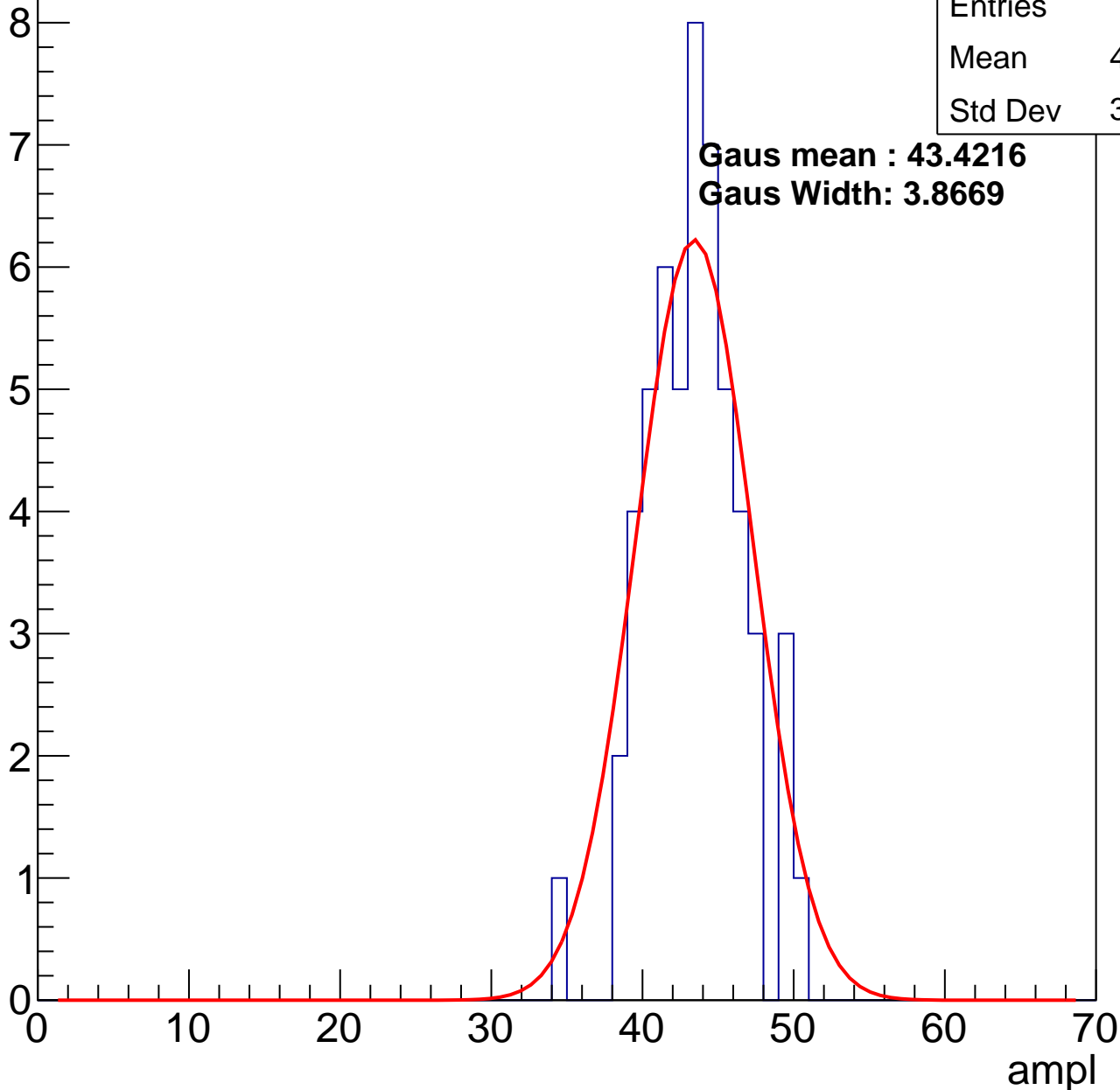
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	42.98
Std Dev	3.153

Gaus mean : 43.4216

Gaus Width: 3.8669



B1L101S, U6-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

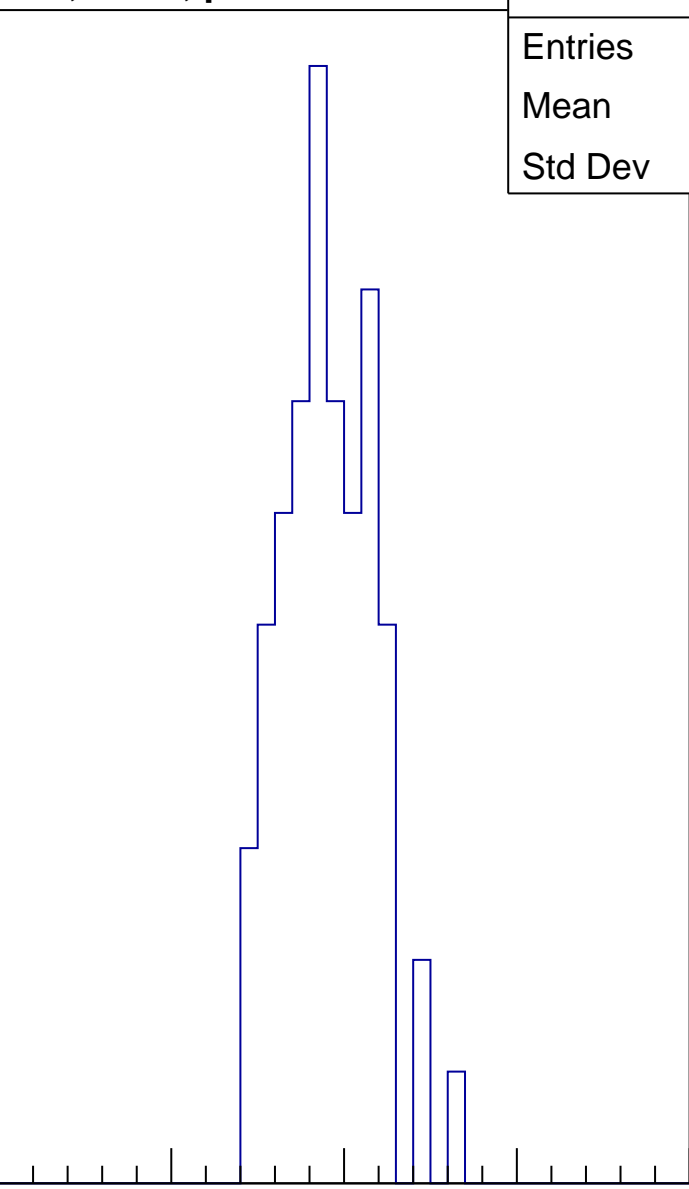
Entries	60
Mean	48.62
Std Dev	2.646

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

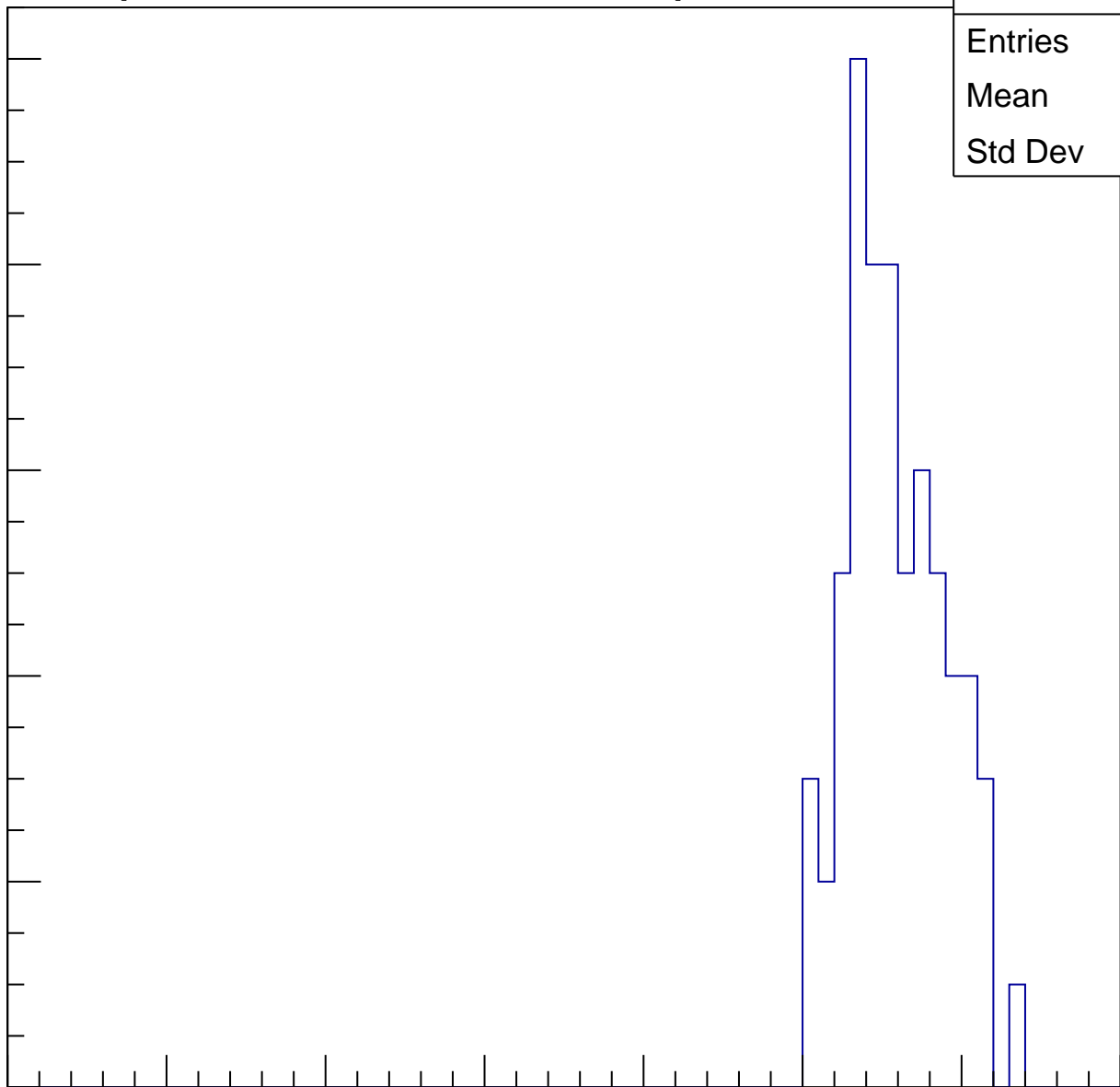
Entries	64
Mean	55.44
Std Dev	3.056

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

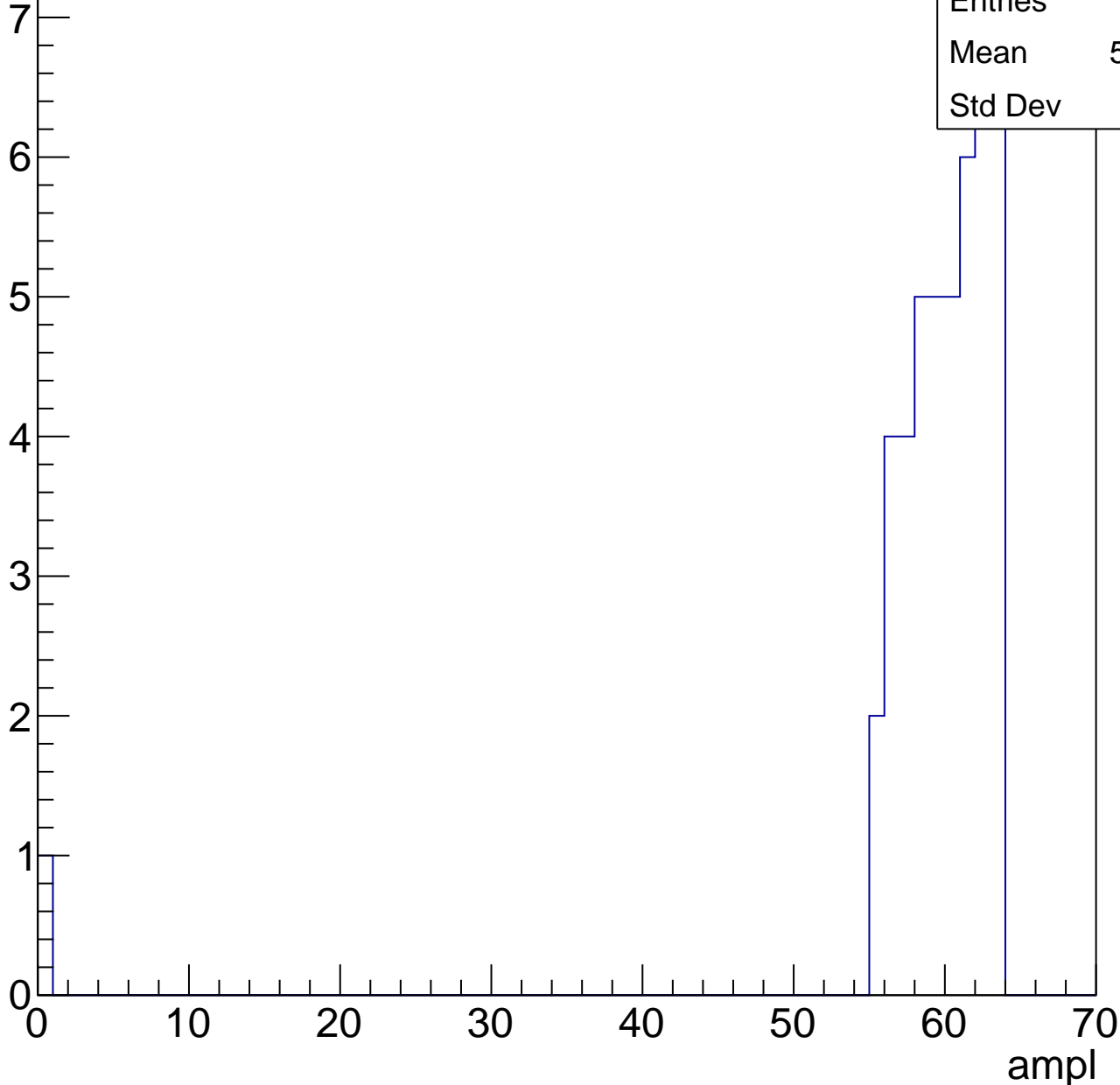


B1L101S, U6-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.43
Std Dev	9.04



B1L101S, U6-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

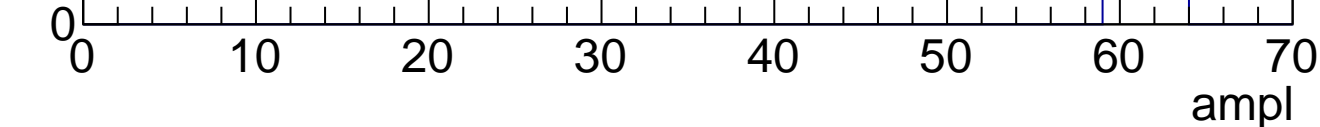
8

Mean

61.5

Std Dev

1.323



B1L101S, U6-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch23, adc0

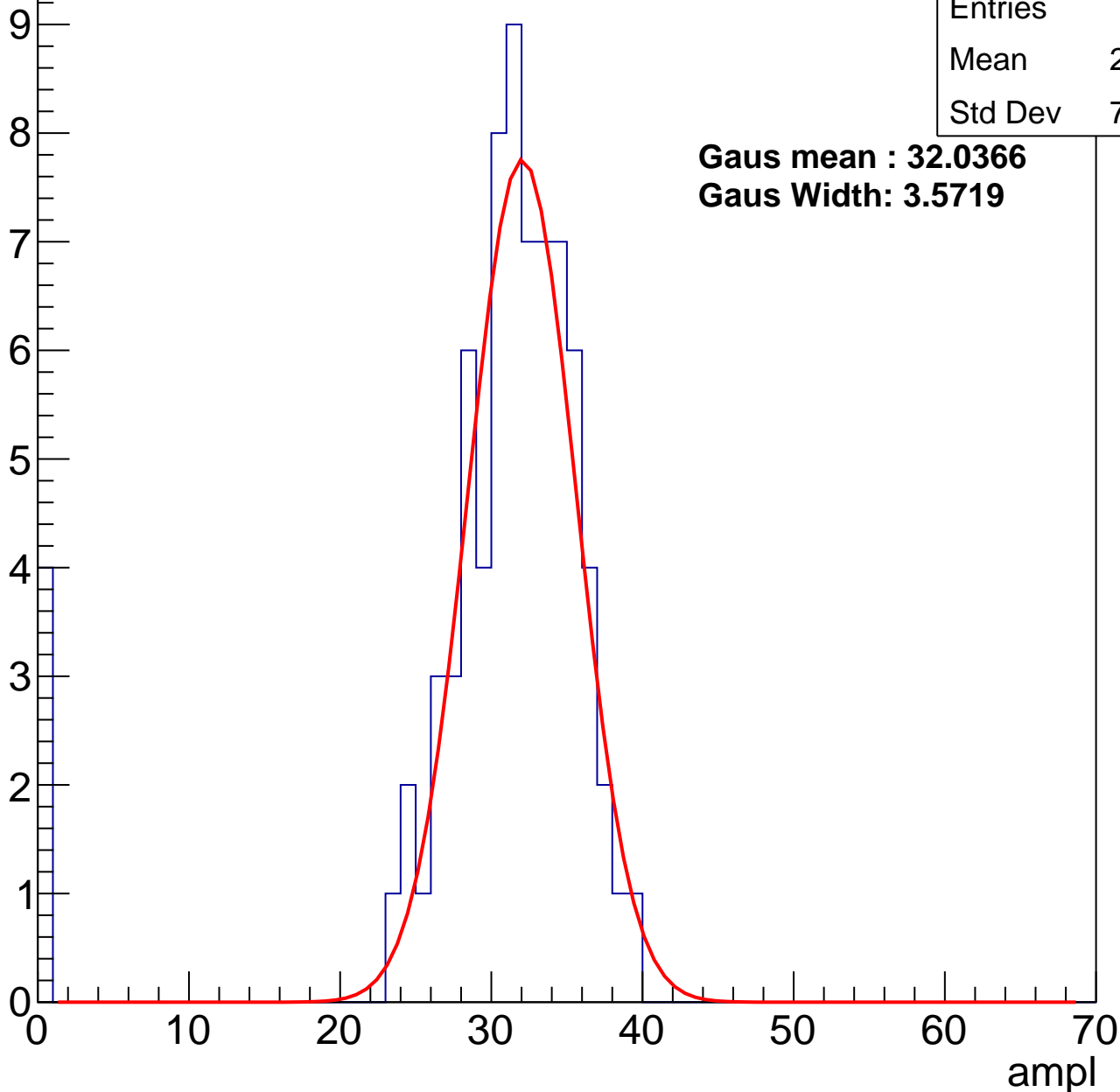
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.68
Std Dev	7.772

Gaus mean : 32.0366

Gaus Width: 3.5719



B1L101S, U6-ch23, adc1

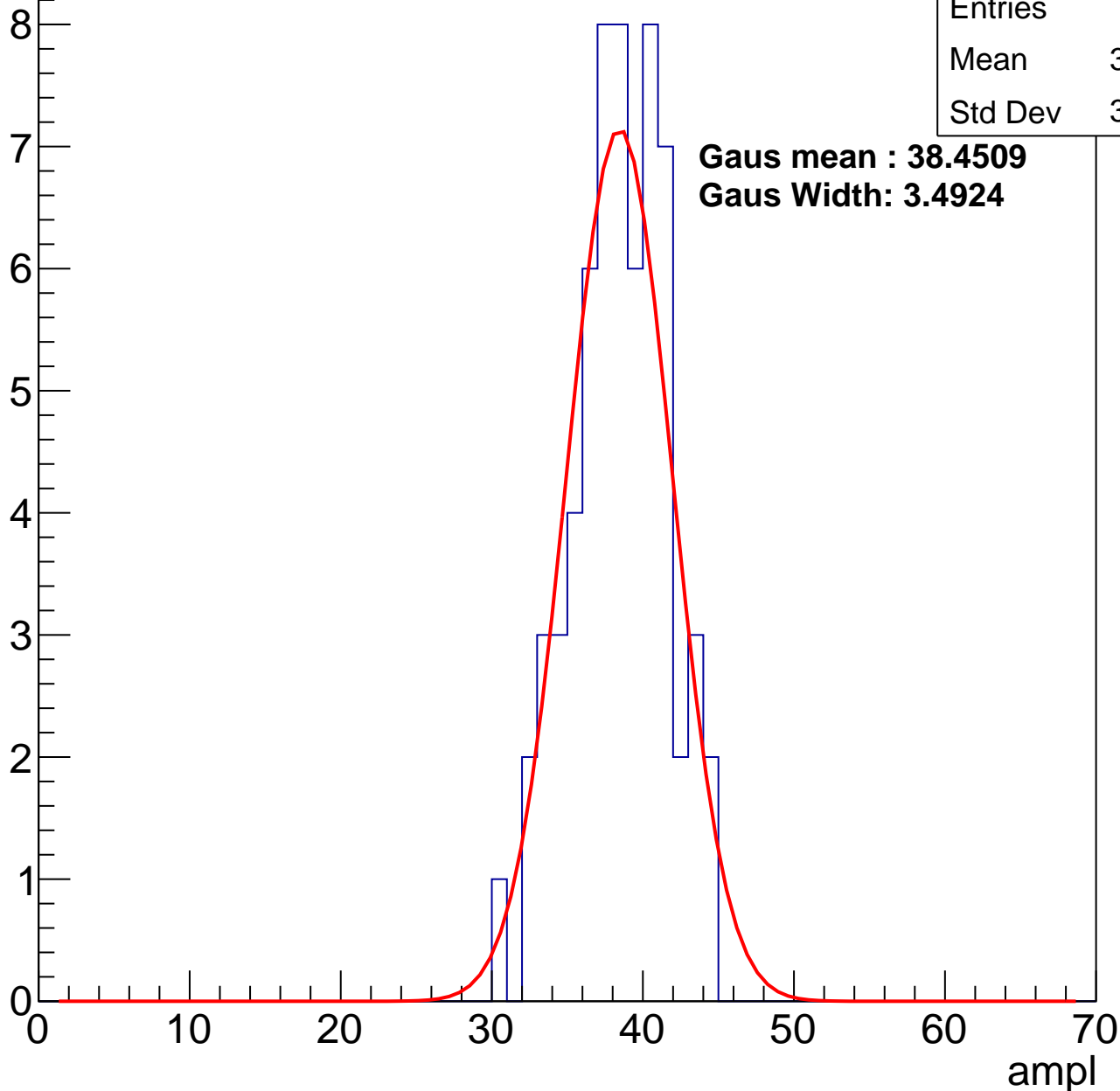
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.98
Std Dev	3.104

Gaus mean : 38.4509

Gaus Width: 3.4924



B1L101S, U6-ch23, adc2

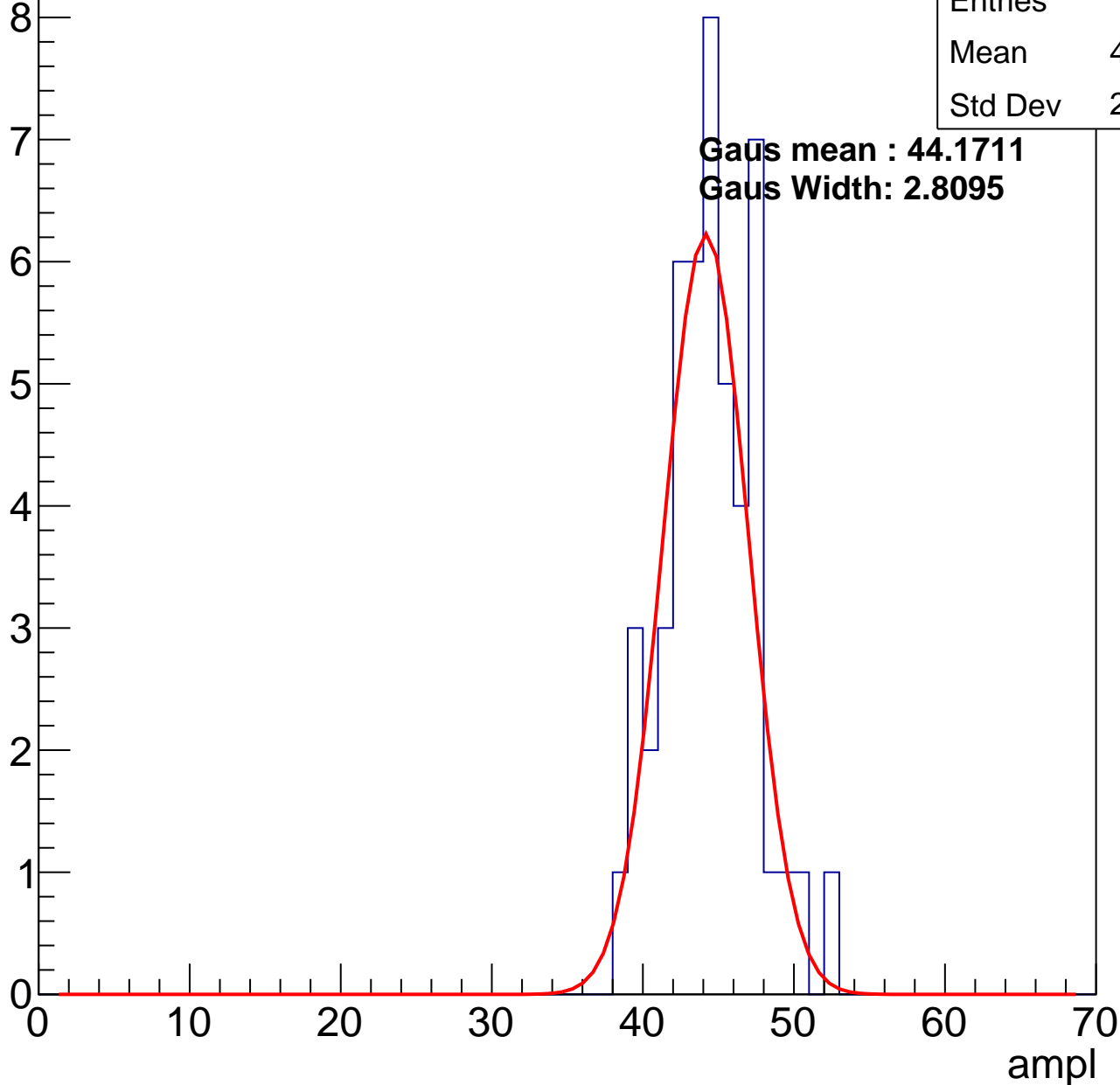
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	44.02
Std Dev	2.945

Gaus mean : 44.1711

Gaus Width: 2.8095

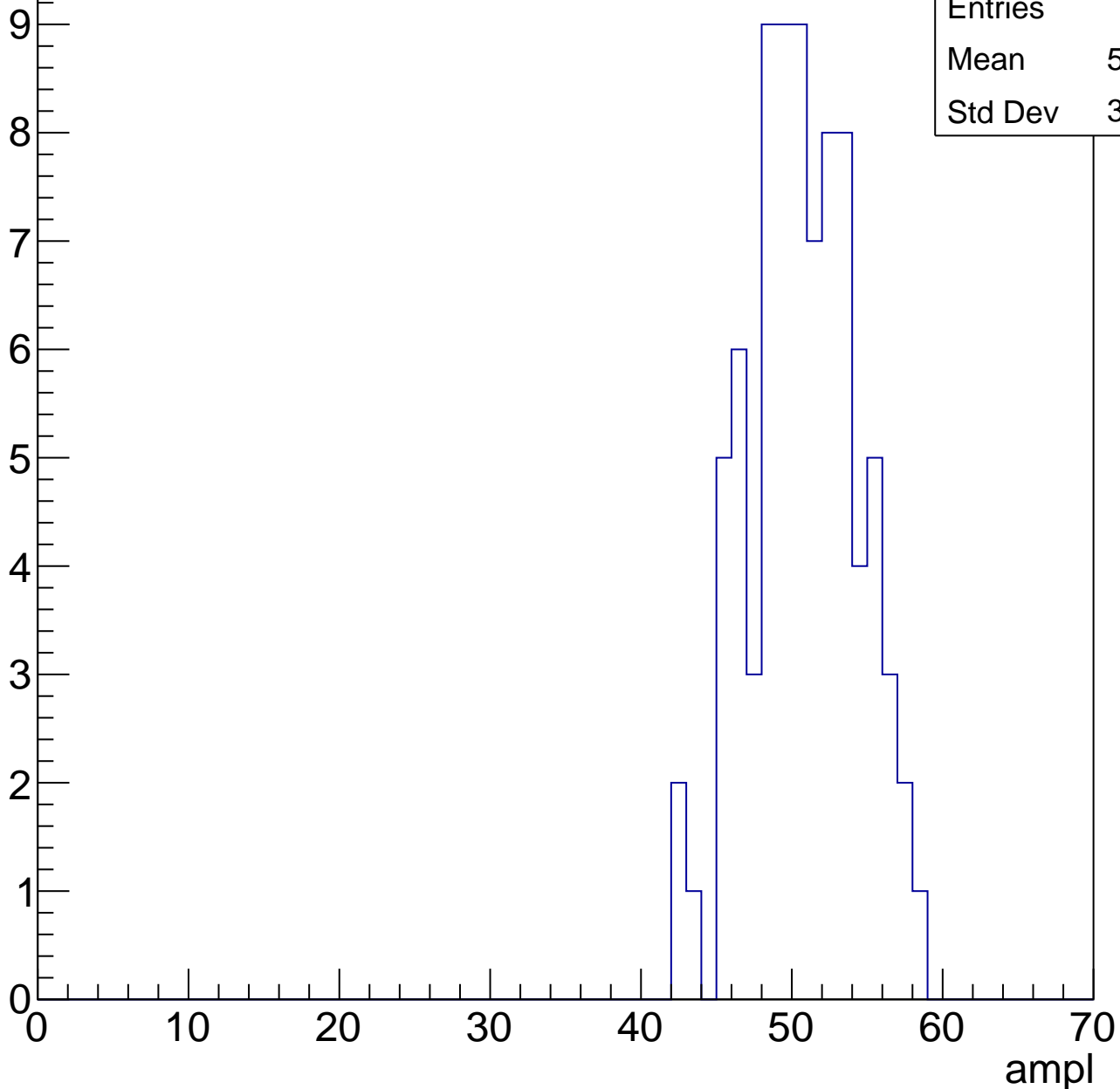


B1L101S, U6-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	50.24
Std Dev	3.553

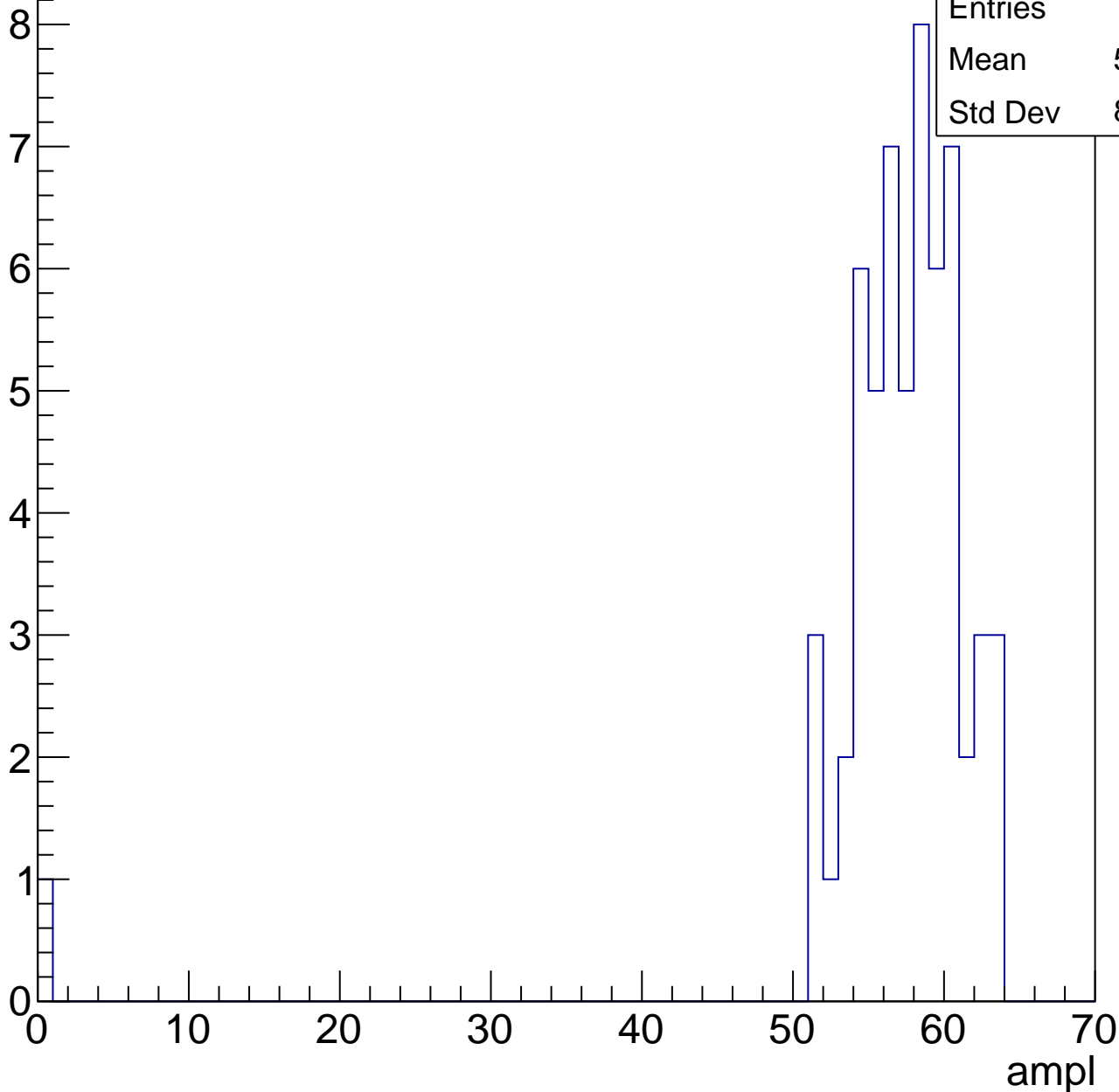


B1L101S, U6-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.31
Std Dev	8.001



B1L101S, U6-ch23, adc5

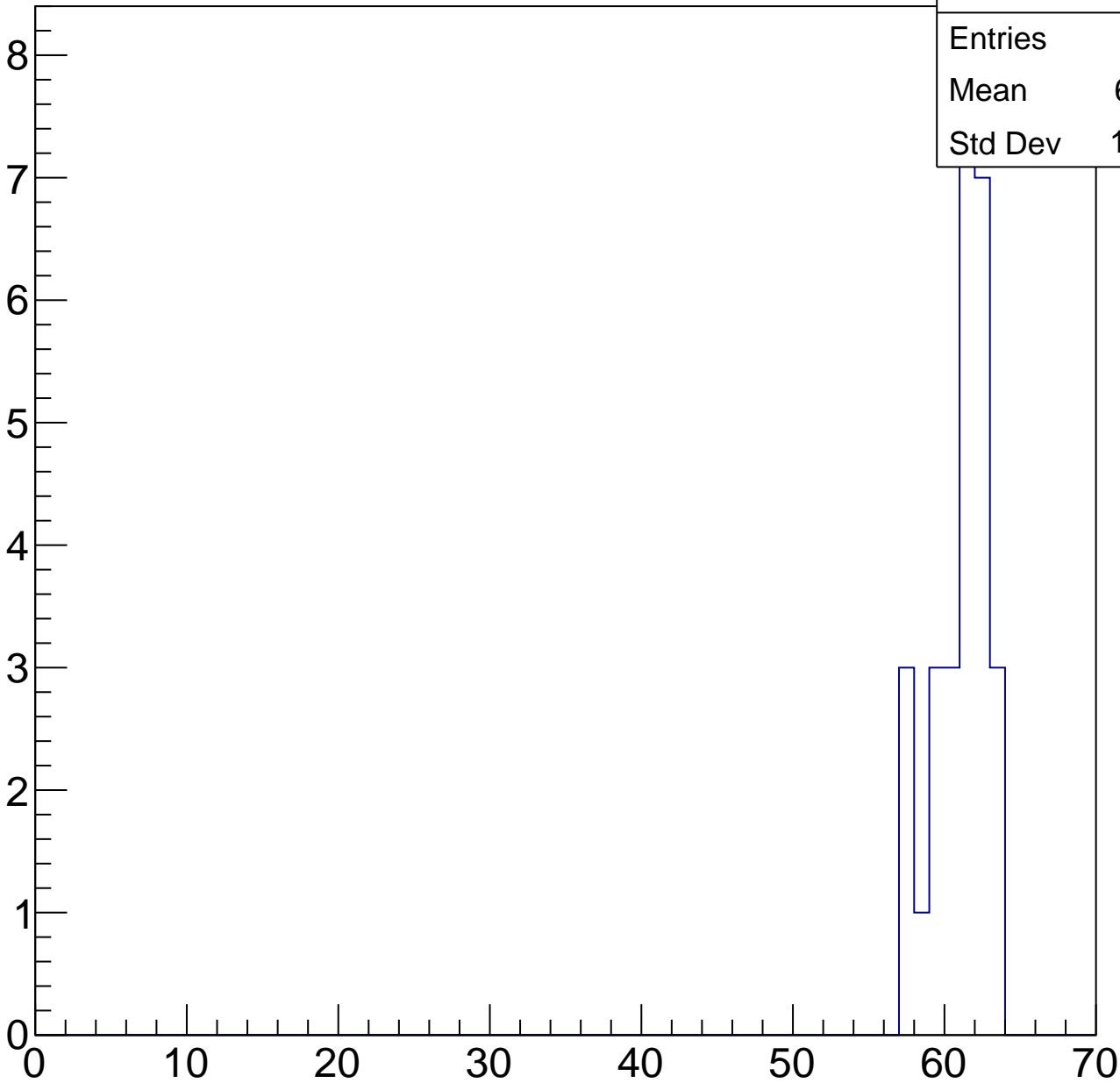
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	28
Mean	60.61
Std Dev	1.759

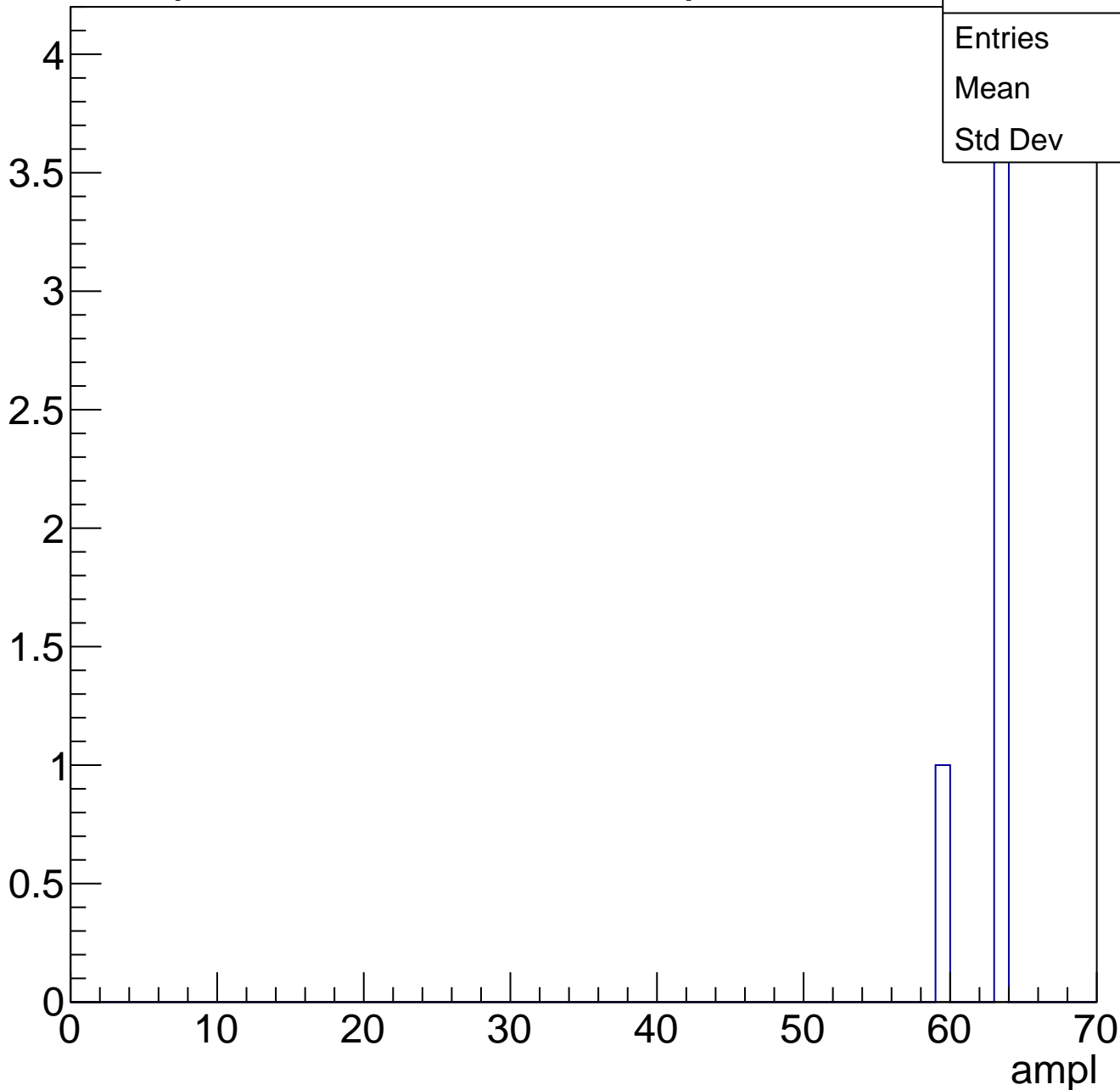
ampl



B1L101S, U6-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch23, adc7

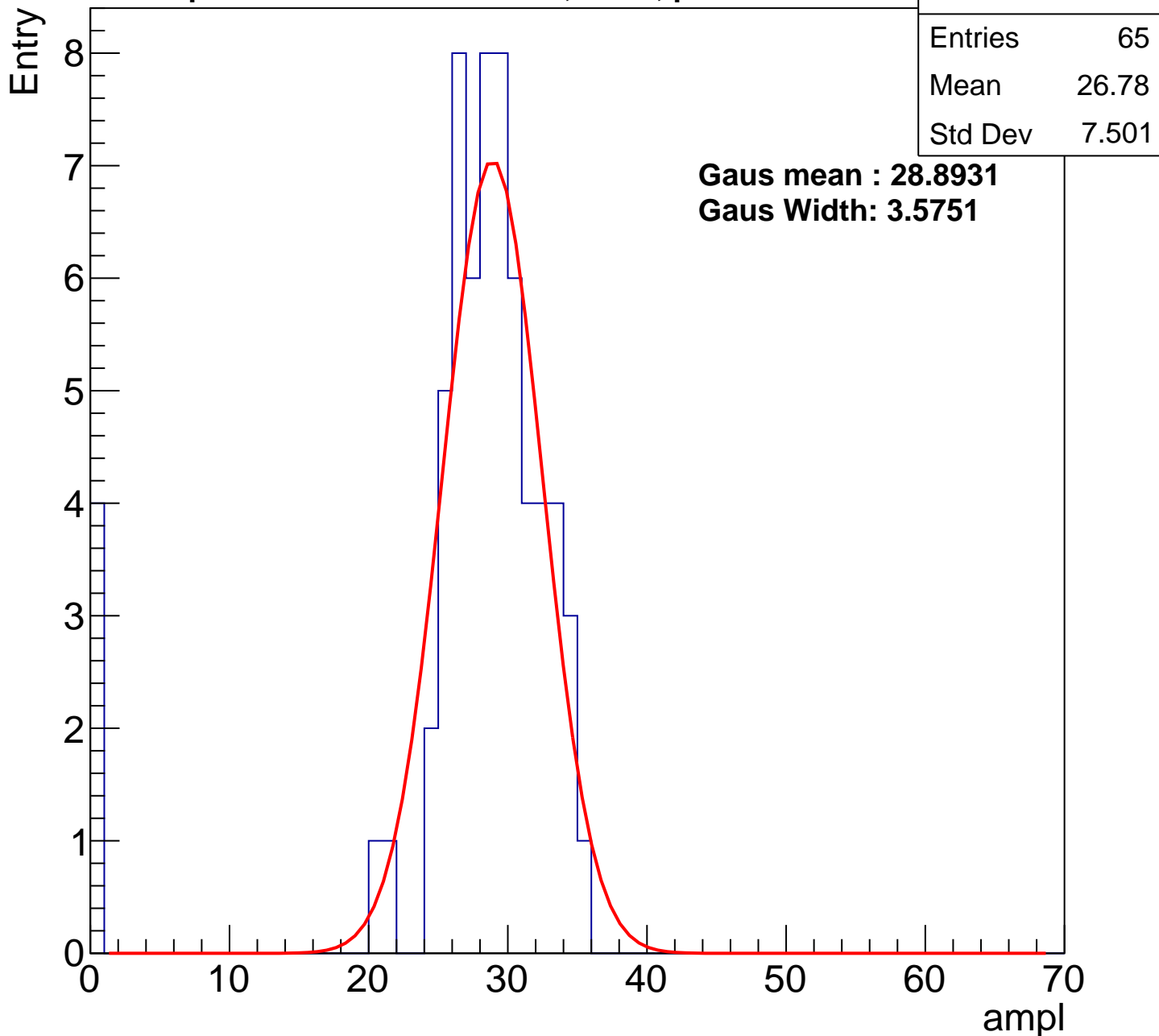
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch24, adc0

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch24, adc1

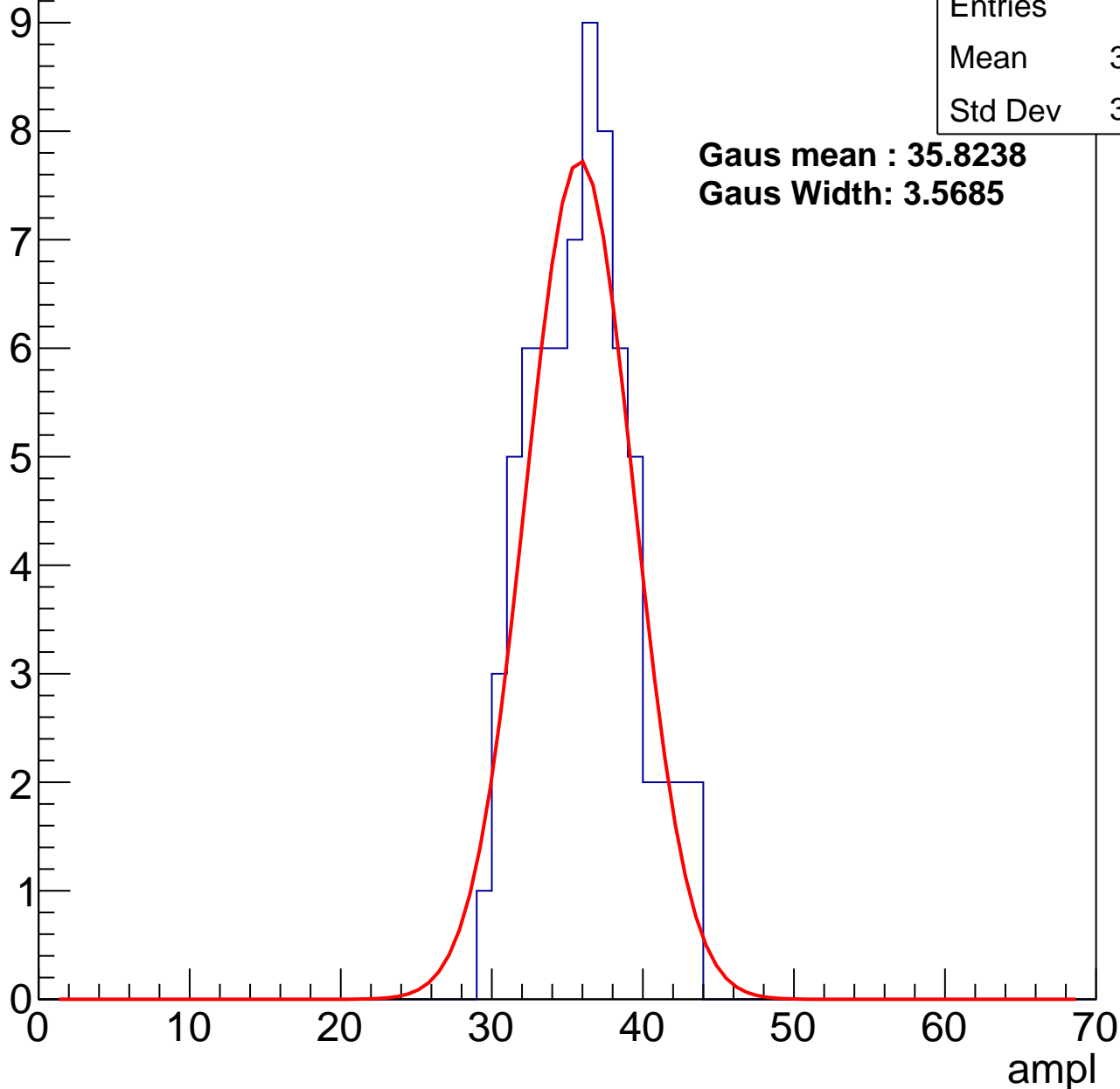
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.54
Std Dev	3.332

Gaus mean : 35.8238

Gaus Width: 3.5685



B1L101S, U6-ch24, adc2

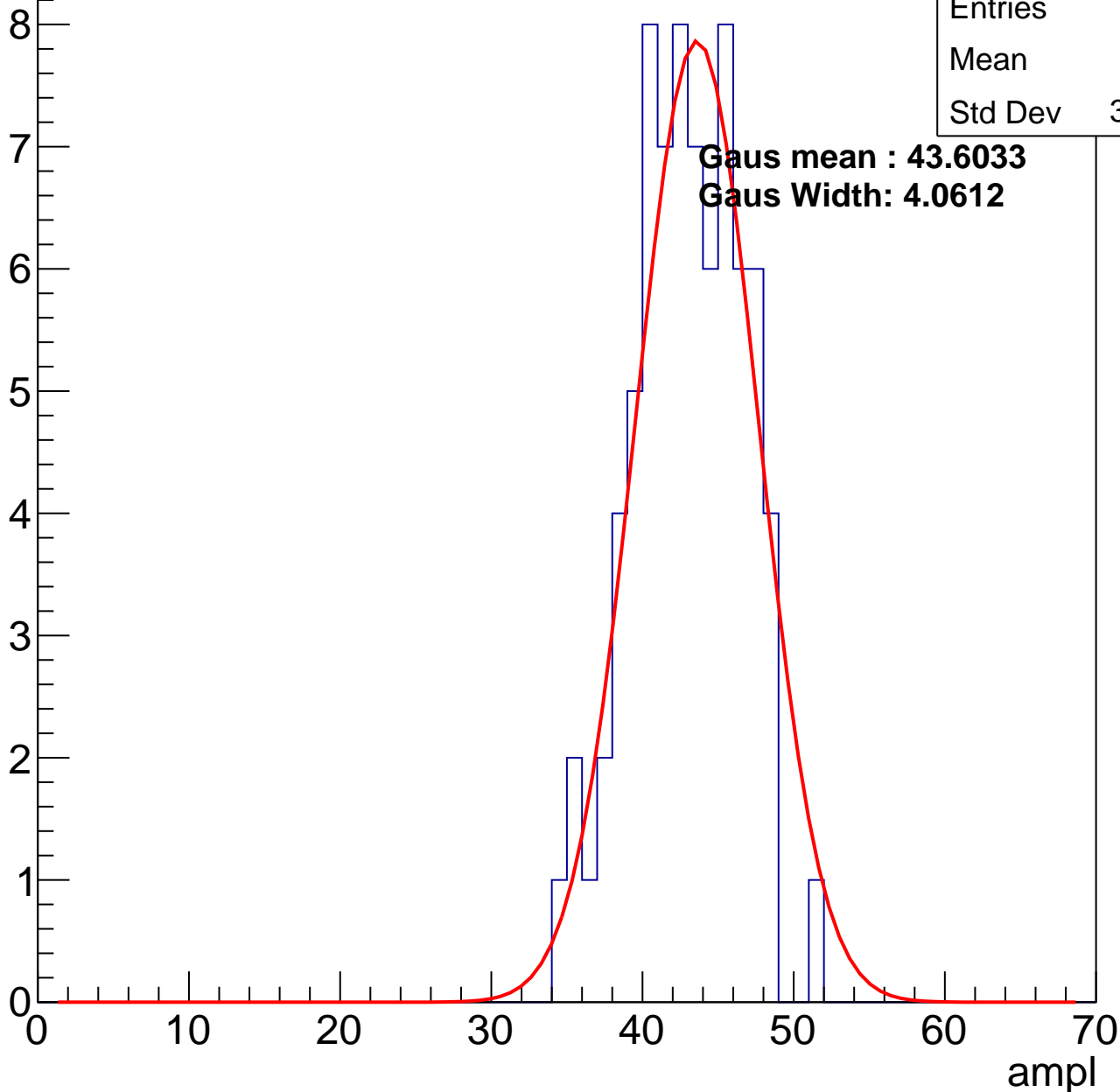
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	42.5
Std Dev	3.537

Gaus mean : 43.6033

Gaus Width: 4.0612

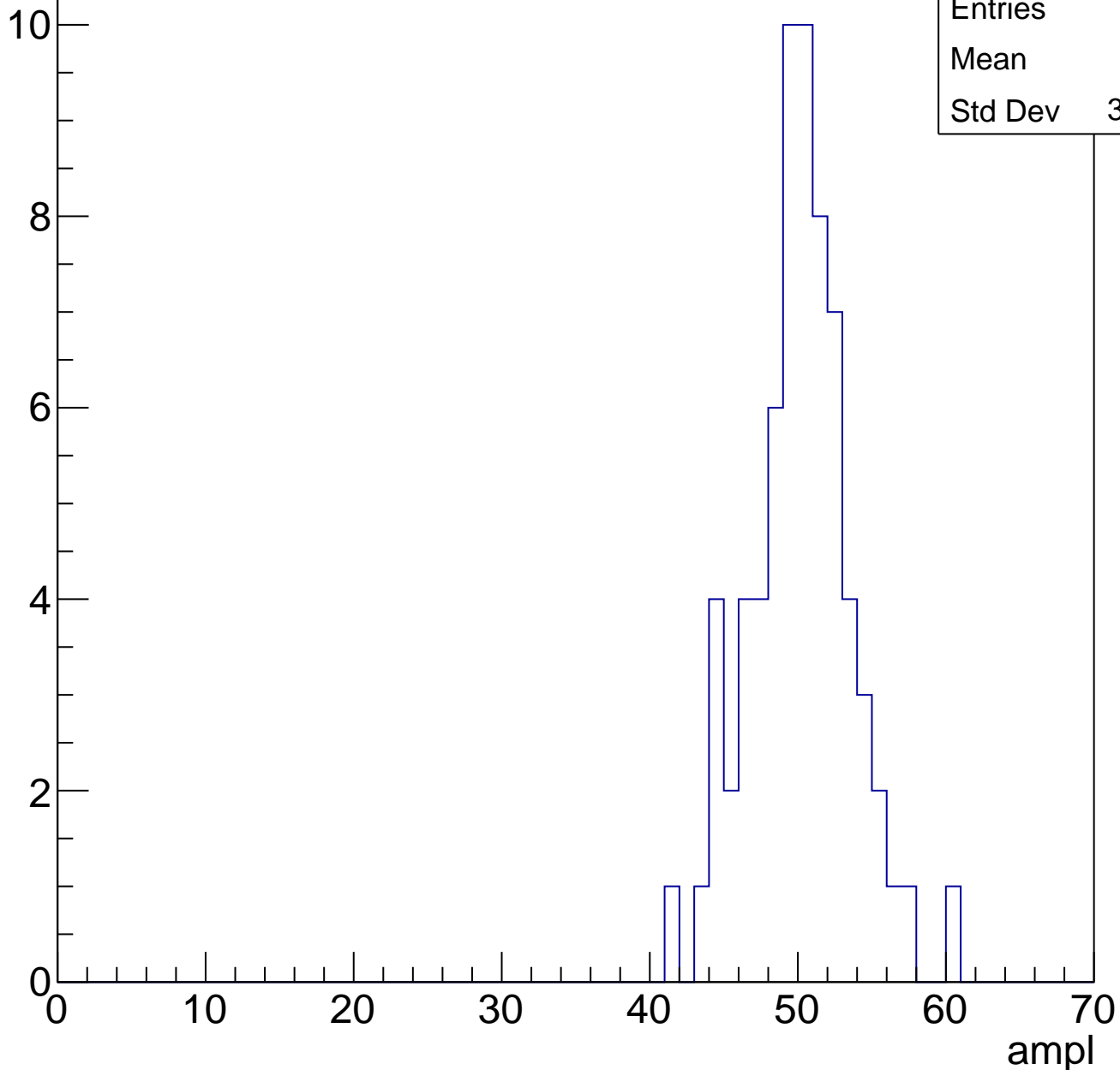


B1L101S, U6-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	49.7
Std Dev	3.419

Entry

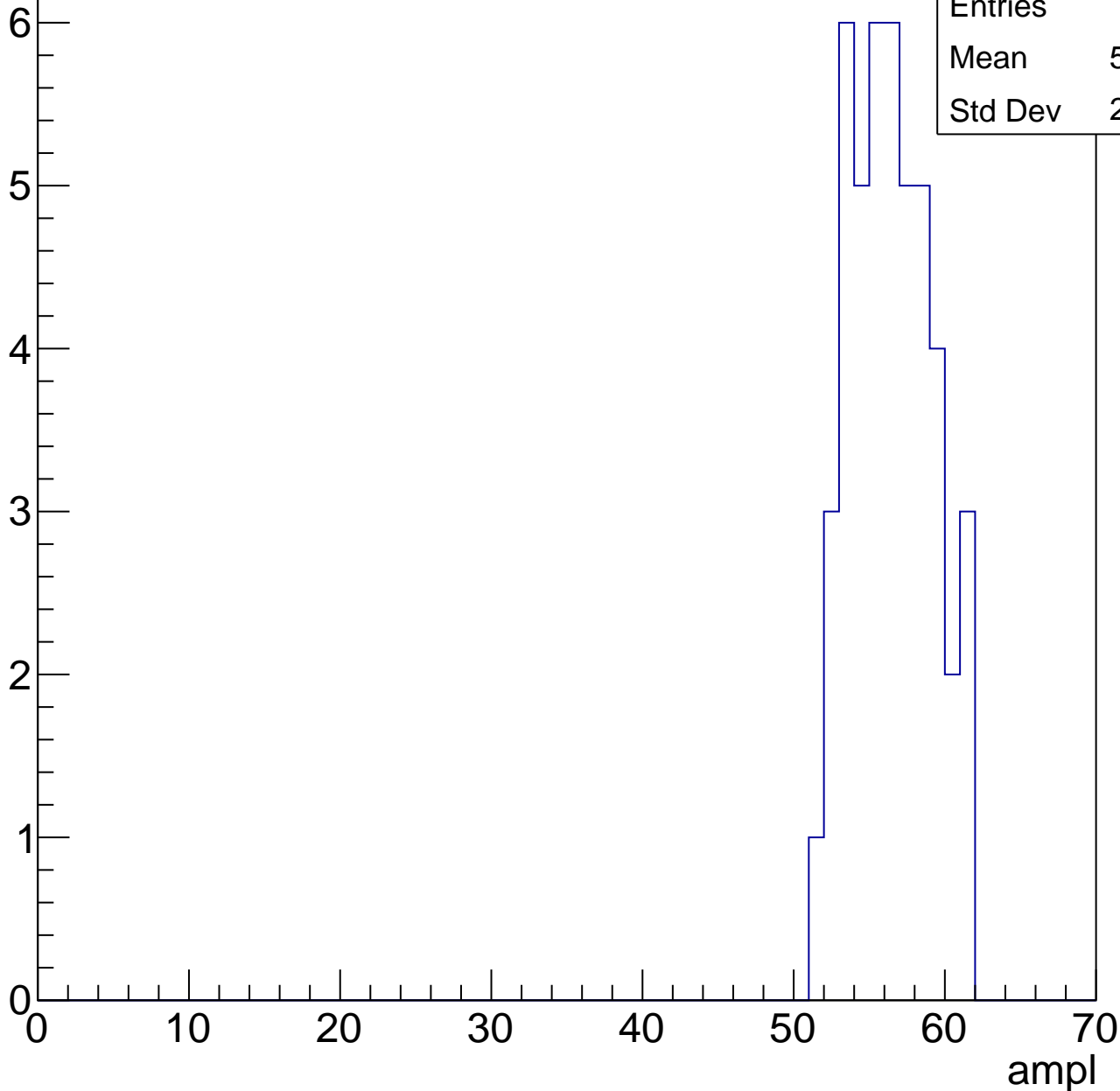


B1L101S, U6-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	55.98
Std Dev	2.642

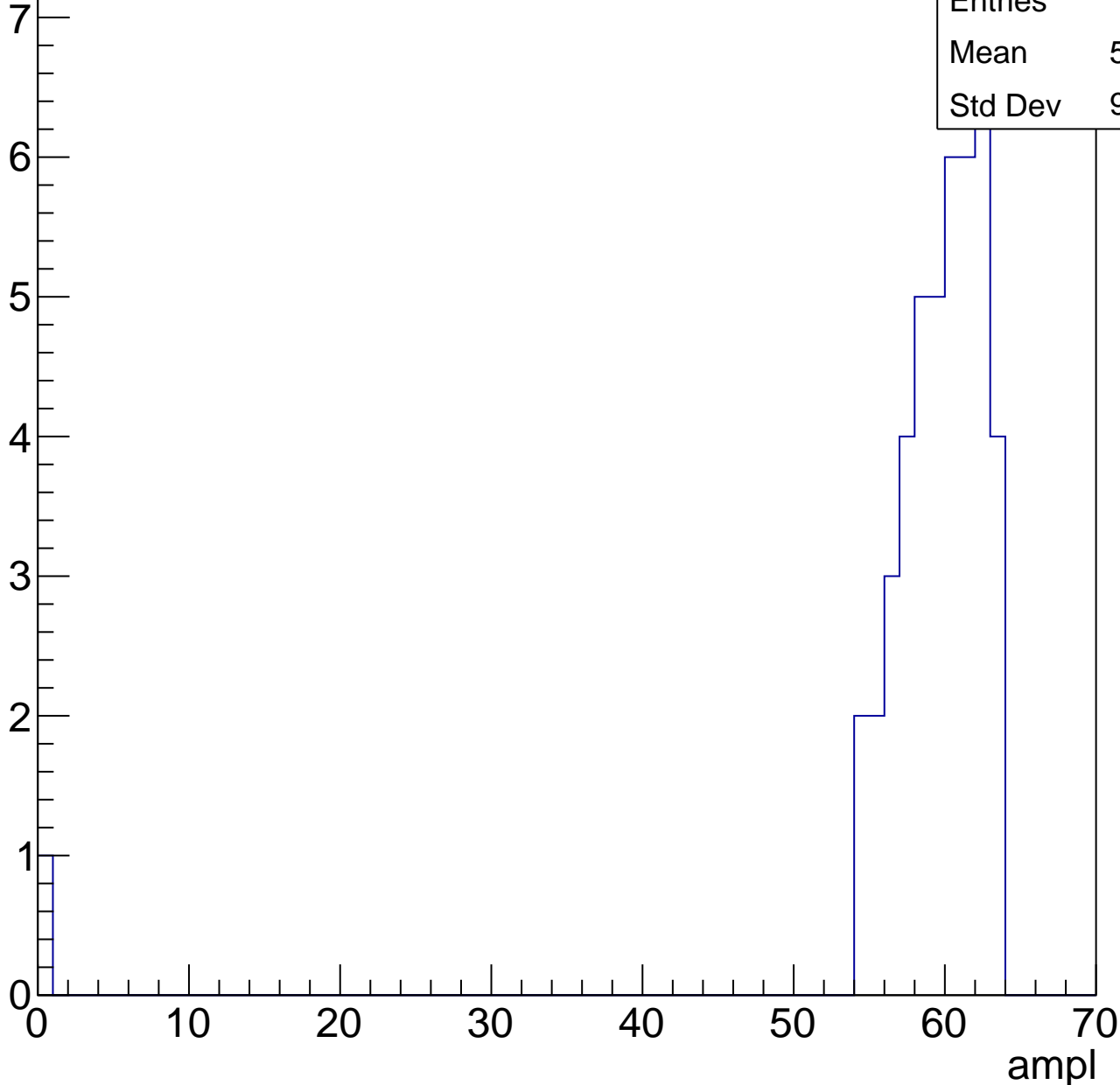


B1L101S, U6-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.02
Std Dev	9.098

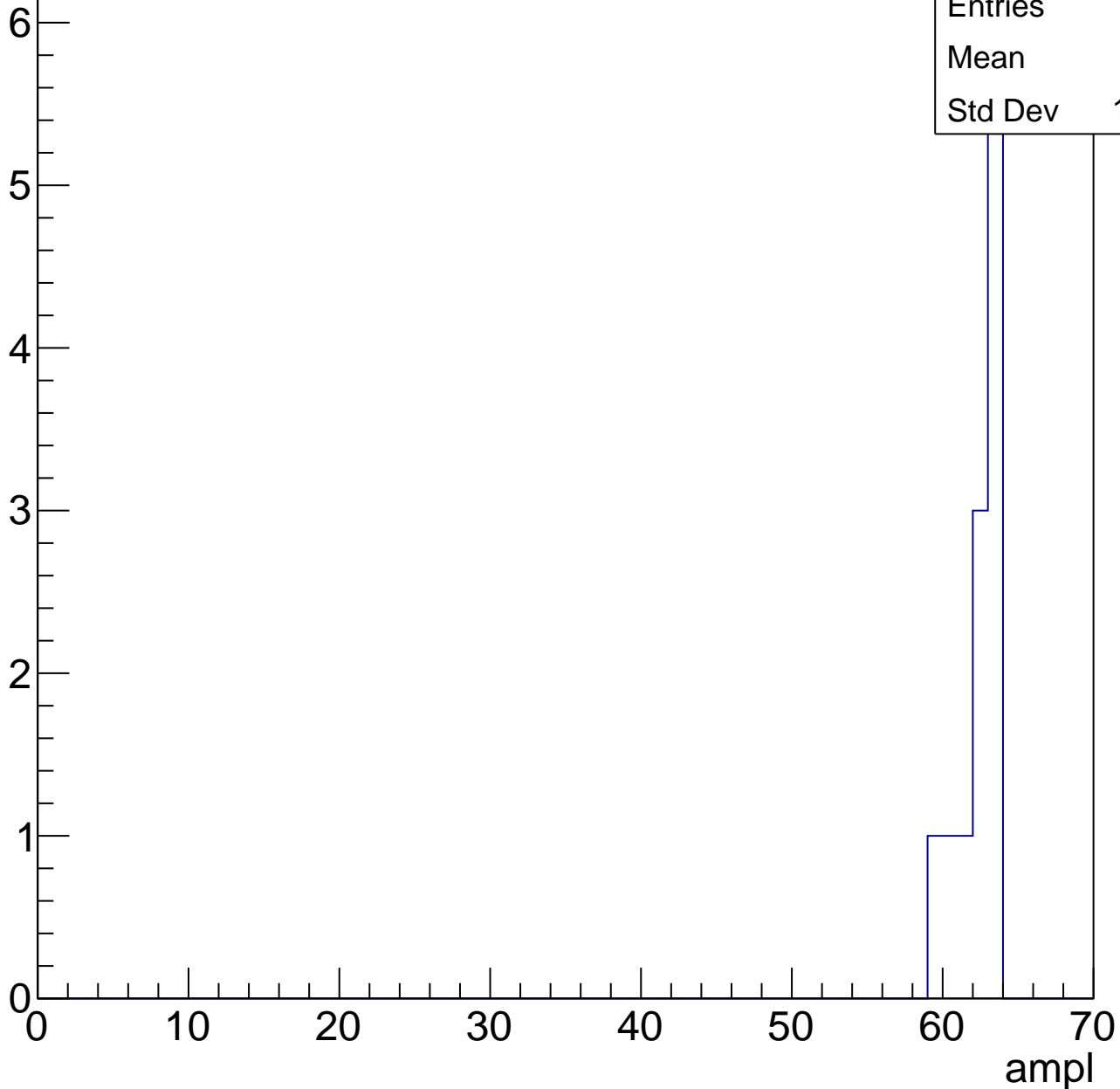


B1L101S, U6-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	62
Std Dev	1.291



B1L101S, U6-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch25, adc0

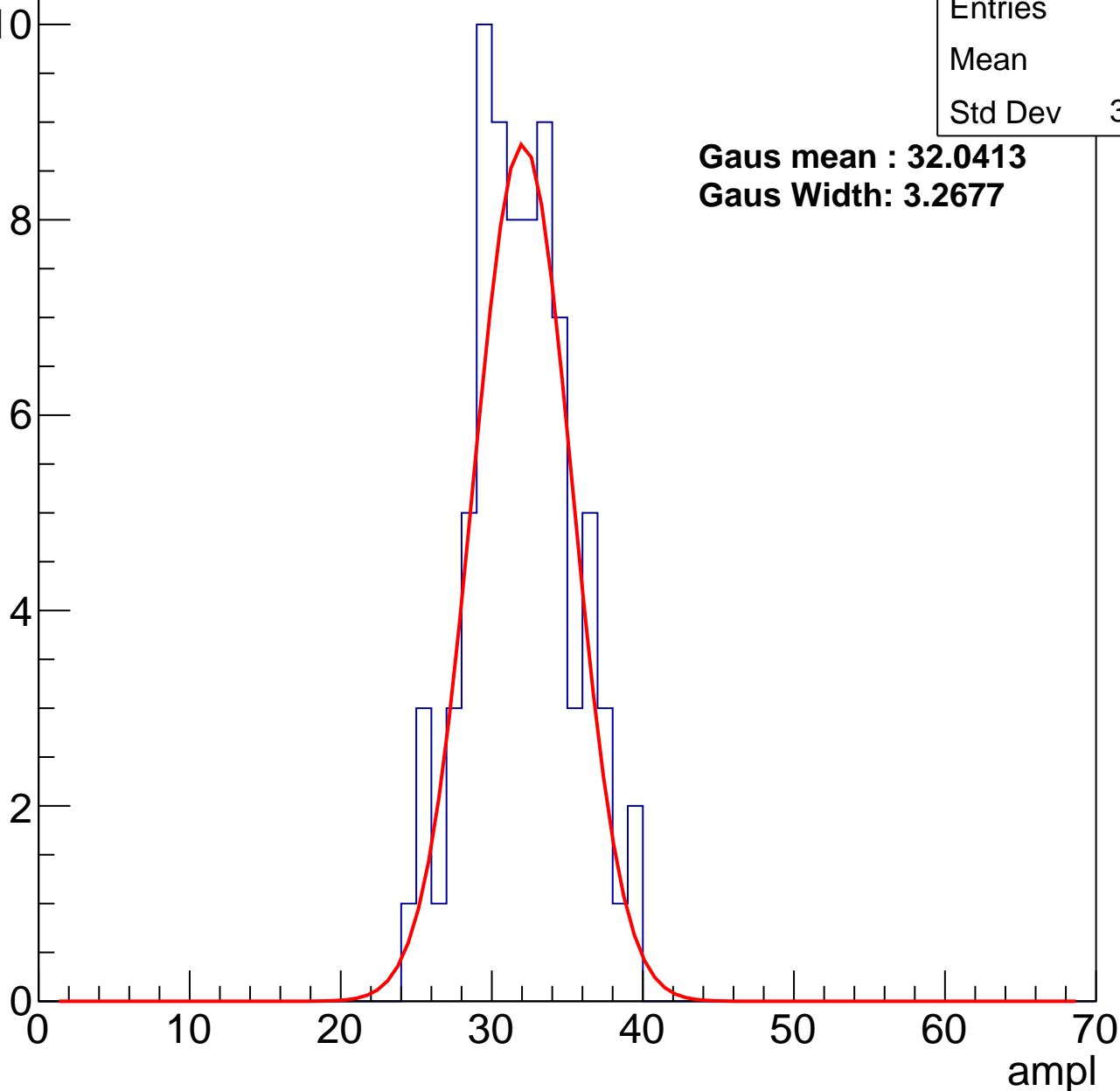
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	31.5
Std Dev	3.339

Gaus mean : 32.0413

Gaus Width: 3.2677



B1L101S, U6-ch25, adc1

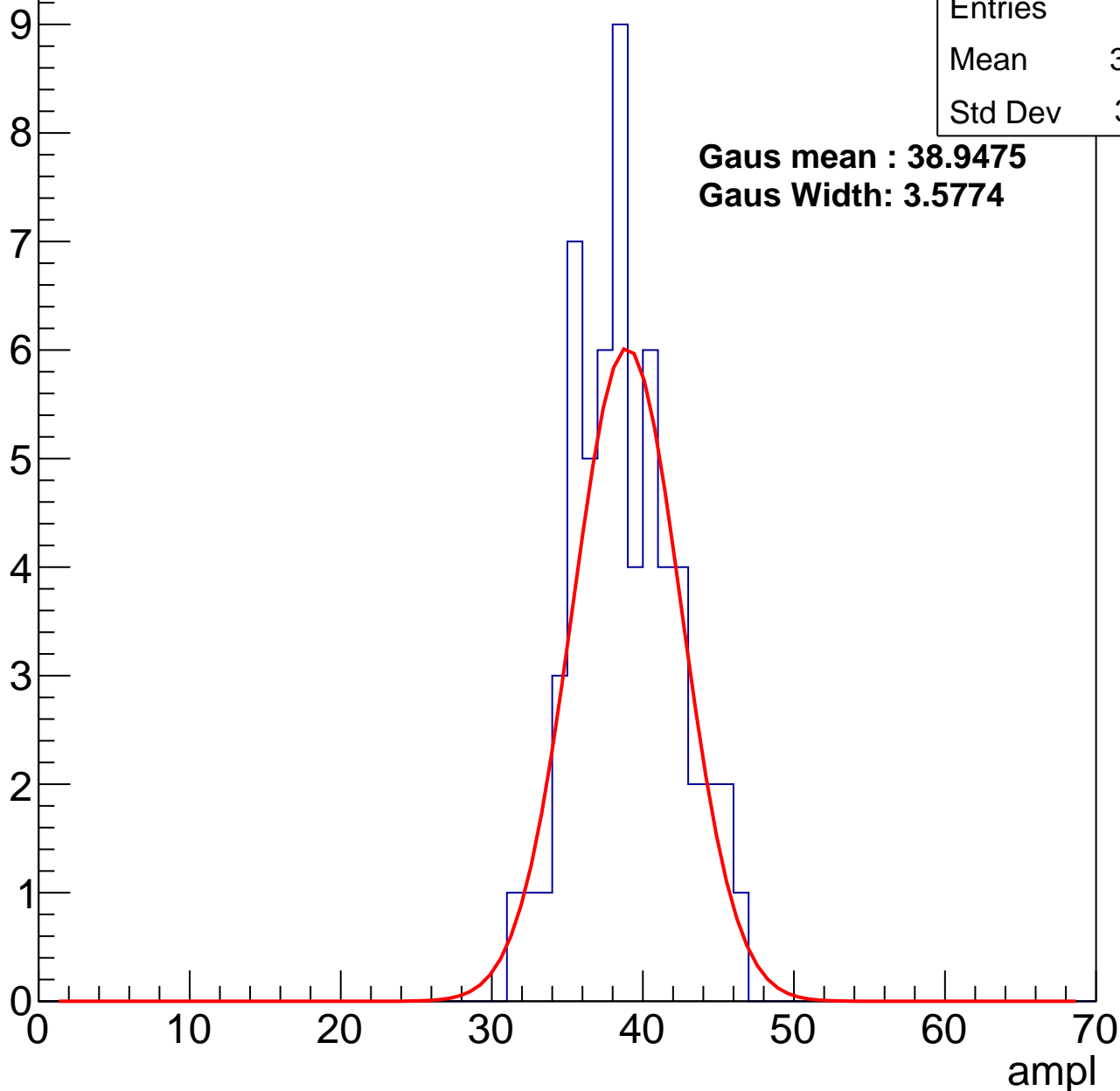
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	38.36
Std Dev	3.351

Gaus mean : 38.9475

Gaus Width: 3.5774



B1L101S, U6-ch25, adc2

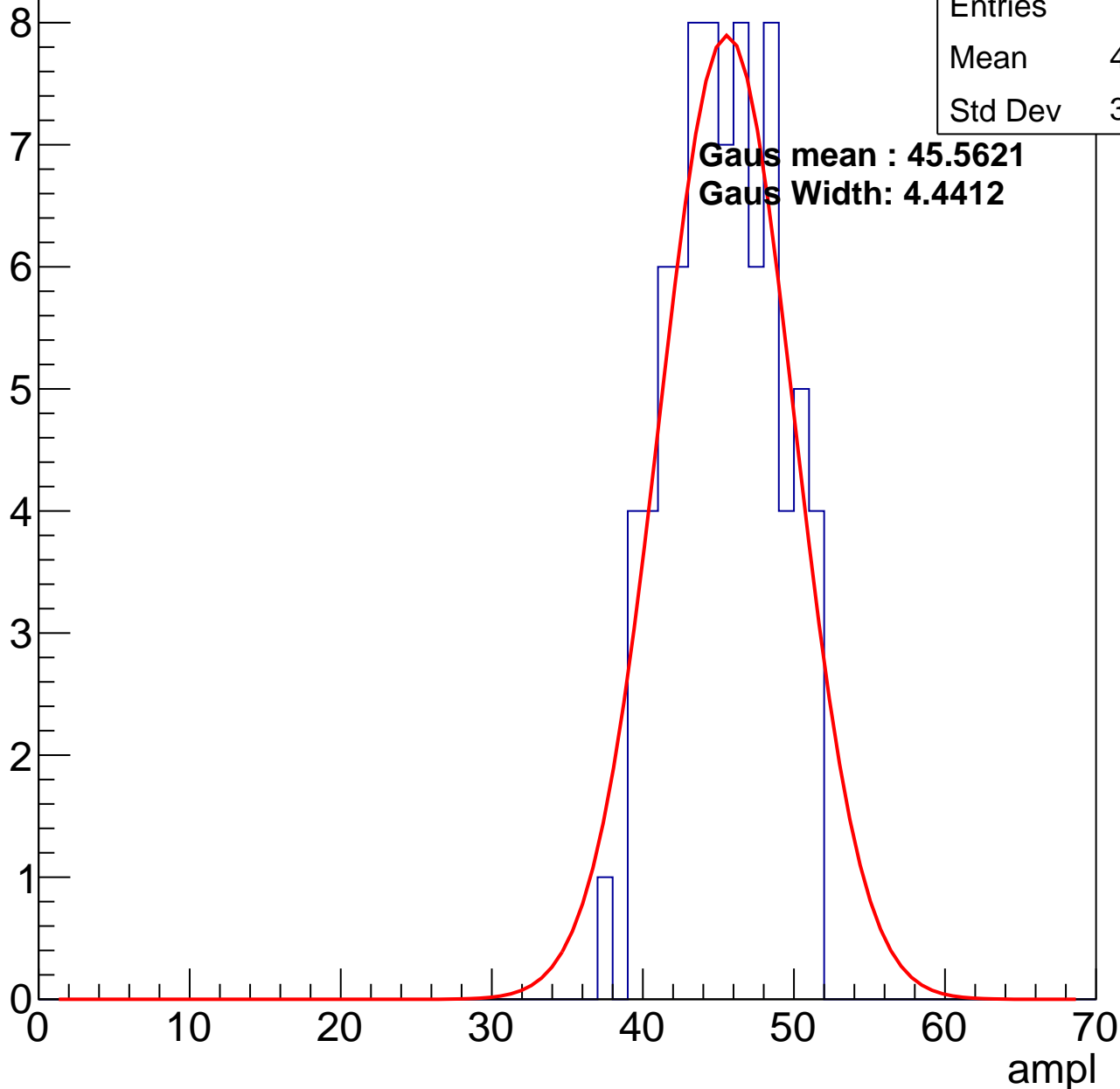
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	44.89
Std Dev	3.438

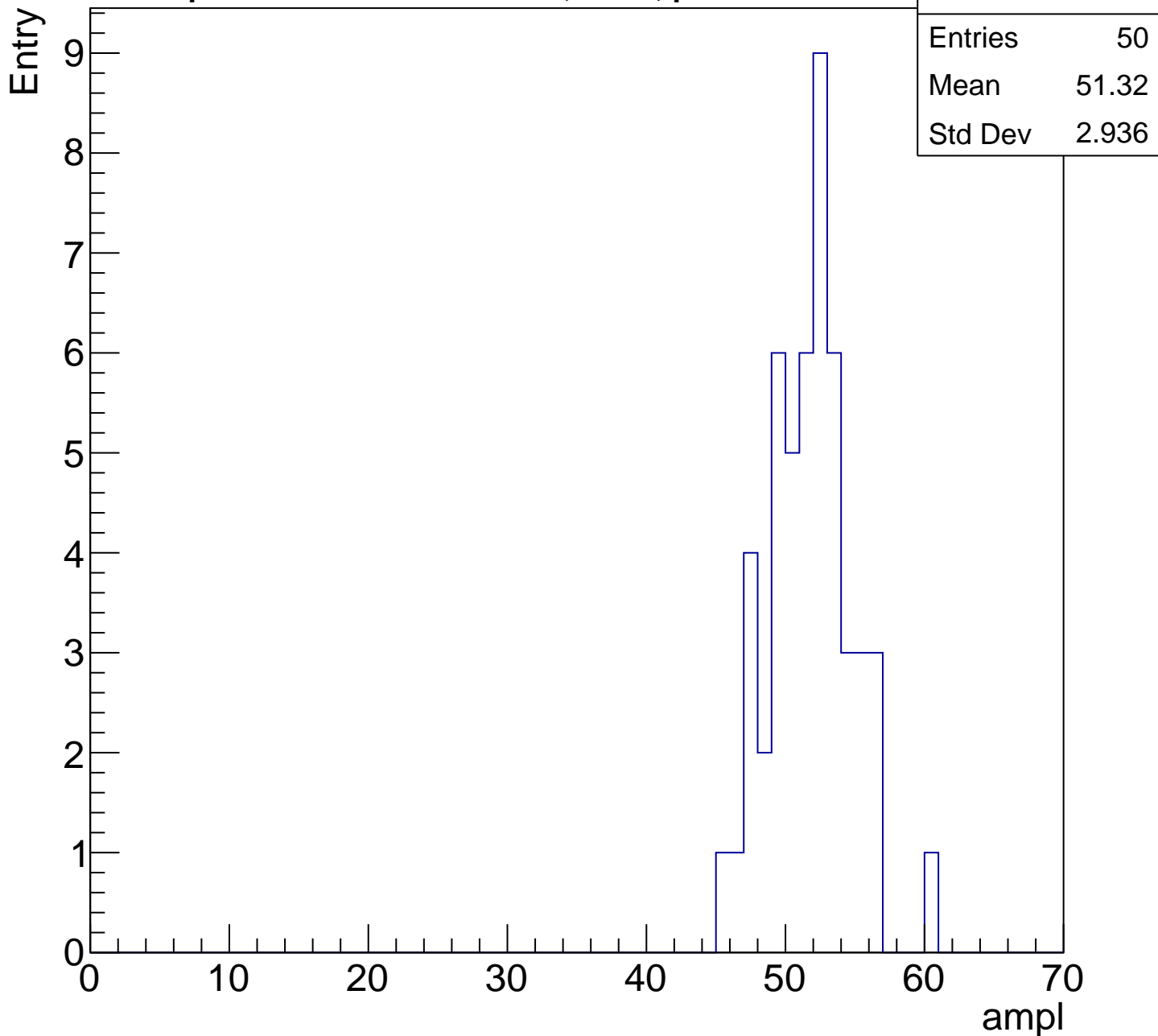
Gaus mean : 45.5621

Gaus Width: 4.4412



B1L101S, U6-ch25, adc3

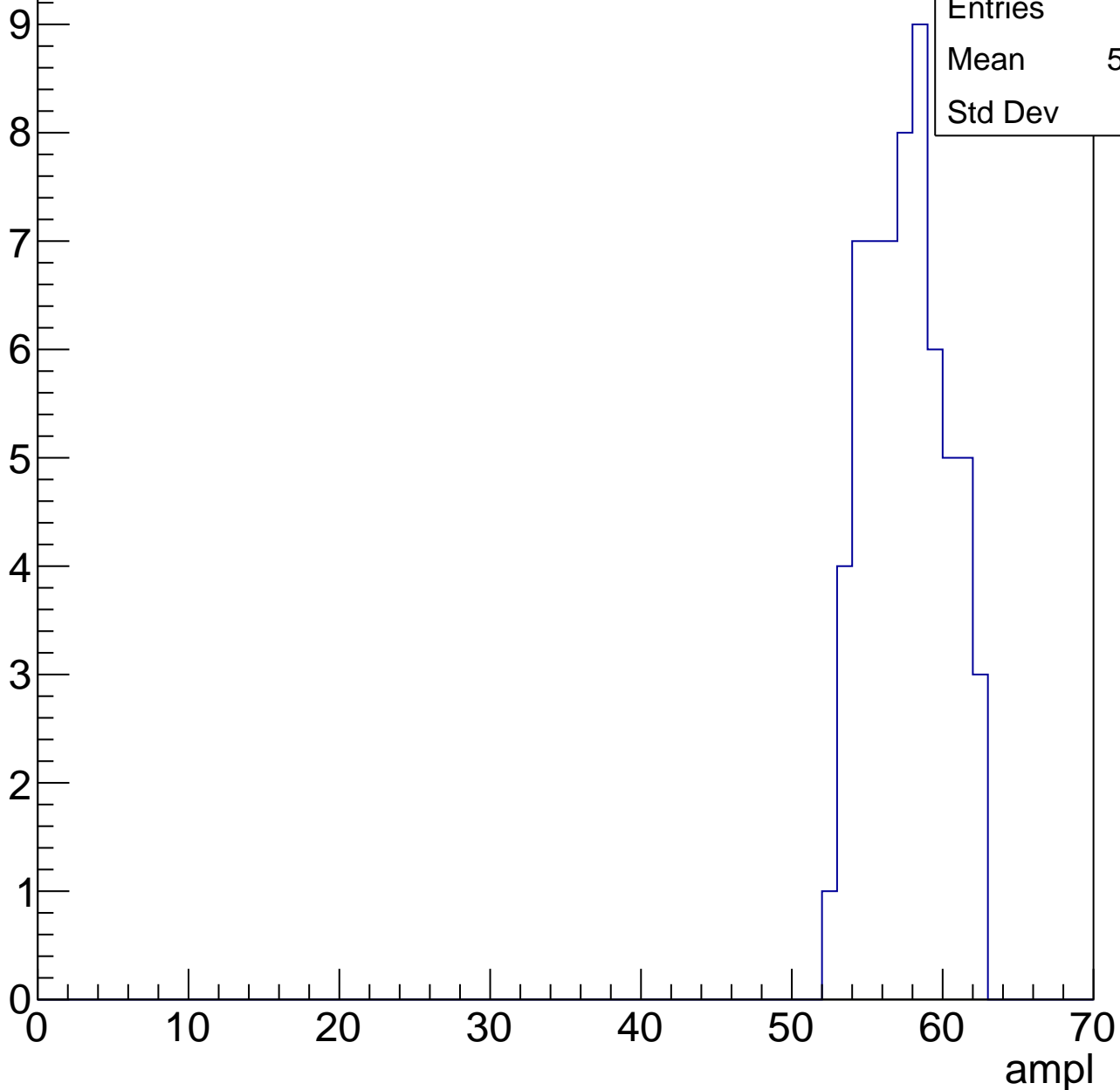
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



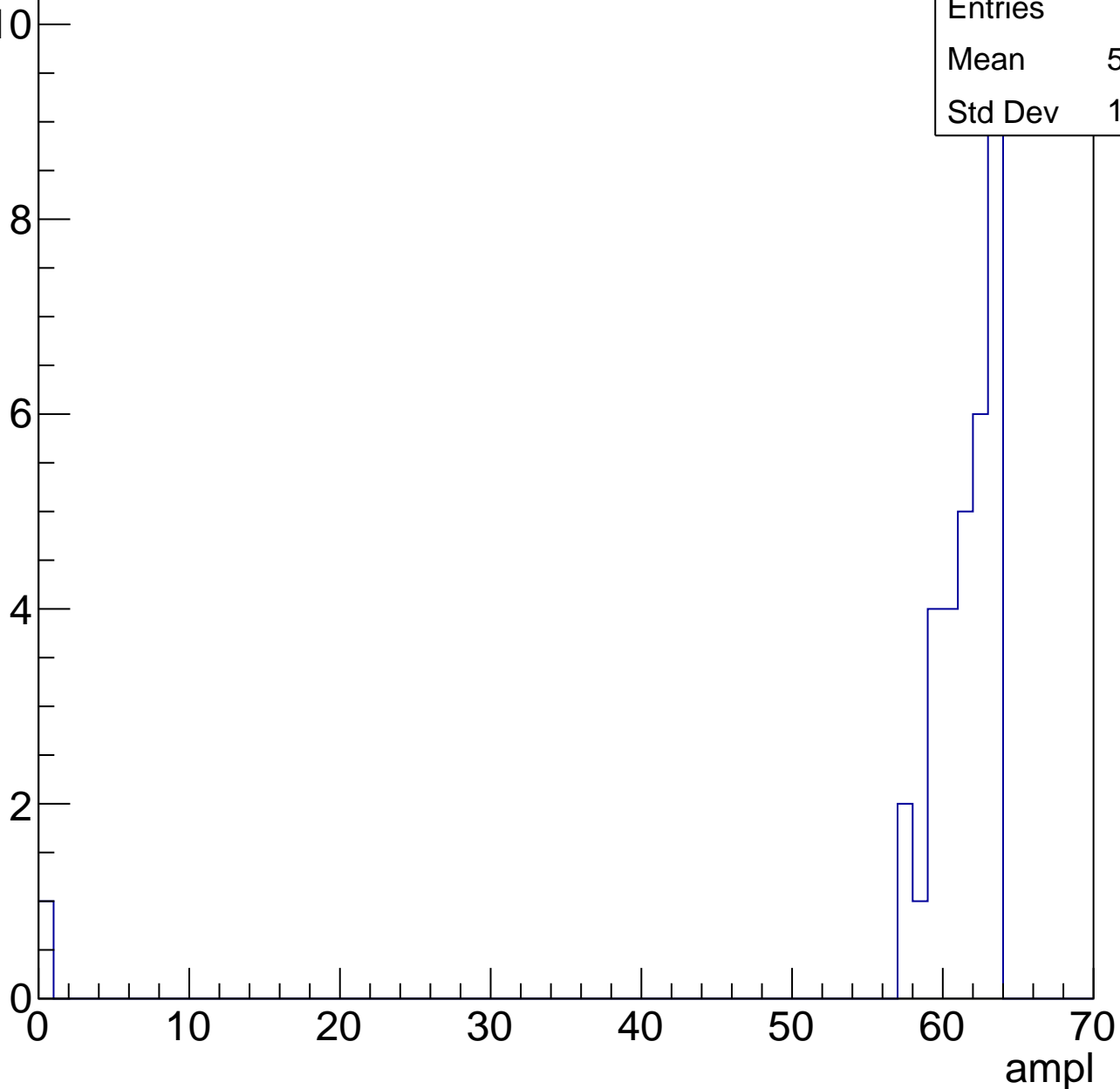
Entries	62
Mean	57.13
Std Dev	2.6

B1L101S, U6-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

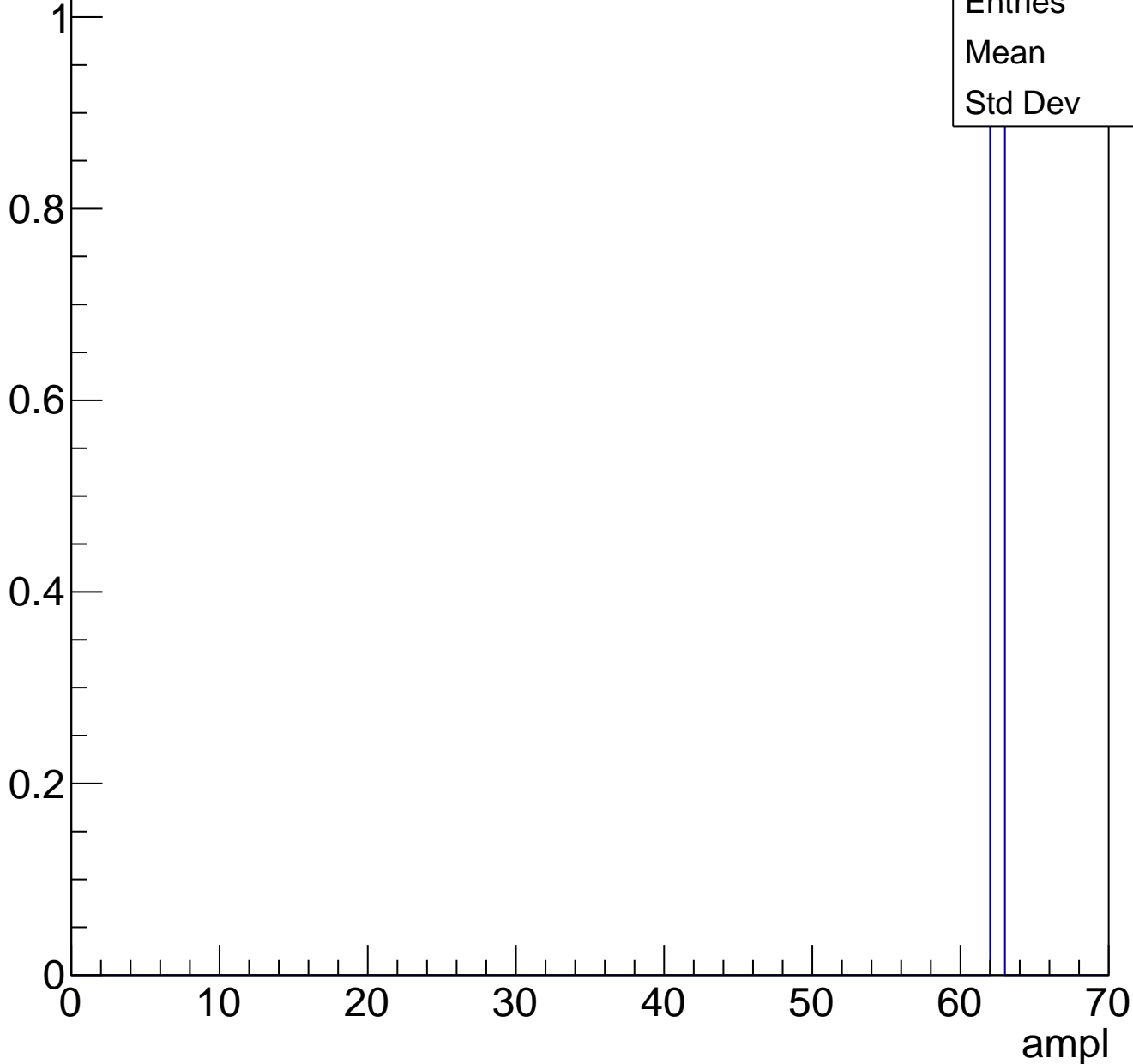
Entries	33
Mean	59.24
Std Dev	10.63



B1L101S, U6-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch26, adc0

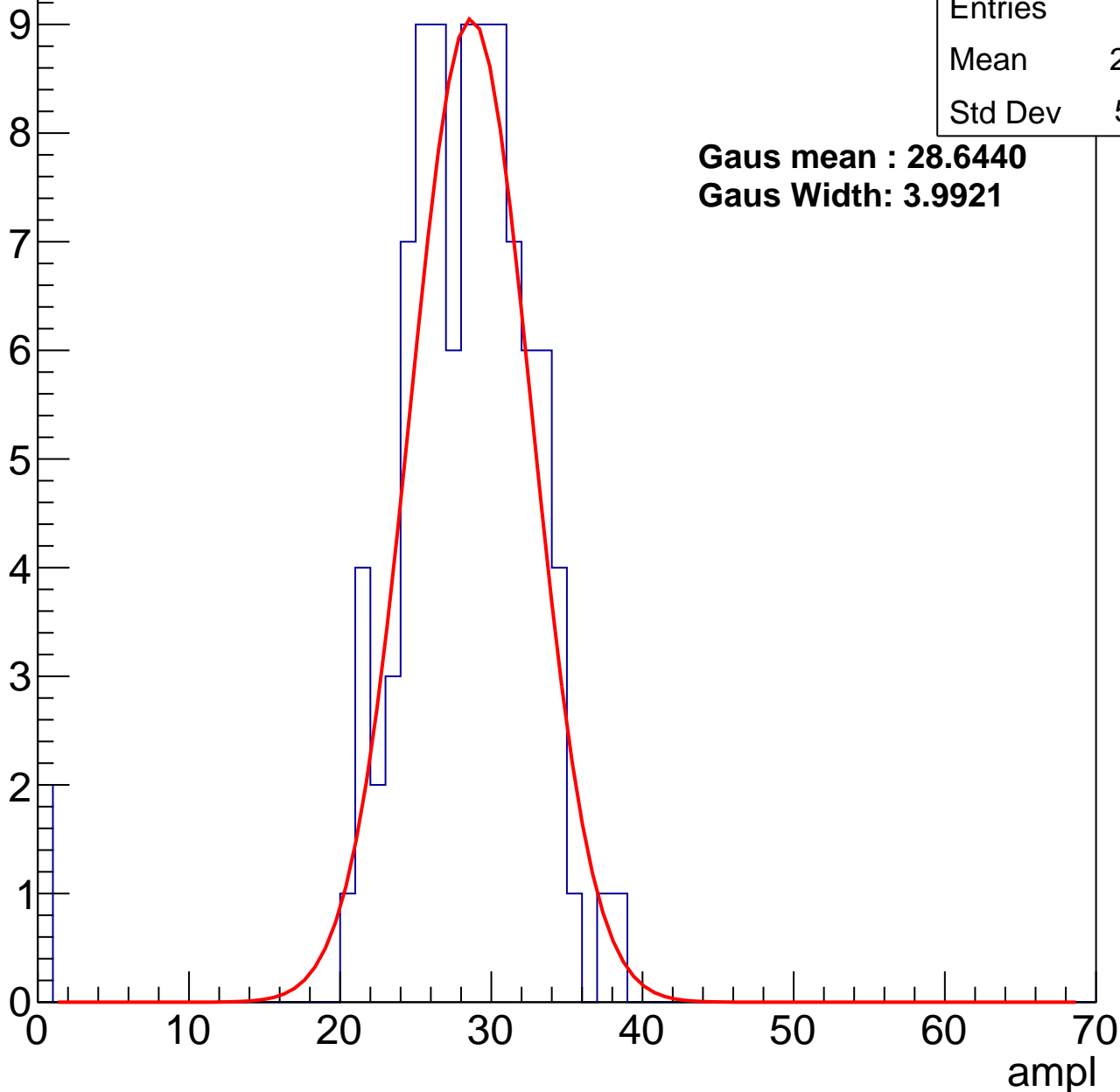
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	96
Mean	27.52
Std Dev	5.511

Gaus mean : 28.6440

Gaus Width: 3.9921



B1L101S, U6-ch26, adc1

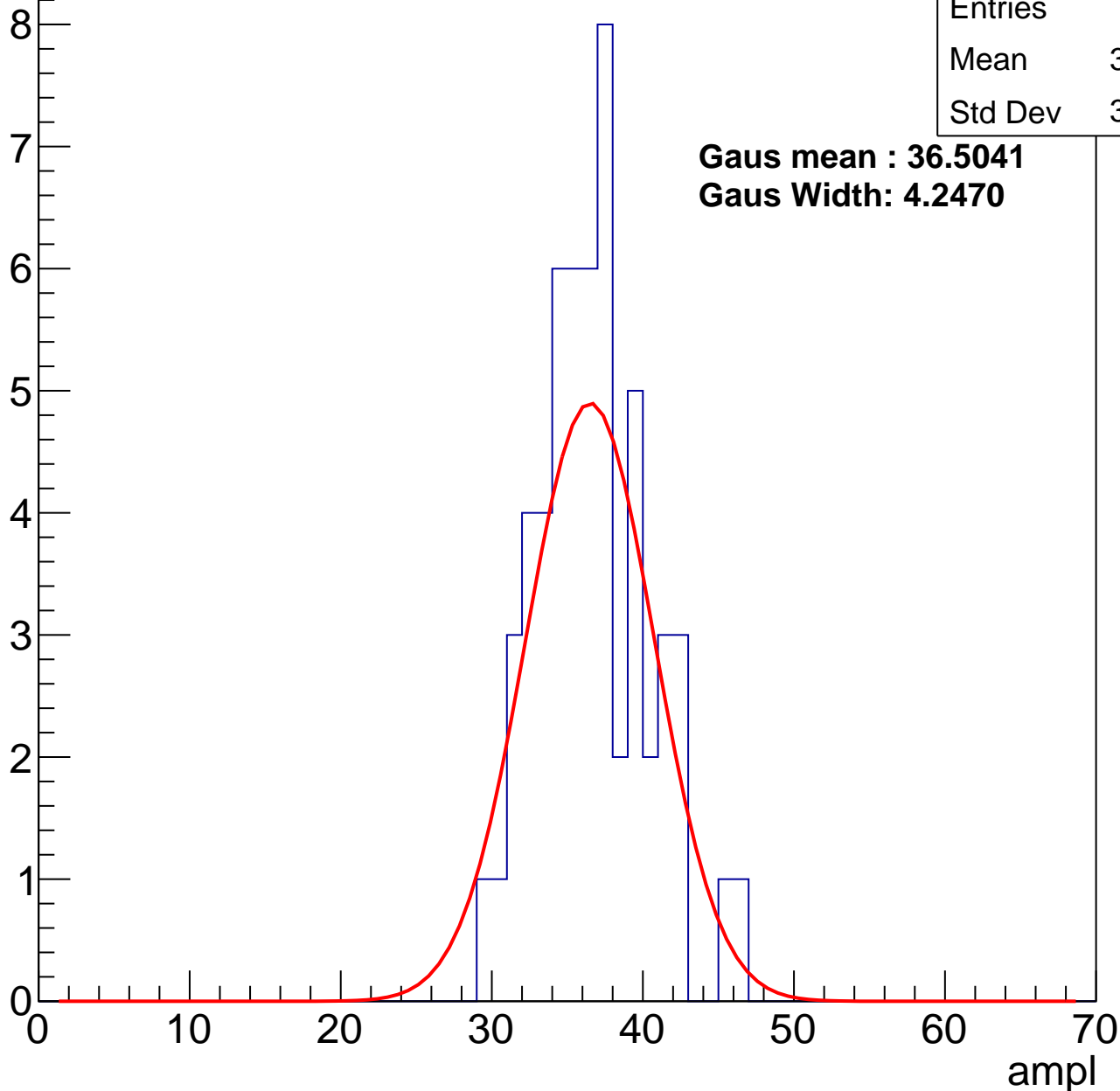
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	36.23
Std Dev	3.645

Gaus mean : 36.5041

Gaus Width: 4.2470



B1L101S, U6-ch26, adc2

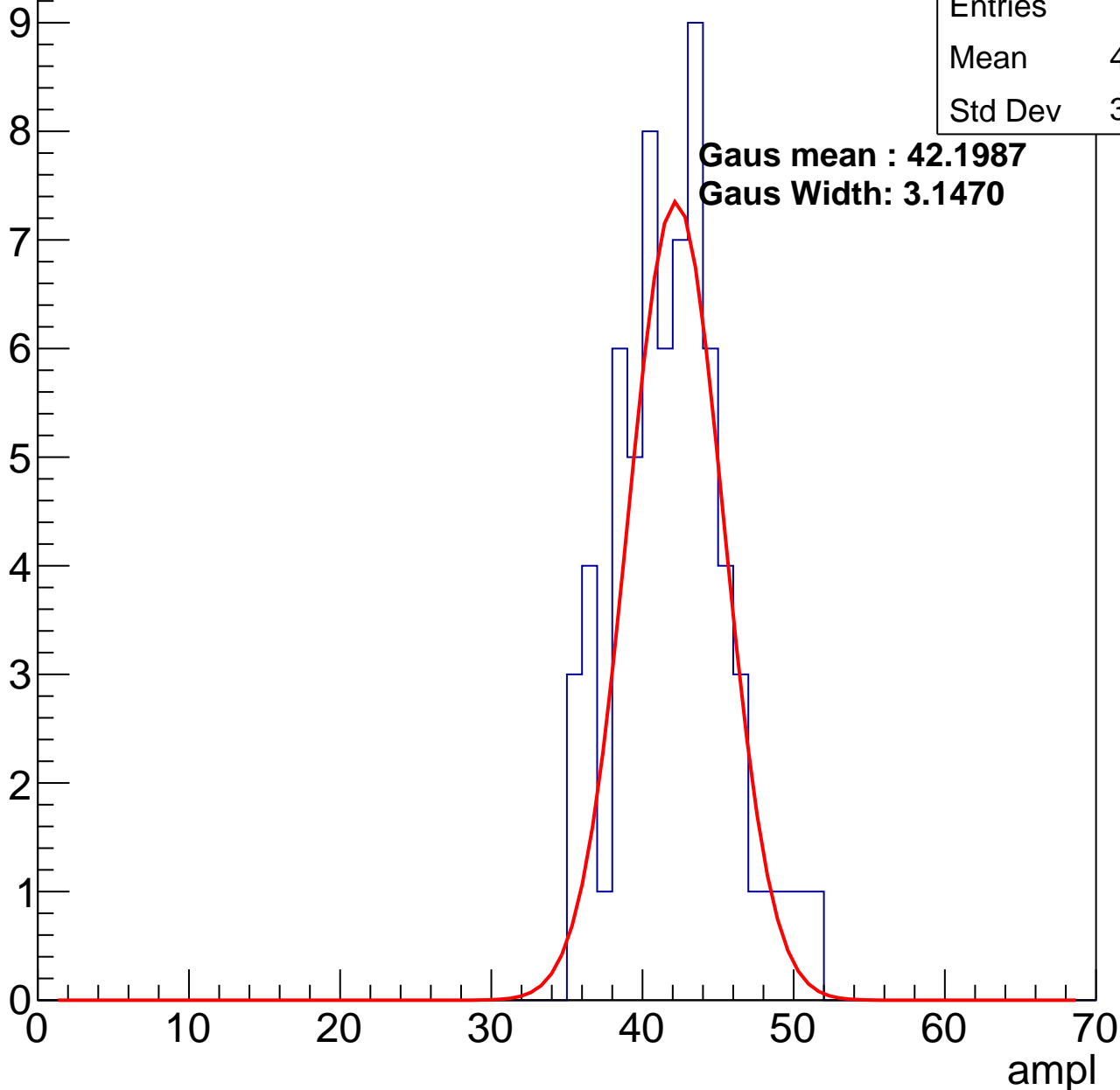
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	41.54
Std Dev	3.567

Gaus mean : 42.1987

Gaus Width: 3.1470

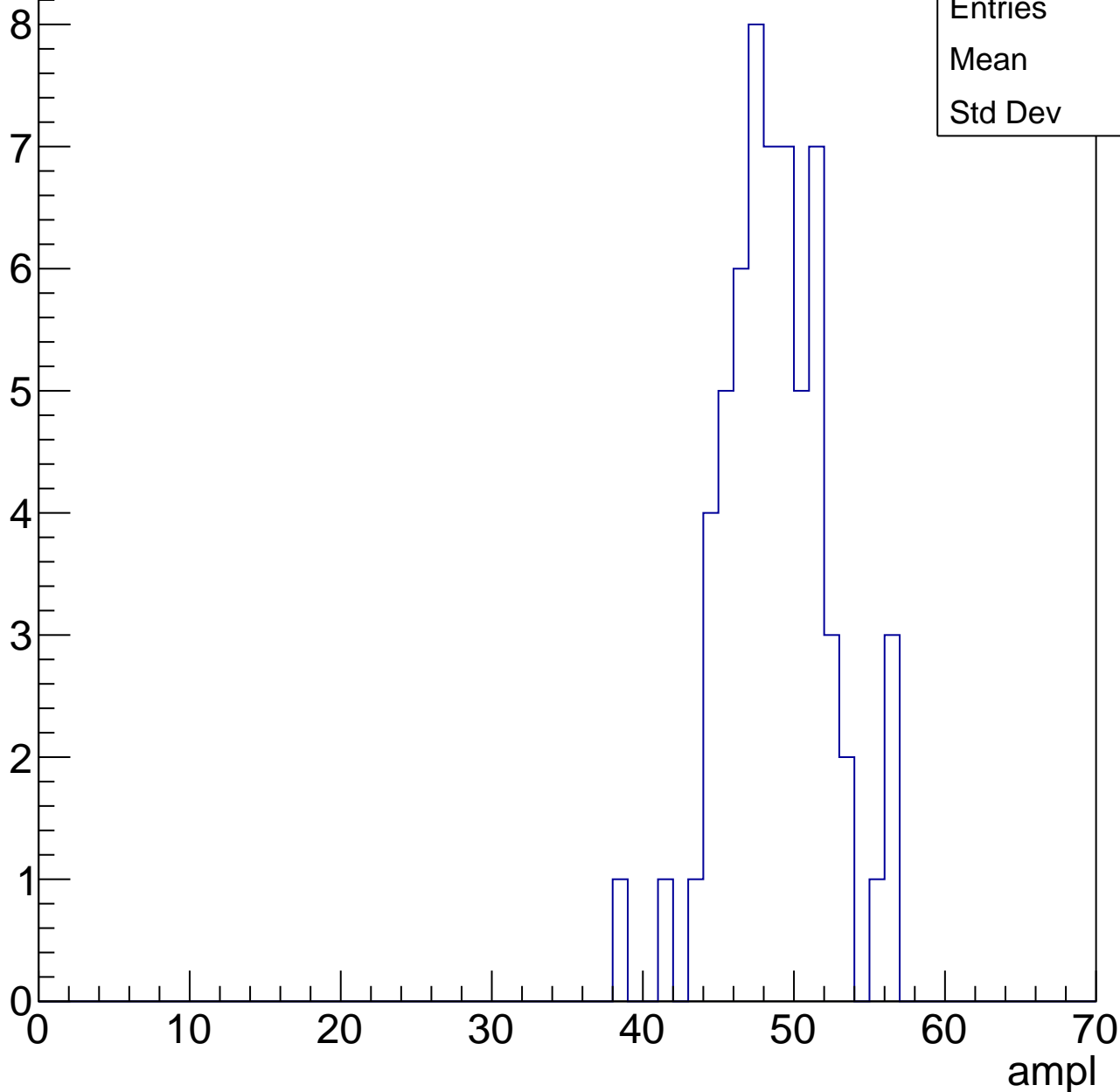


B1L101S, U6-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

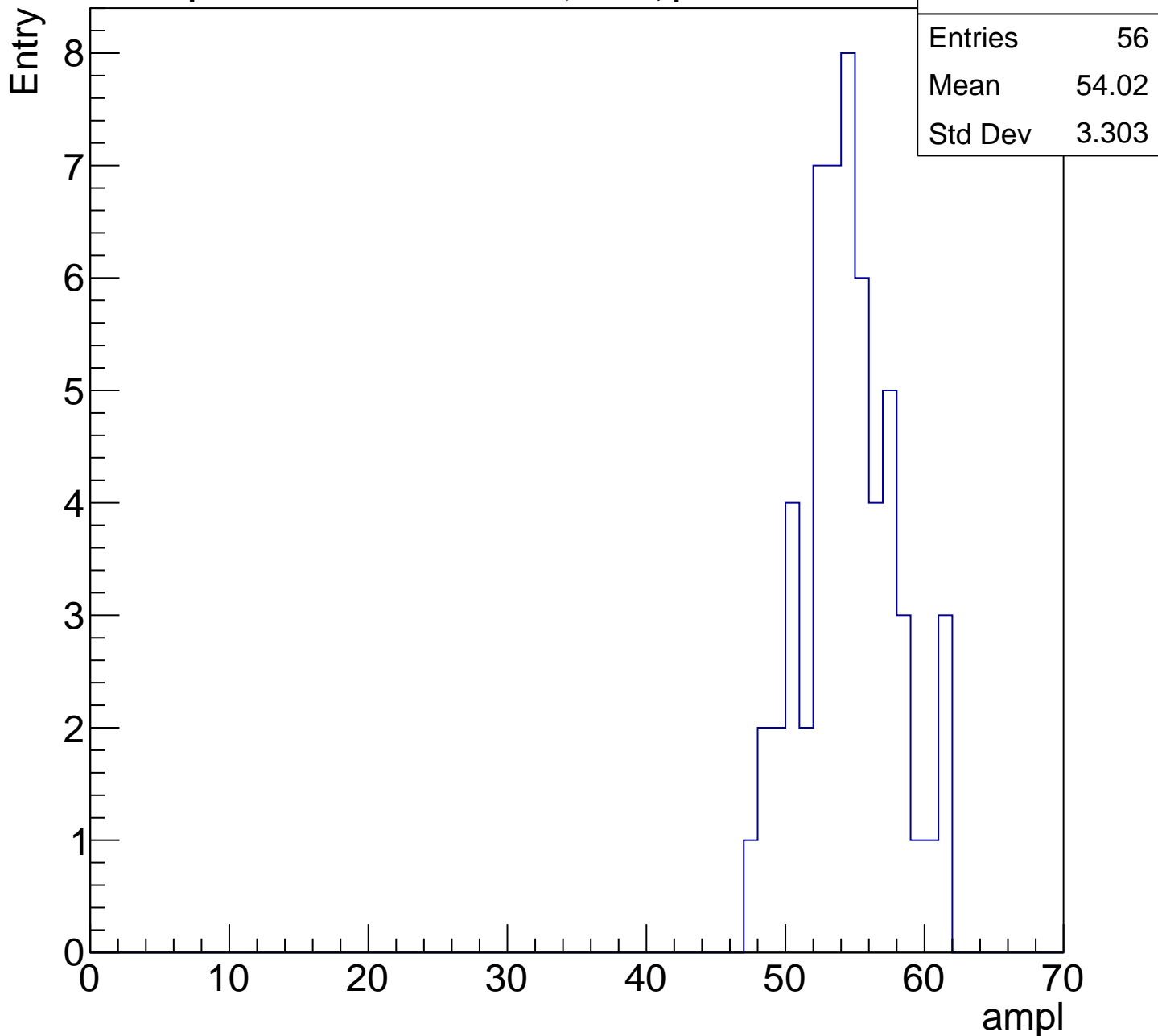
Entry

Entries	61
Mean	48.3
Std Dev	3.48



B1L101S, U6-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch26, adc5

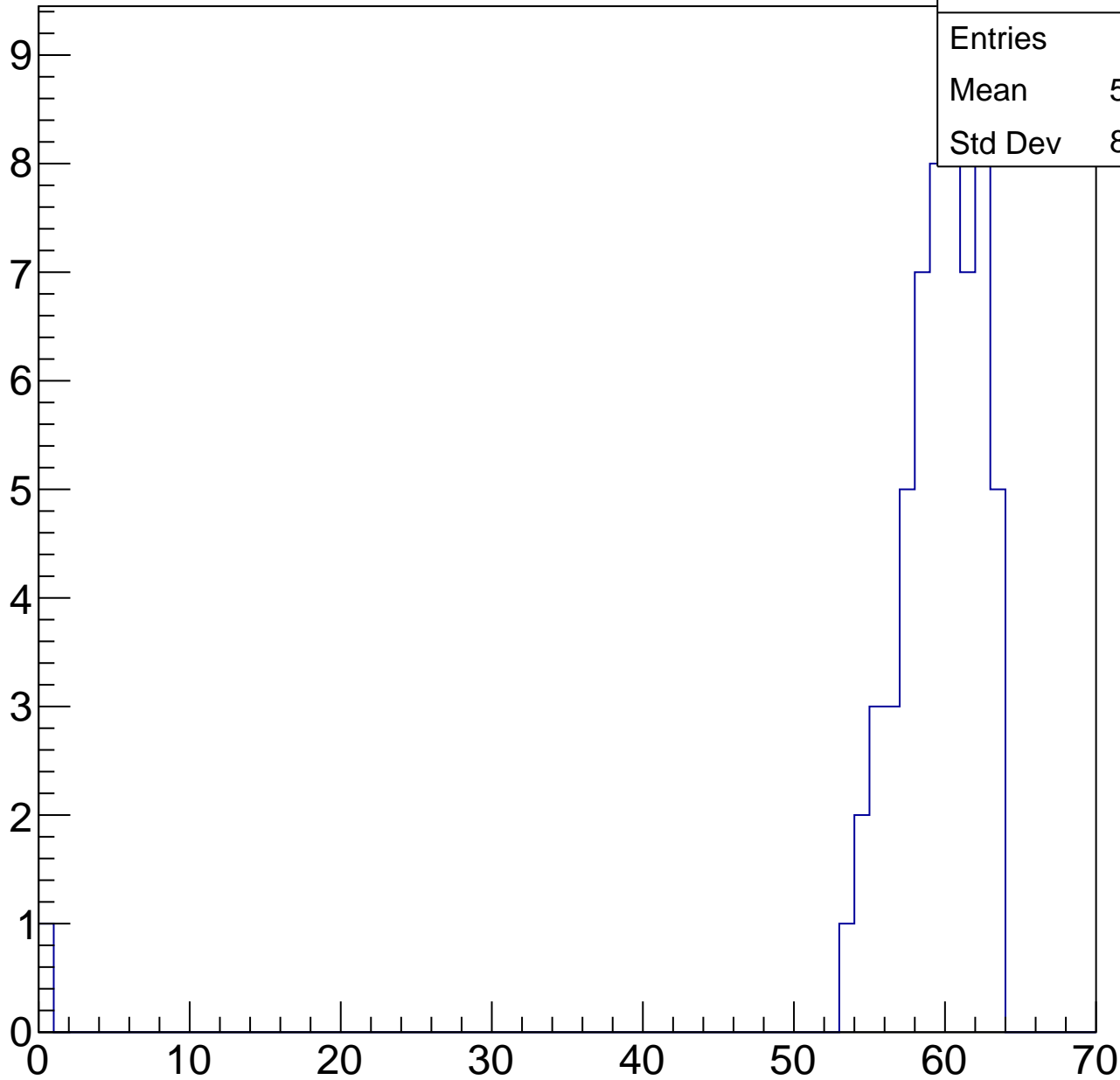
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.22
Std Dev	8.049

ampl

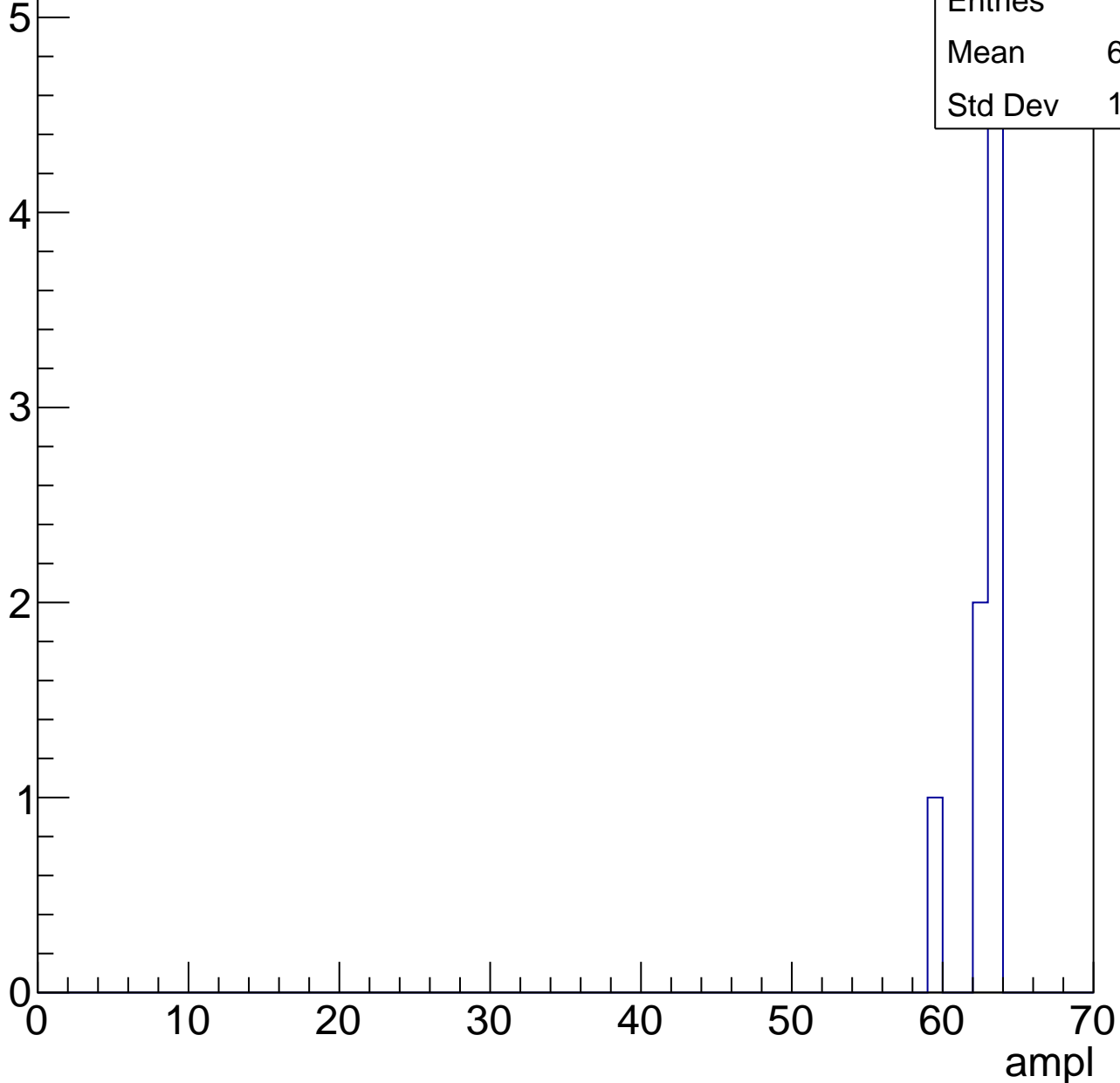


B1L101S, U6-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.25
Std Dev	1.299



B1L101S, U6-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch27, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	29.85
Std Dev	4.984

Gaus mean : 30.1426

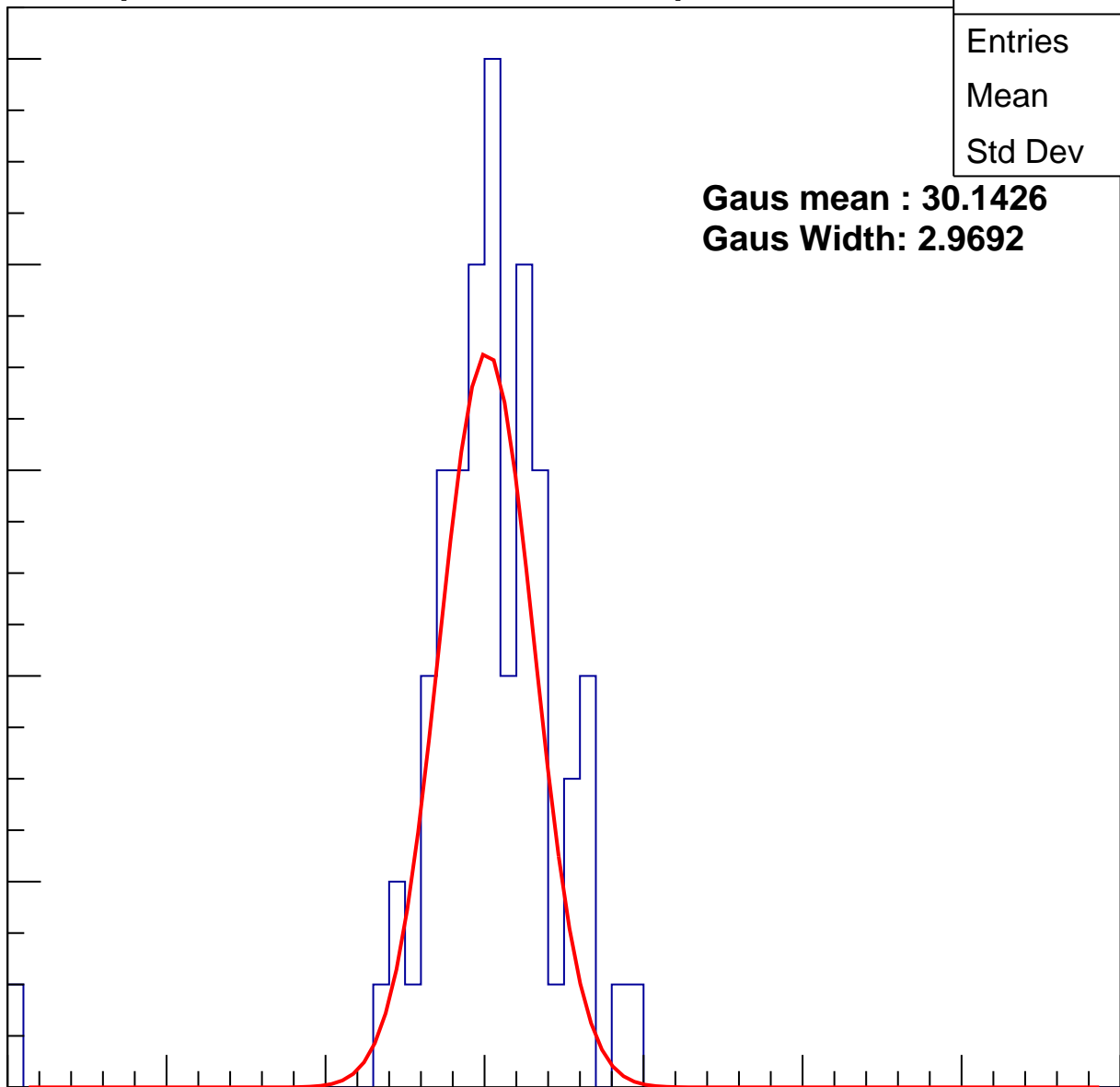
Gaus Width: 2.9692

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch27, adc1

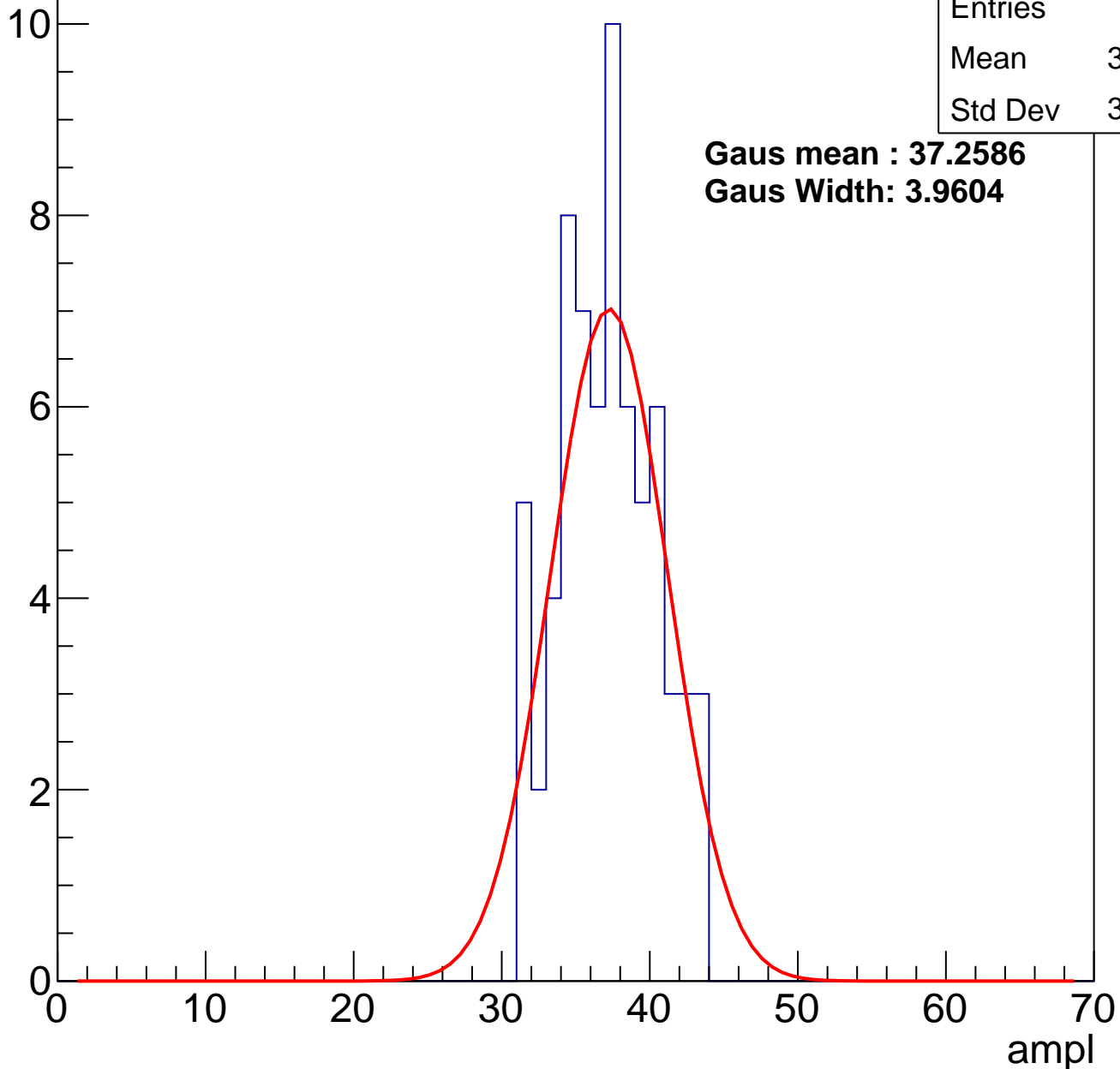
calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	36.69
Std Dev	3.219

Gaus mean : 37.2586

Gaus Width: 3.9604

Entry



B1L101S, U6-ch27, adc2

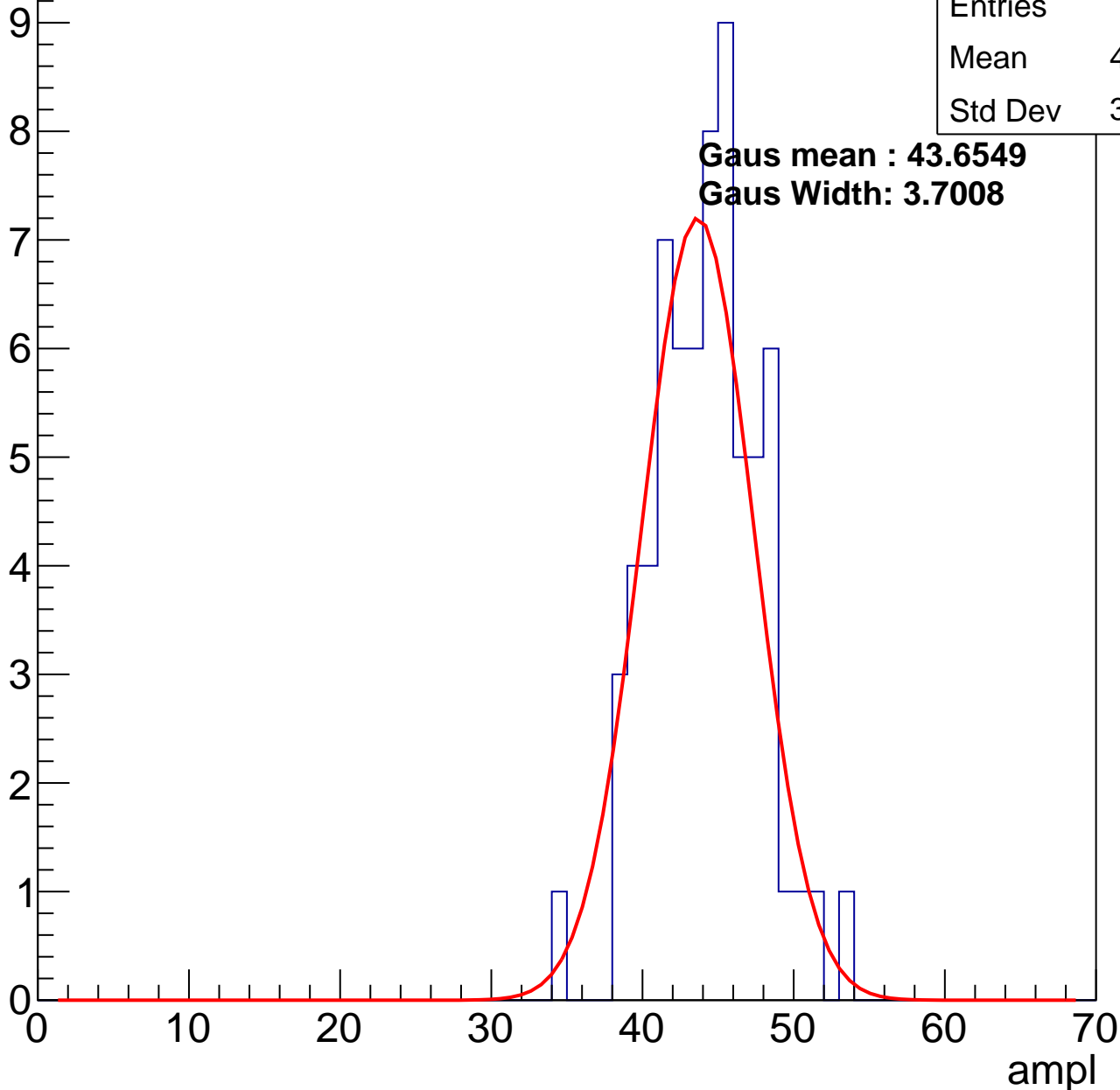
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.74
Std Dev	3.475

Gaus mean : 43.6549

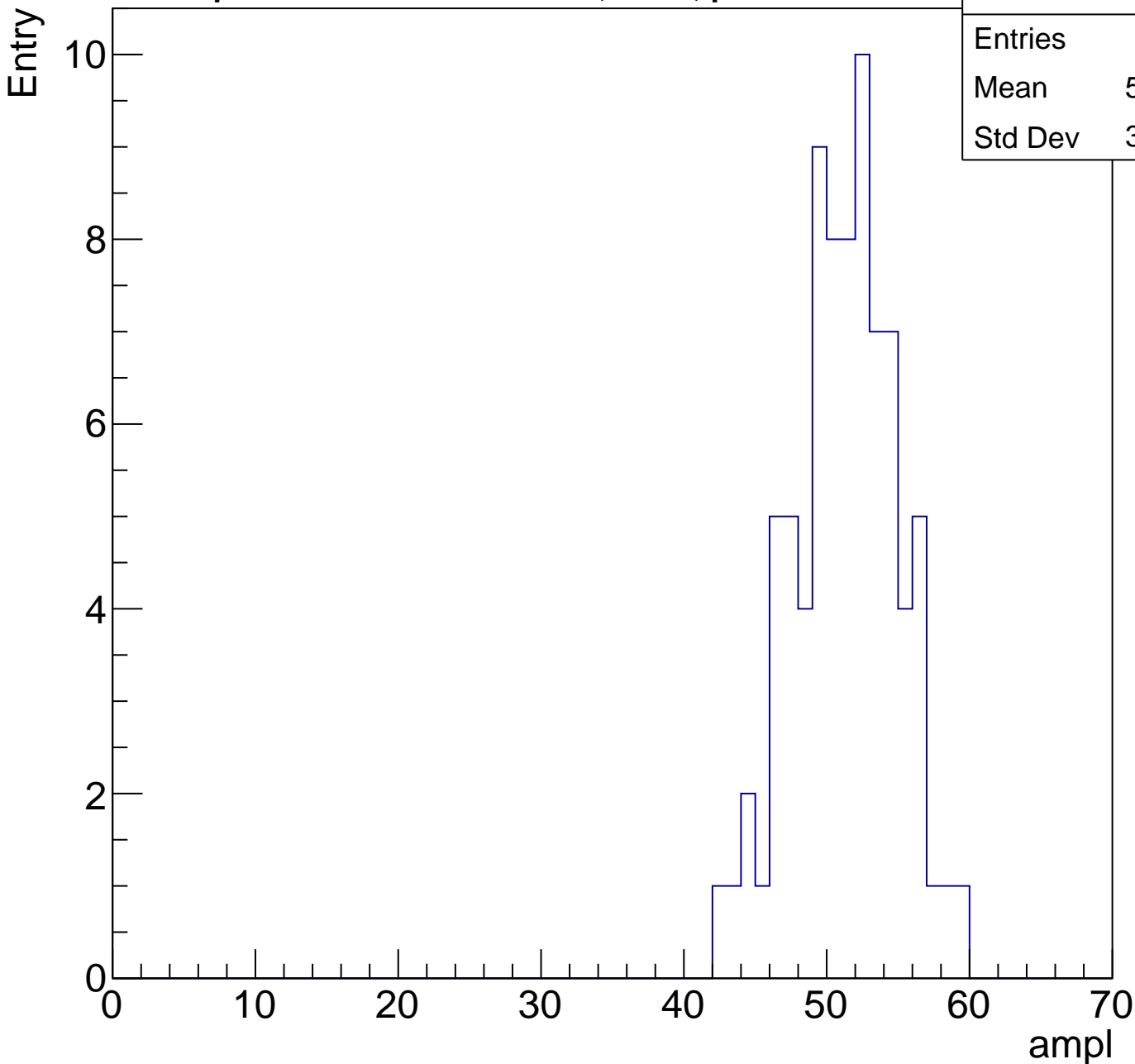
Gaus Width: 3.7008



B1L101S, U6-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	50.84
Std Dev	3.537

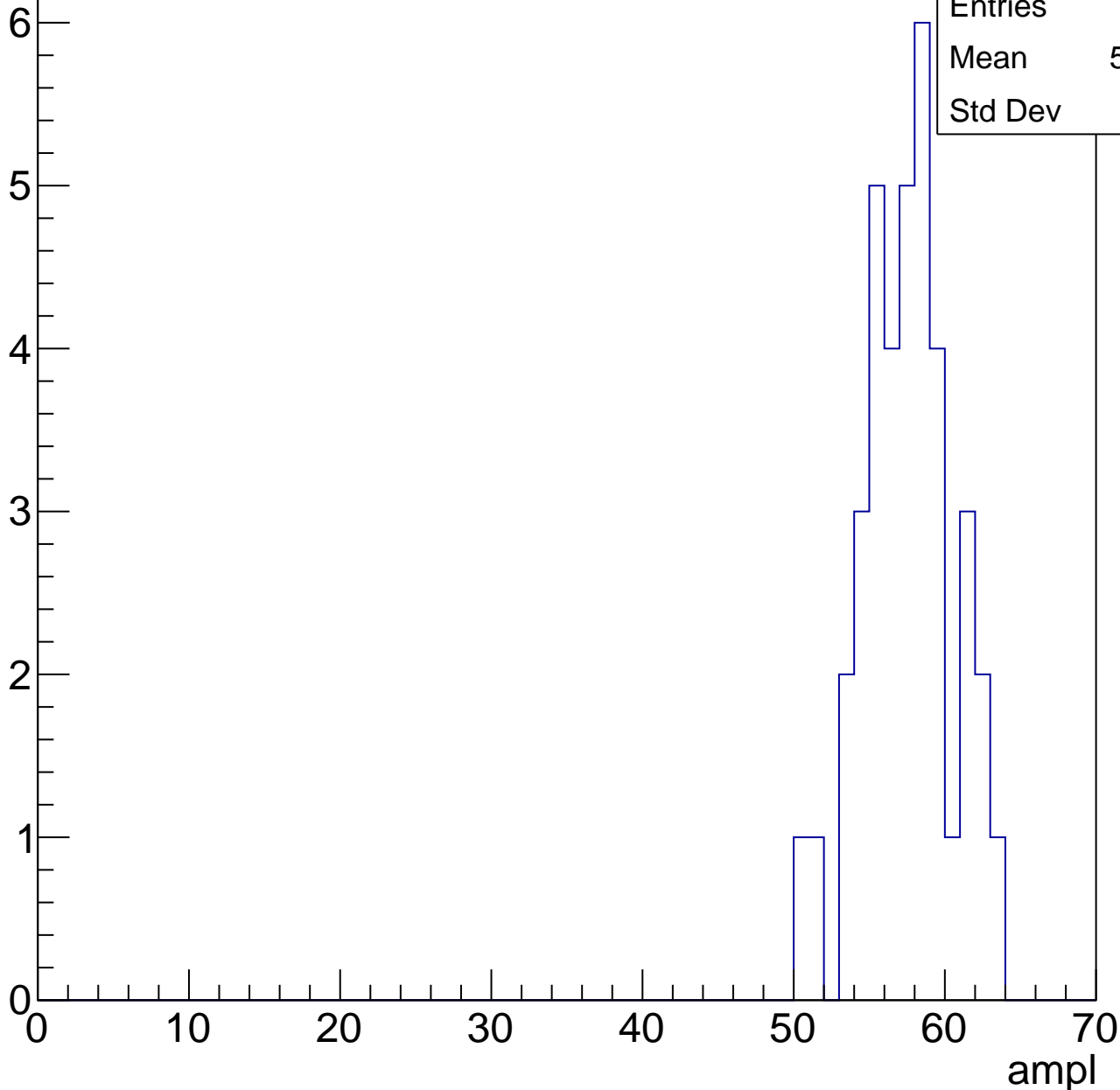


B1L101S, U6-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

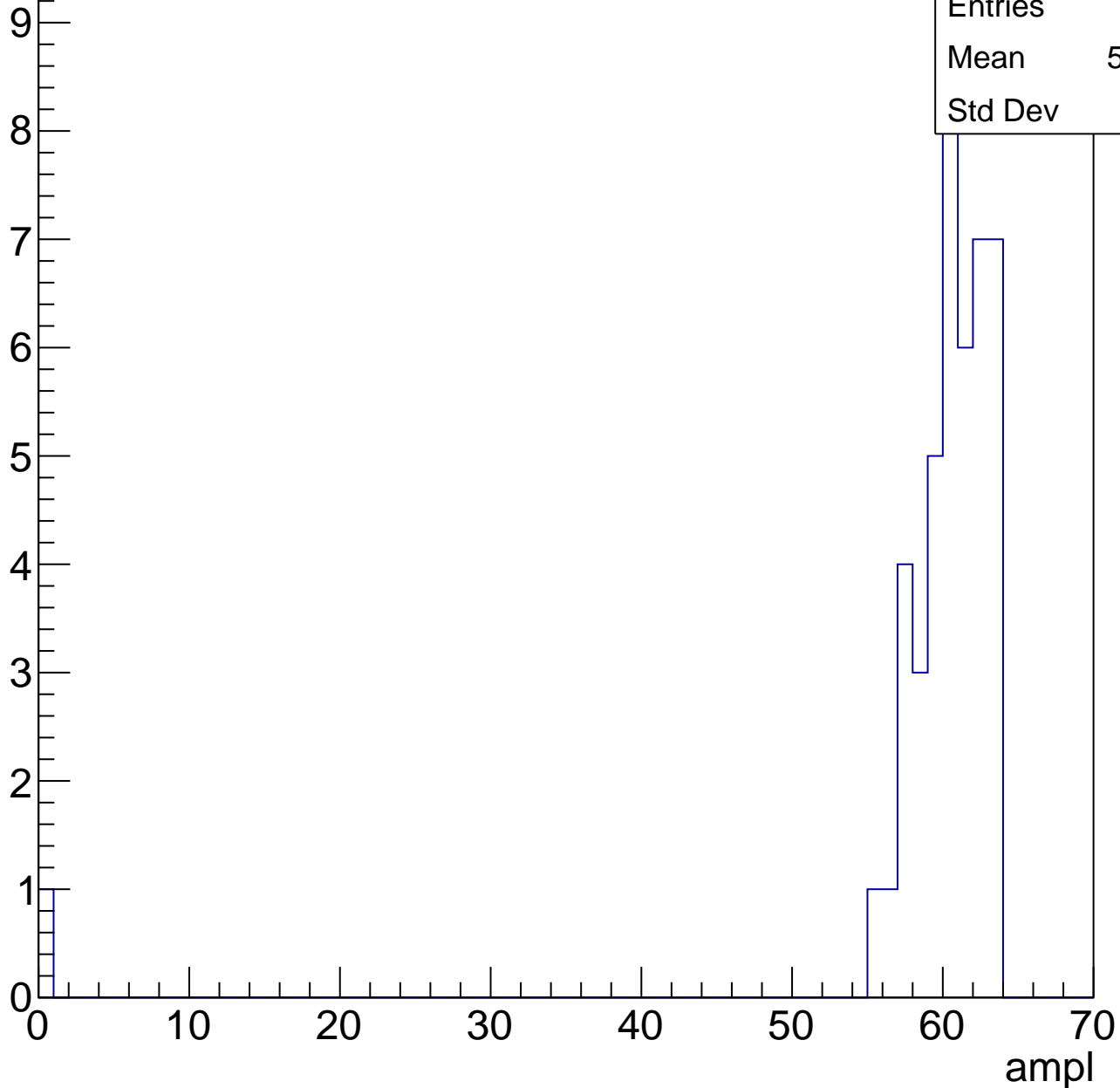
Entries	38
Mean	57.03
Std Dev	2.96



B1L101S, U6-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch28, adc0

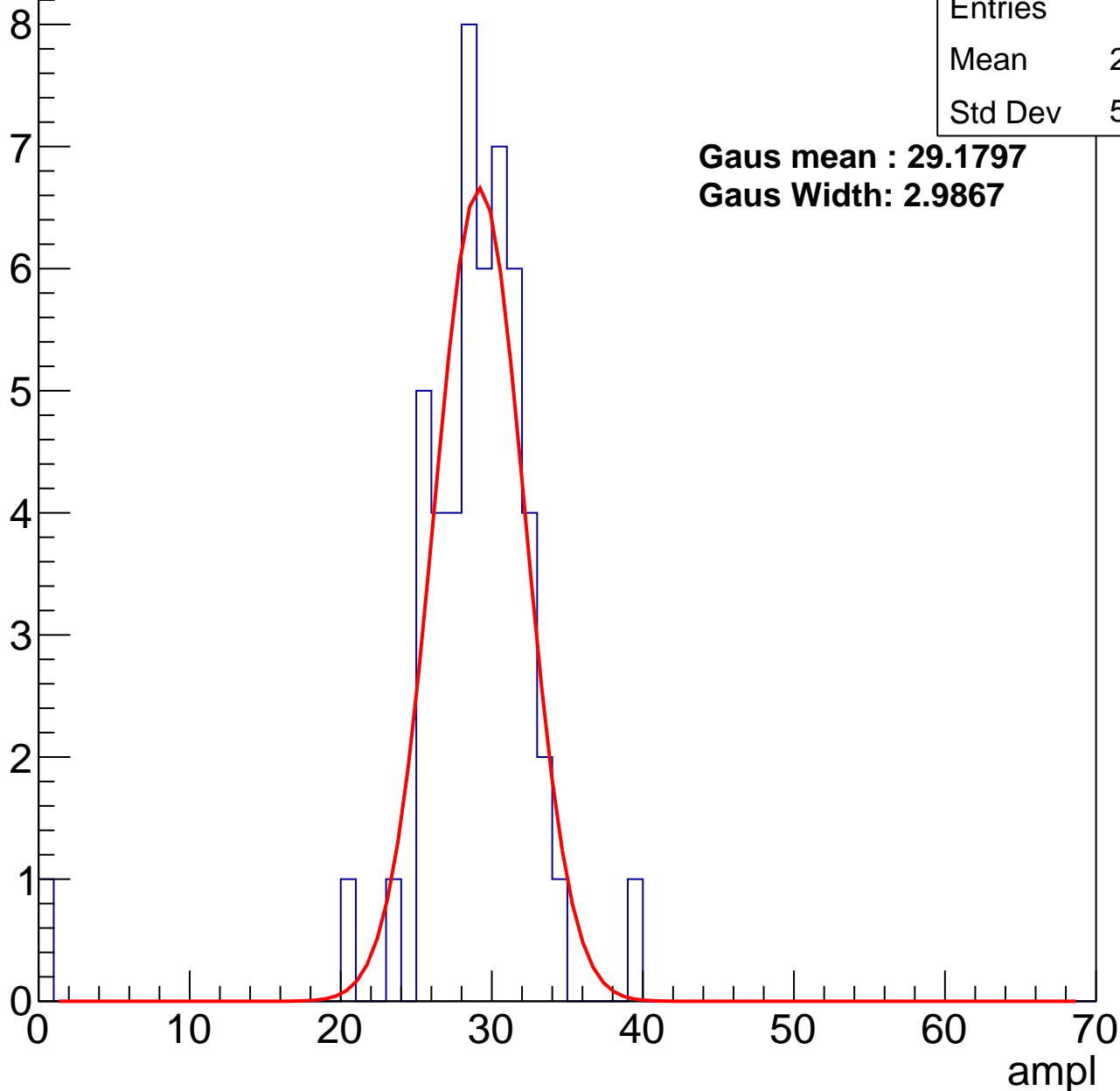
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	28.25
Std Dev	5.037

Gaus mean : 29.1797

Gaus Width: 2.9867



B1L101S, U6-ch28, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	34.78
Std Dev	3.071

Gaus mean : 35.1936

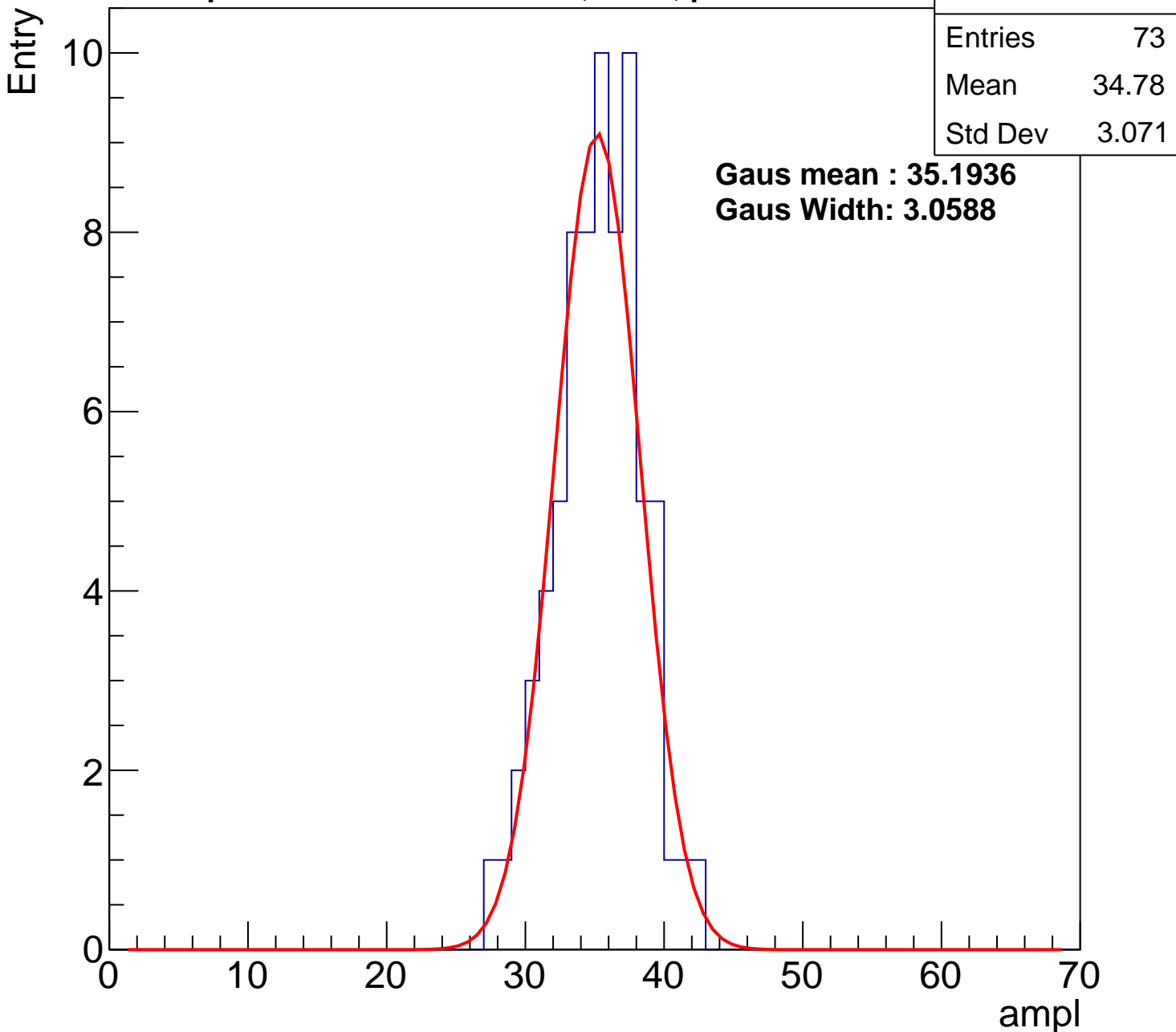
Gaus Width: 3.0588

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch28, adc2

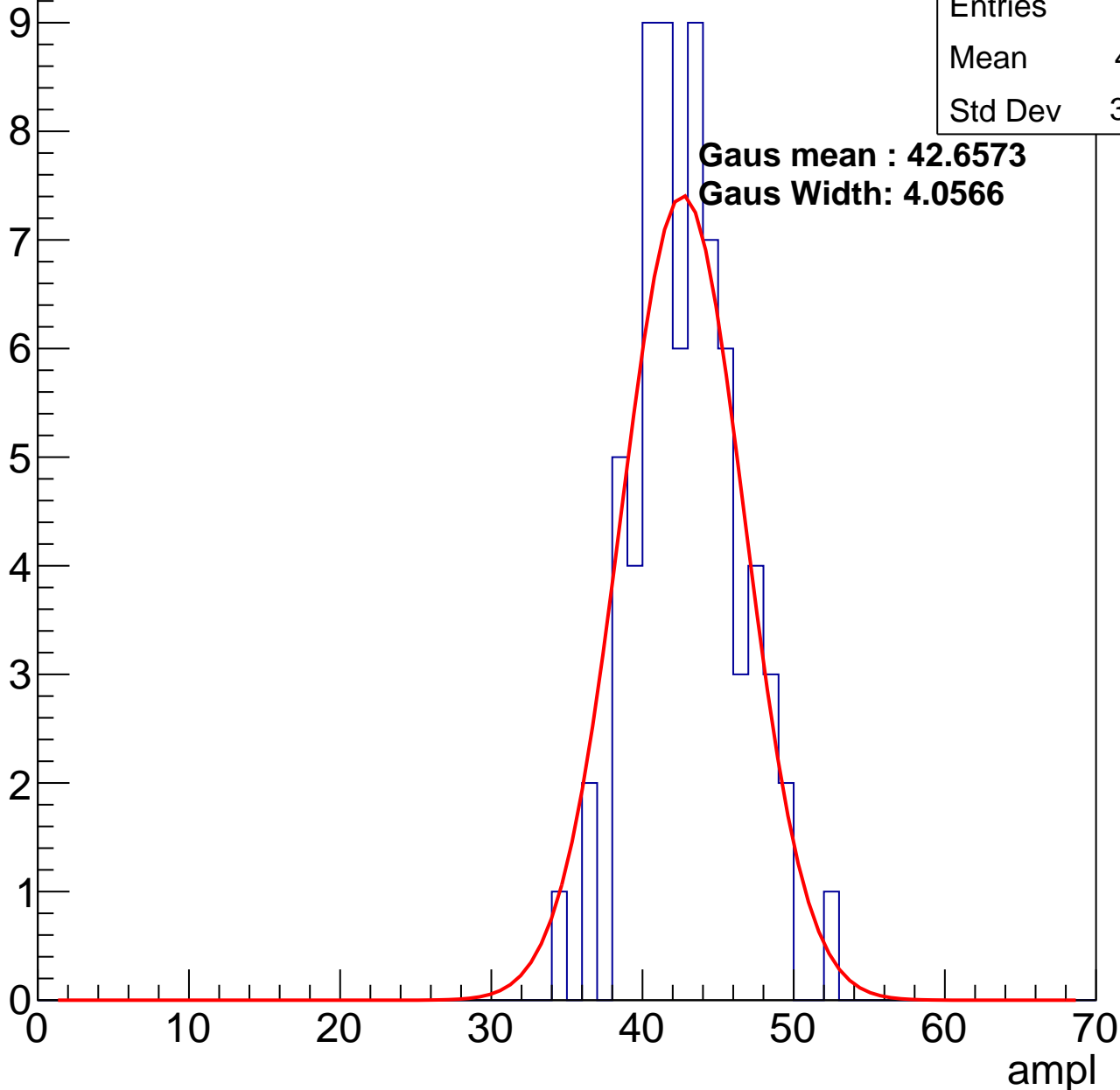
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.51
Std Dev	3.406

Gaus mean : 42.6573

Gaus Width: 4.0566

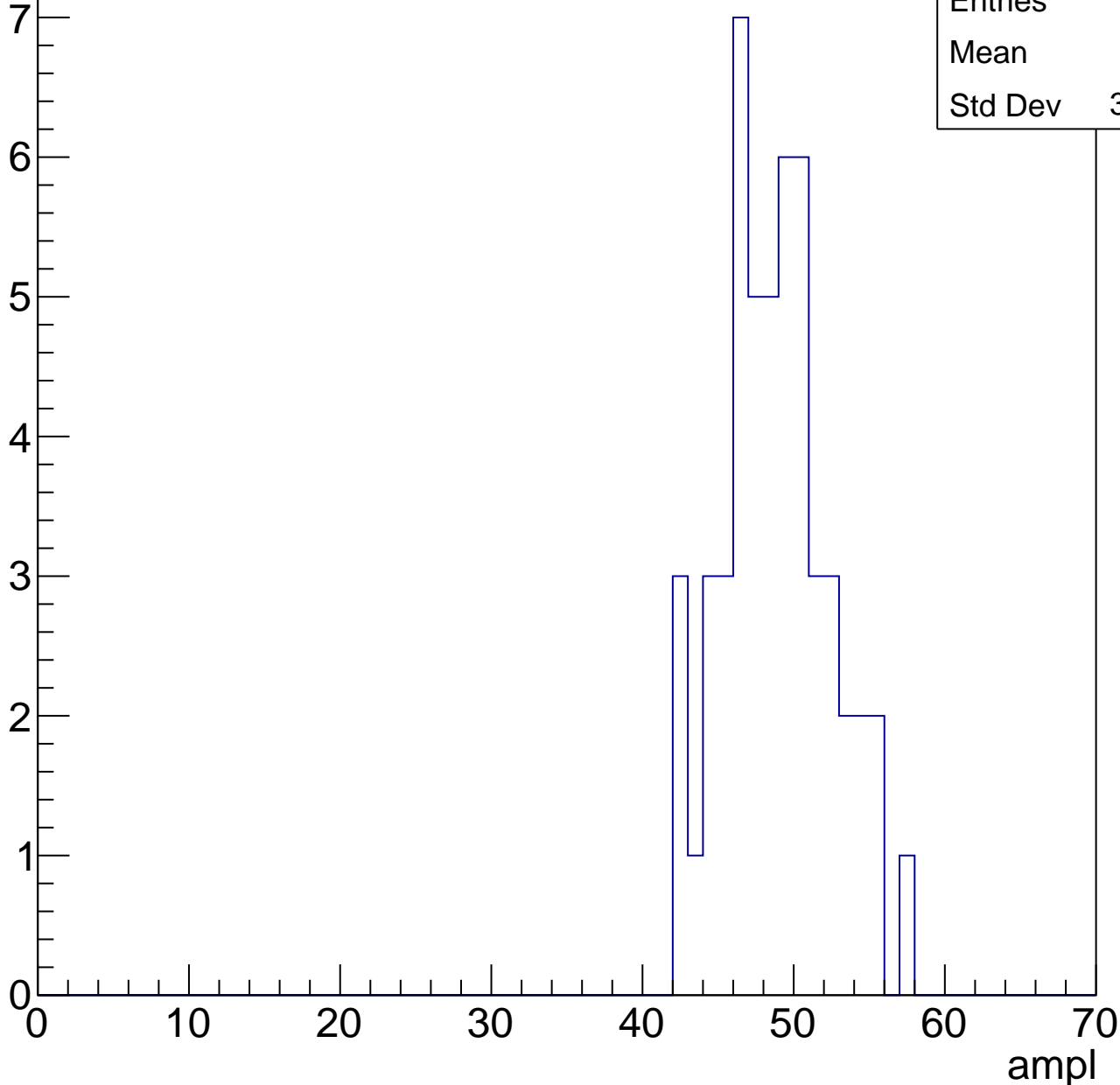


B1L101S, U6-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.4
Std Dev	3.504

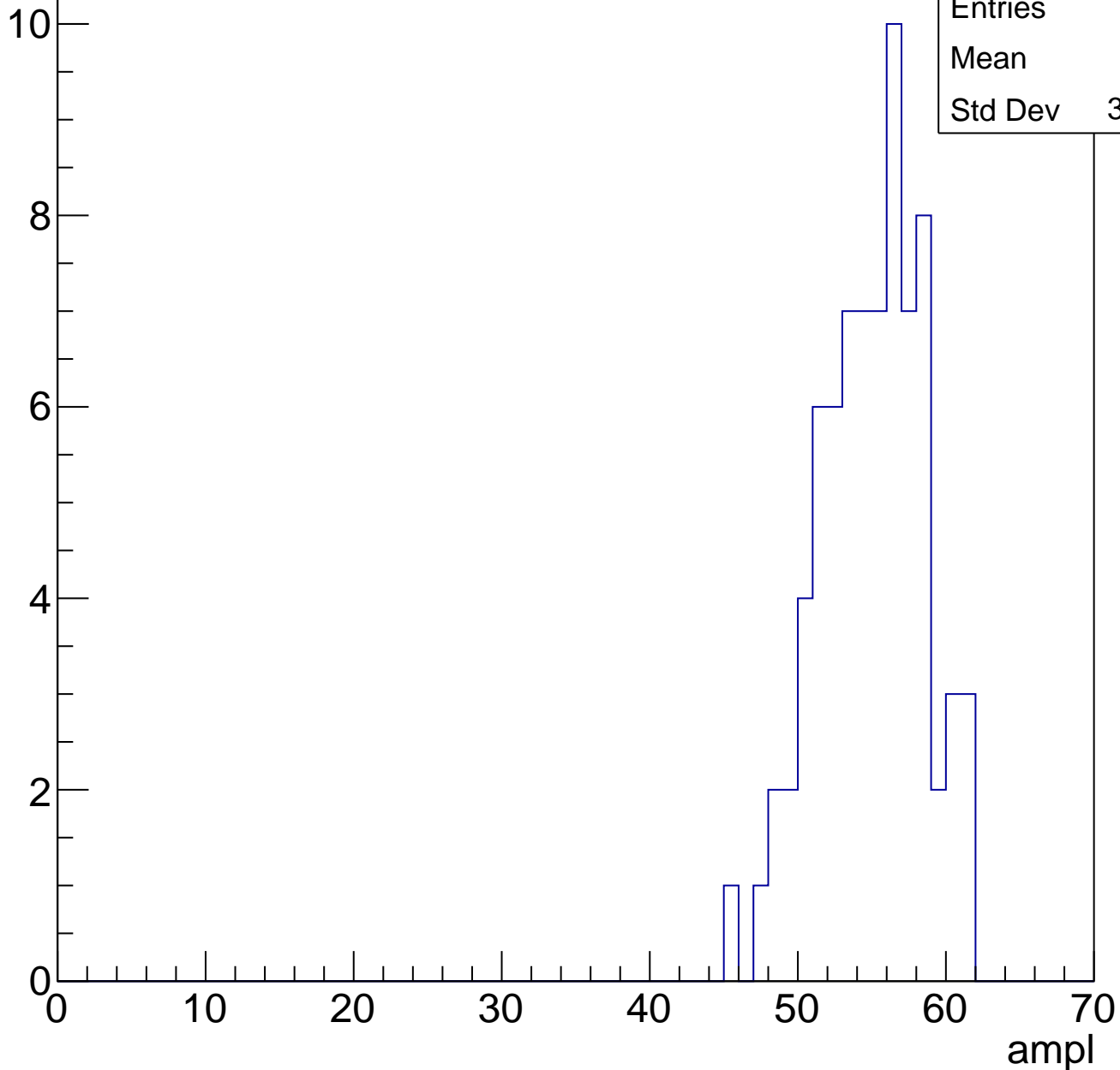


B1L101S, U6-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	54.5
Std Dev	3.489

Entry



B1L101S, U6-ch28, adc5

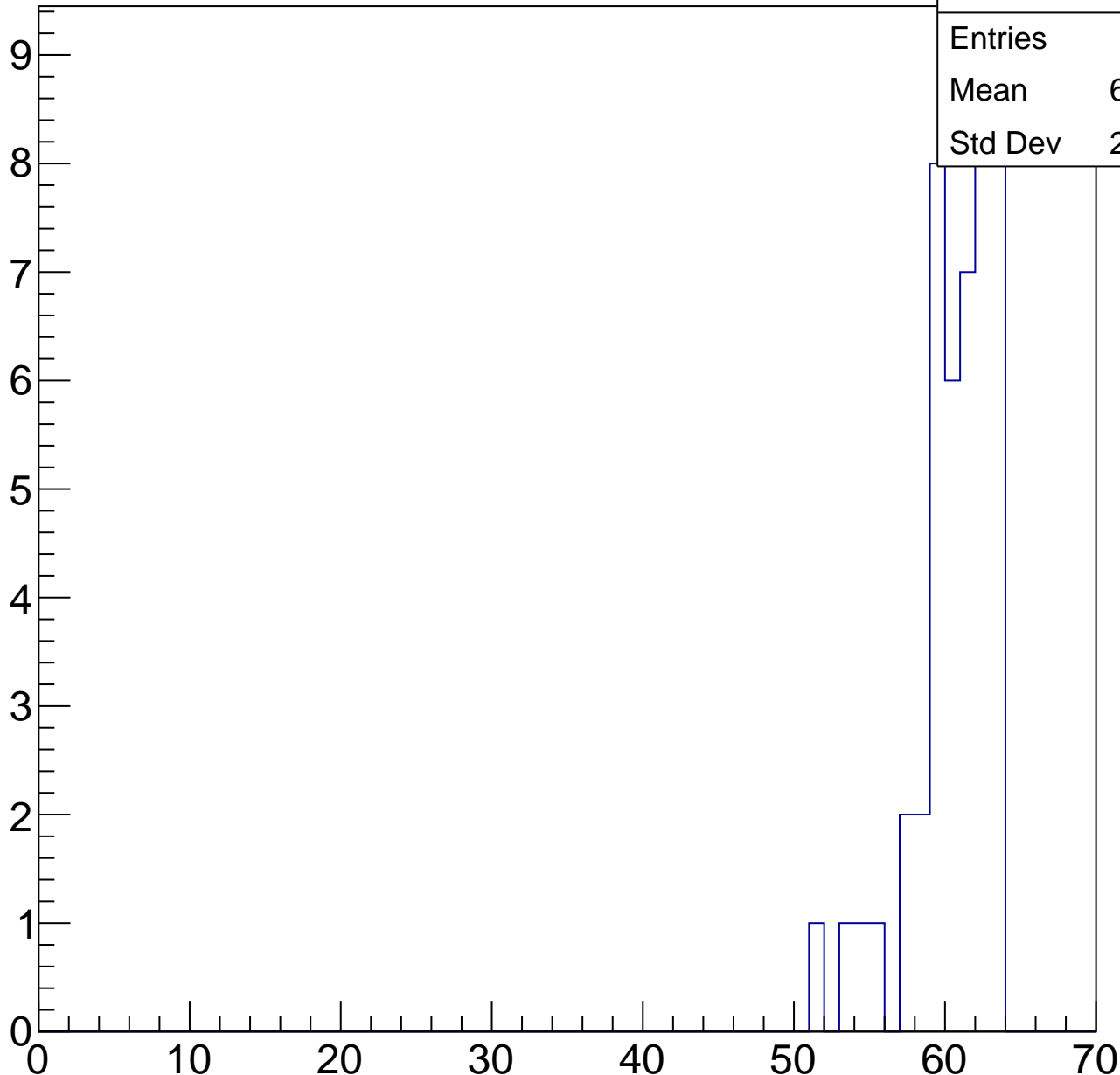
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.09
Std Dev	2.717

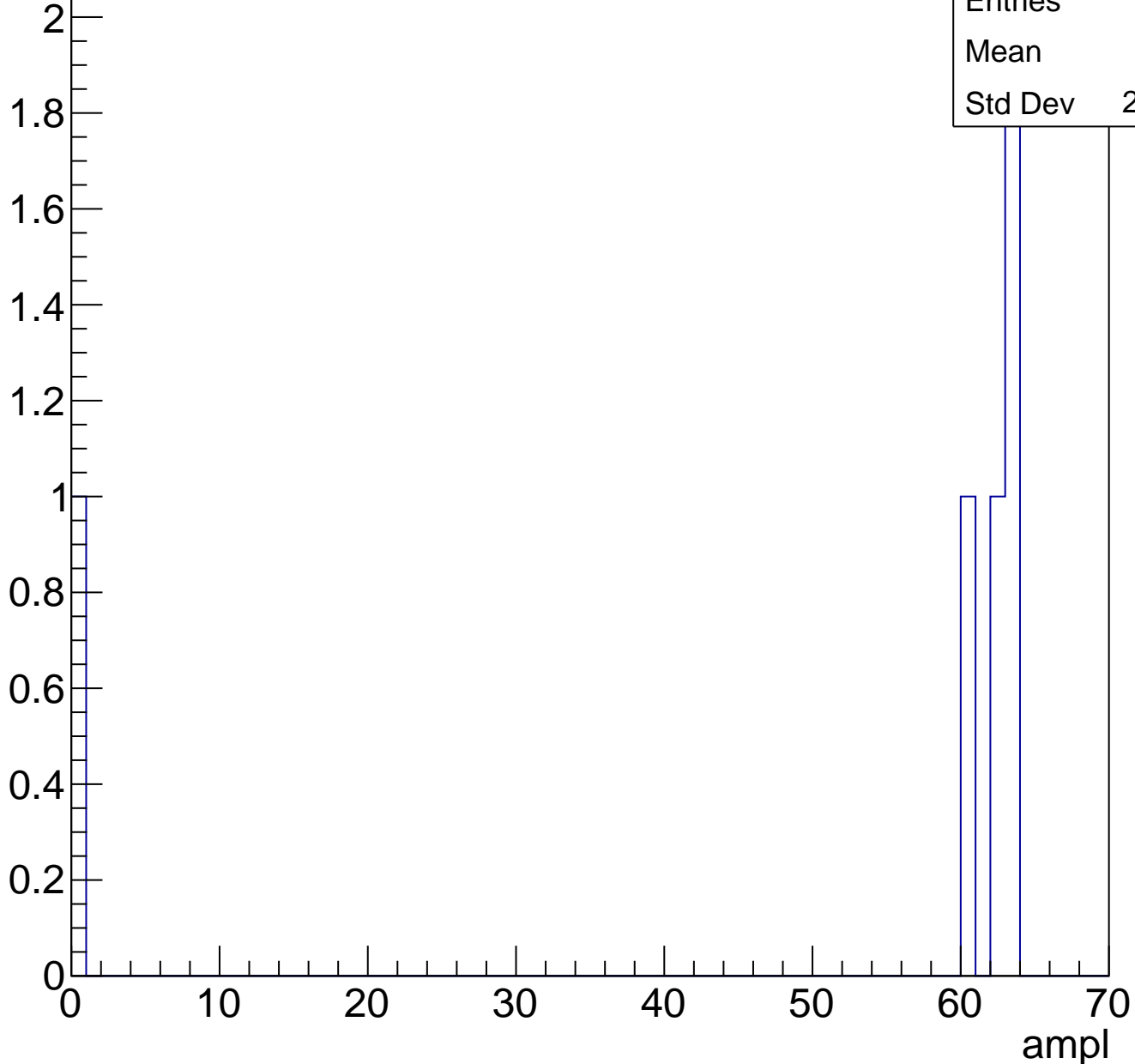
ampl



B1L101S, U6-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch29, adc0

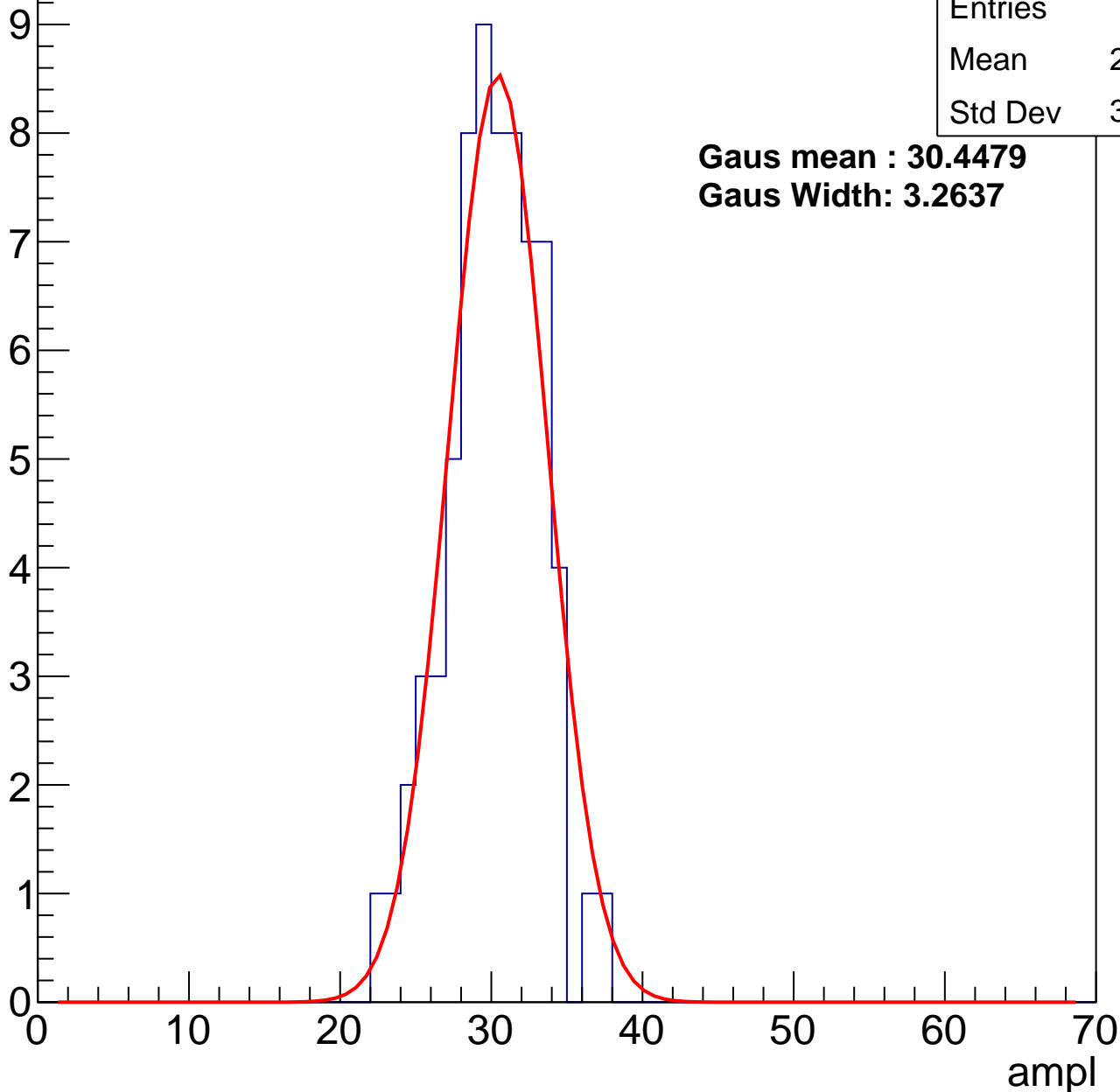
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.68
Std Dev	3.056

Gaus mean : 30.4479

Gaus Width: 3.2637



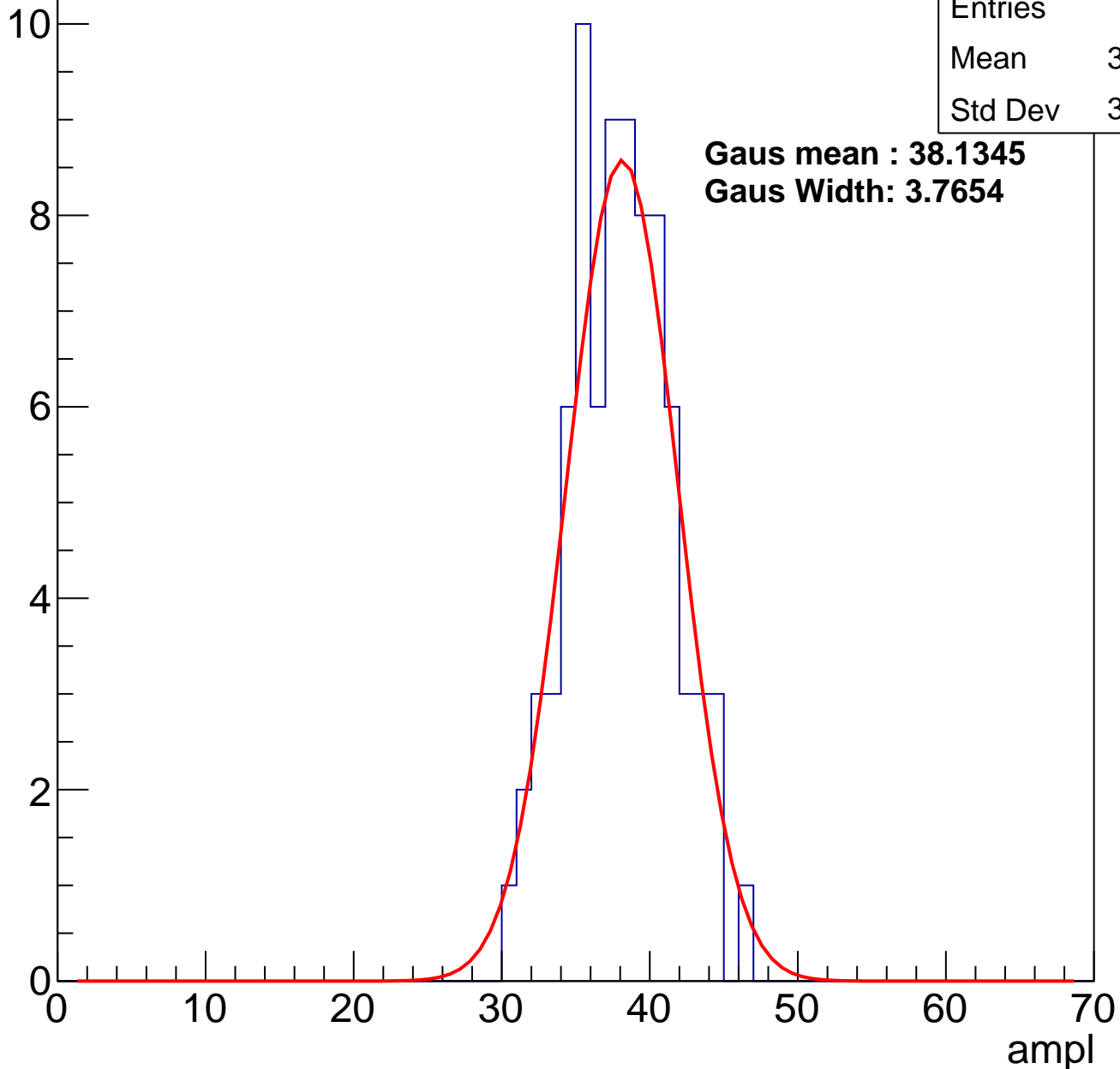
B1L101S, U6-ch29, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	37.57
Std Dev	3.403

Gaus mean : 38.1345
Gaus Width: 3.7654

Entry

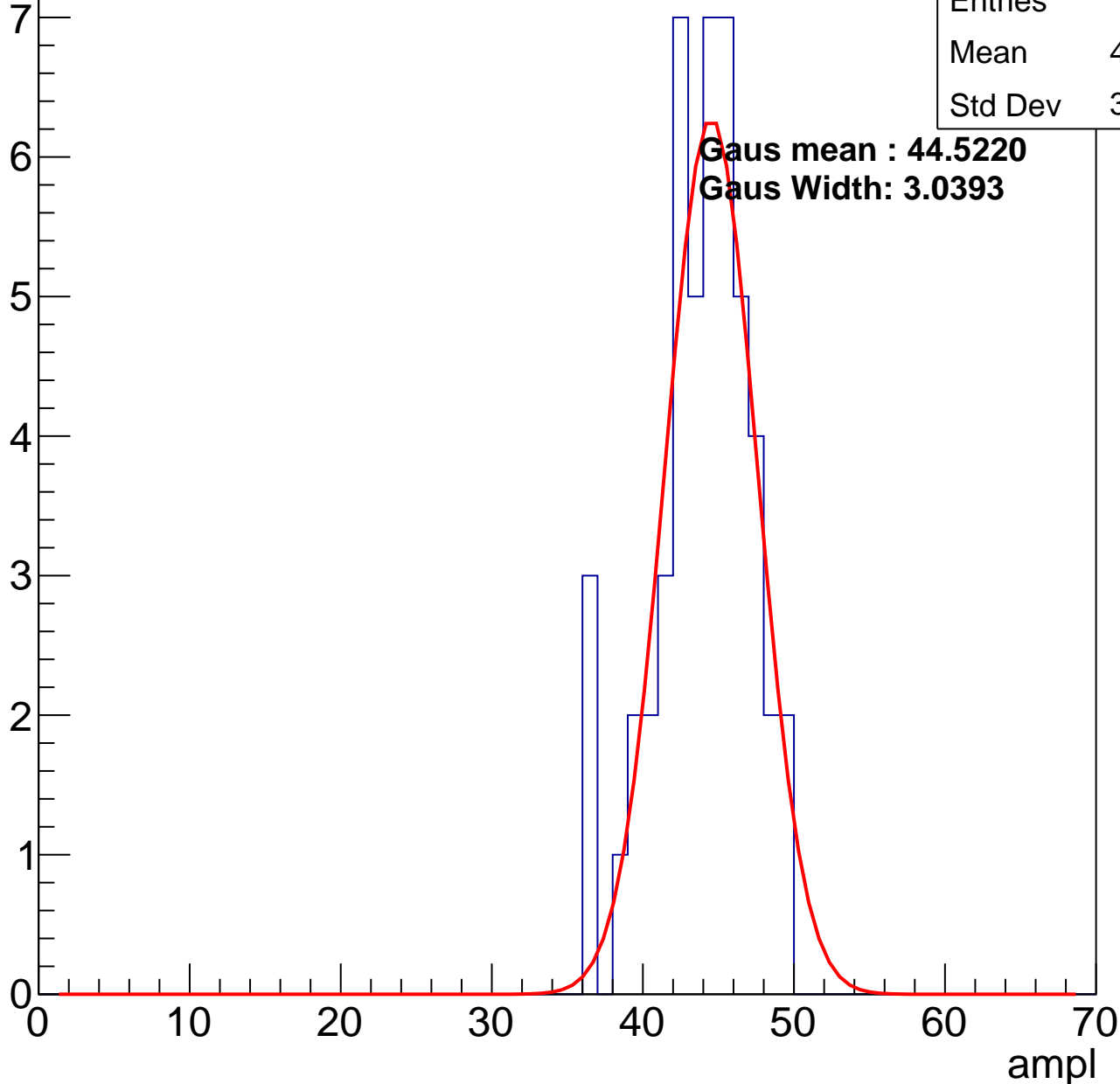


B1L101S, U6-ch29, adc2

calib_packv5_042523_0143.root, FC#0, port D2

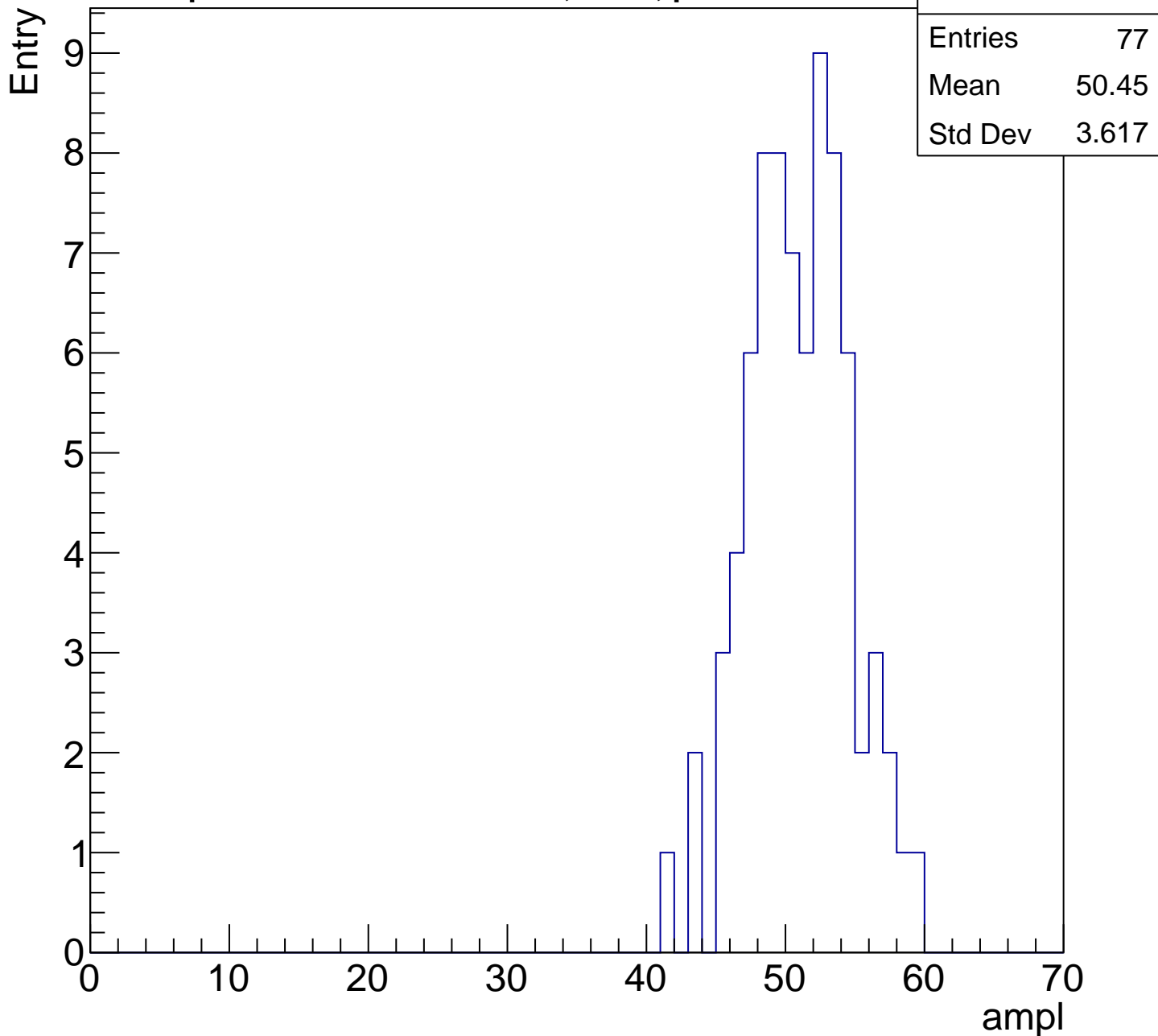
Entry

Entries	50
Mean	43.42
Std Dev	3.157



B1L101S, U6-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

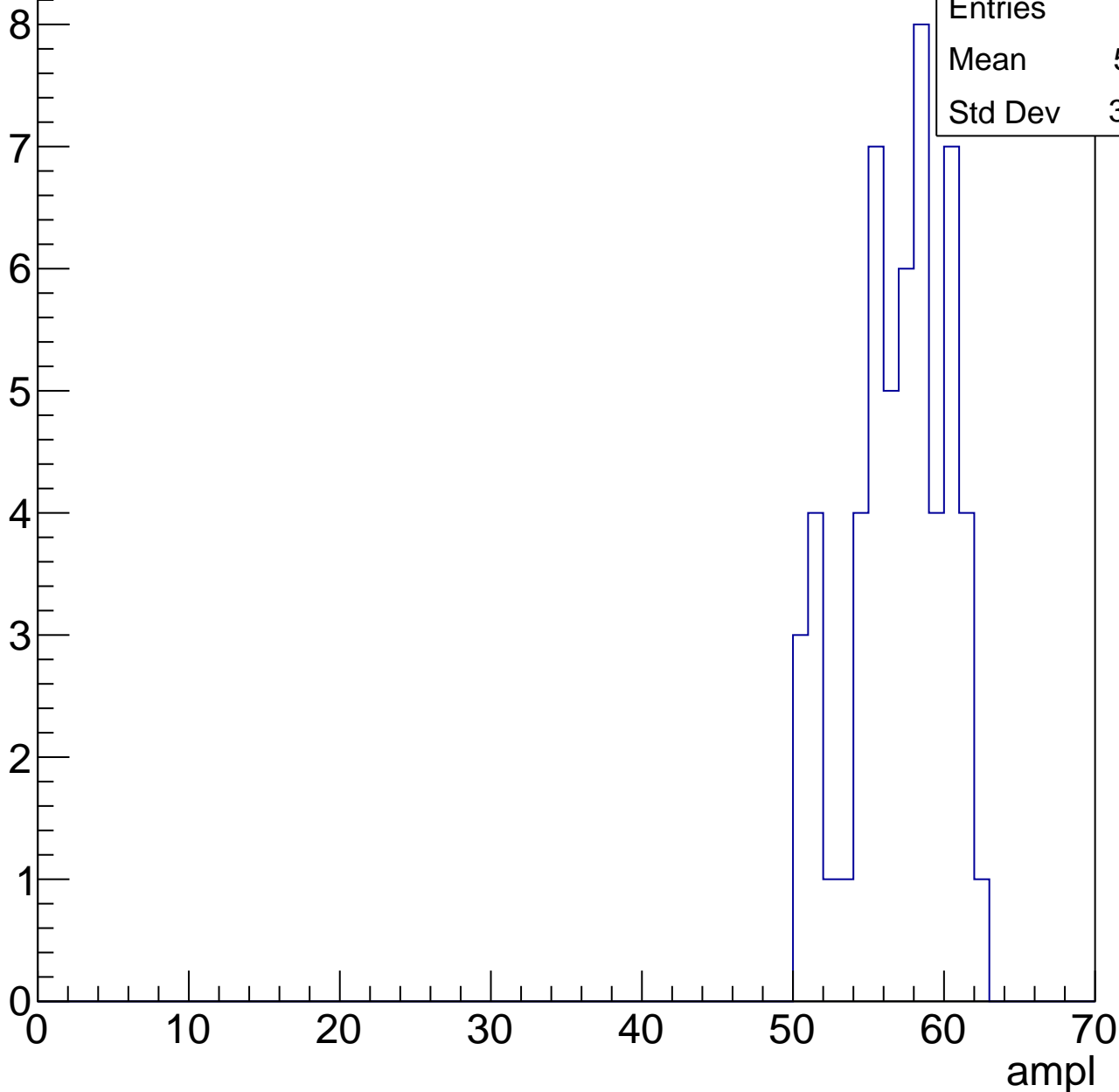


B1L101S, U6-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.51
Std Dev	3.202

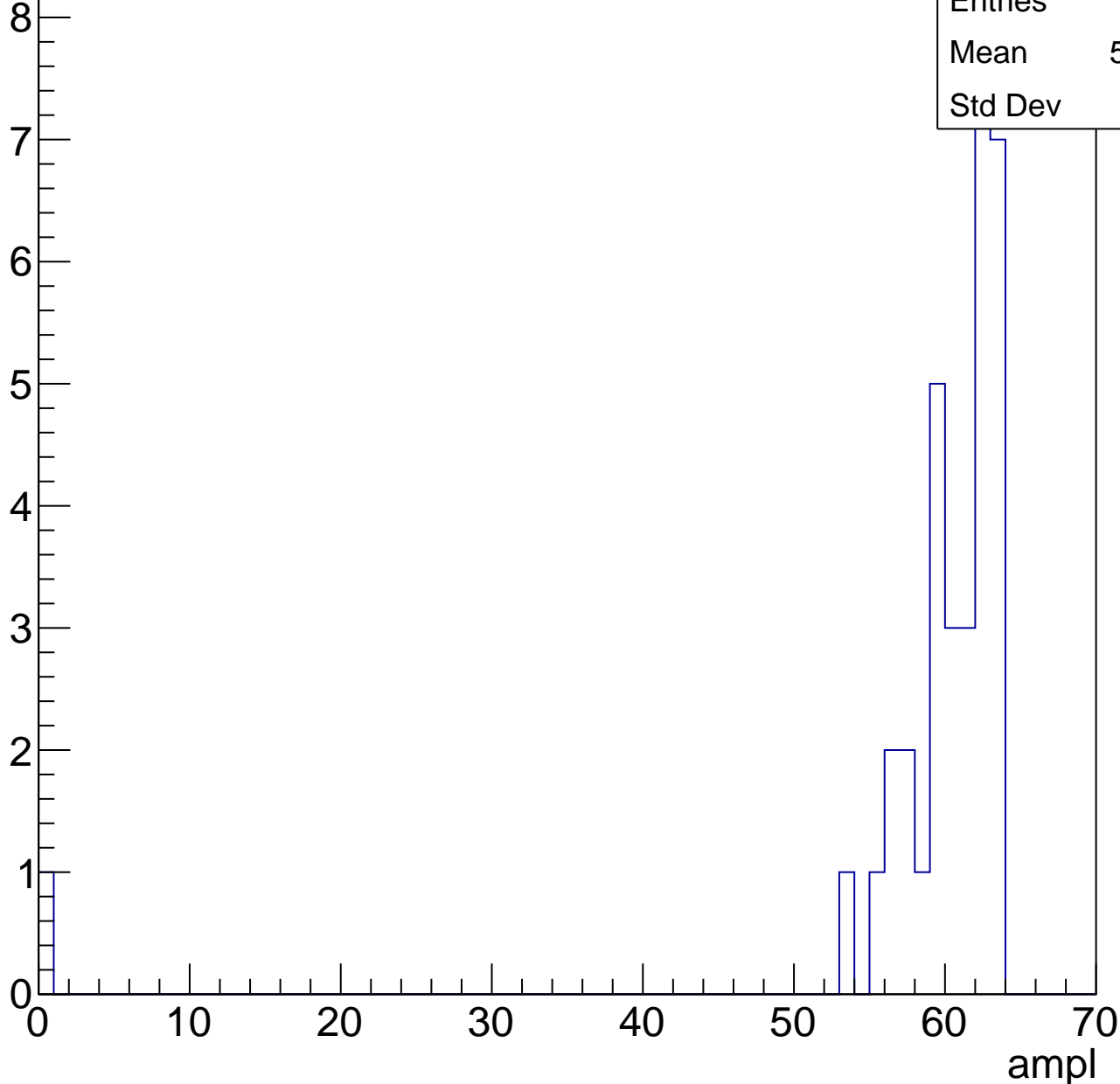


B1L101S, U6-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

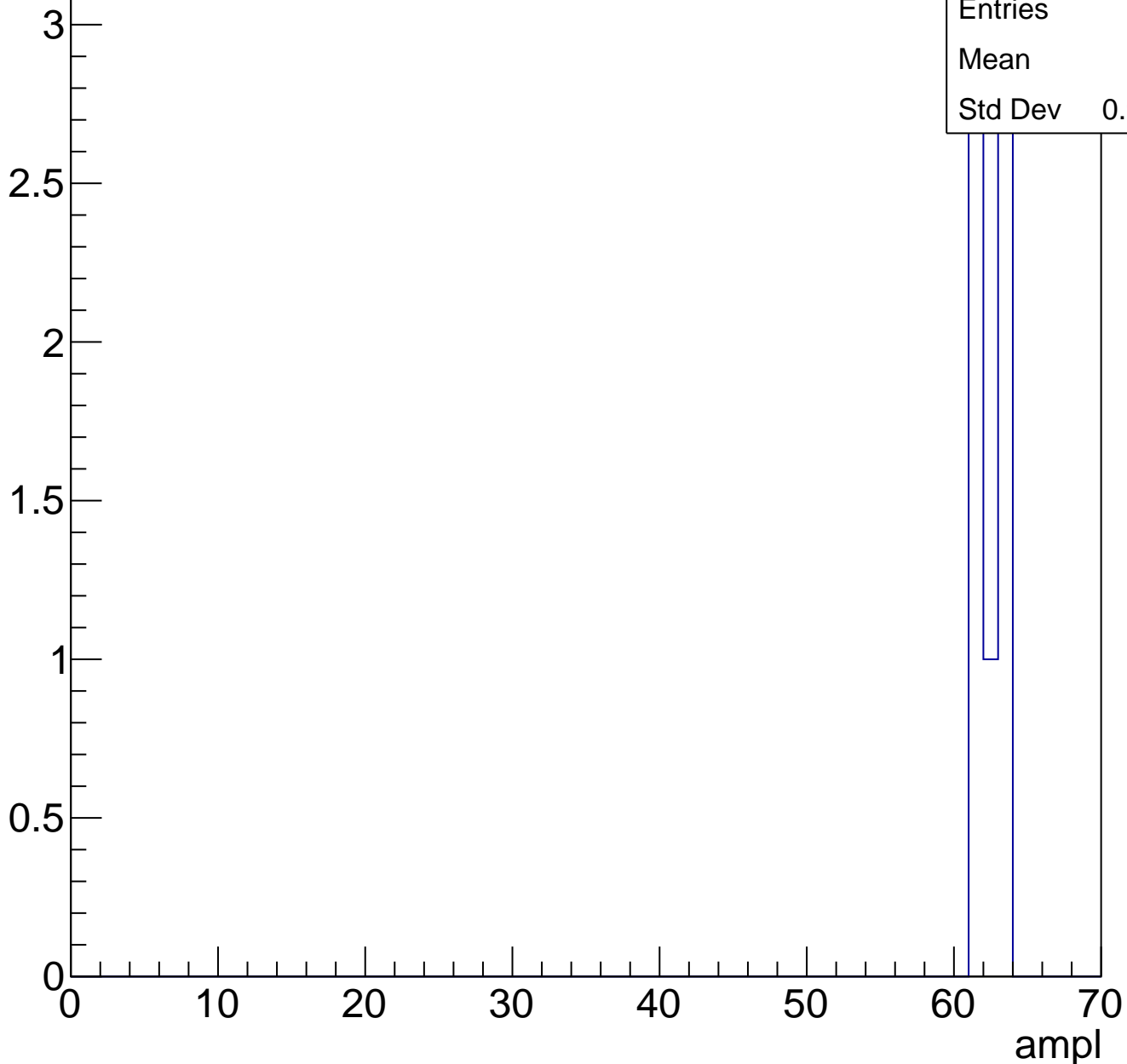
Entries	34
Mean	58.44
Std Dev	10.5



B1L101S, U6-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch30, adc0

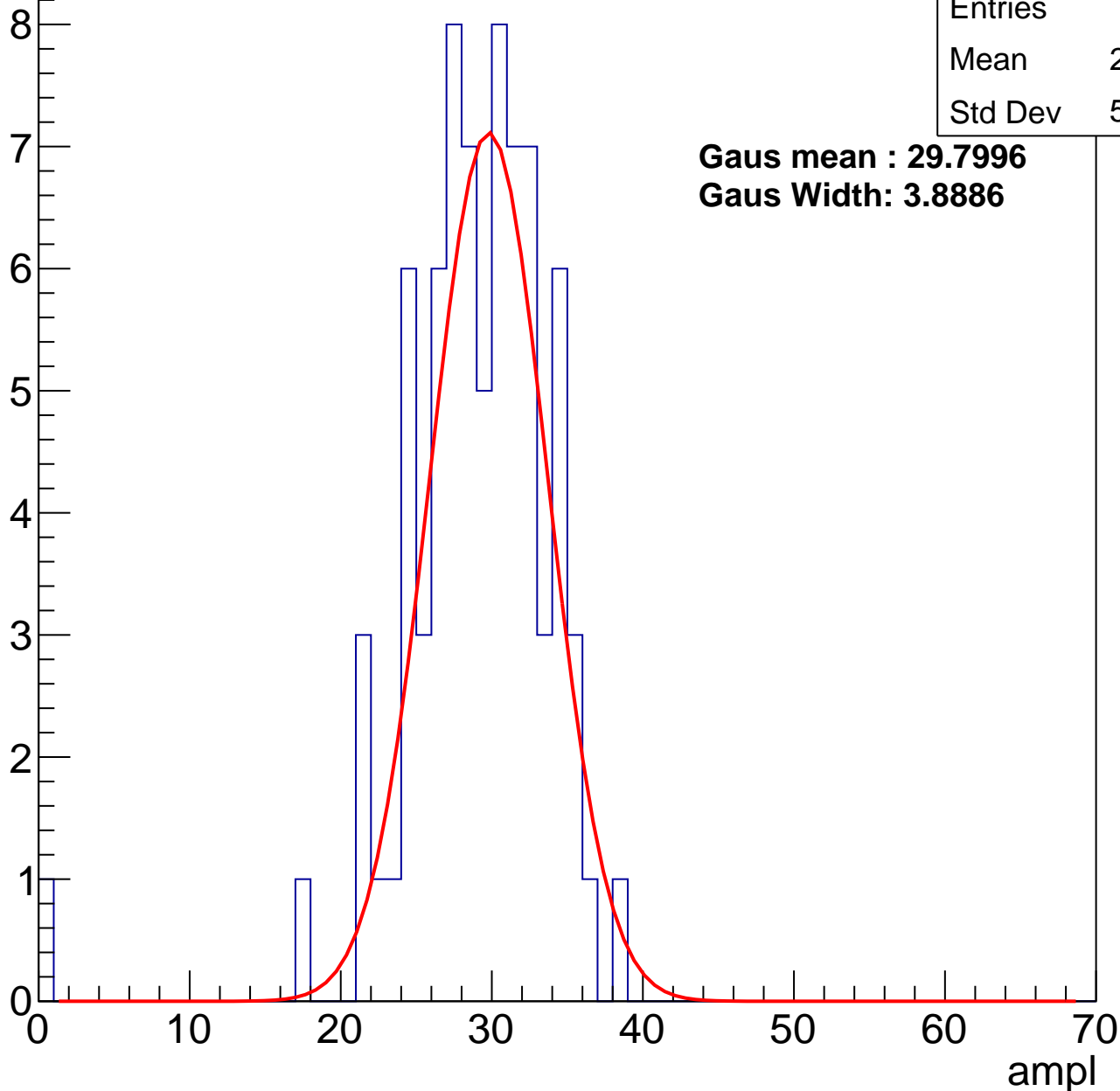
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.46
Std Dev	5.146

Gaus mean : 29.7996

Gaus Width: 3.8886



B1L101S, U6-ch30, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	36.81
Std Dev	3.746

Gaus mean : 38.3190

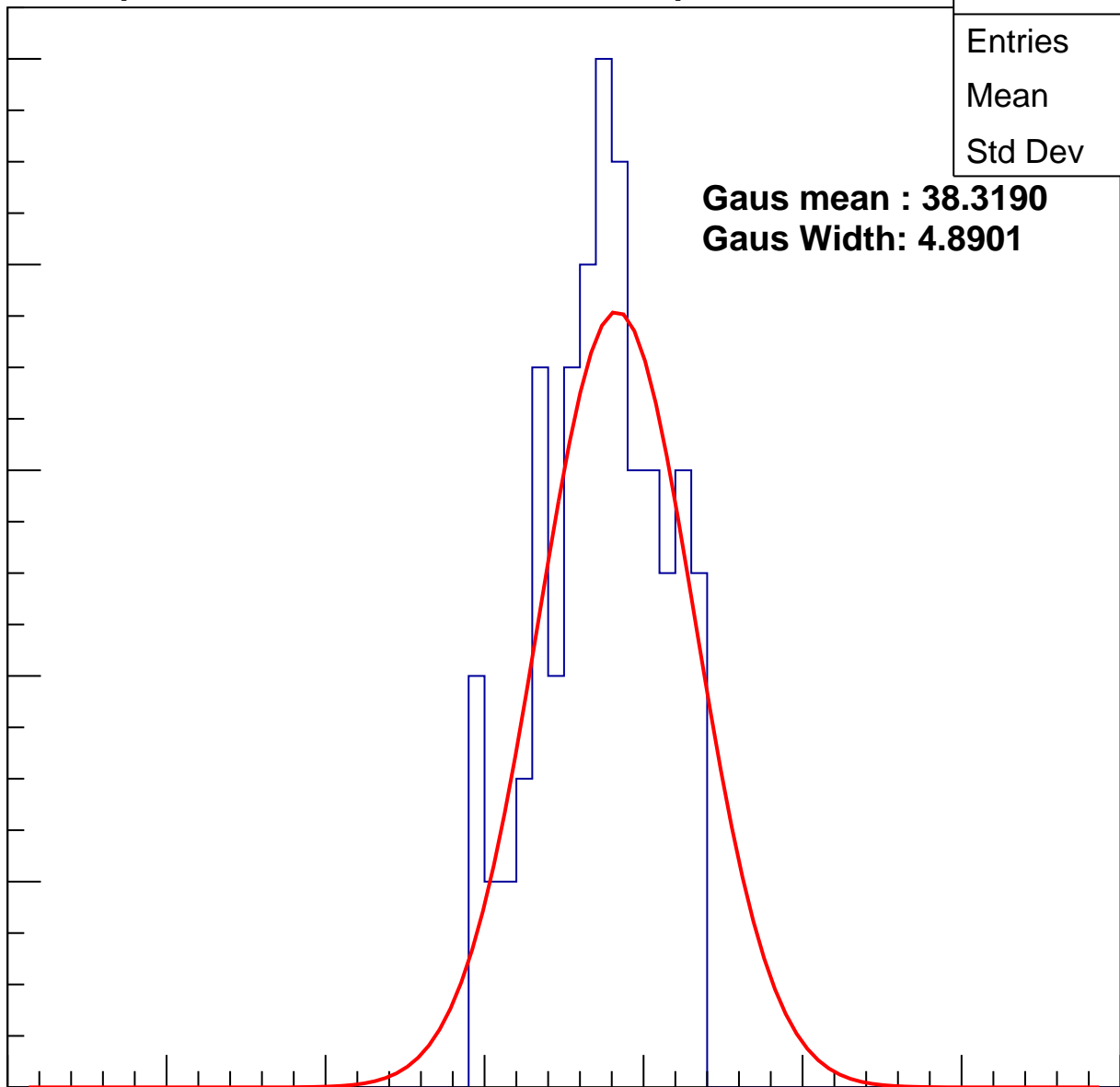
Gaus Width: 4.8901

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch30, adc2

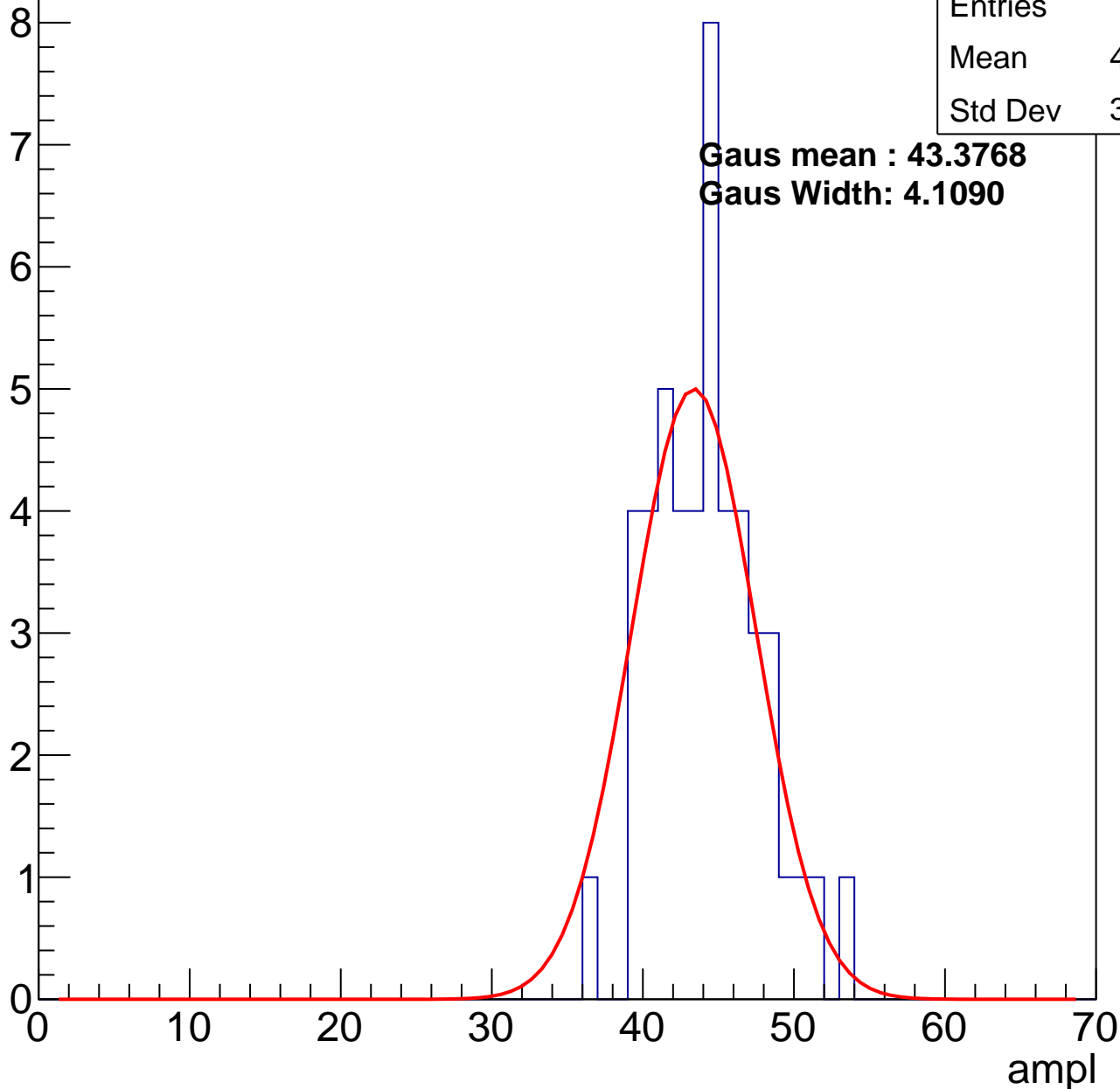
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	43.77
Std Dev	3.466

Gaus mean : 43.3768

Gaus Width: 4.1090

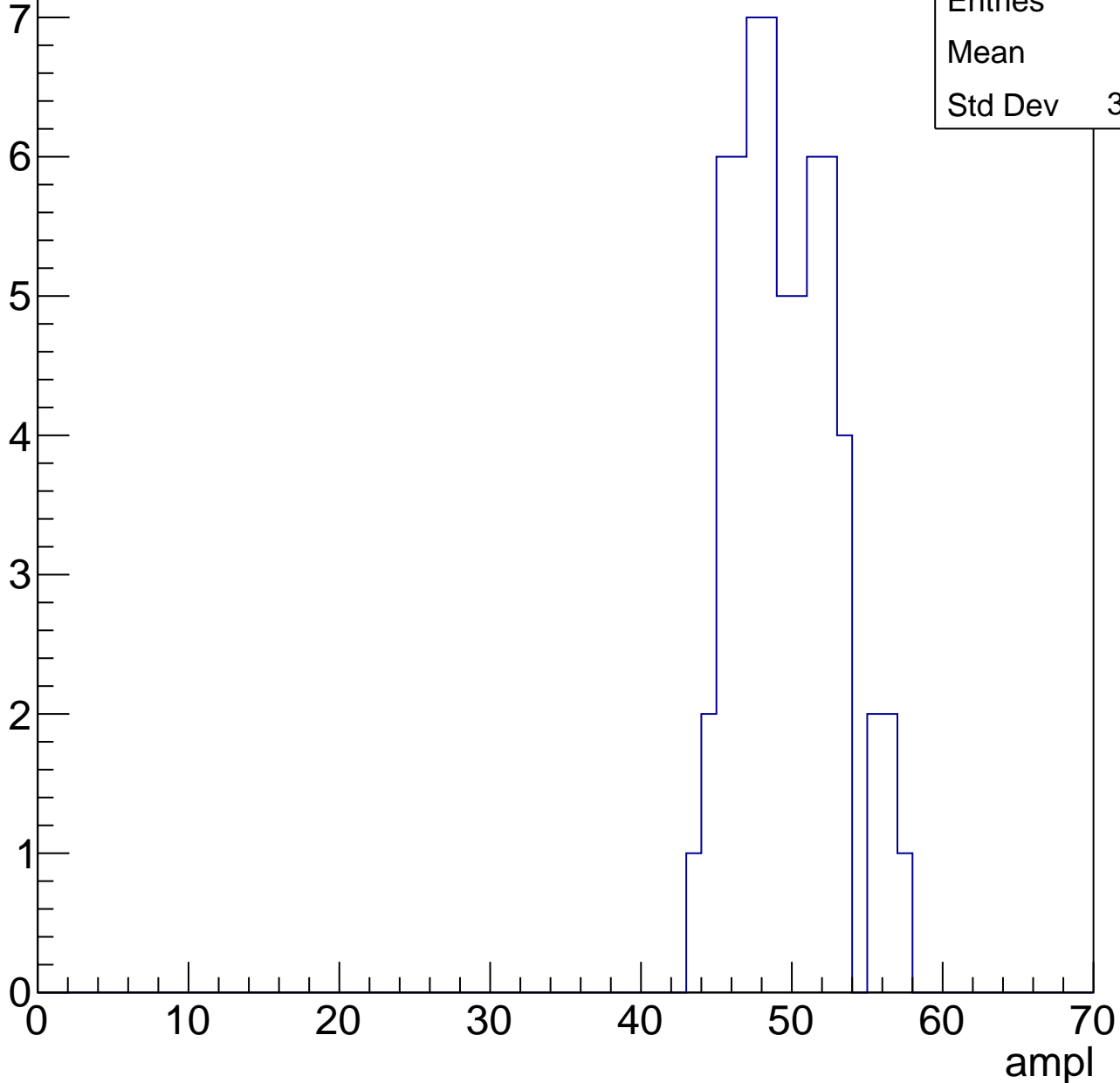


B1L101S, U6-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

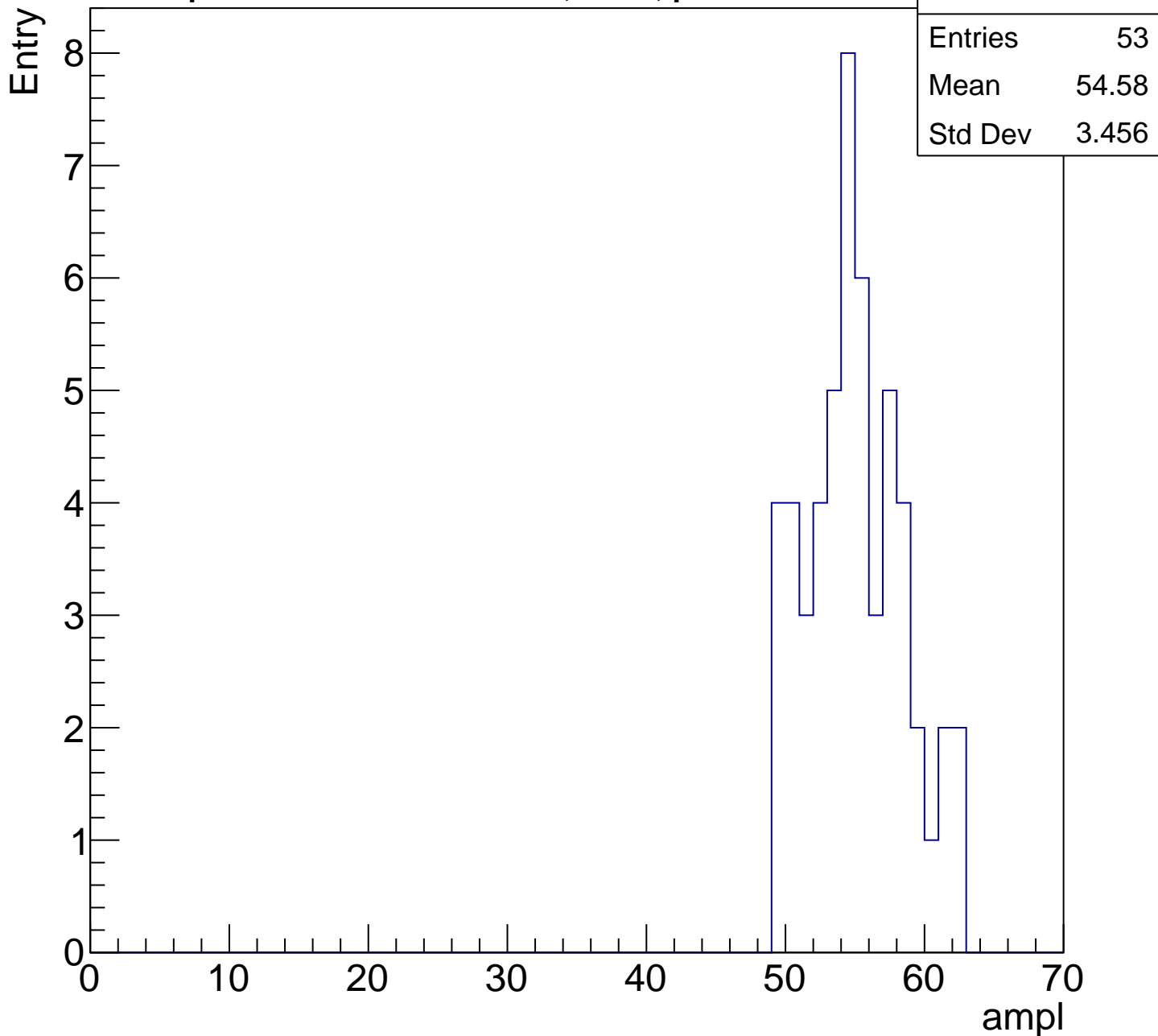
Entry

Entries	60
Mean	49.1
Std Dev	3.295



B1L101S, U6-ch30, adc4

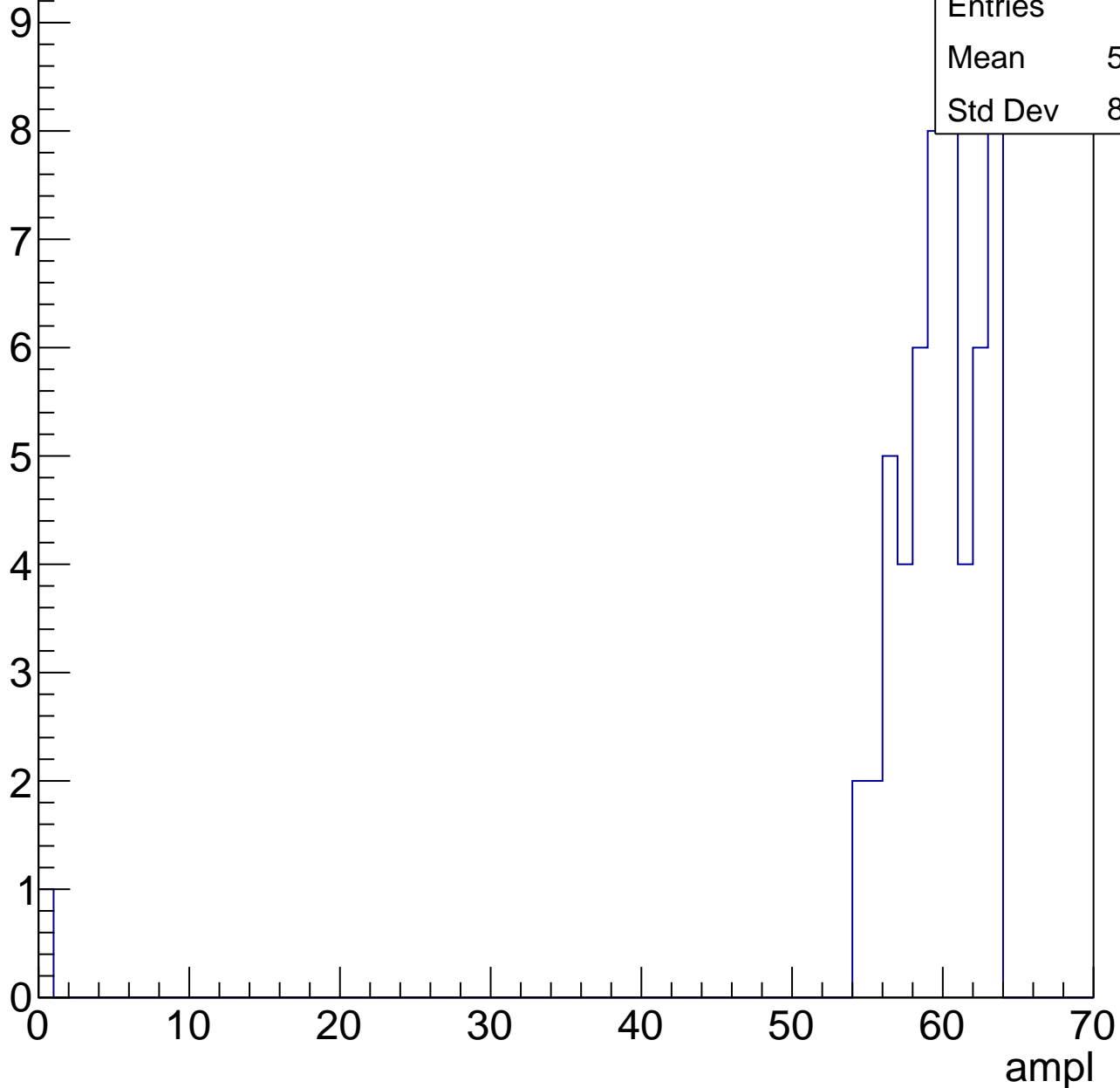
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	55
Mean	58.29
Std Dev	8.322

B1L101S, U6-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U6-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch31, adc0

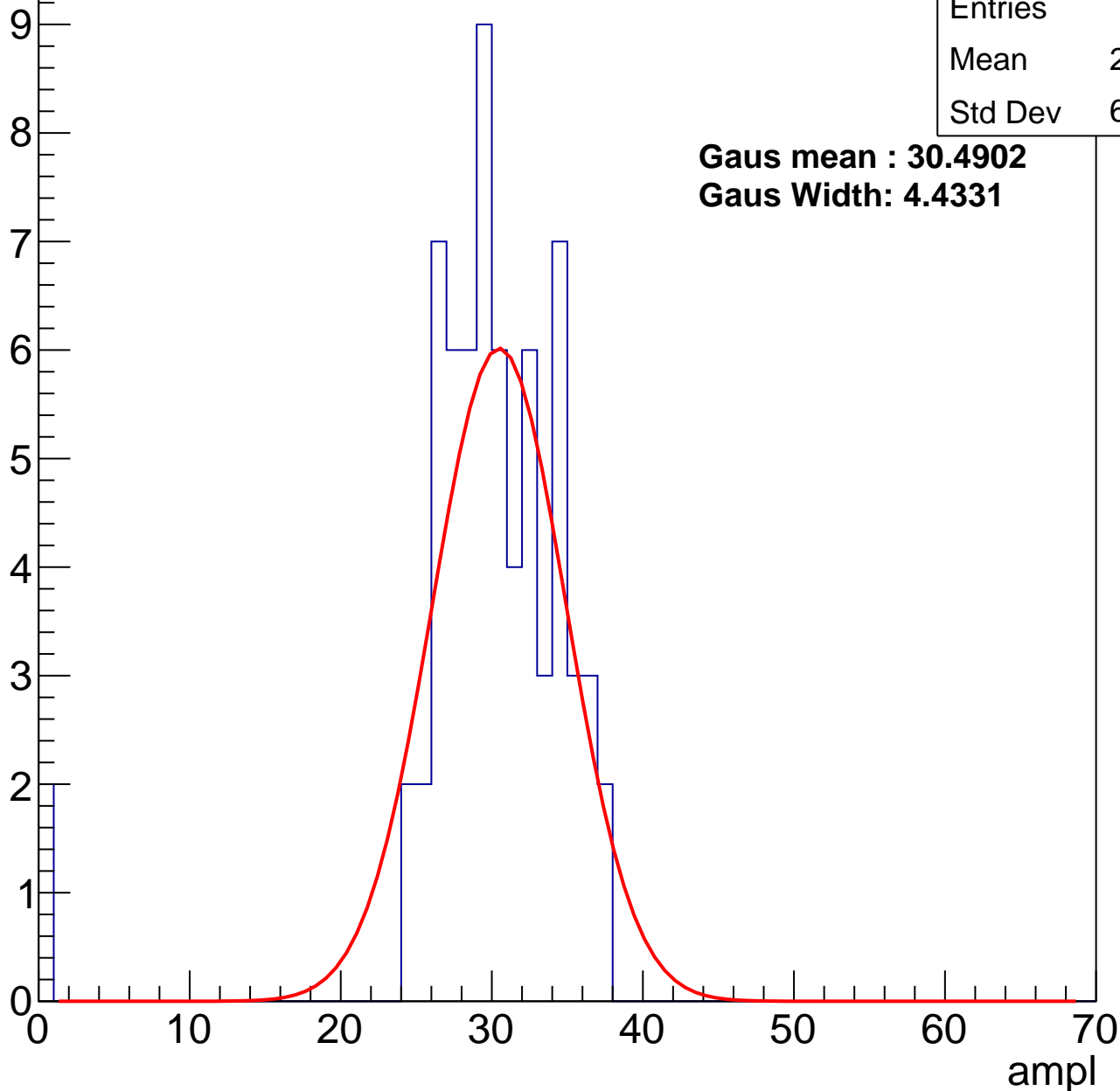
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.28
Std Dev	6.104

Gaus mean : 30.4902

Gaus Width: 4.4331



B1L101S, U6-ch31, adc1

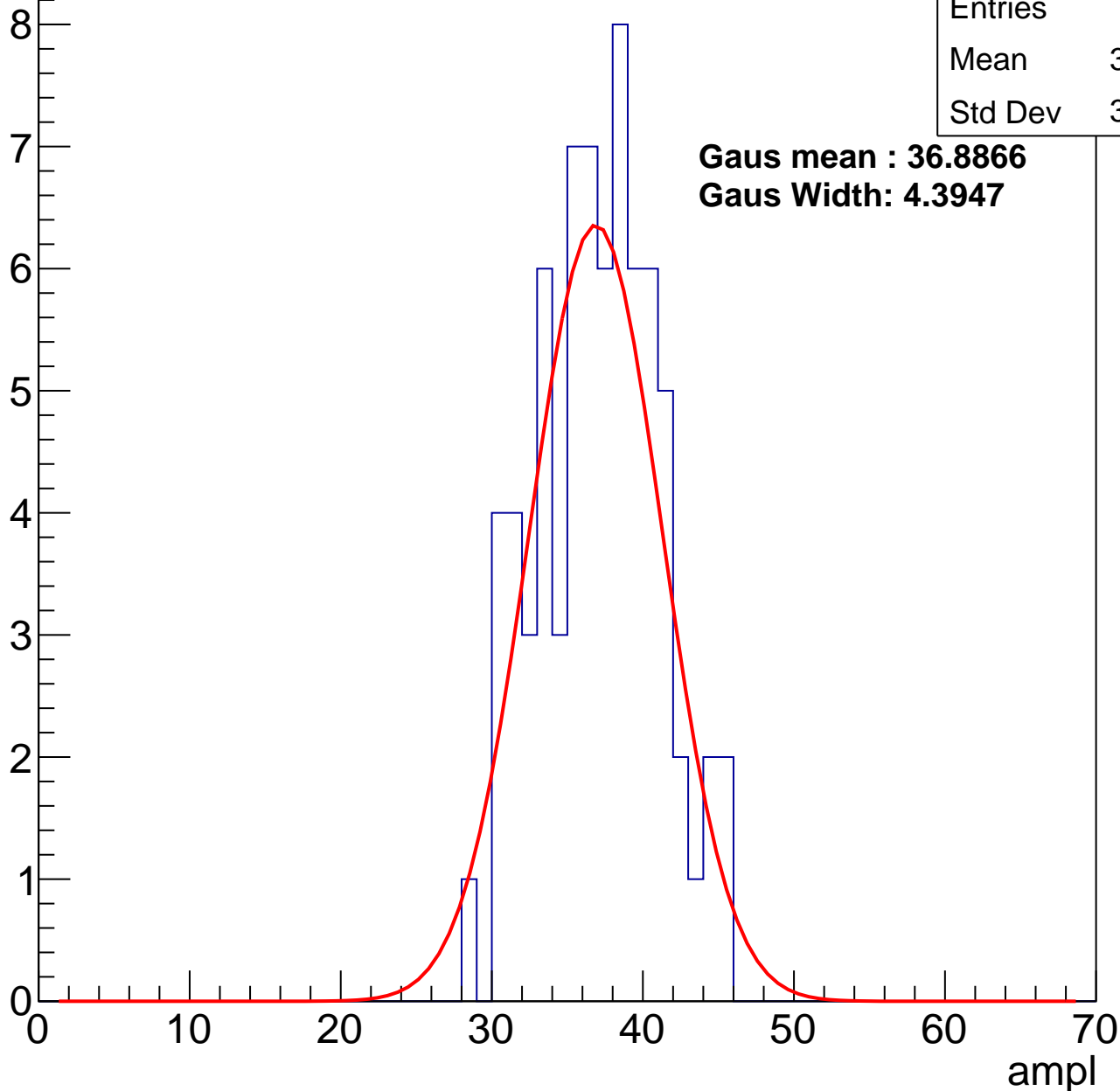
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.64
Std Dev	3.922

Gaus mean : 36.8866

Gaus Width: 4.3947



B1L101S, U6-ch31, adc2

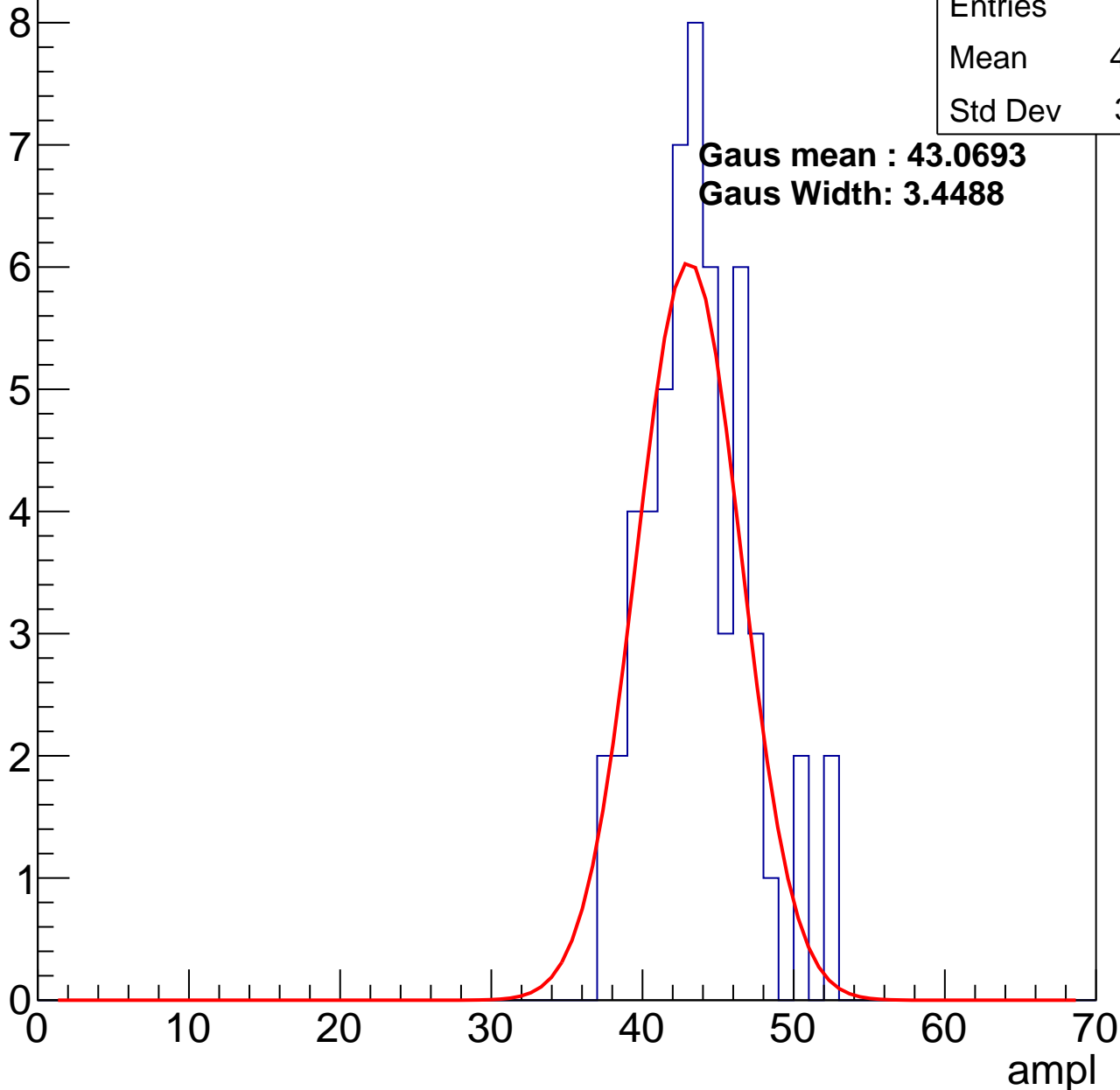
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.22
Std Dev	3.441

Gaus mean : 43.0693

Gaus Width: 3.4488

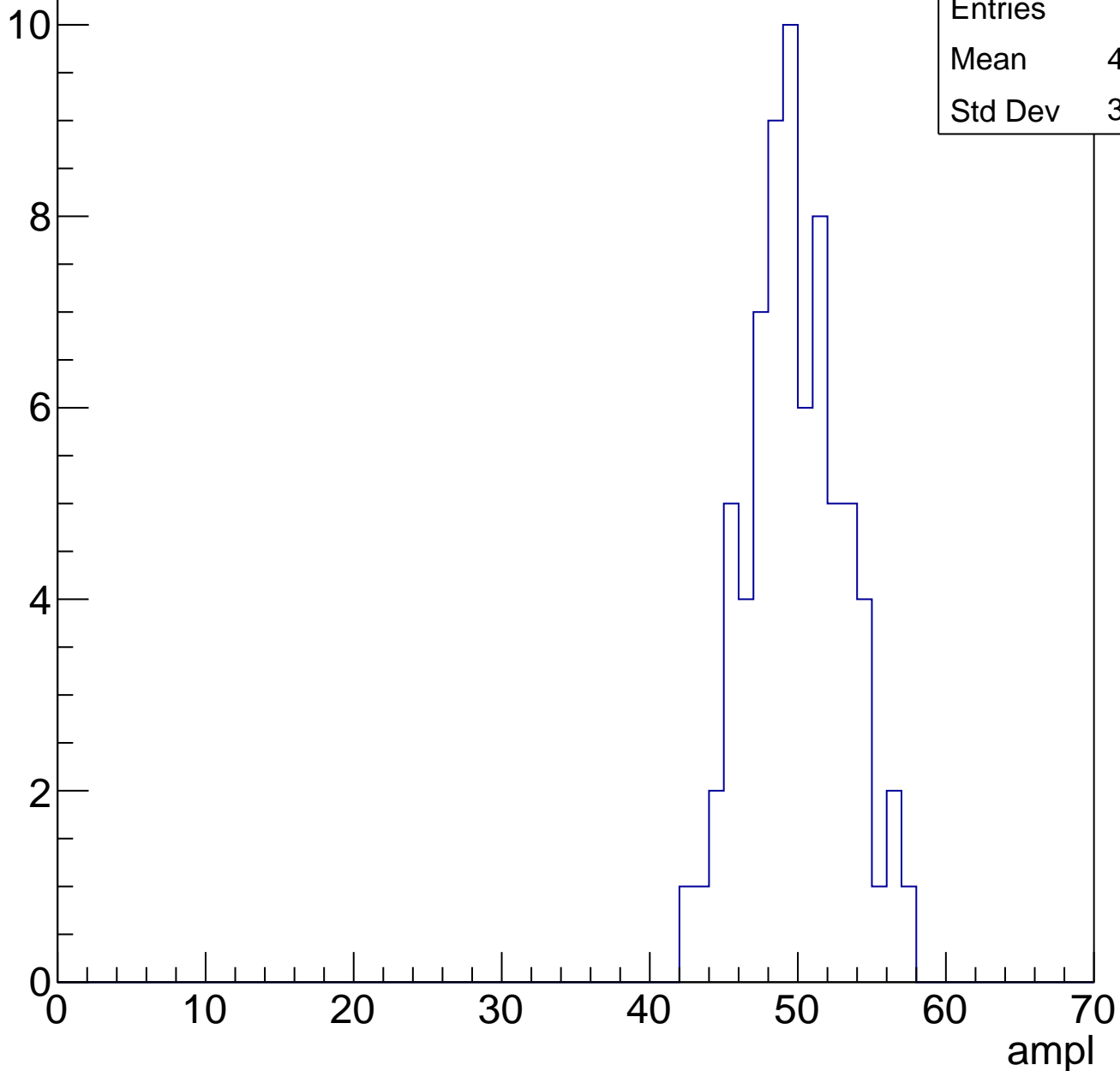


B1L101S, U6-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

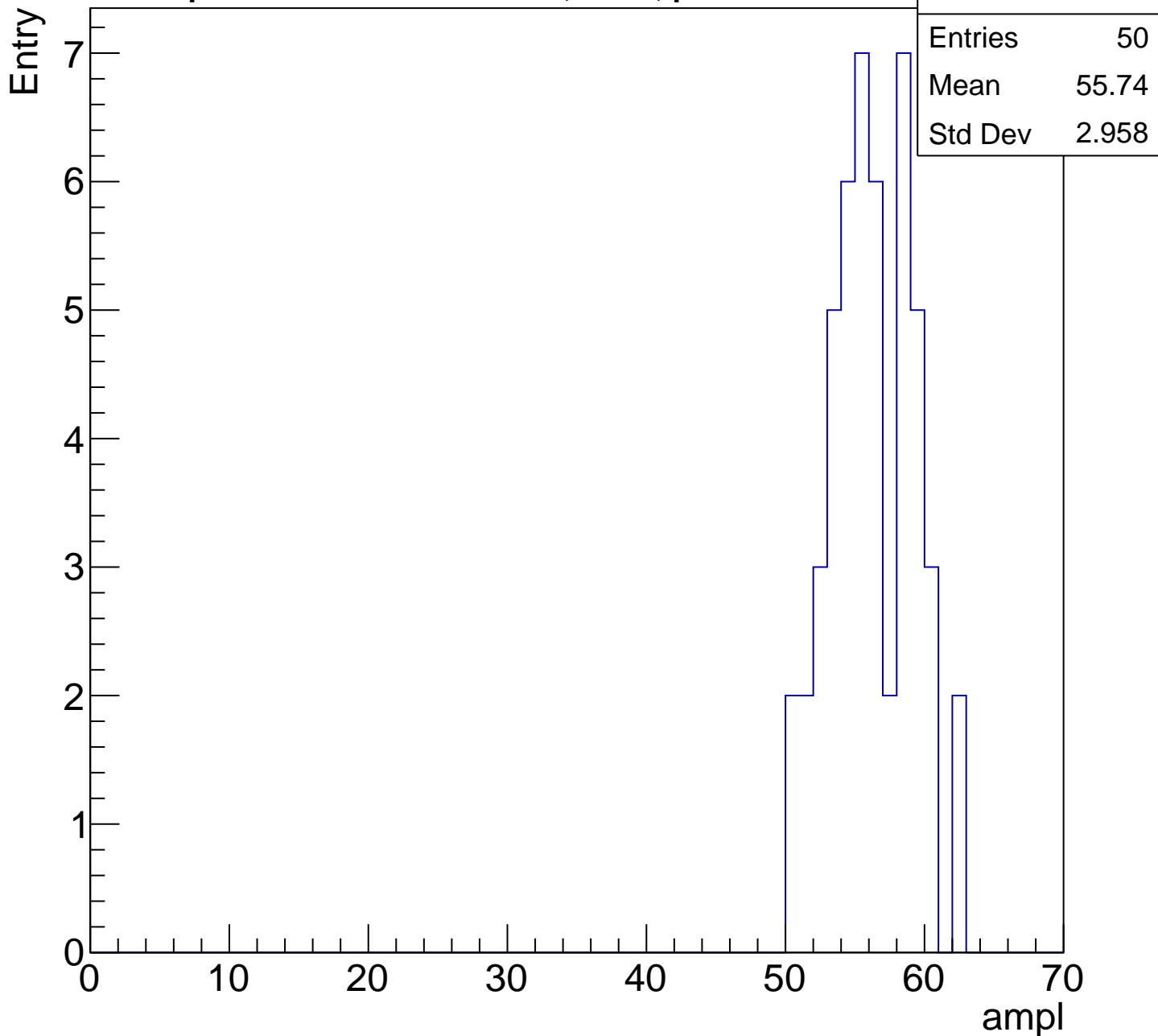
Entries	71
Mean	49.38
Std Dev	3.226

Entry



B1L101S, U6-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

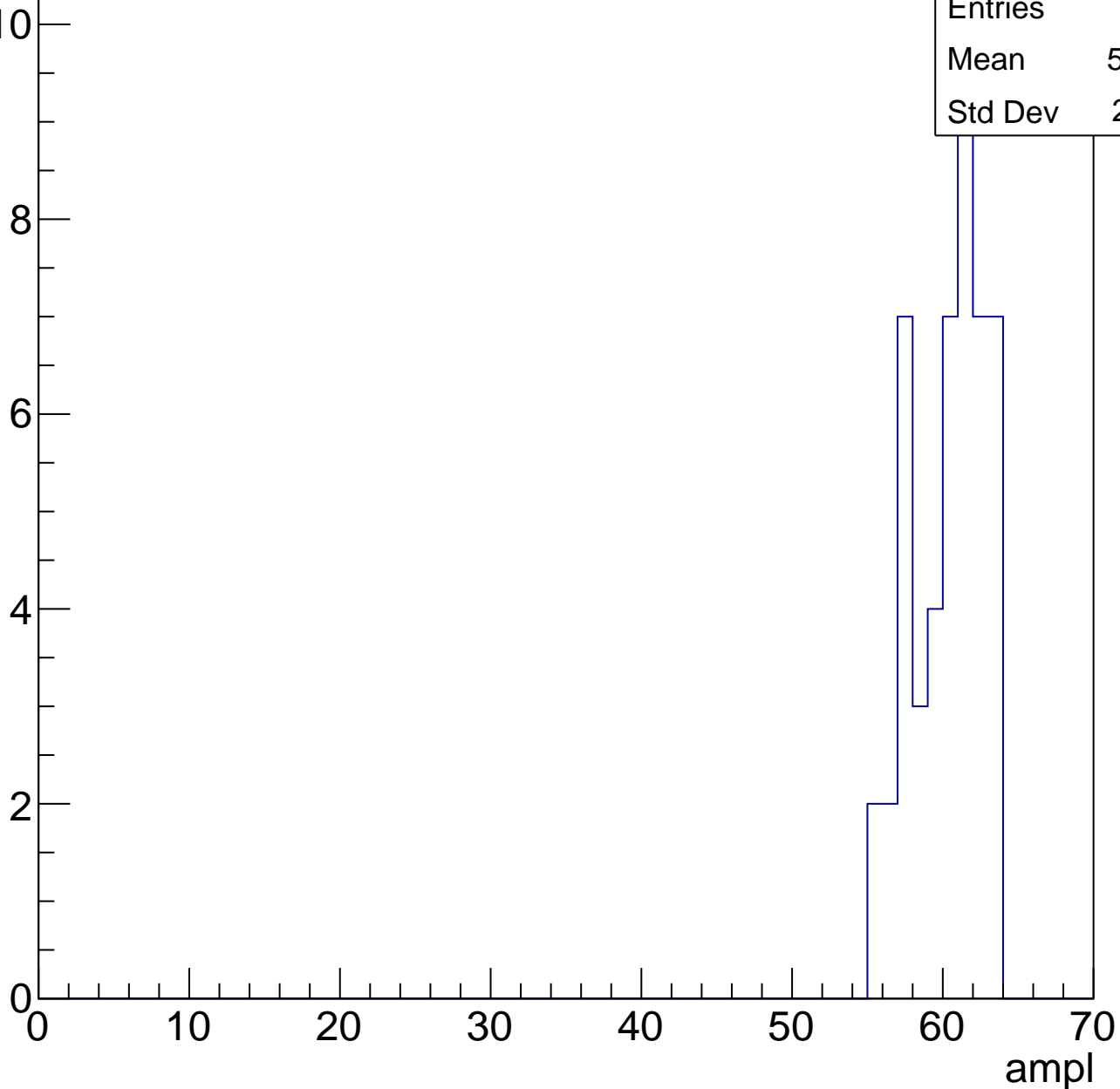


B1L101S, U6-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

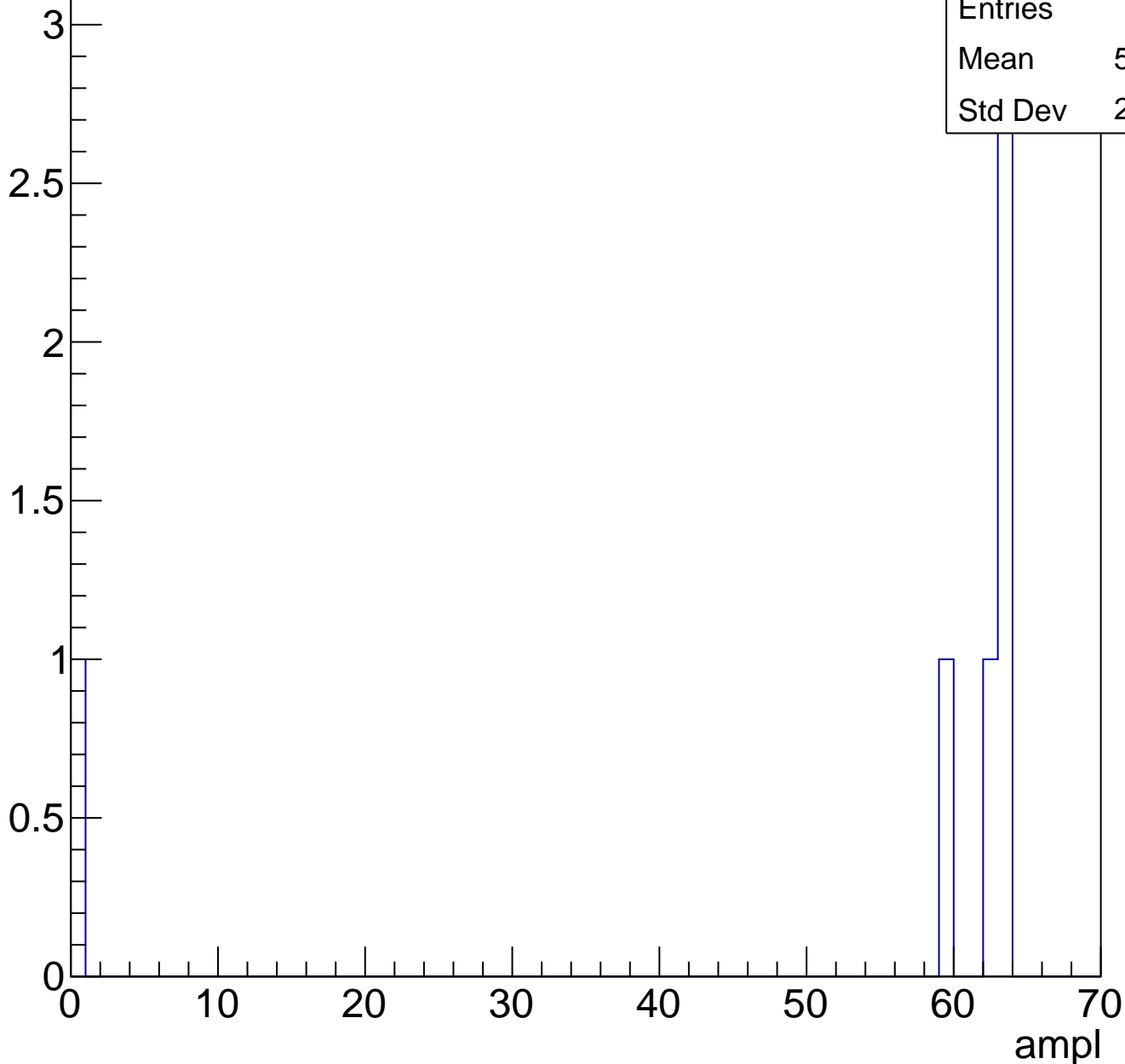
Entries	49
Mean	59.92
Std Dev	2.311



B1L101S, U6-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch31, adc7

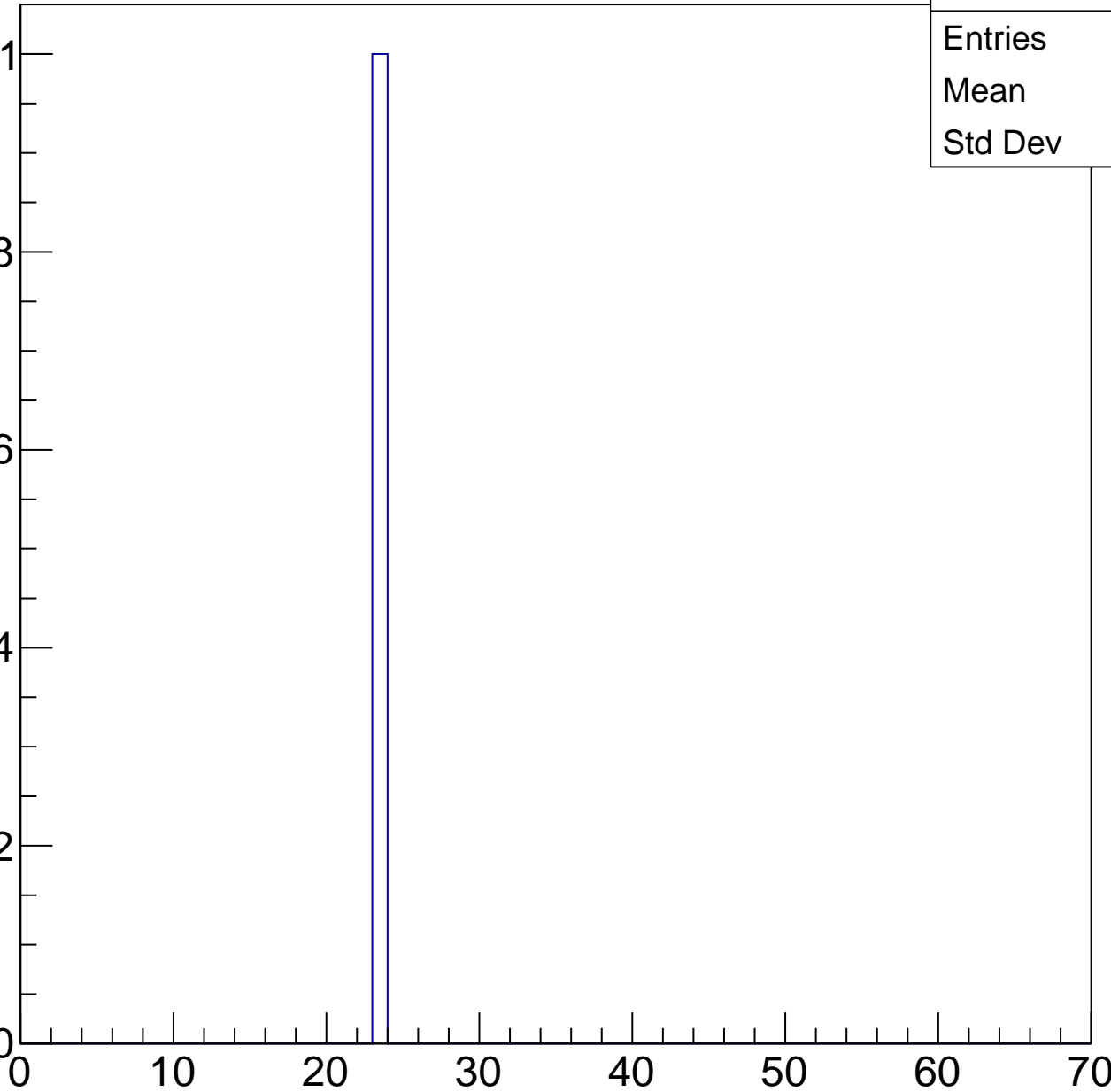
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L101S, U6-ch32, adc0

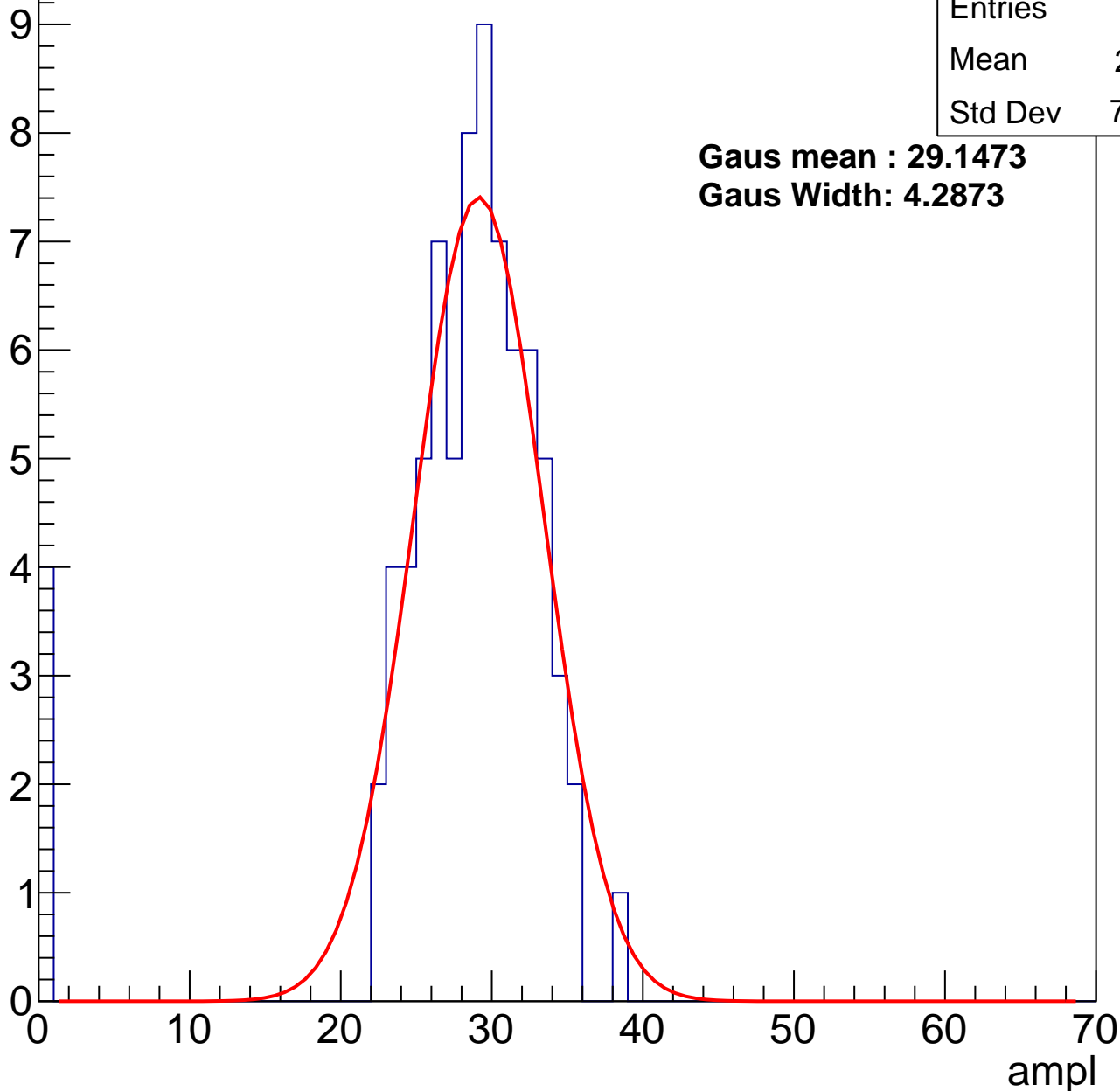
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	27.21
Std Dev	7.178

Gaus mean : 29.1473

Gaus Width: 4.2873



B1L101S, U6-ch32, adc1

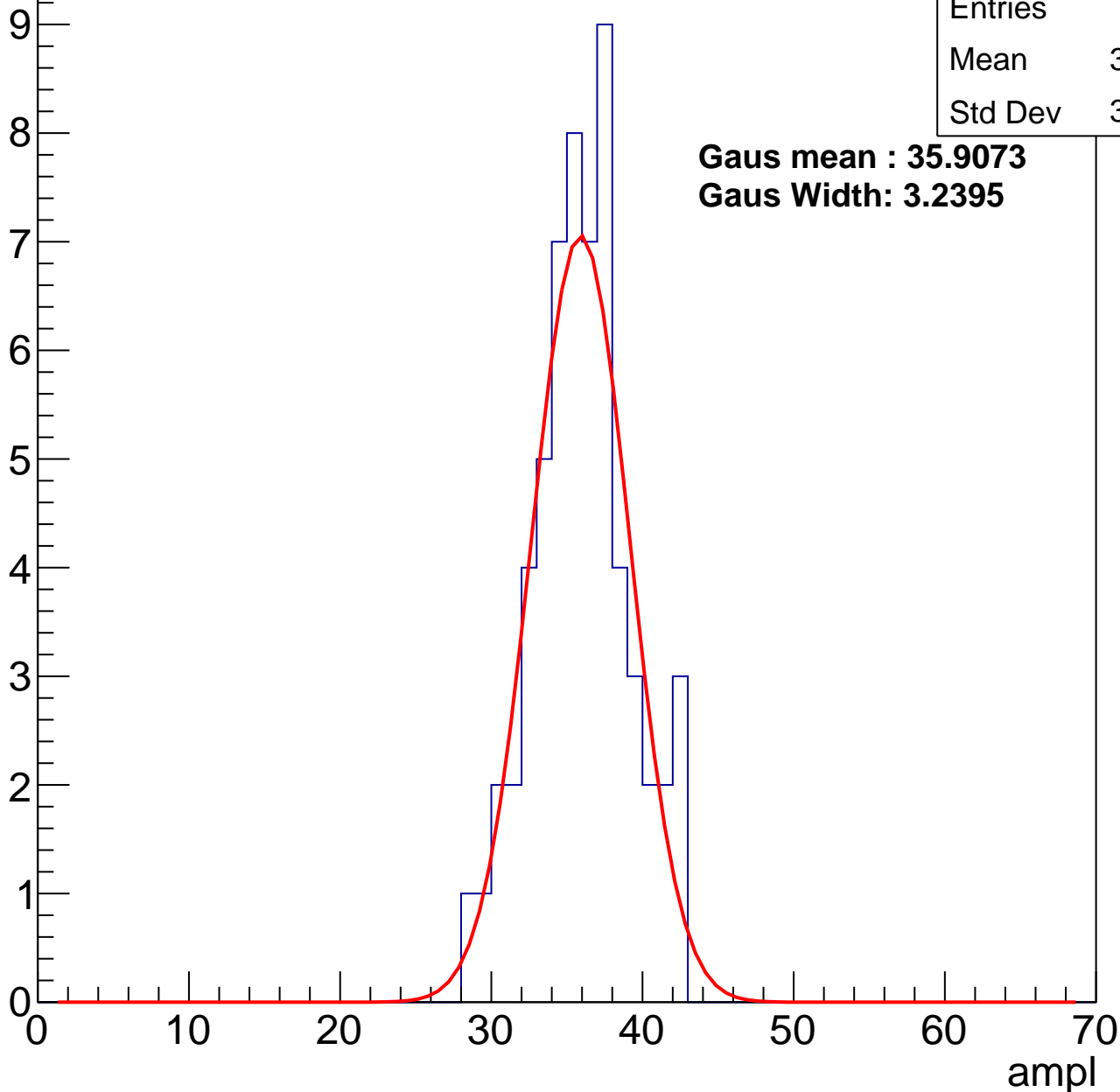
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.53
Std Dev	3.186

Gaus mean : 35.9073

Gaus Width: 3.2395



B1L101S, U6-ch32, adc2

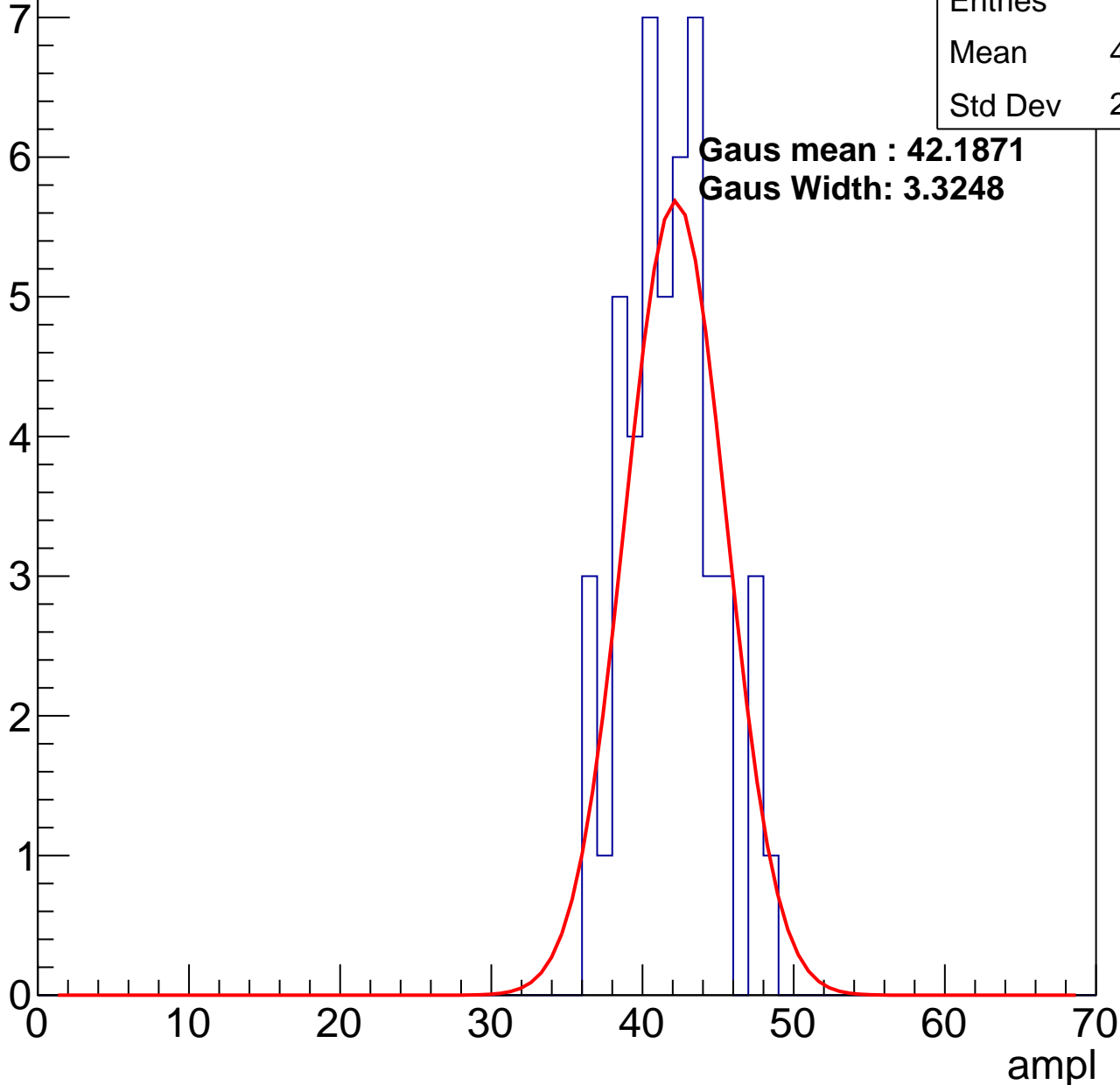
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	41.35
Std Dev	2.954

Gaus mean : 42.1871

Gaus Width: 3.3248

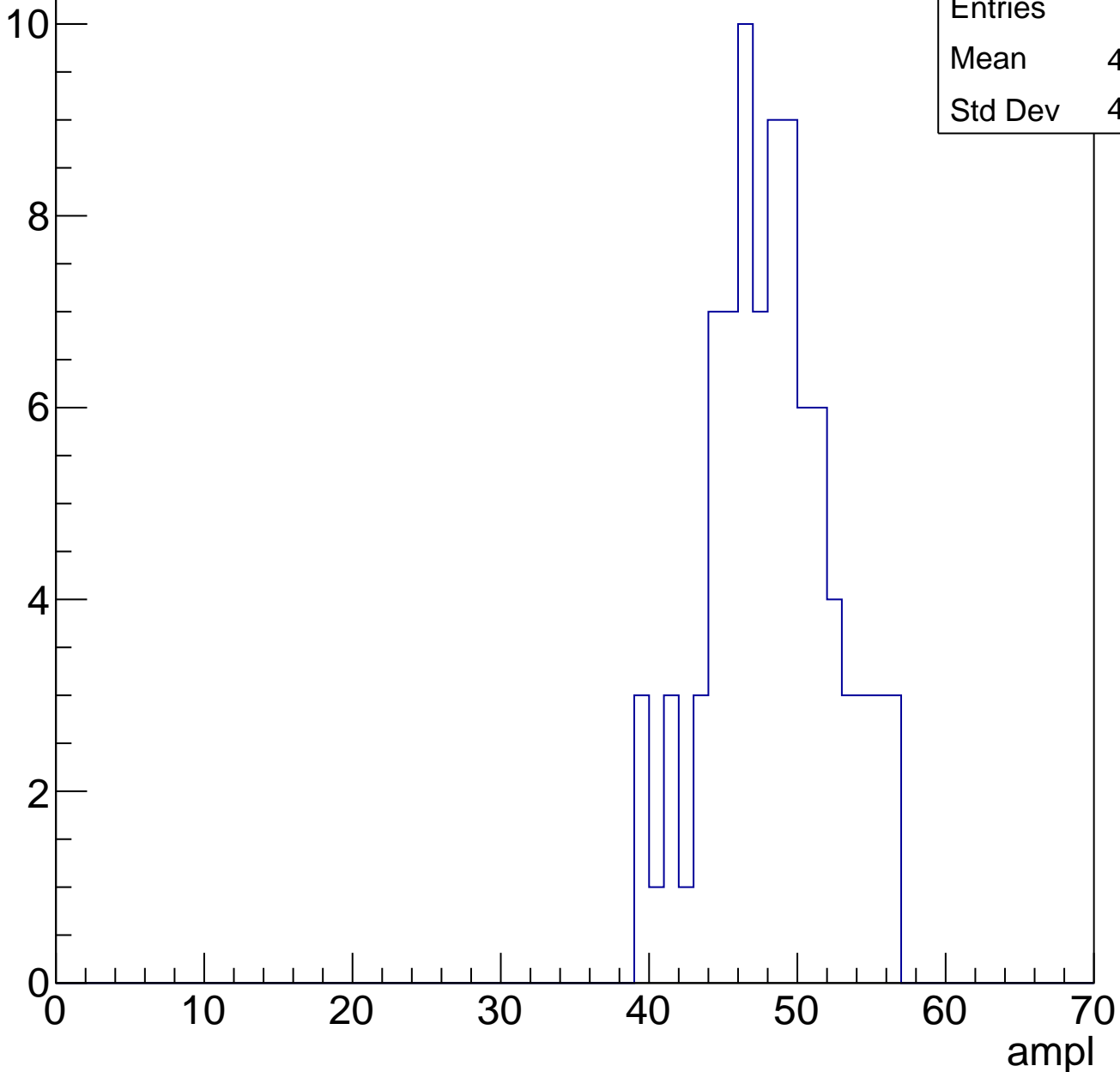


B1L101S, U6-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	47.77
Std Dev	4.067

Entry

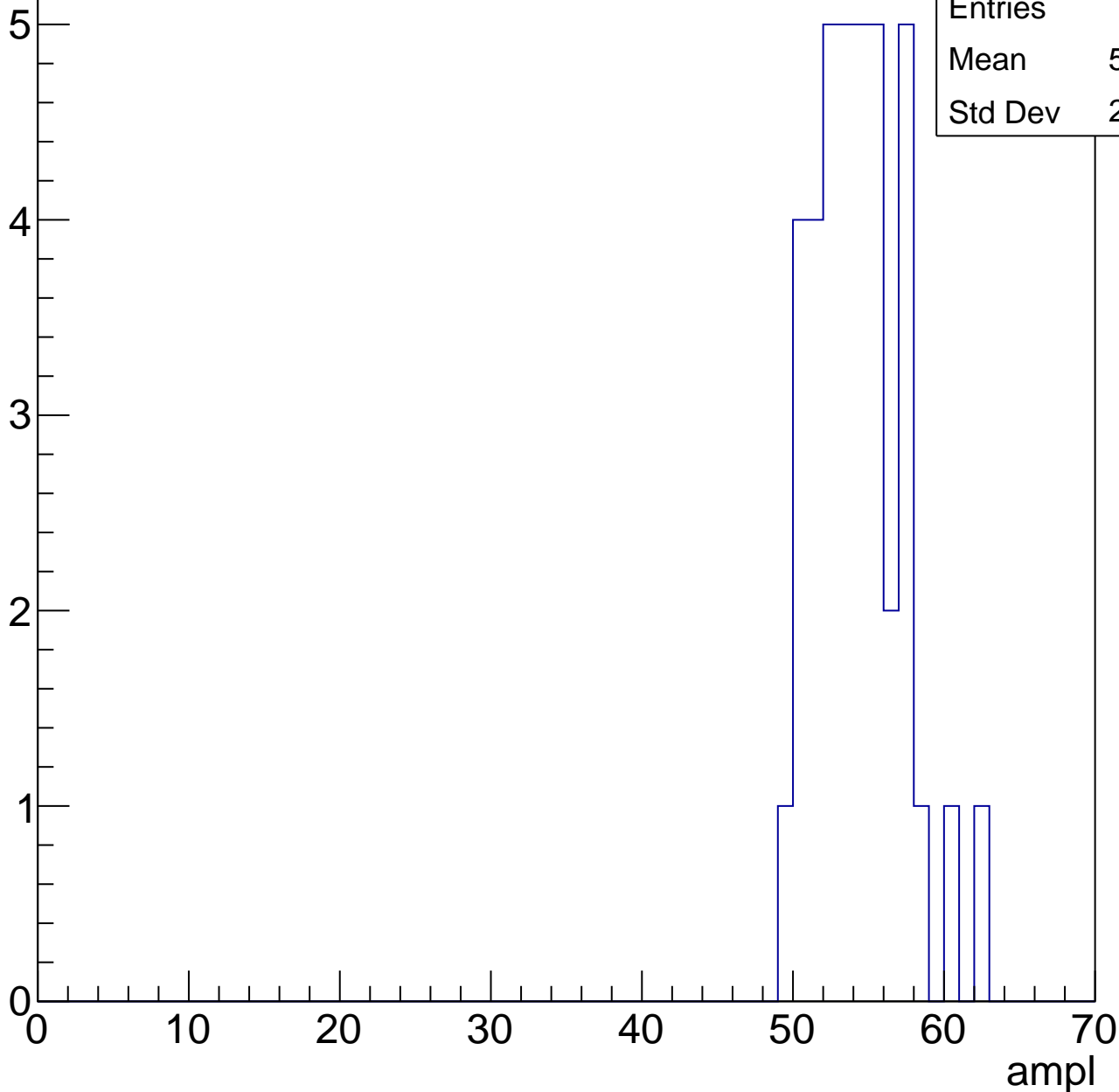


B1L101S, U6-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	53.85
Std Dev	2.878



B1L101S, U6-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 65

Mean 58.02

Std Dev 7.733

8

6

4

2

0

0

10

20

30

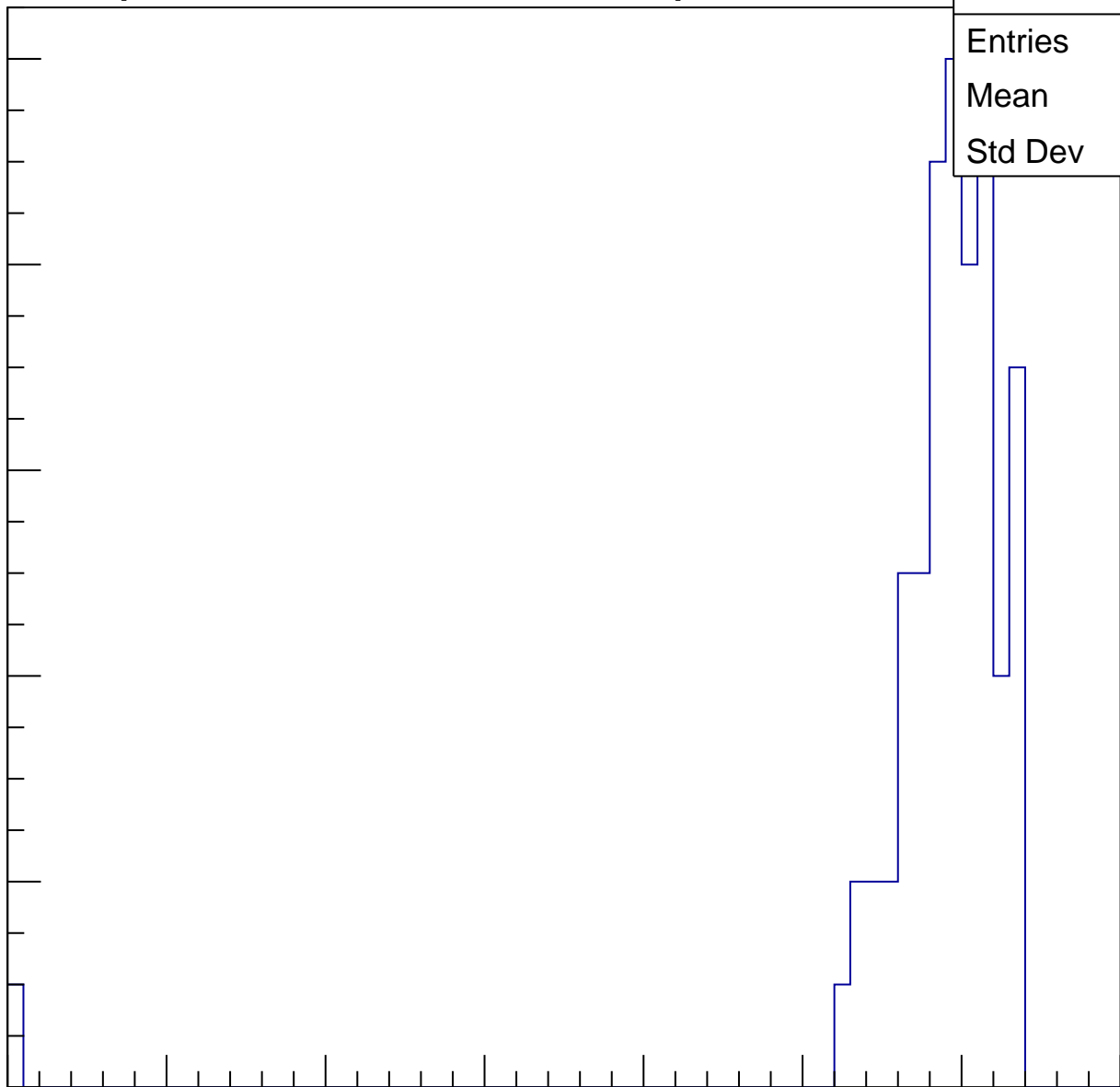
40

50

60

70

ampl

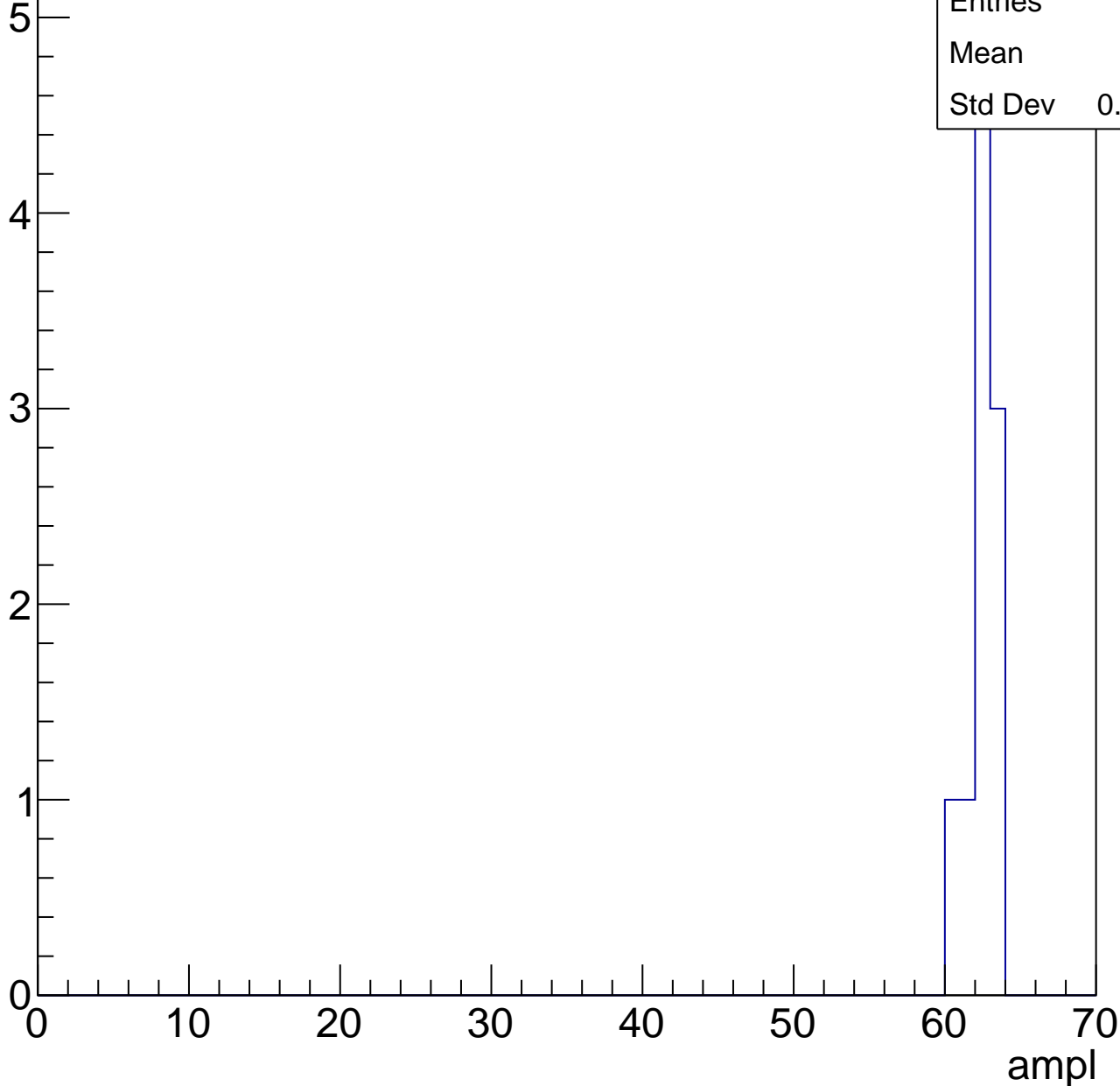


B1L101S, U6-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	62
Std Dev	0.8944



B1L101S, U6-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch33, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	30.62
Std Dev	3.116

Gaus mean : 31.3754

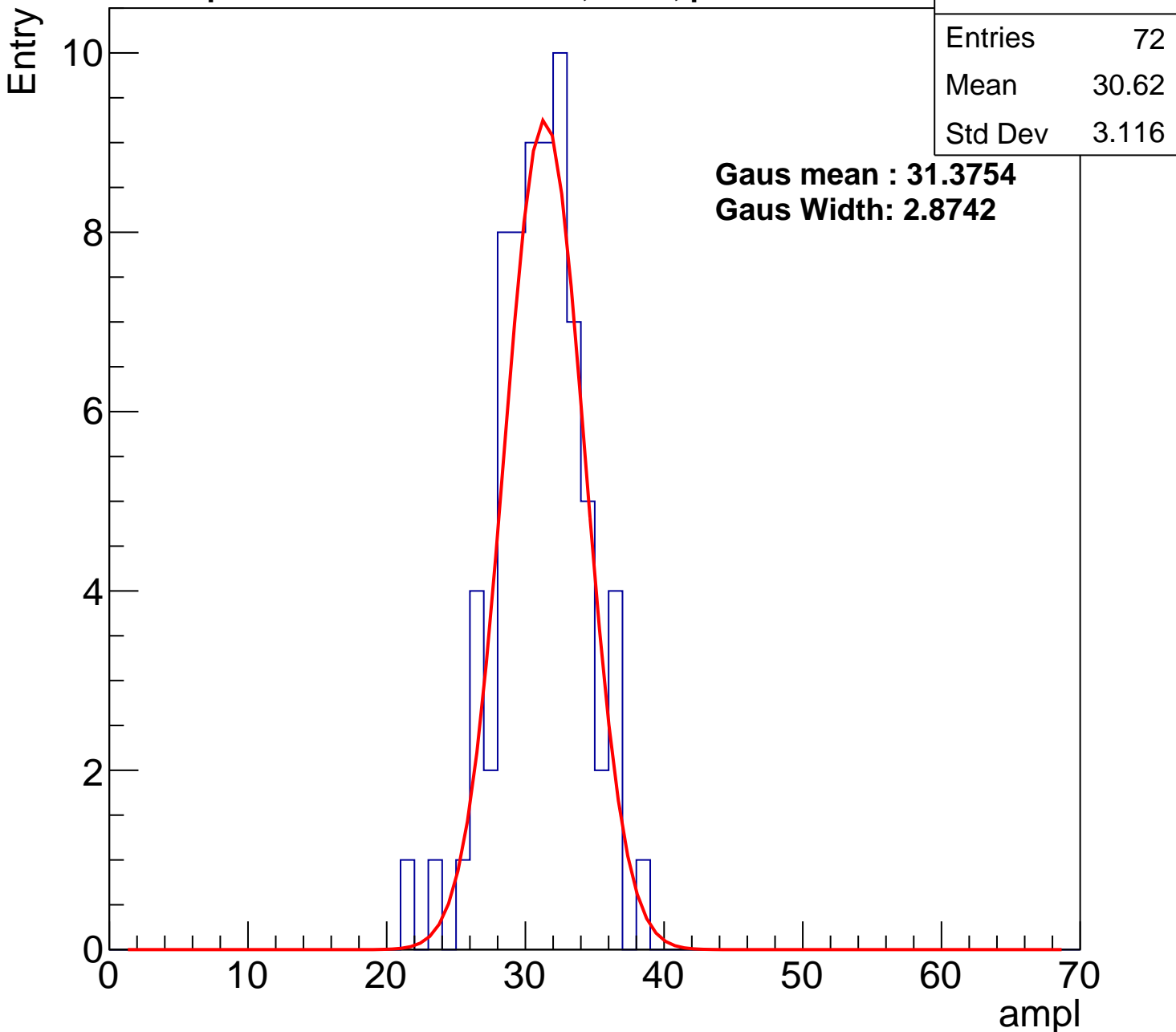
Gaus Width: 2.8742

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch33, adc1

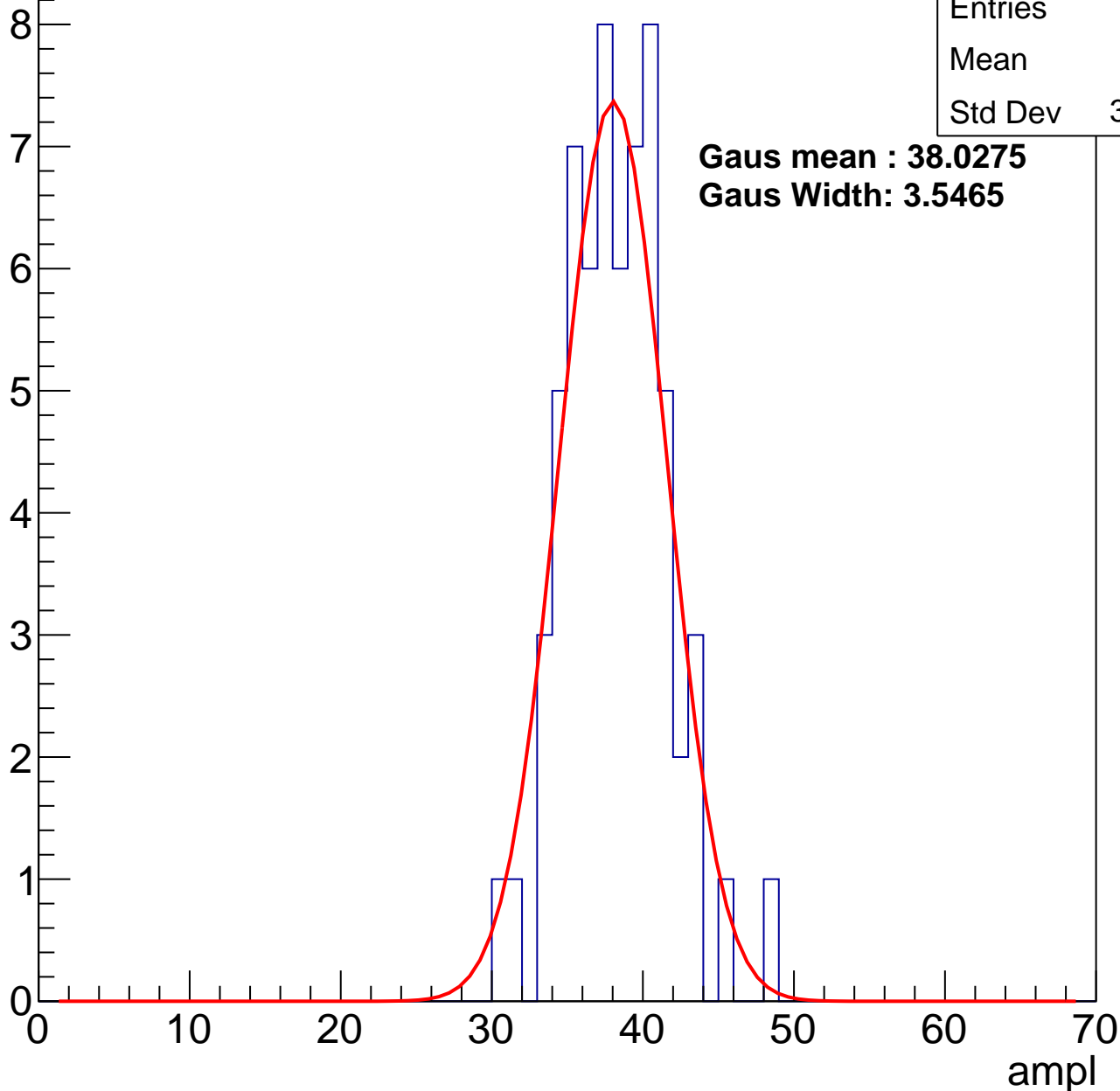
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.8
Std Dev	3.308

Gaus mean : 38.0275

Gaus Width: 3.5465



B1L101S, U6-ch33, adc2

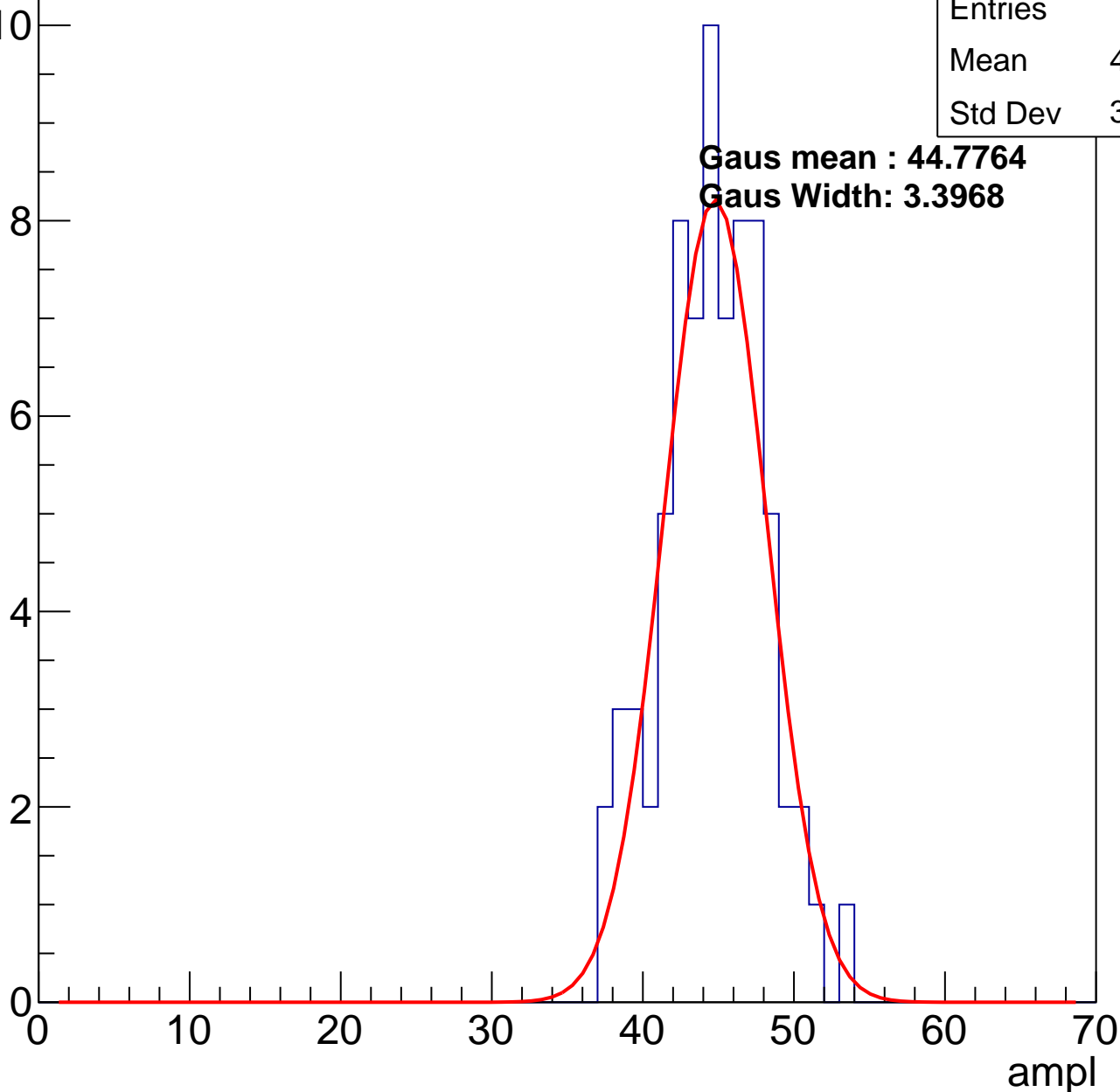
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.16
Std Dev	3.365

Gaus mean : 44.7764

Gaus Width: 3.3968

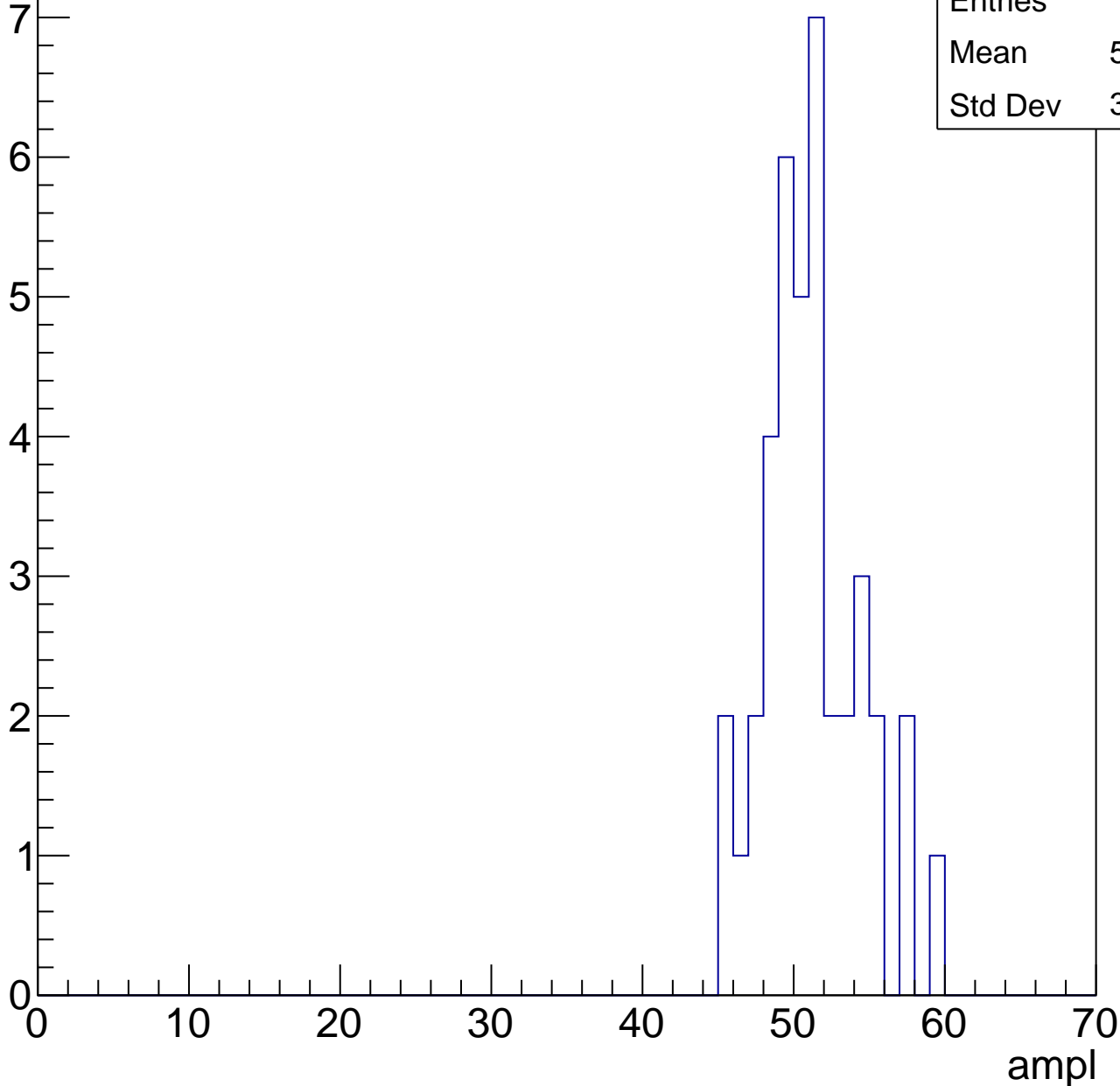


B1L101S, U6-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	50.72
Std Dev	3.186

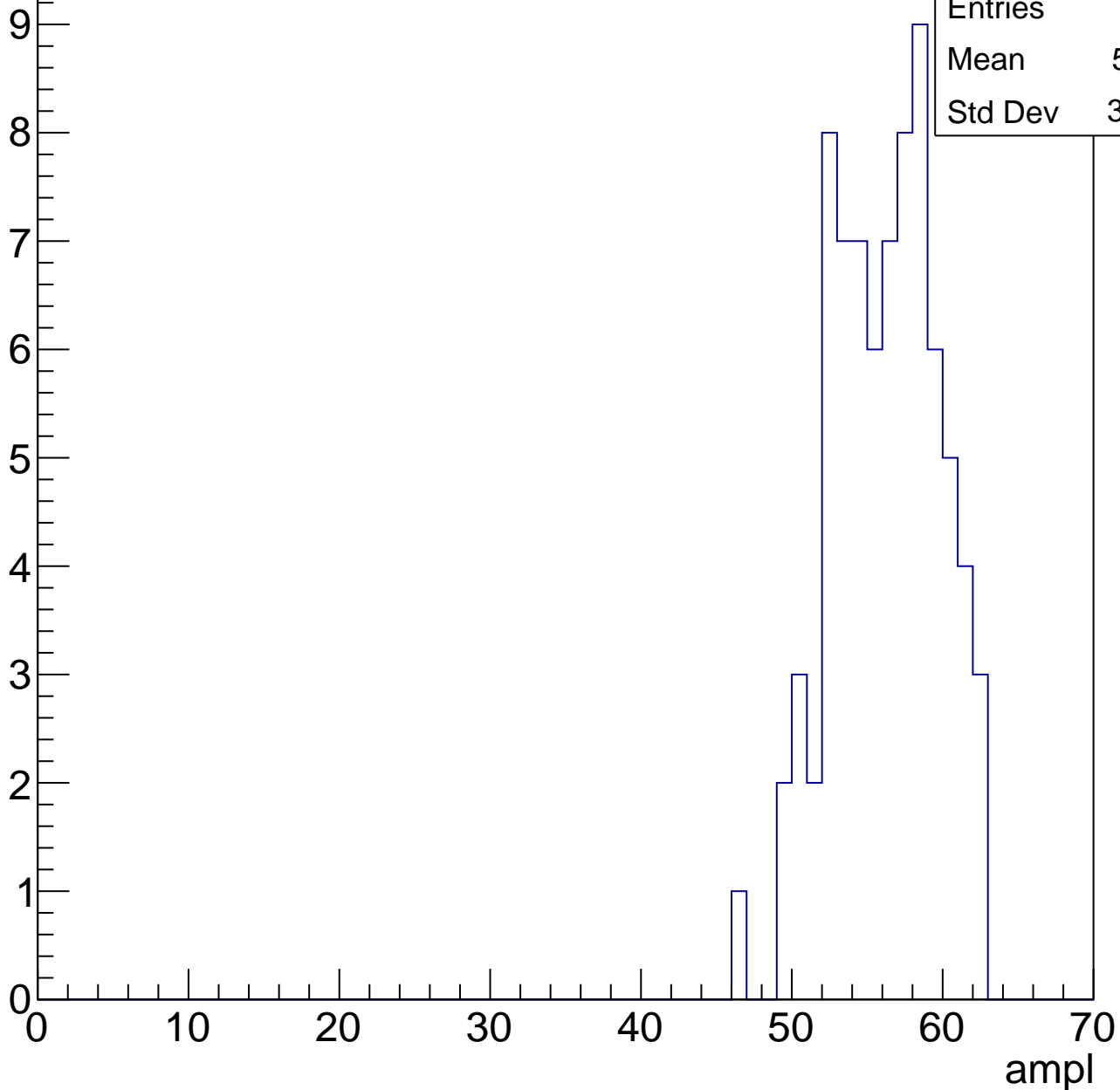


B1L101S, U6-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	55.71
Std Dev	3.509

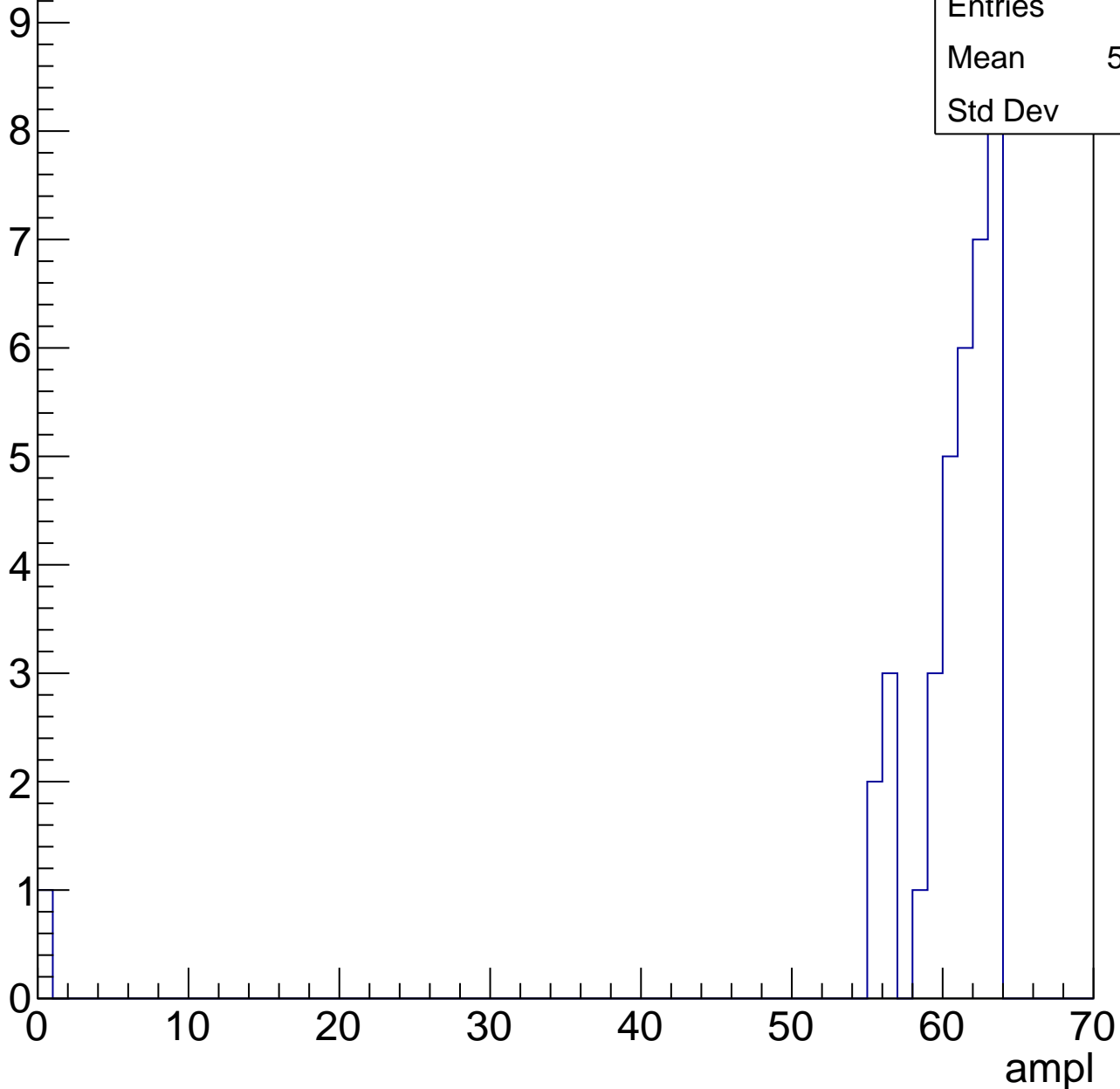


B1L101S, U6-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.92
Std Dev	10.1



B1L101S, U6-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch34, adc0

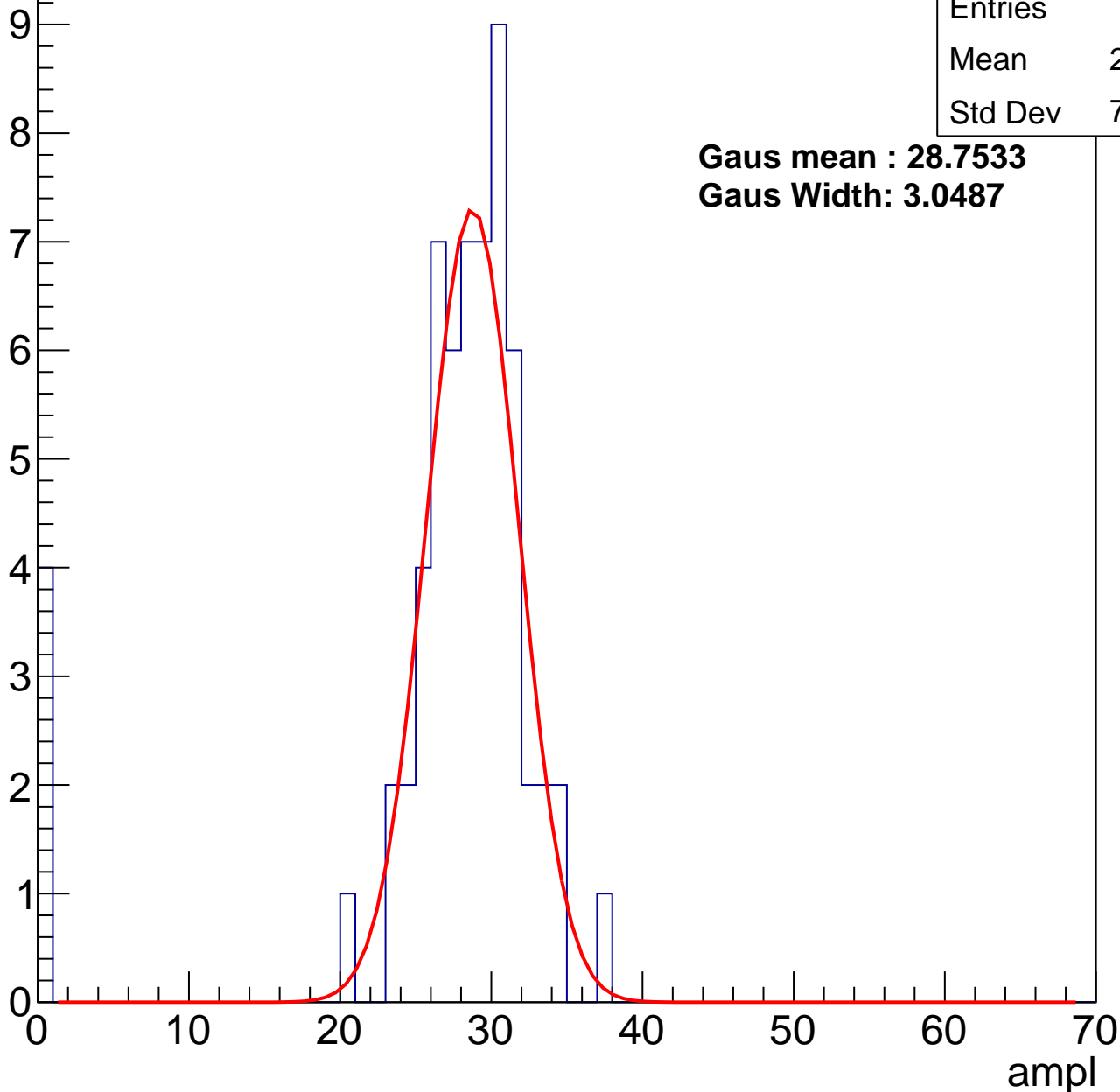
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	26.58
Std Dev	7.577

Gaus mean : 28.7533

Gaus Width: 3.0487

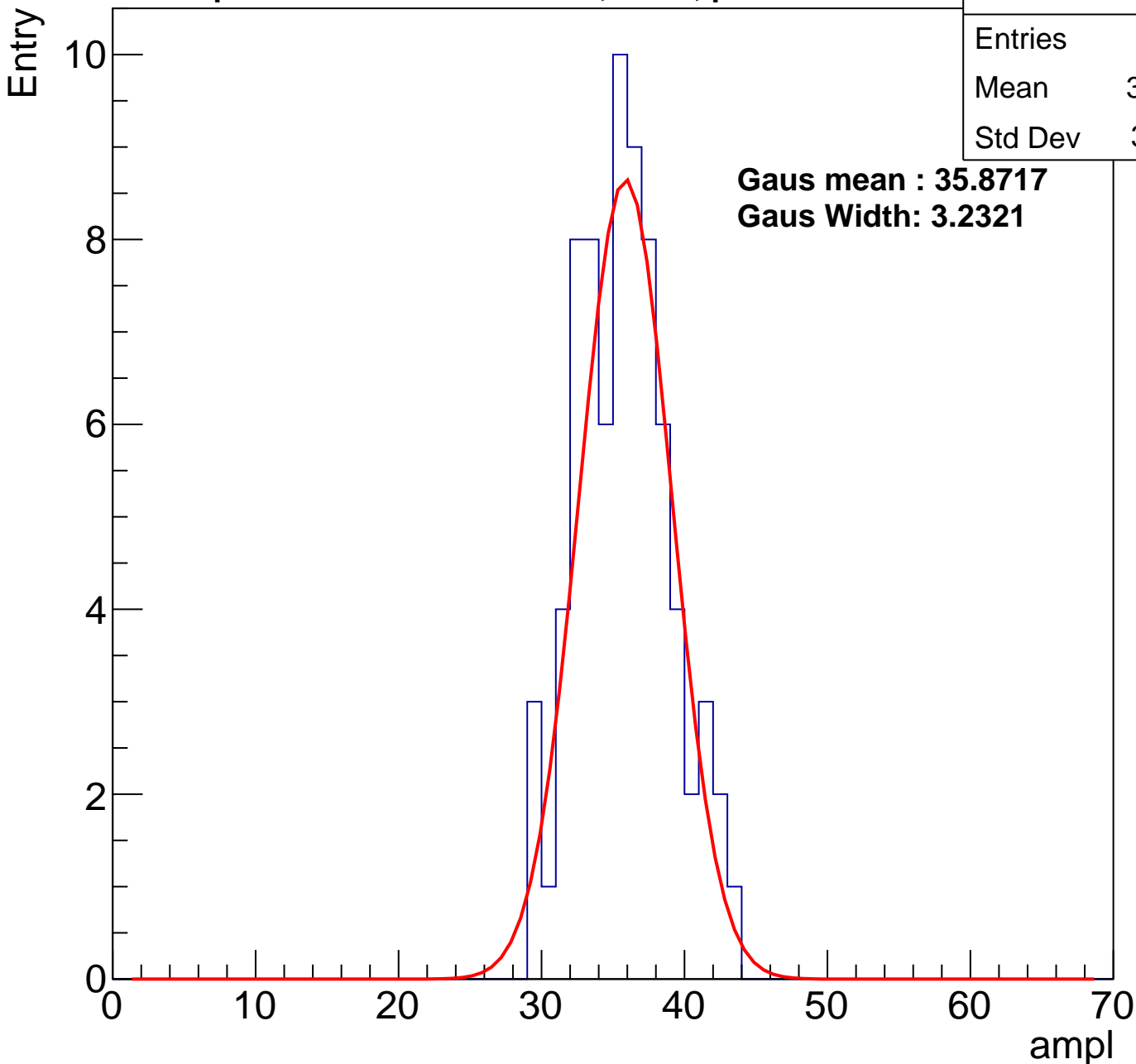


B1L101S, U6-ch34, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	35.32
Std Dev	3.221

Gaus mean : 35.8717
Gaus Width: 3.2321



B1L101S, U6-ch34, adc2

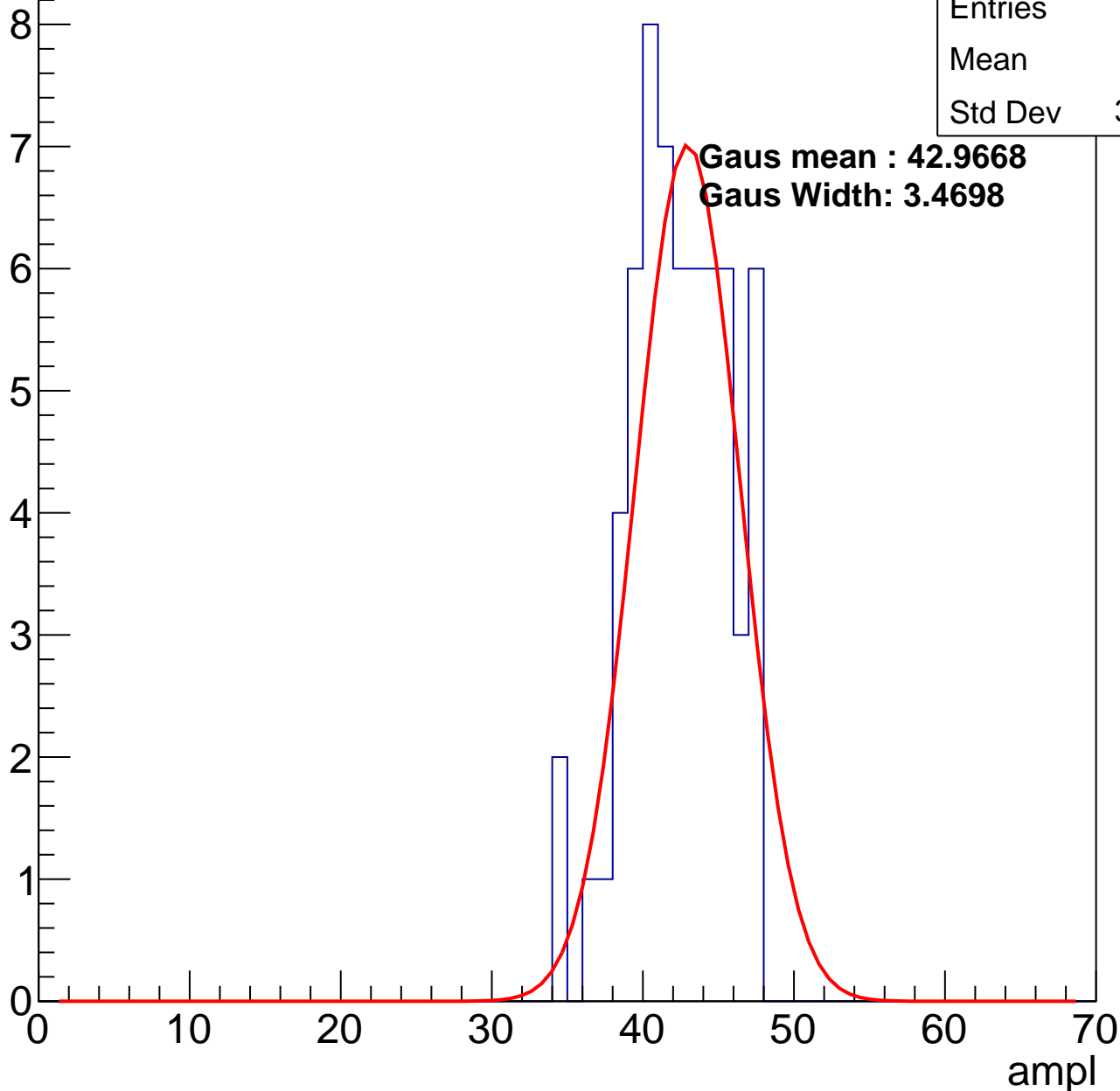
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.9
Std Dev	3.181

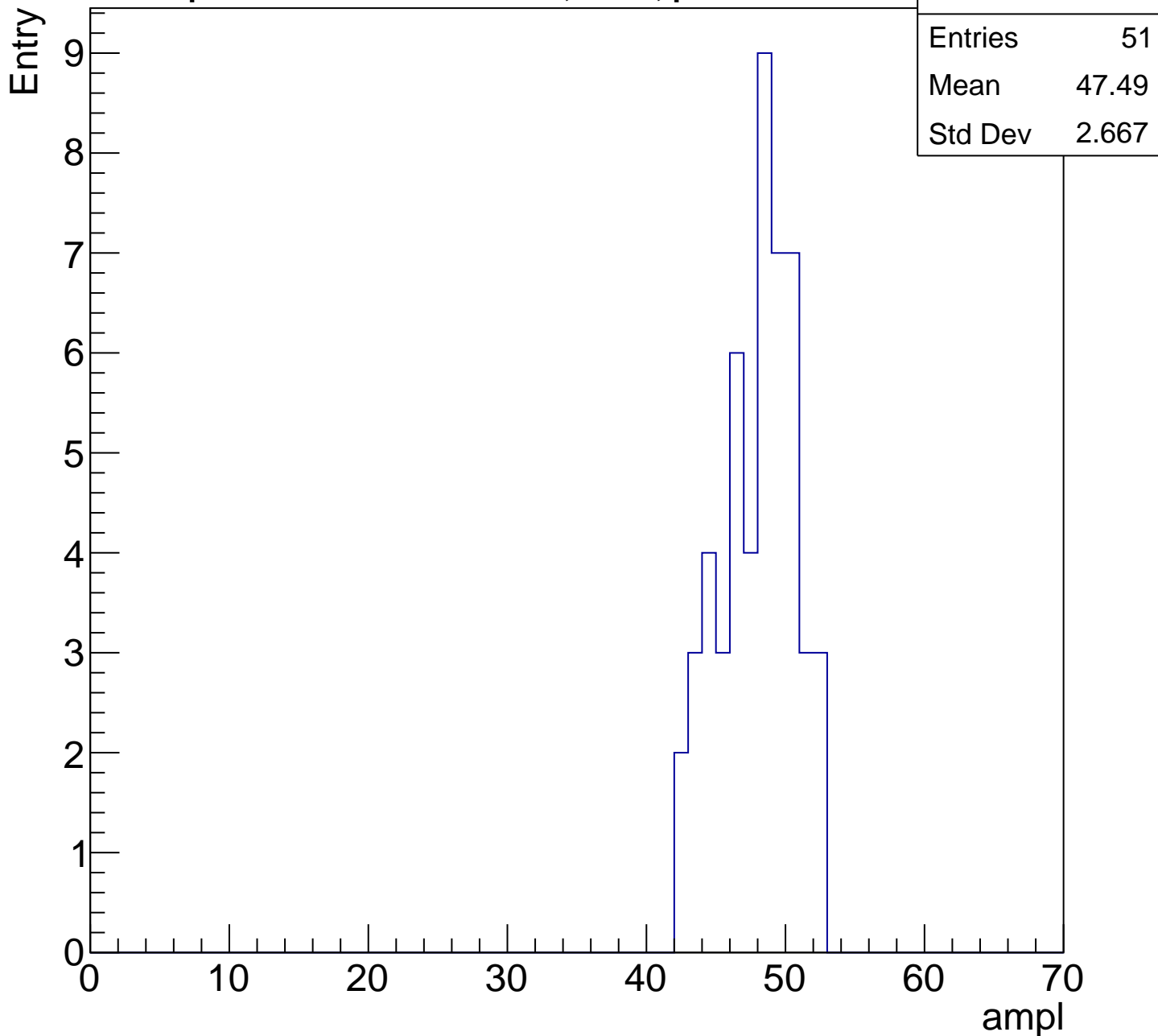
Gaus mean : 42.9668

Gaus Width: 3.4698



B1L101S, U6-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	53.53
Std Dev	3.125

Entry

10

8

6

4

2

0

0

10

20

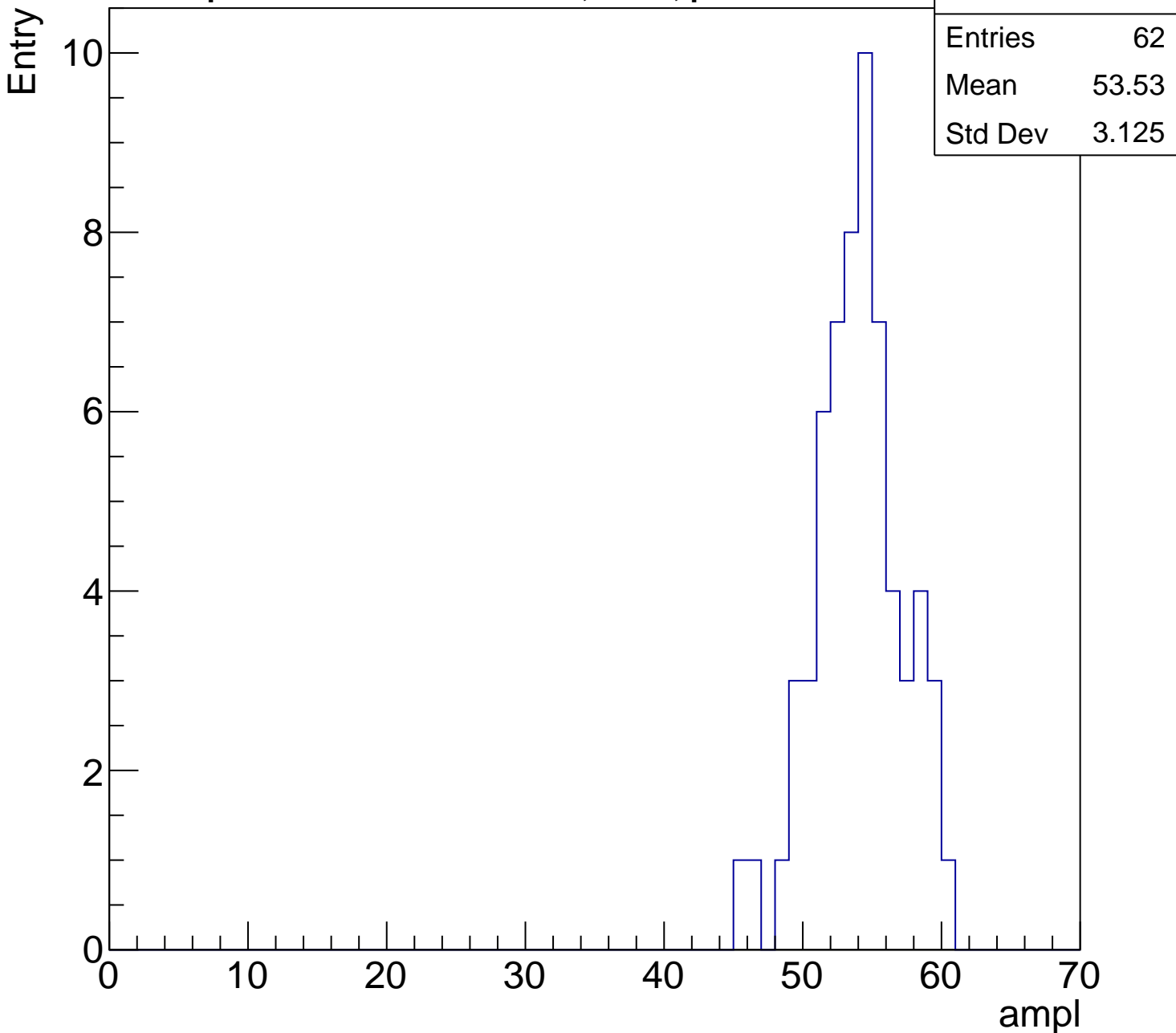
30

40

50

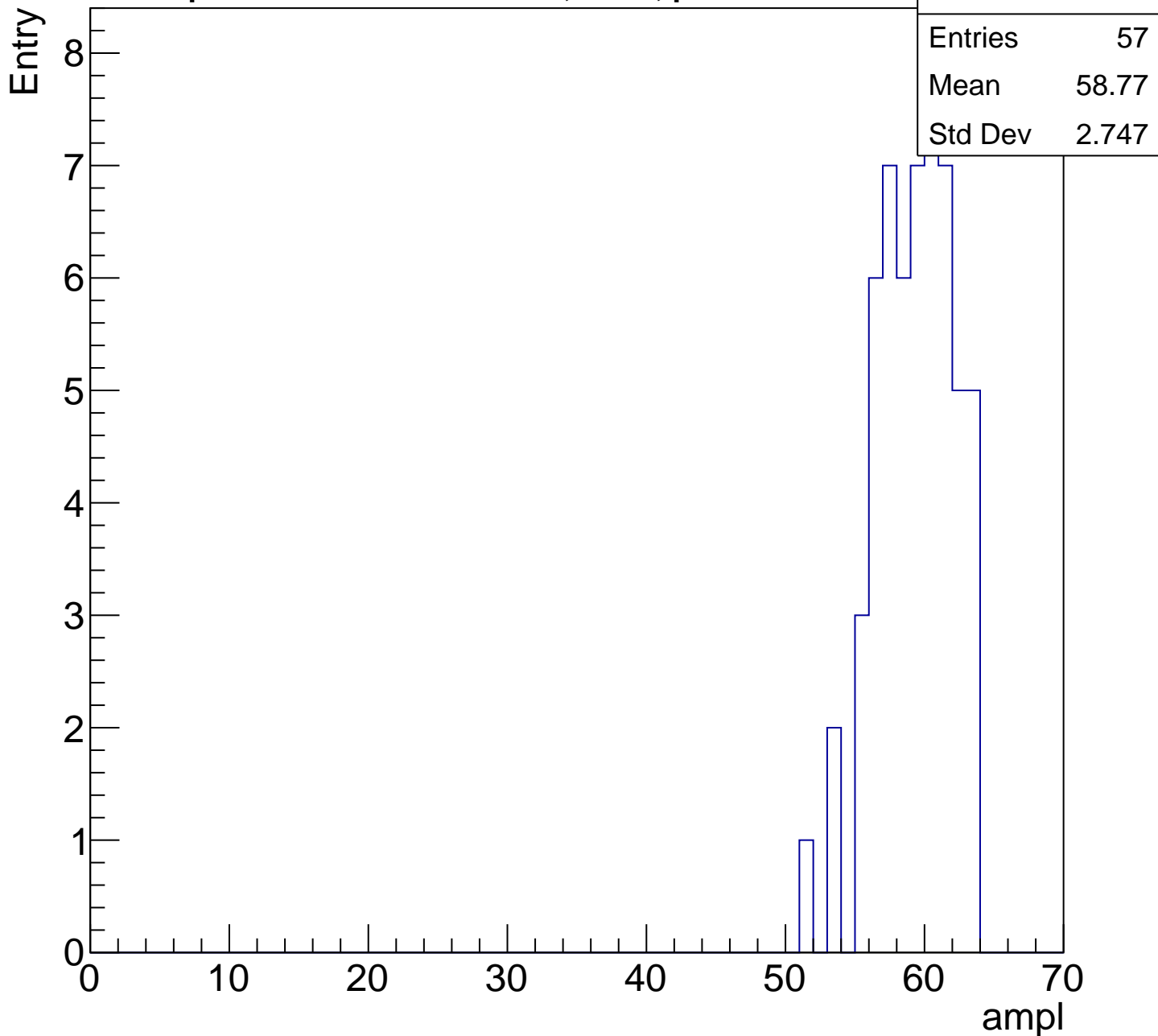
60

ampl



B1L101S, U6-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

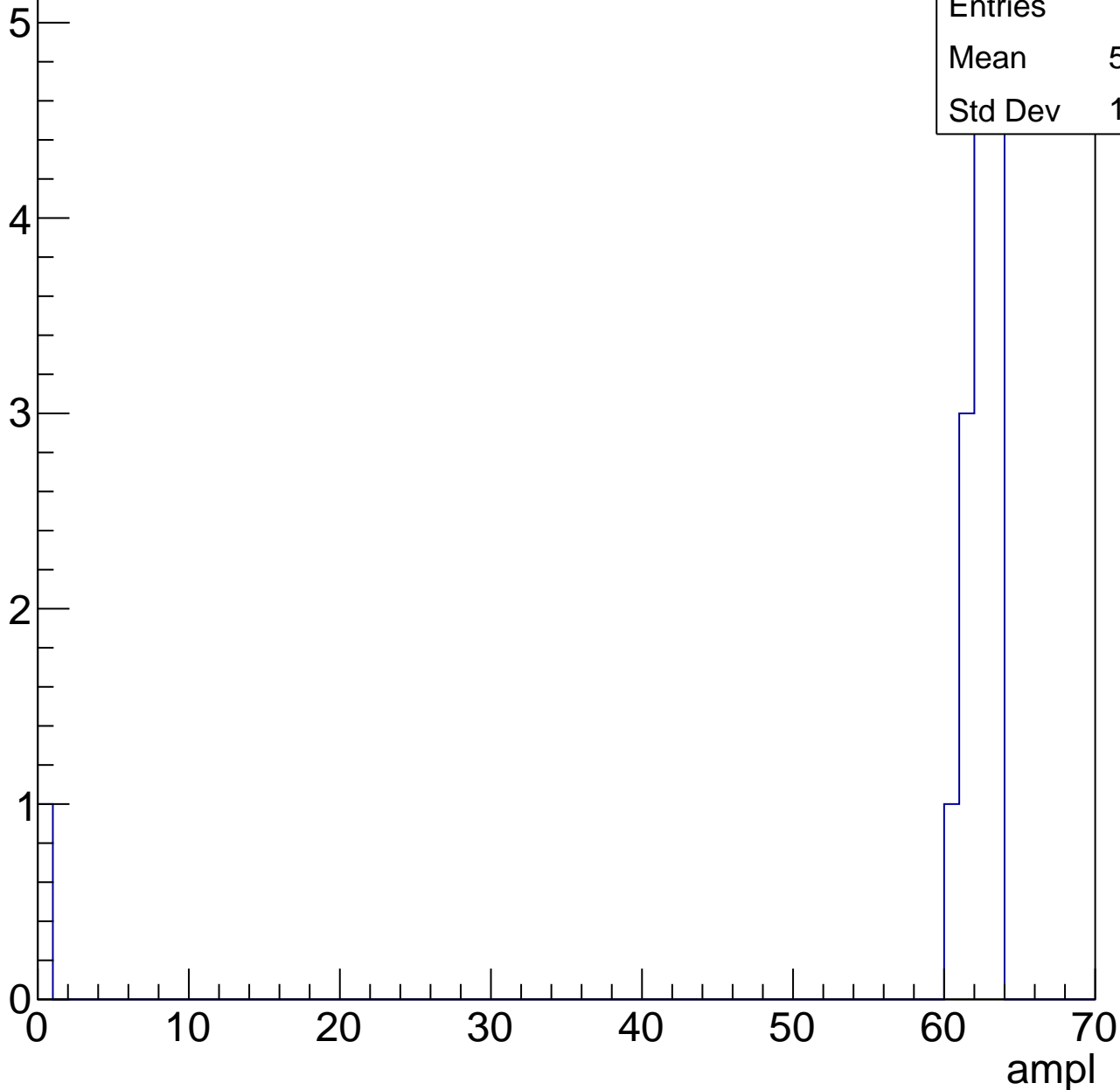


B1L101S, U6-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.87
Std Dev	15.49



B1L101S, U6-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch35, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.05
Std Dev	5.754

Gaus mean : 30.9050

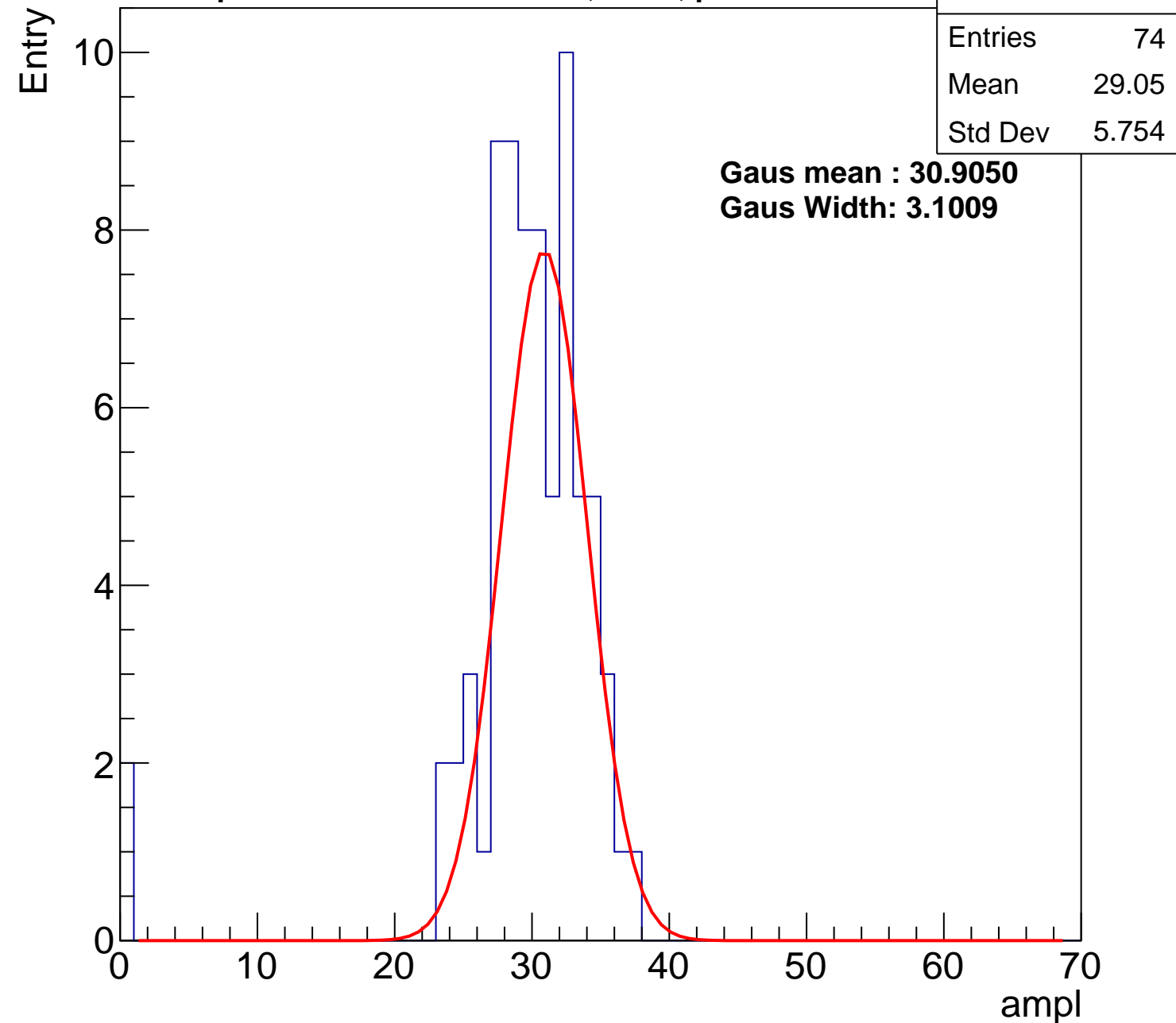
Gaus Width: 3.1009

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch35, adc1

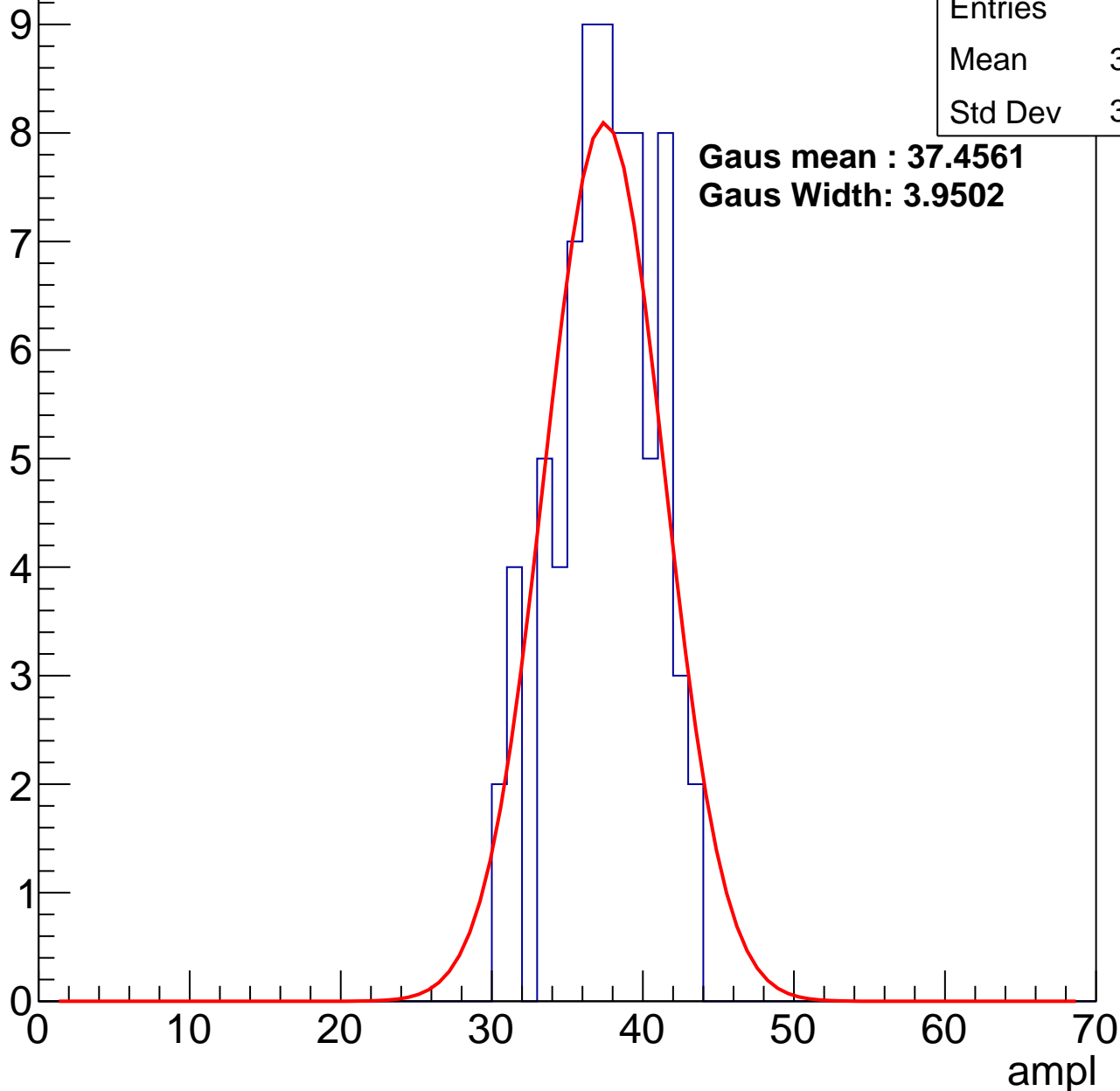
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.07
Std Dev	3.193

Gaus mean : 37.4561

Gaus Width: 3.9502



B1L101S, U6-ch35, adc2

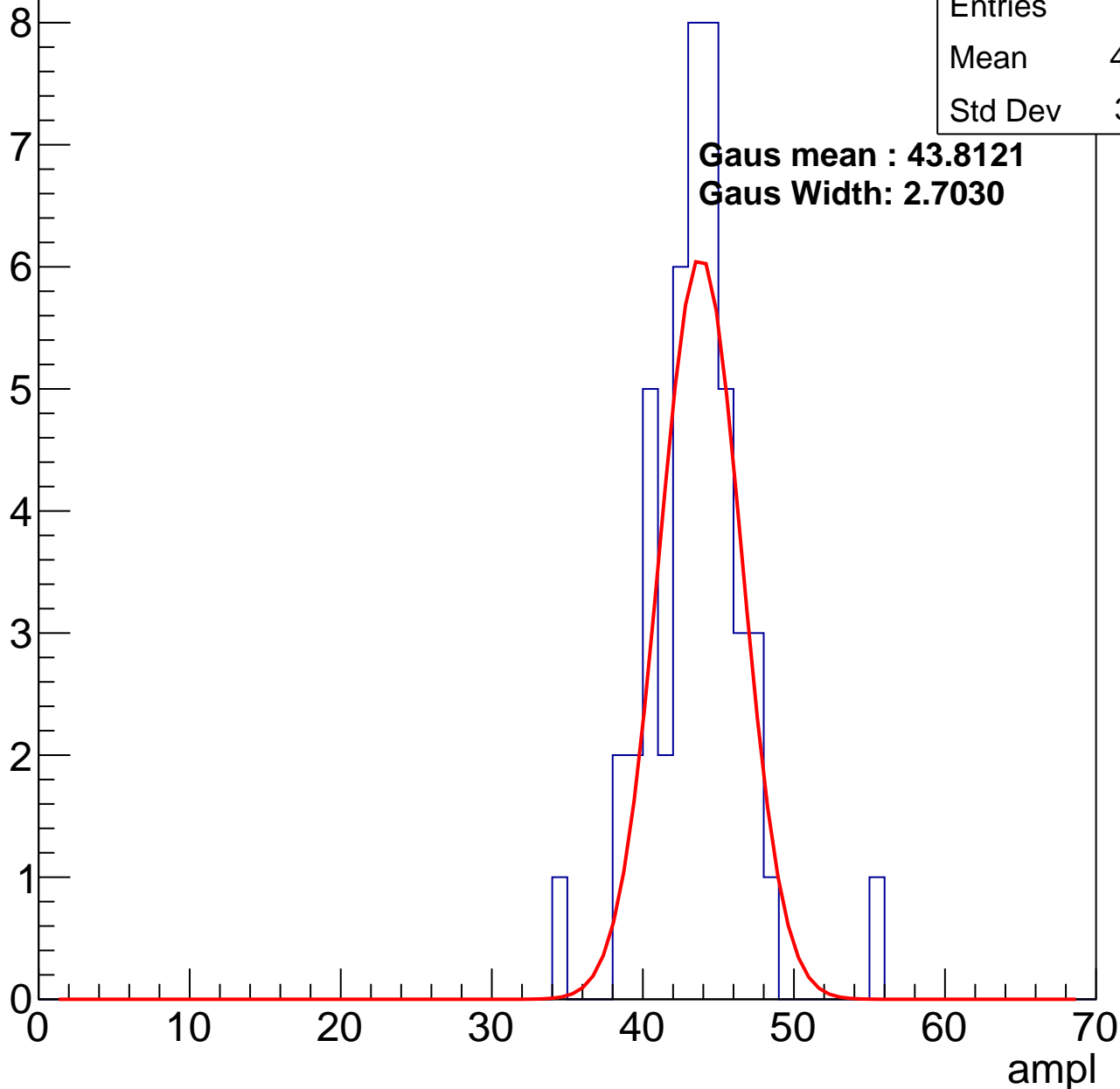
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	43.09
Std Dev	3.241

Gaus mean : 43.8121

Gaus Width: 2.7030

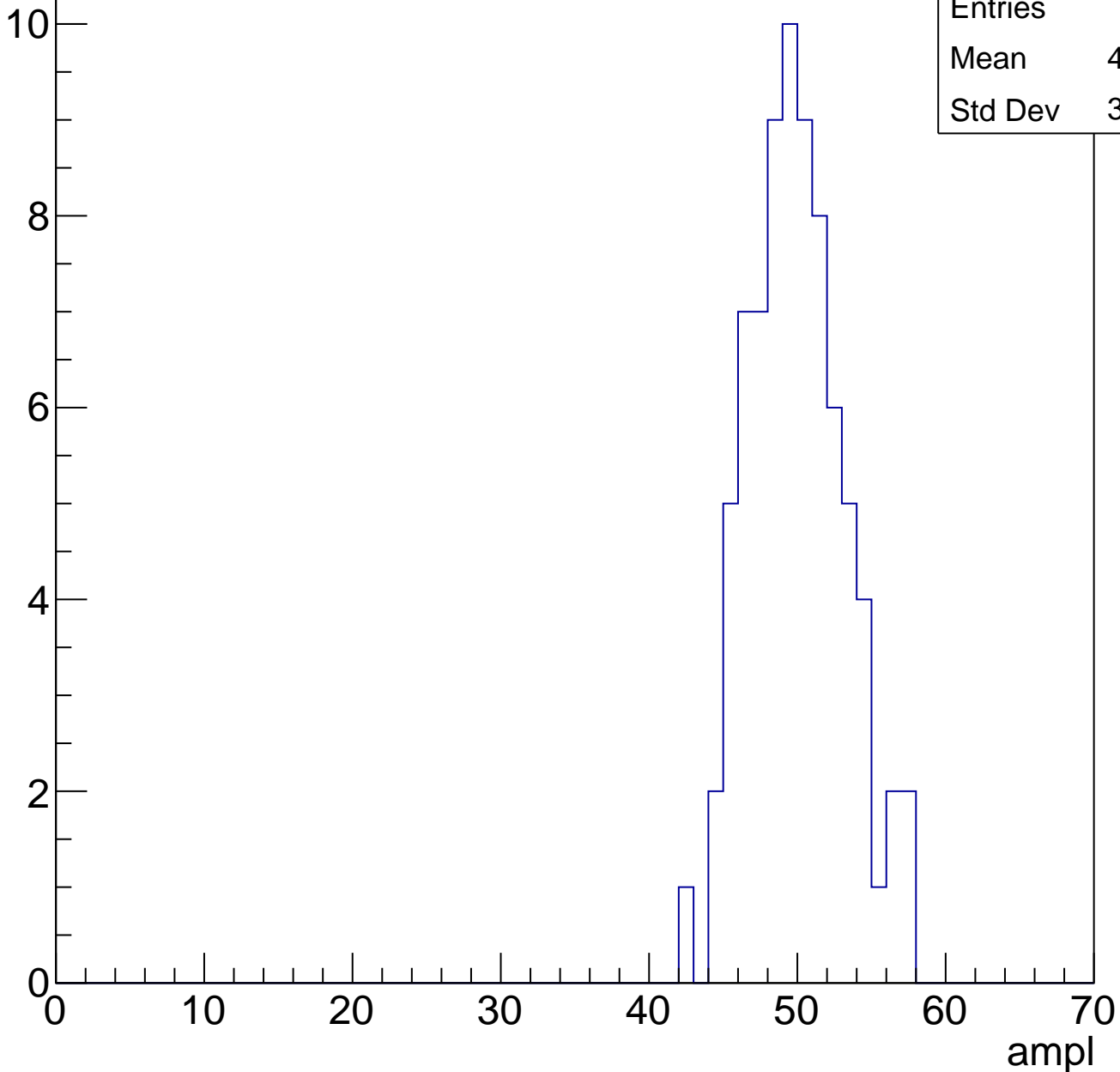


B1L101S, U6-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	49.49
Std Dev	3.198

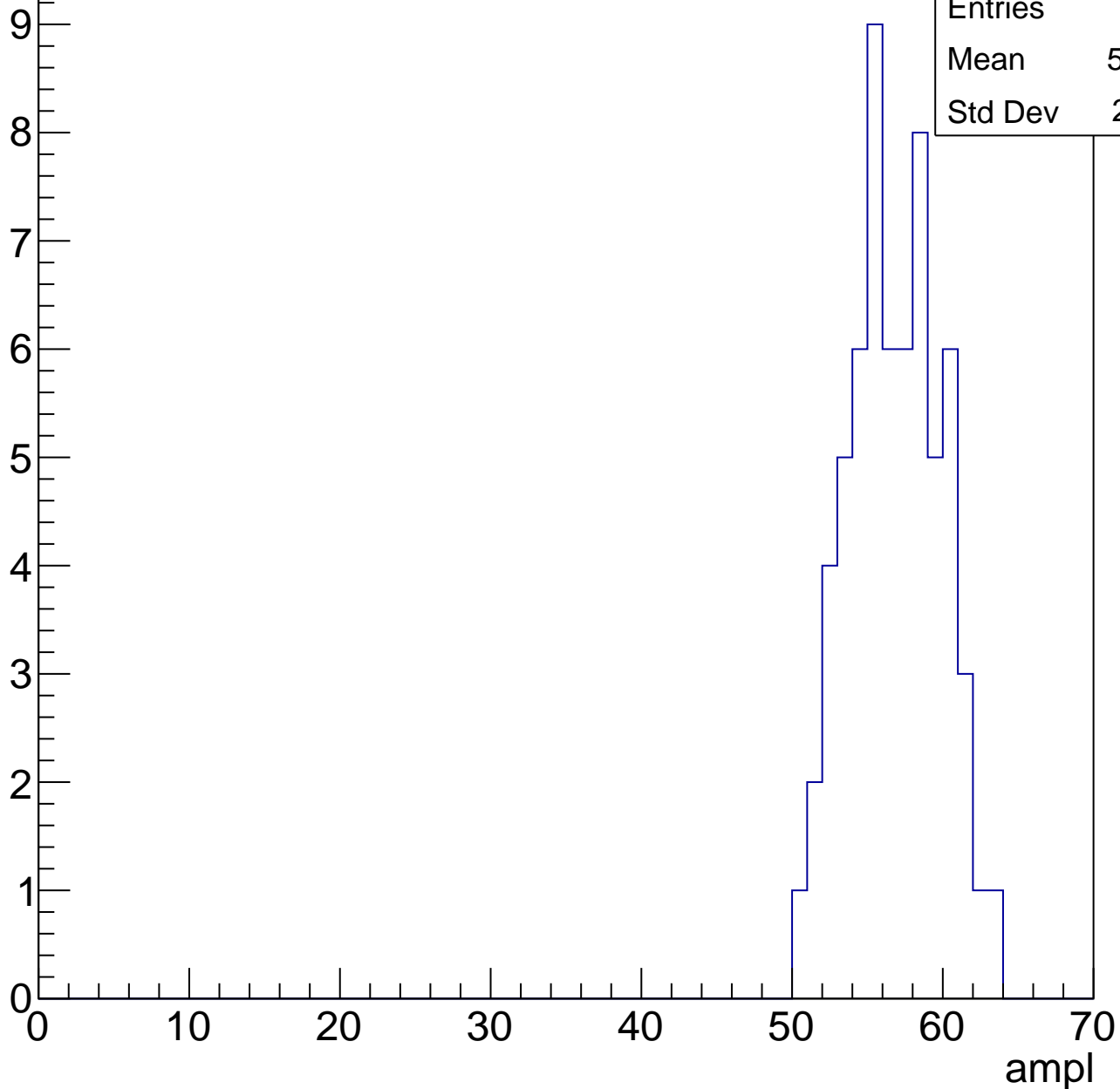
Entry



B1L101S, U6-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



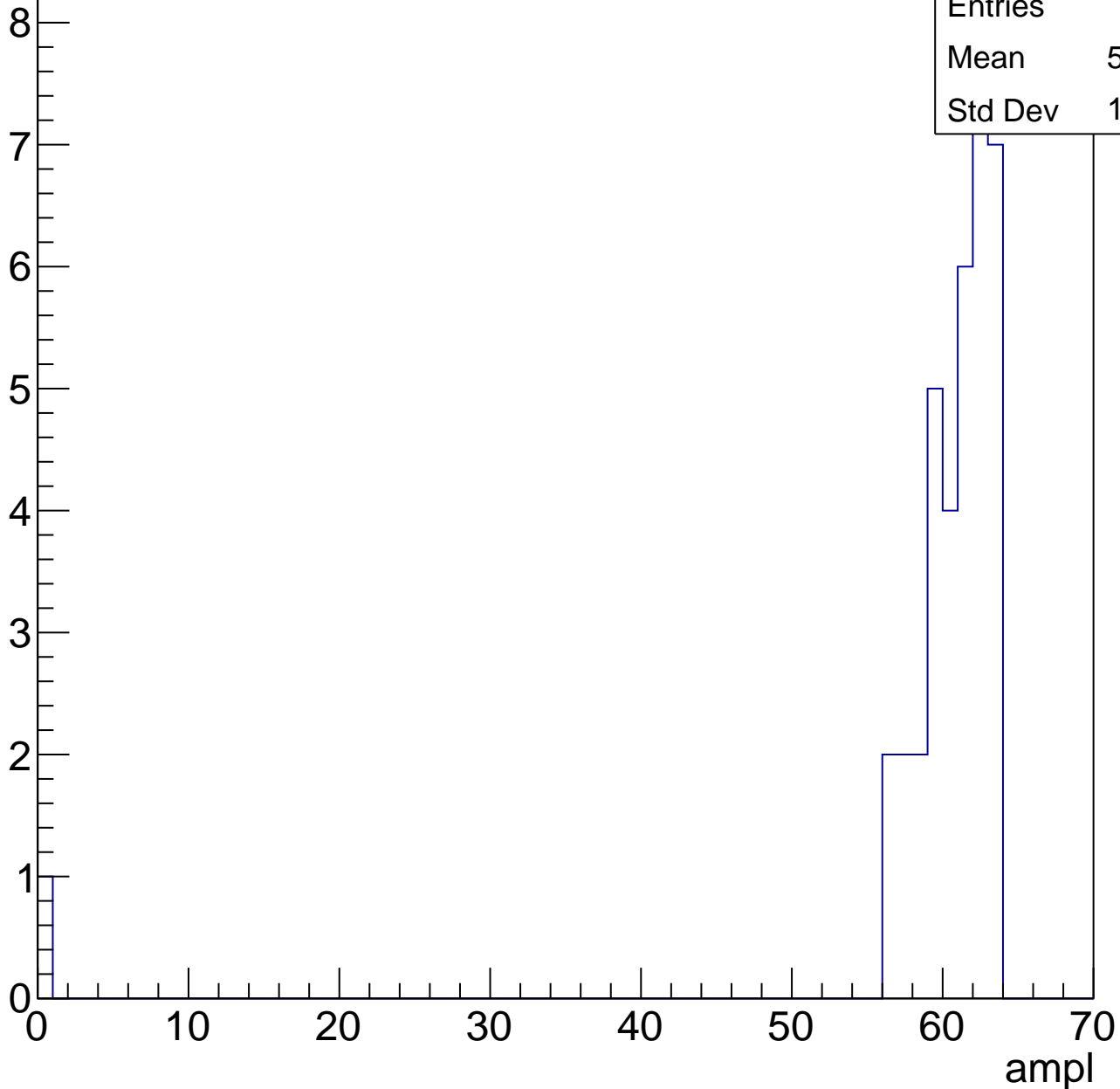
Entries	63
Mean	56.33
Std Dev	2.981

B1L101S, U6-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.92
Std Dev	10.03



B1L101S, U6-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

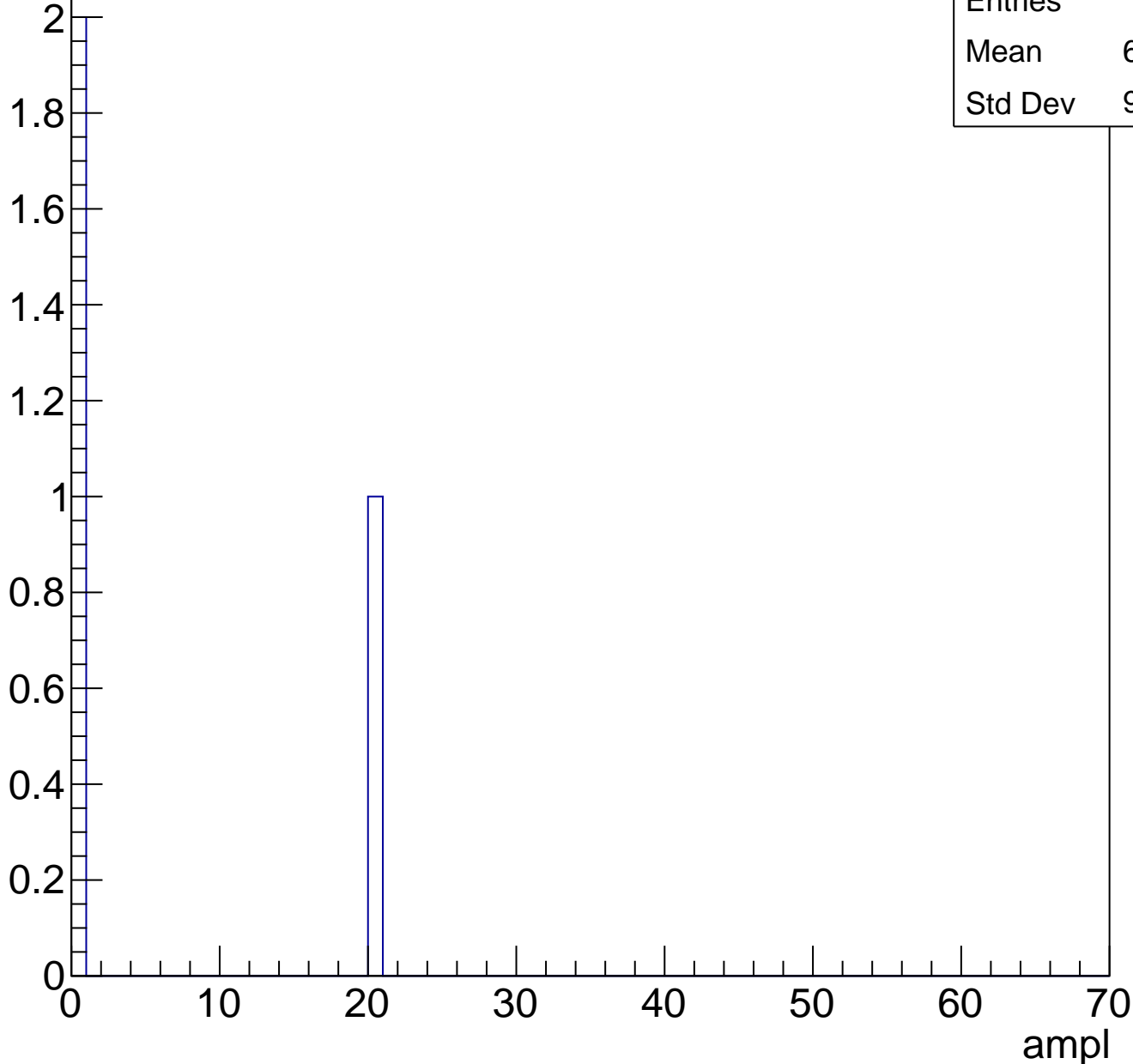
Entry



B1L101S, U6-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L101S, U6-ch36, adc0

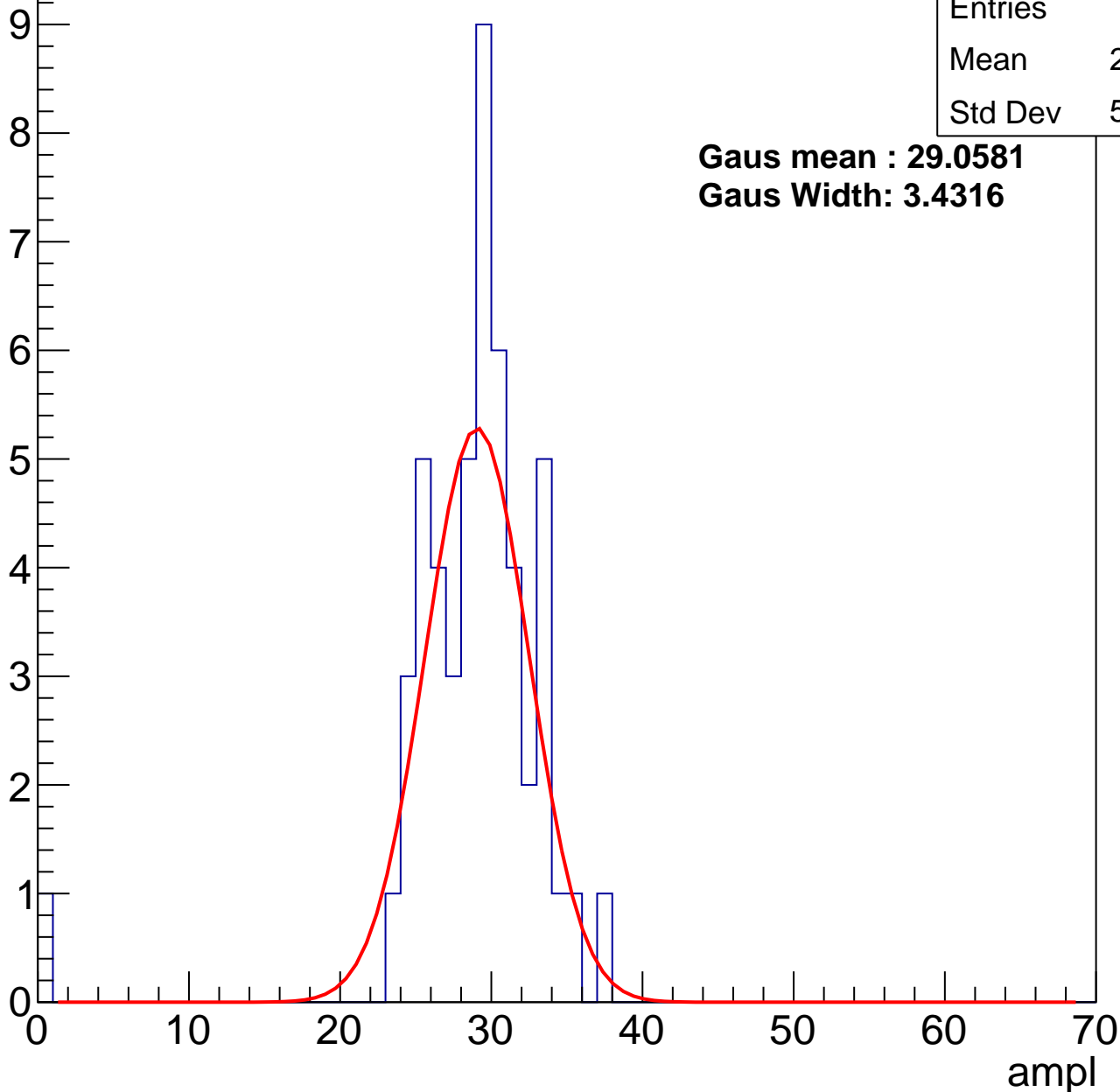
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	28.33
Std Dev	5.063

Gaus mean : 29.0581

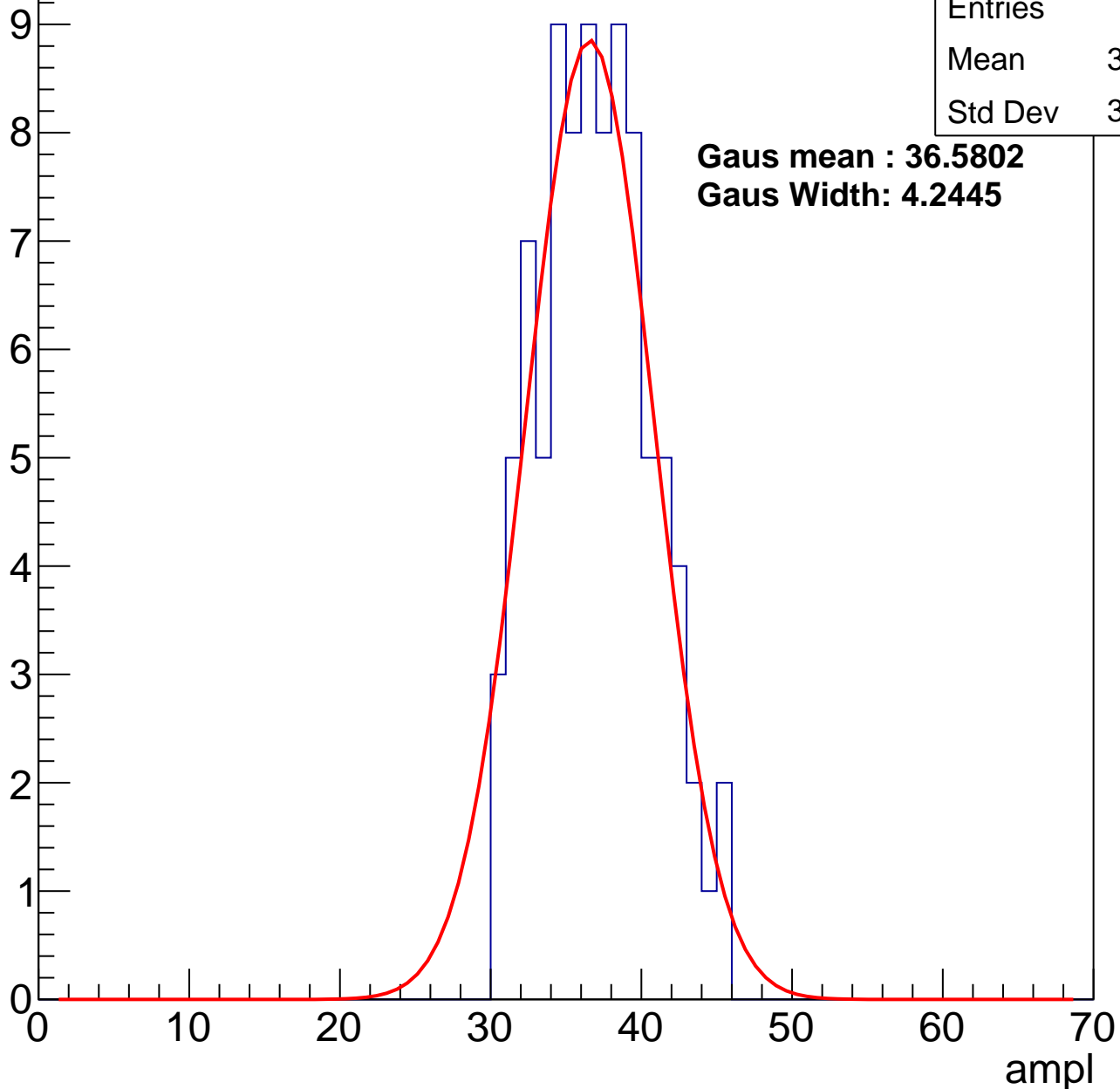
Gaus Width: 3.4316



B1L101S, U6-ch36, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch36, adc2

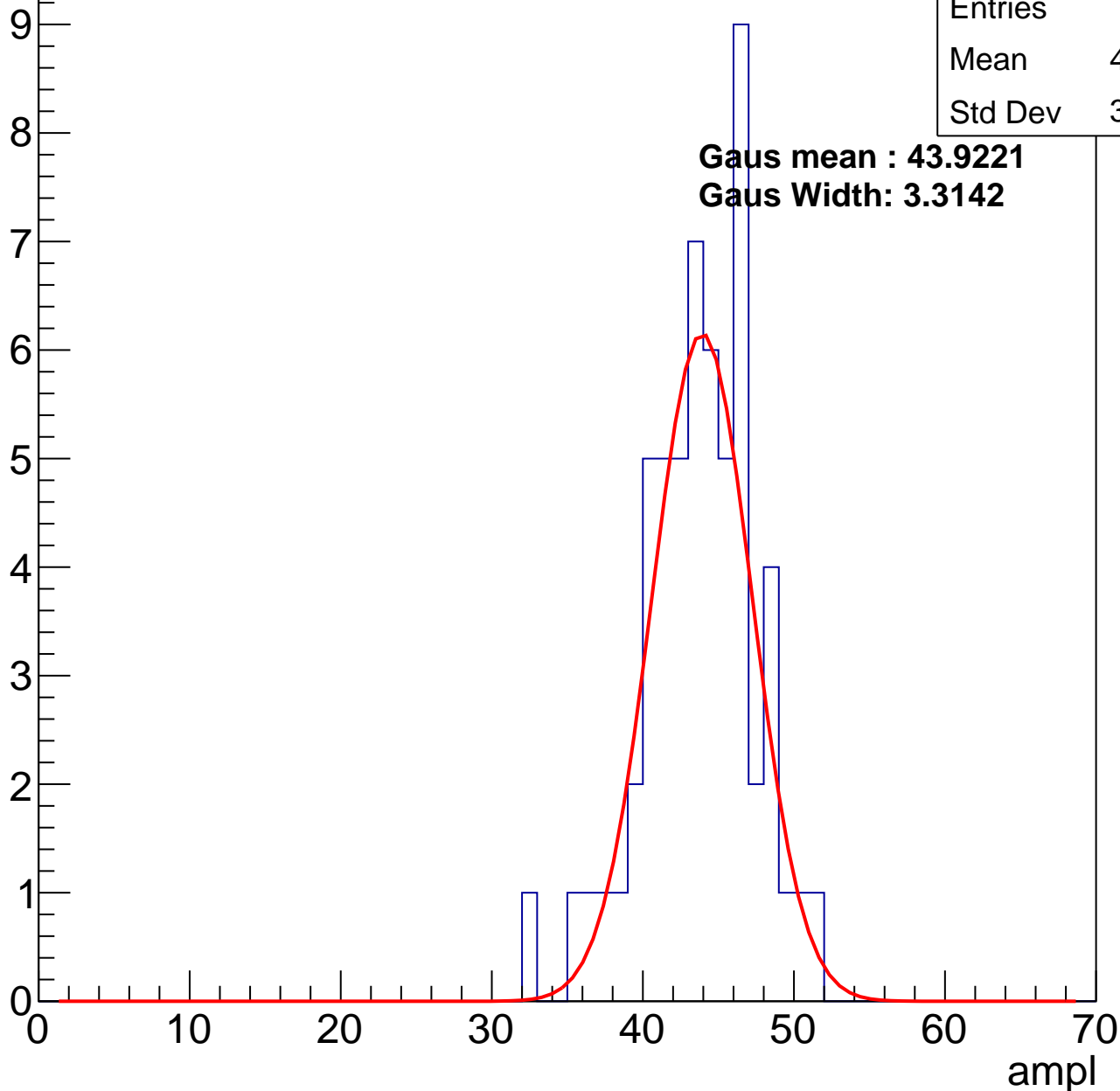
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.29
Std Dev	3.667

Gaus mean : 43.9221

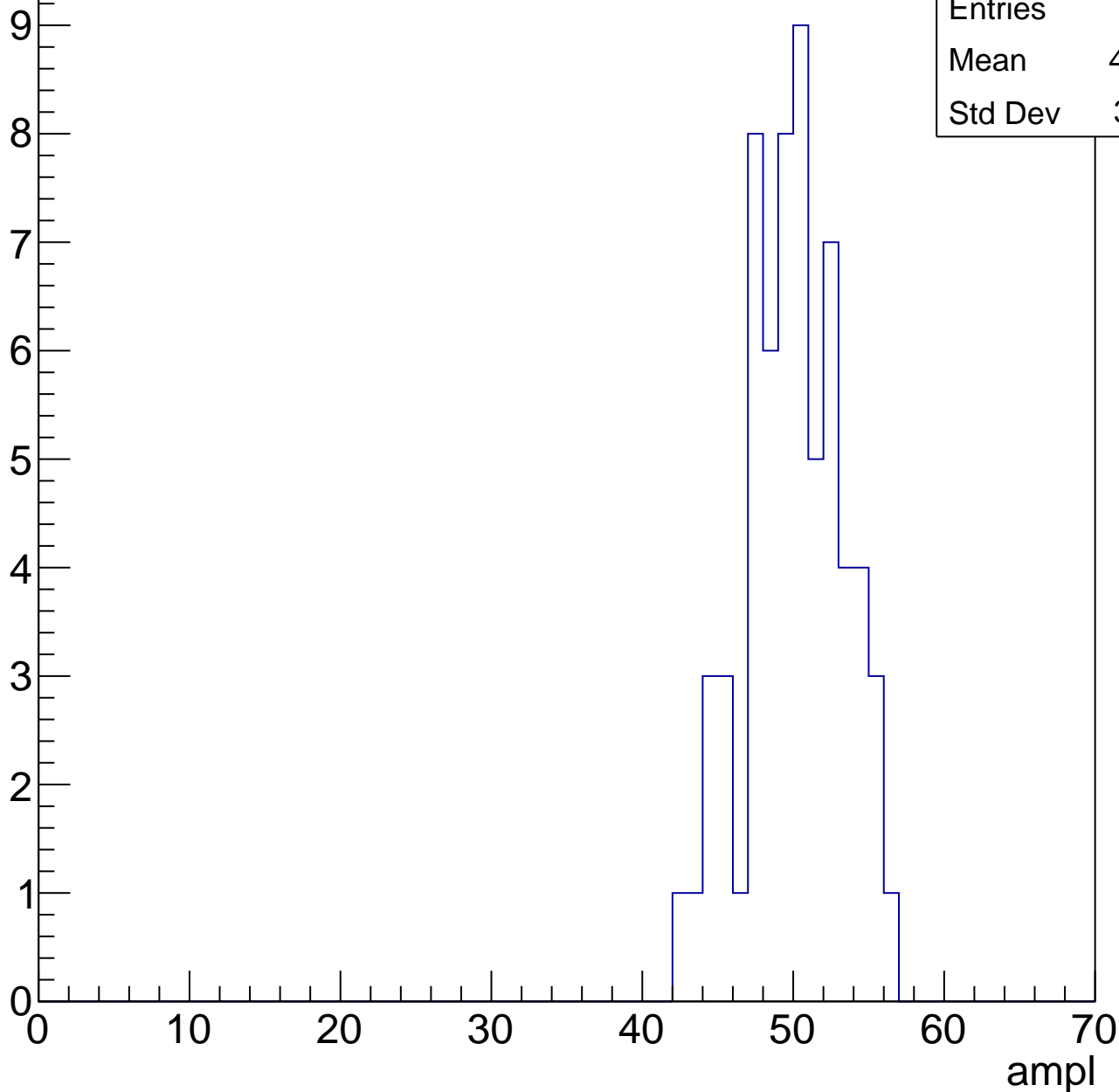
Gaus Width: 3.3142



B1L101S, U6-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

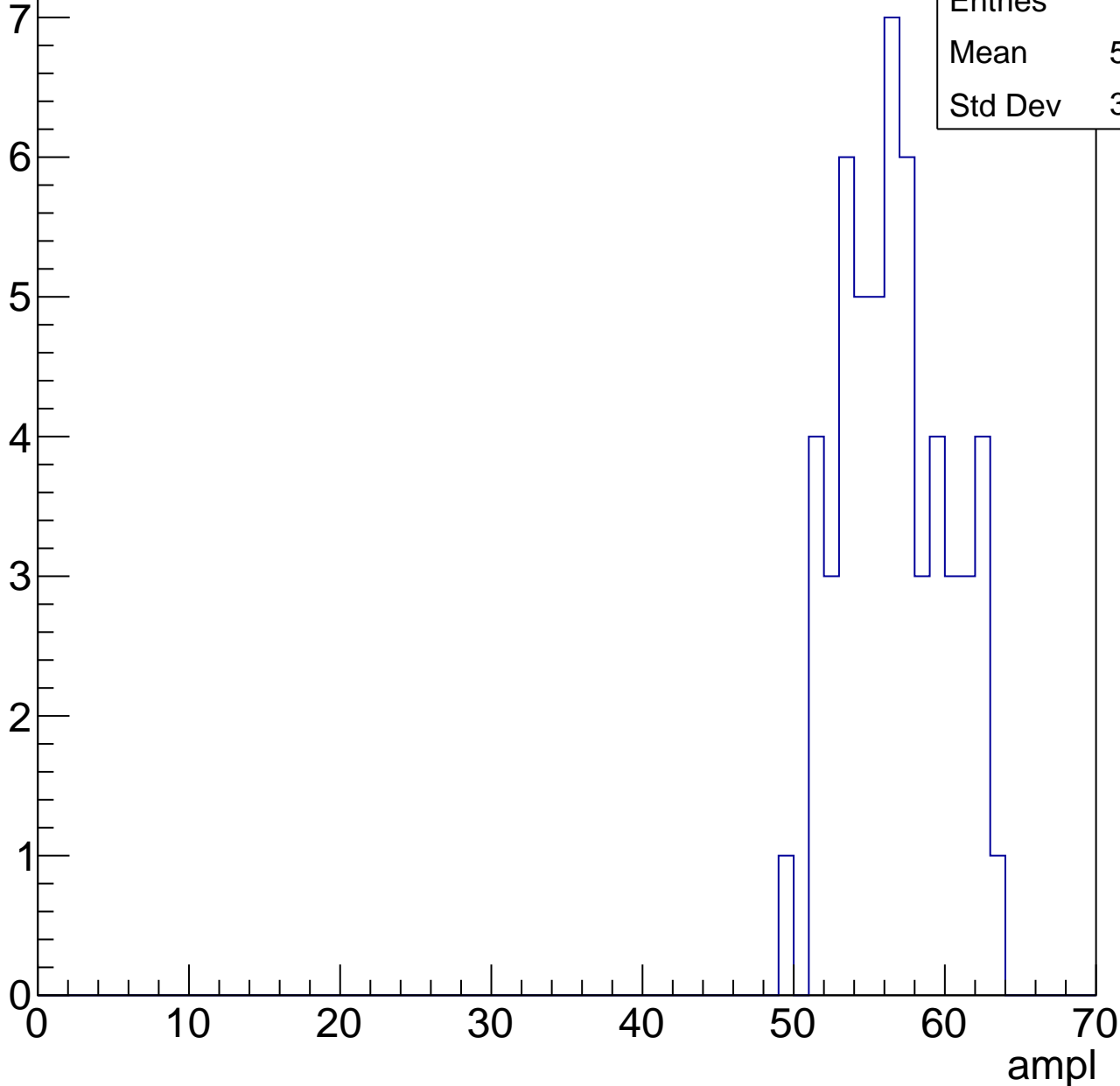


B1L101S, U6-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.18
Std Dev	3.422

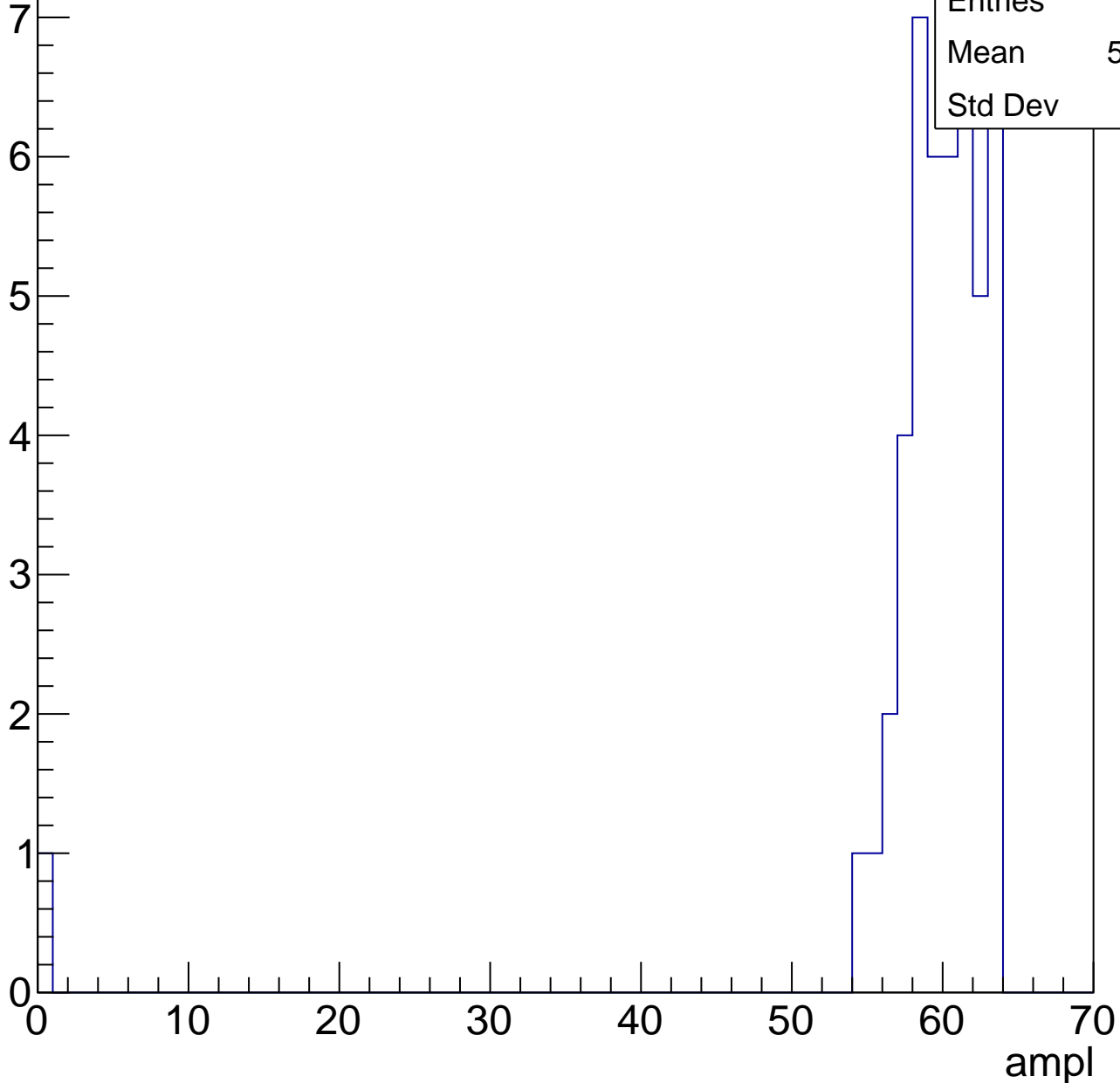


B1L101S, U6-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.45
Std Dev	8.92



B1L101S, U6-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

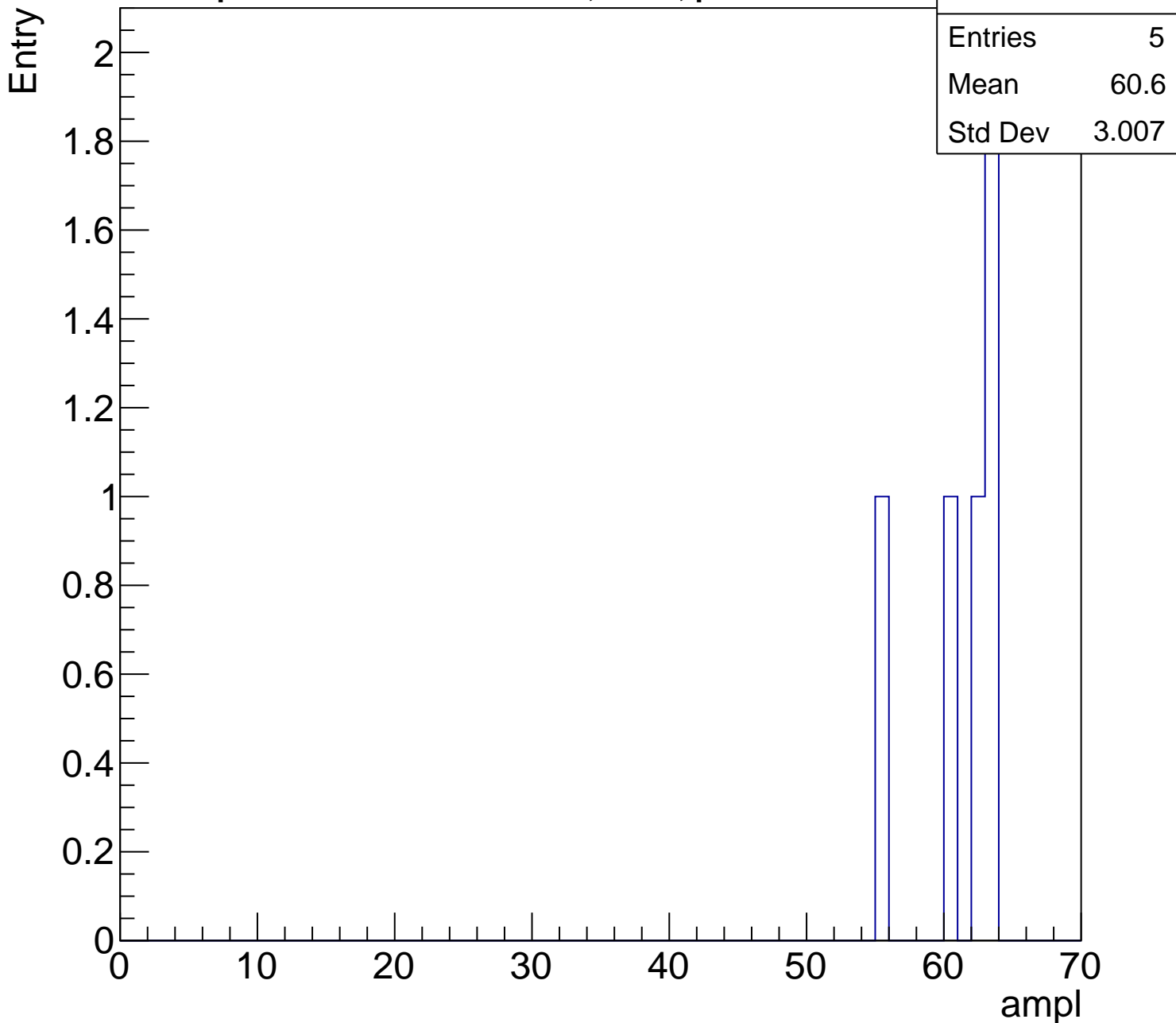
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.6
Std Dev	3.007

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch37, adc0

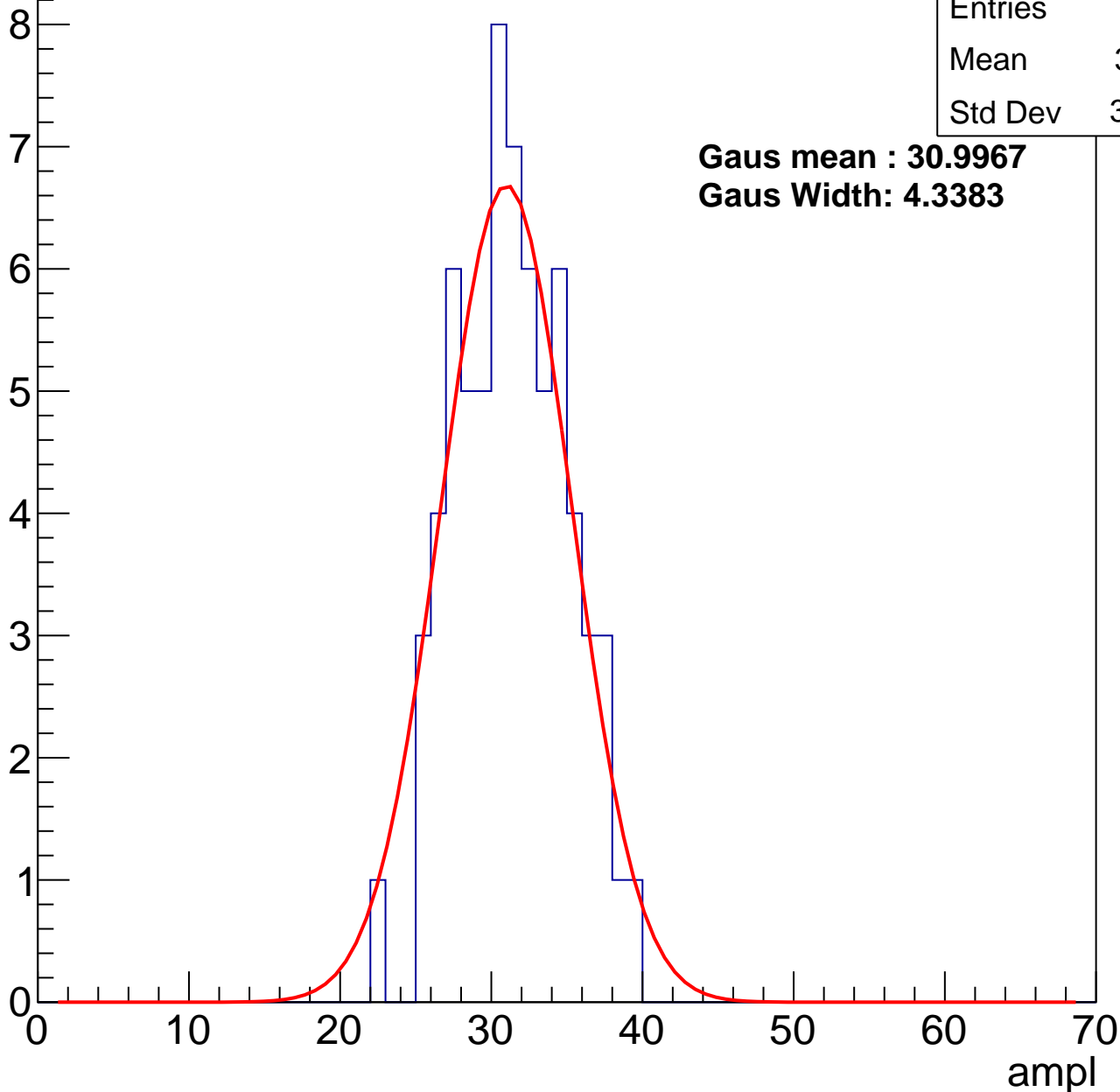
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.91
Std Dev	3.633

Gaus mean : 30.9967

Gaus Width: 4.3383



B1L101S, U6-ch37, adc1

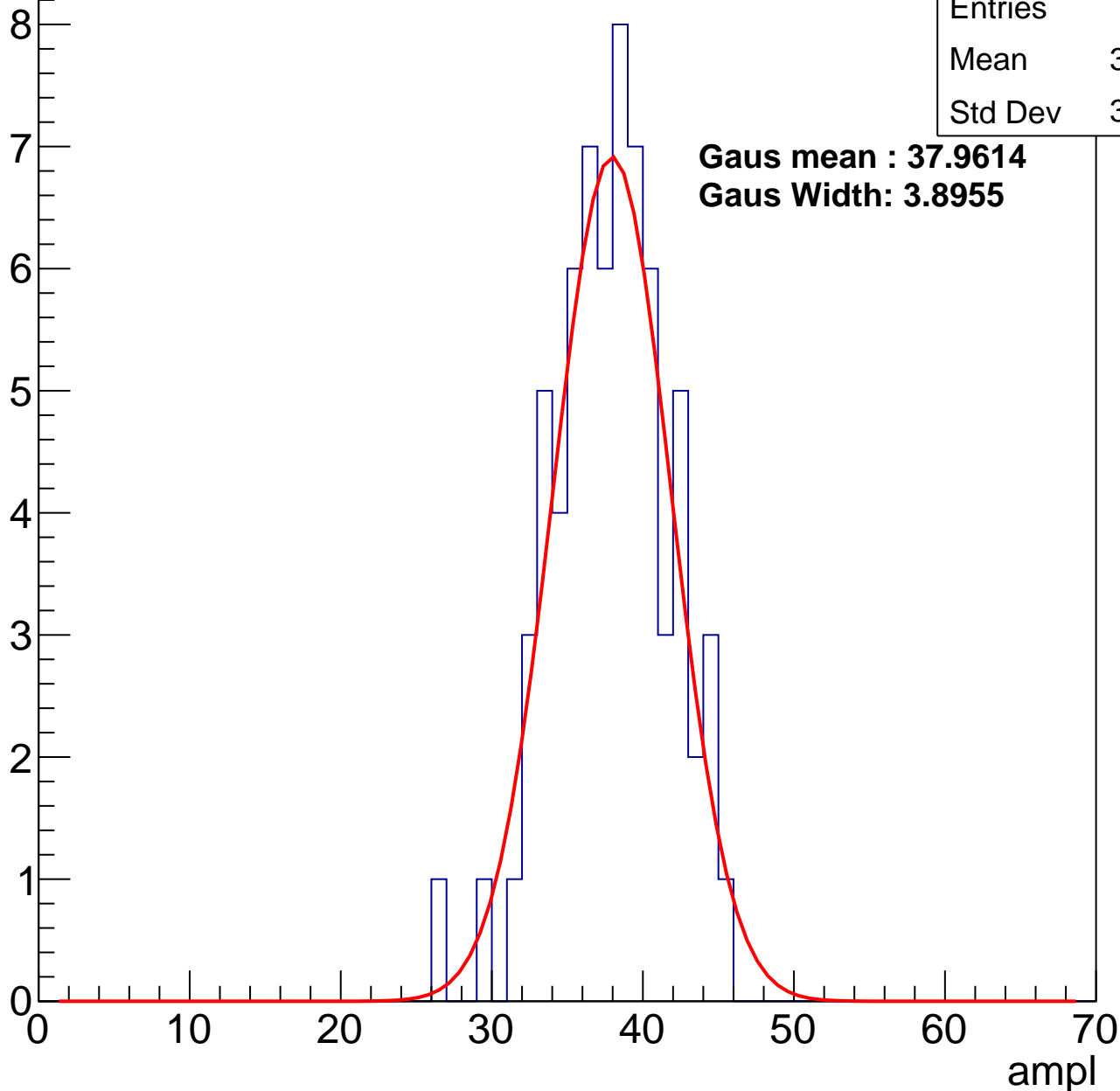
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.39
Std Dev	3.766

Gaus mean : 37.9614

Gaus Width: 3.8955



B1L101S, U6-ch37, adc2

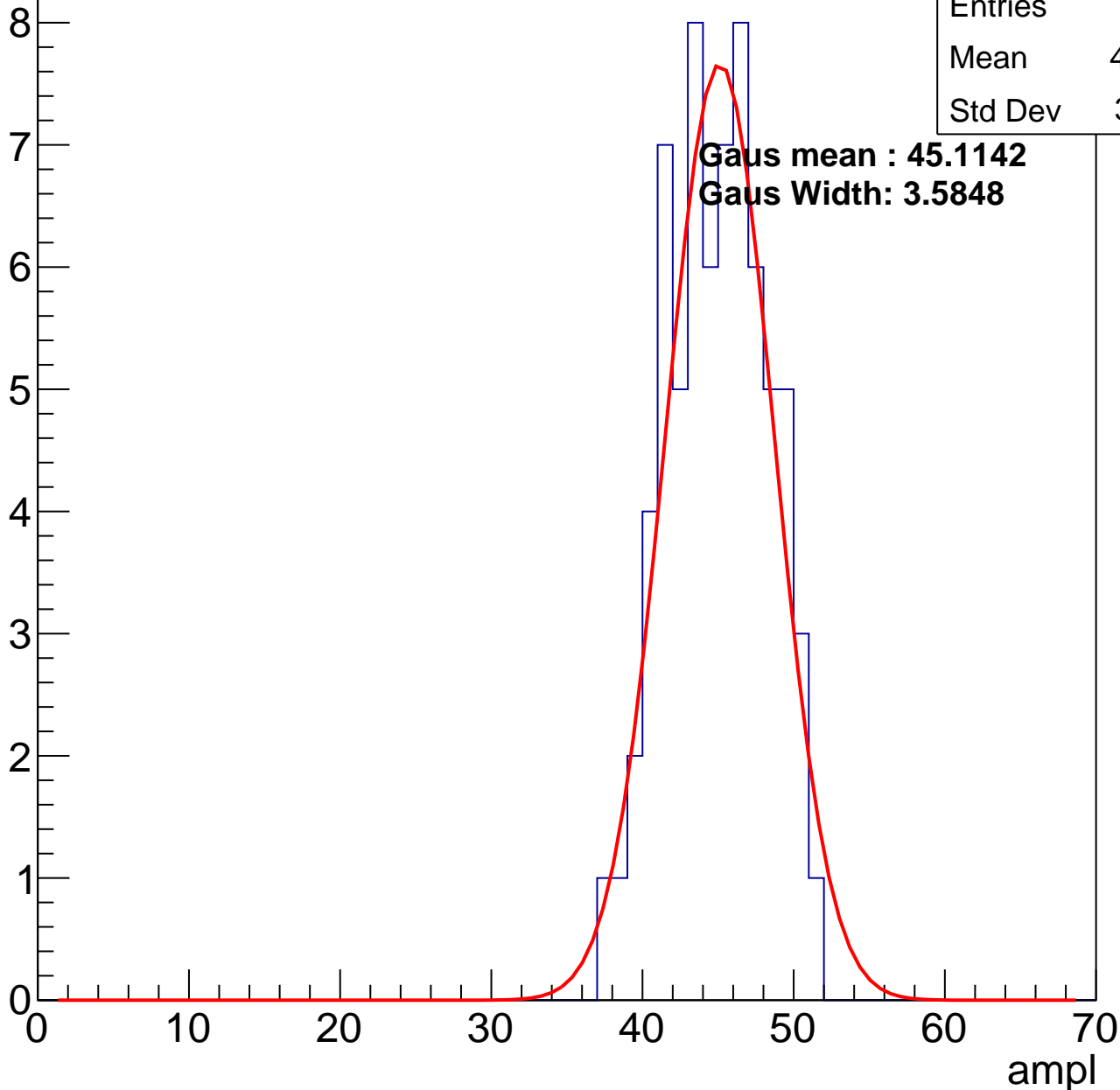
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.48
Std Dev	3.251

Gaus mean : 45.1142

Gaus Width: 3.5848

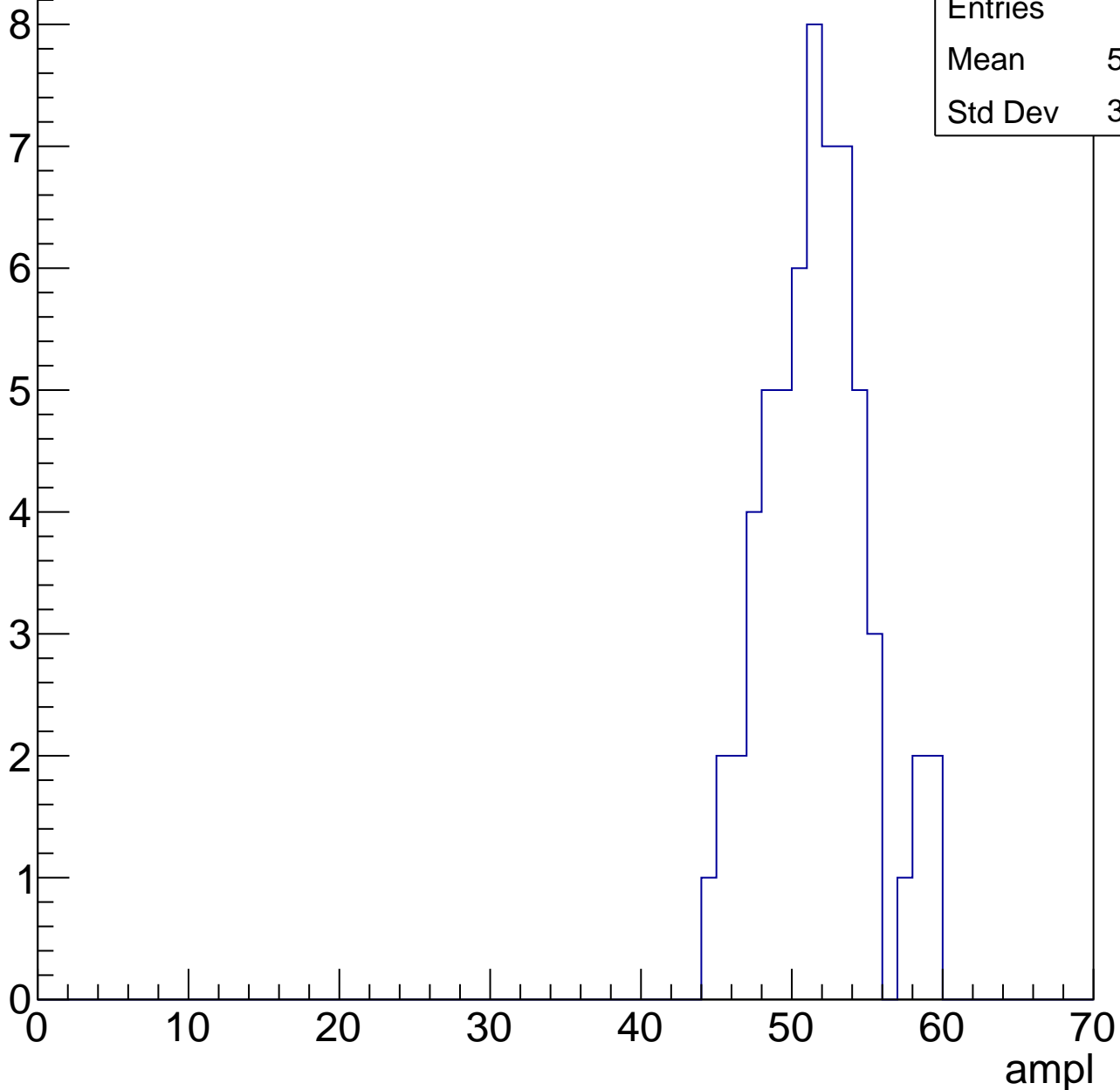


B1L101S, U6-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

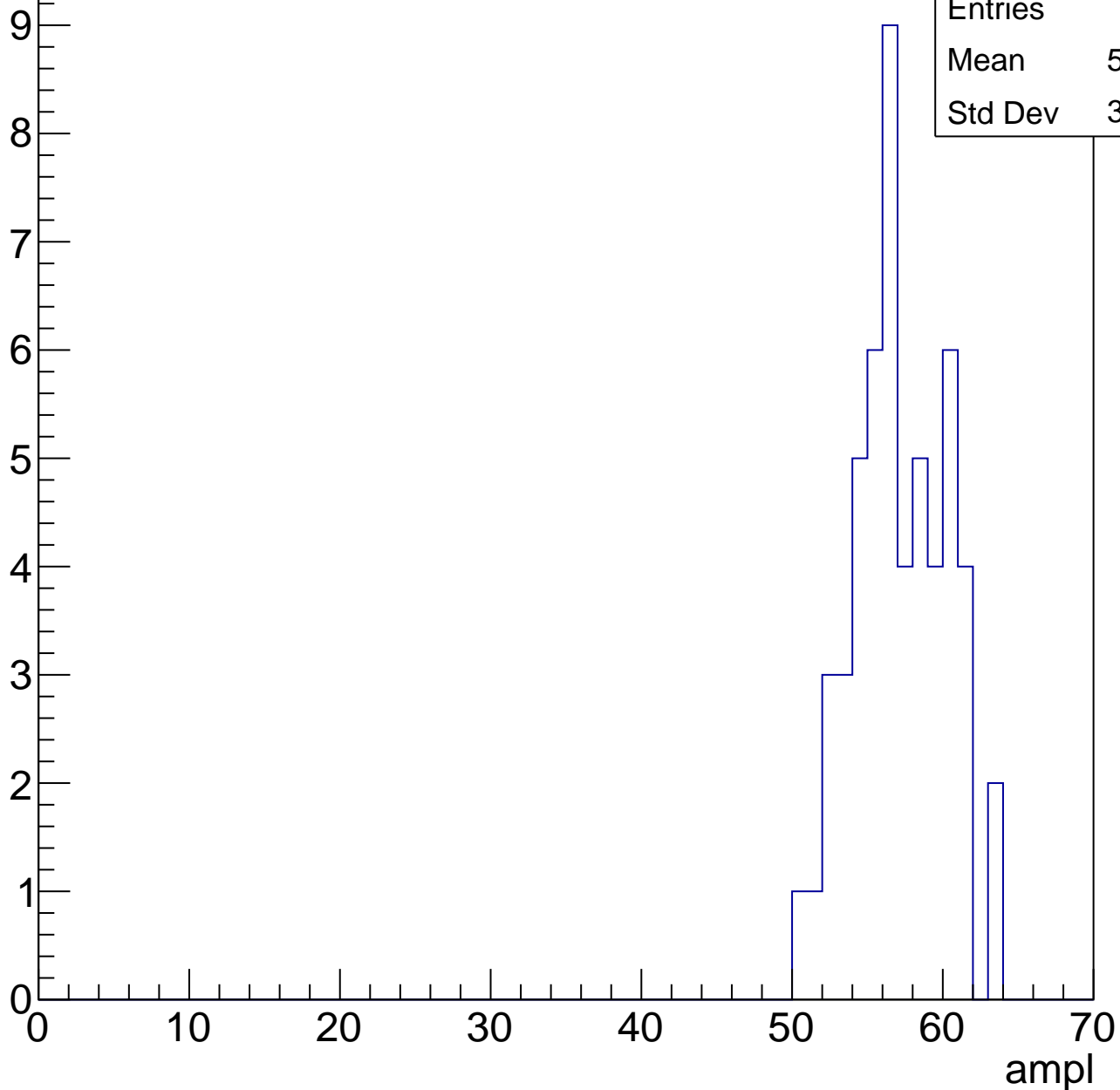
Entries	60
Mean	51.13
Std Dev	3.403



B1L101S, U6-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



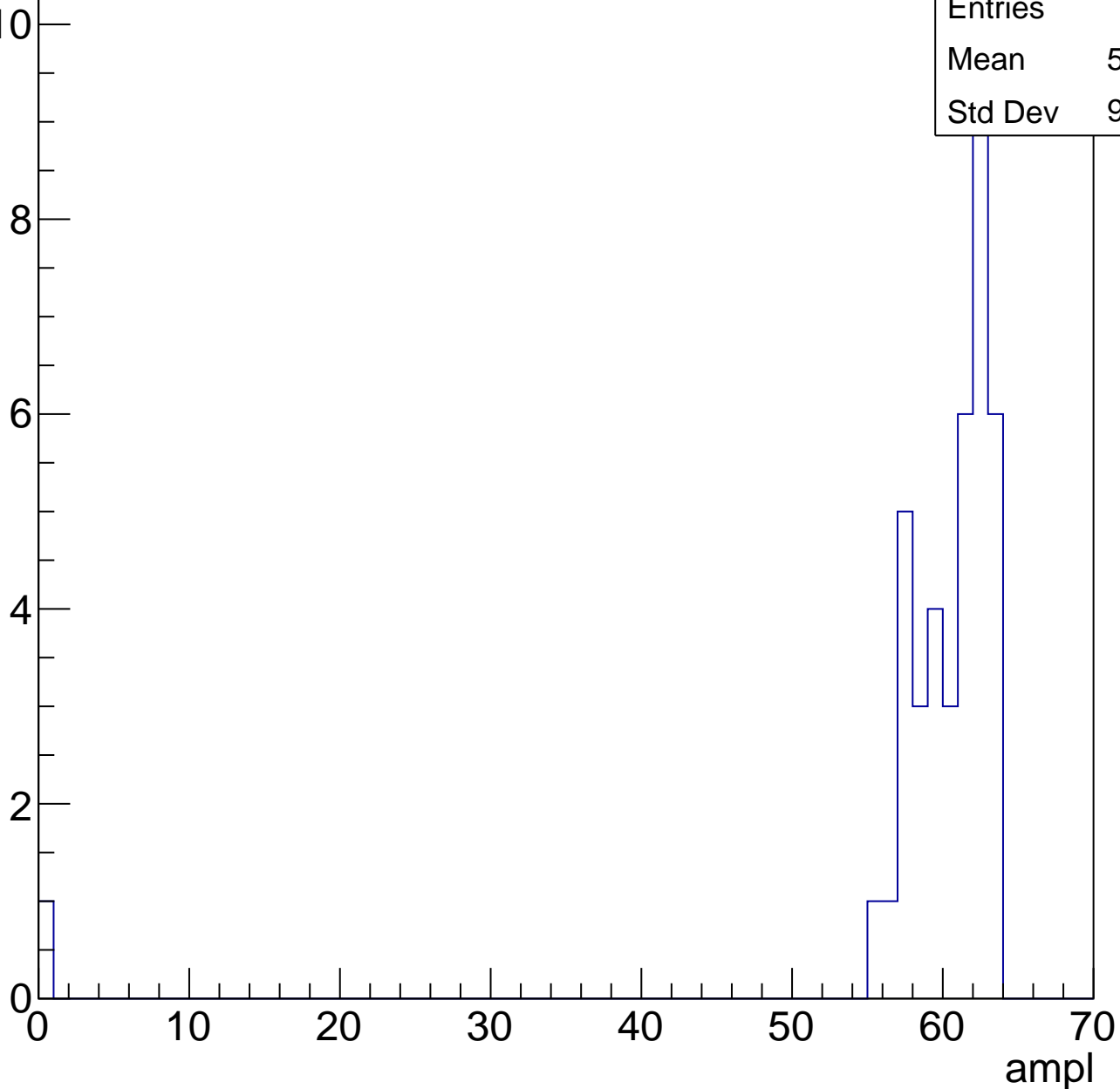
Entries	53
Mean	56.68
Std Dev	3.045

B1L101S, U6-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.75
Std Dev	9.669



B1L101S, U6-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L101S, U6-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch38, adc0

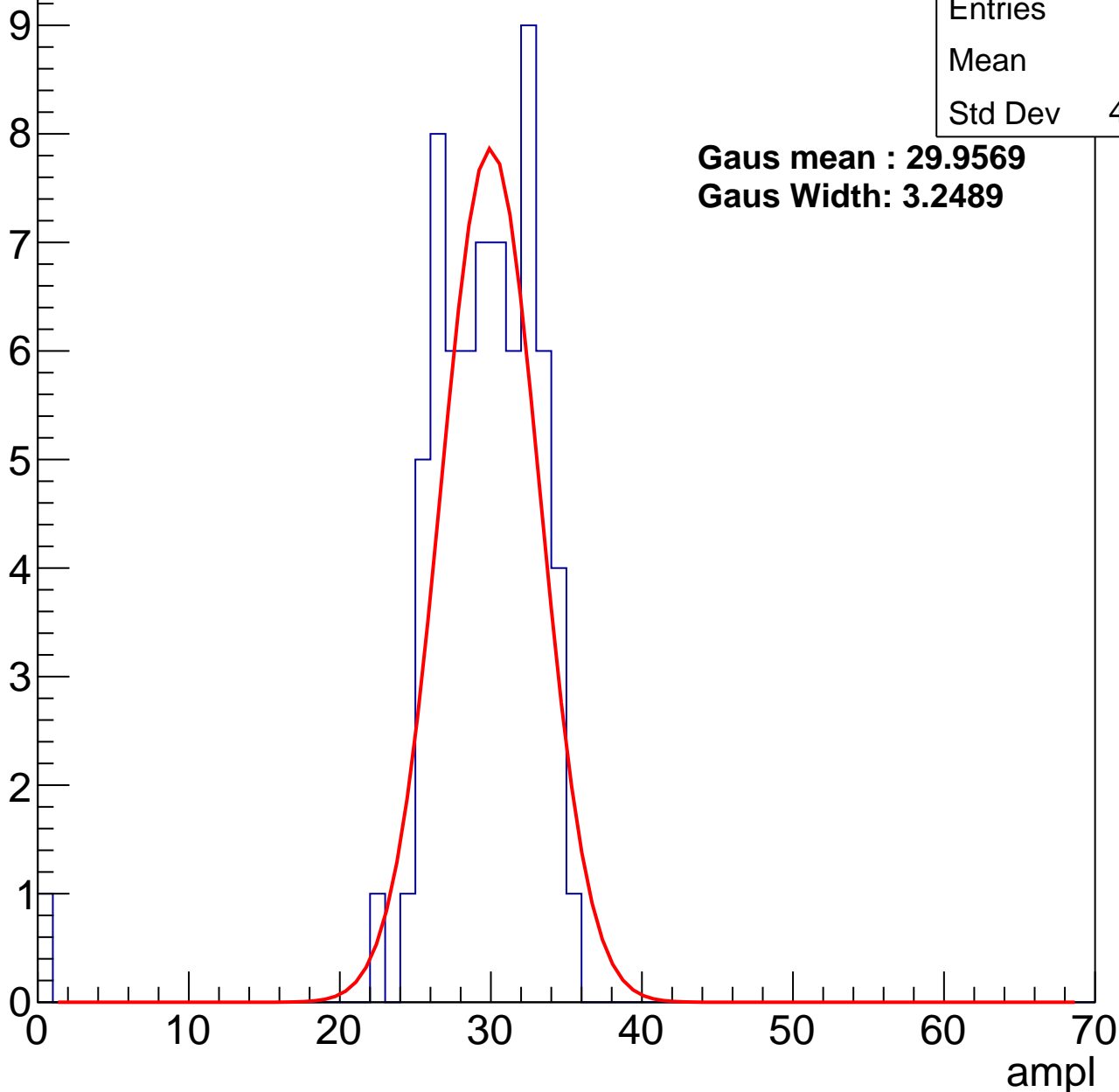
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.9
Std Dev	4.602

Gaus mean : 29.9569

Gaus Width: 3.2489



B1L101S, U6-ch38, adc1

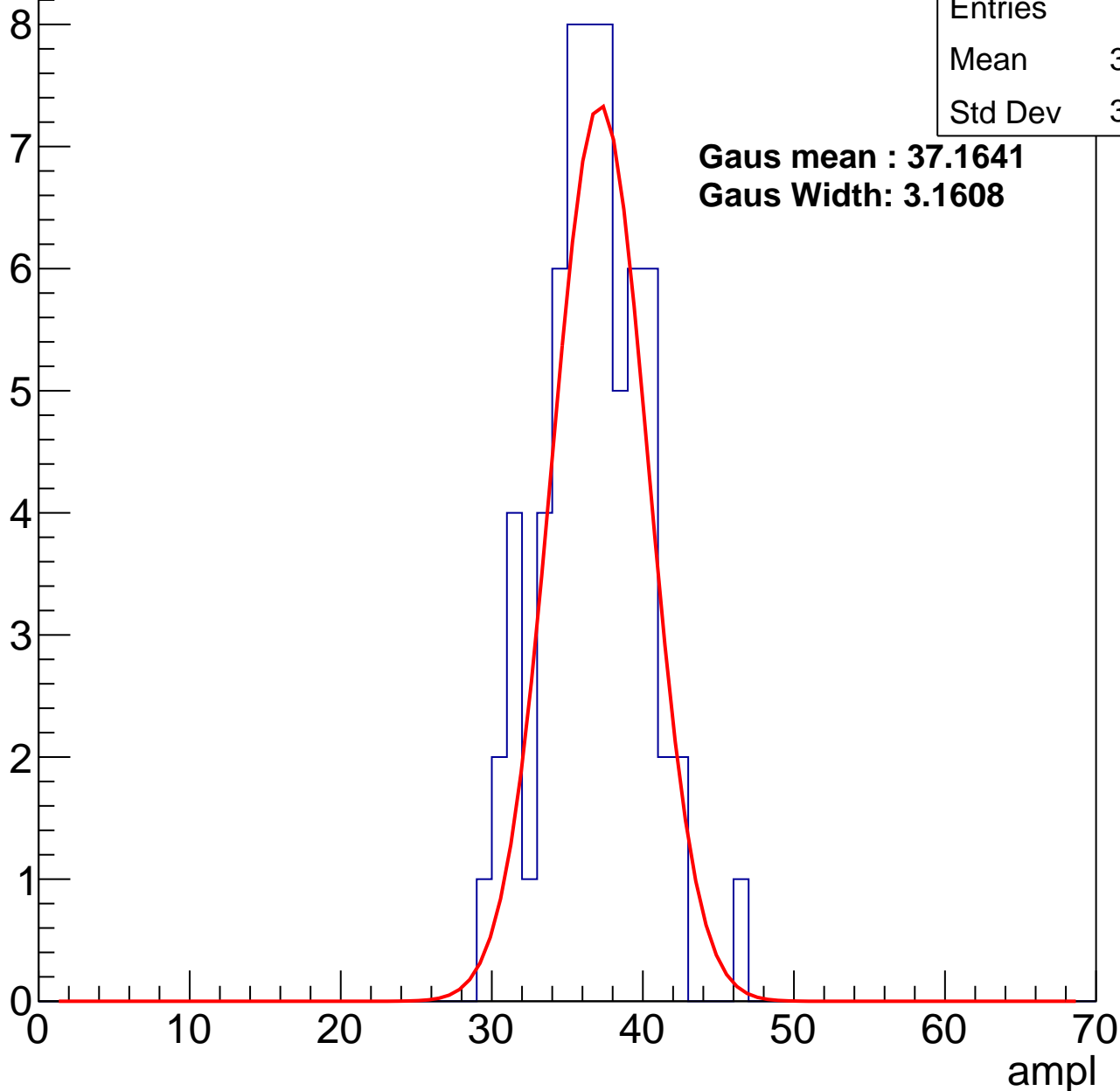
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.27
Std Dev	3.308

Gaus mean : 37.1641

Gaus Width: 3.1608



B1L101S, U6-ch38, adc2

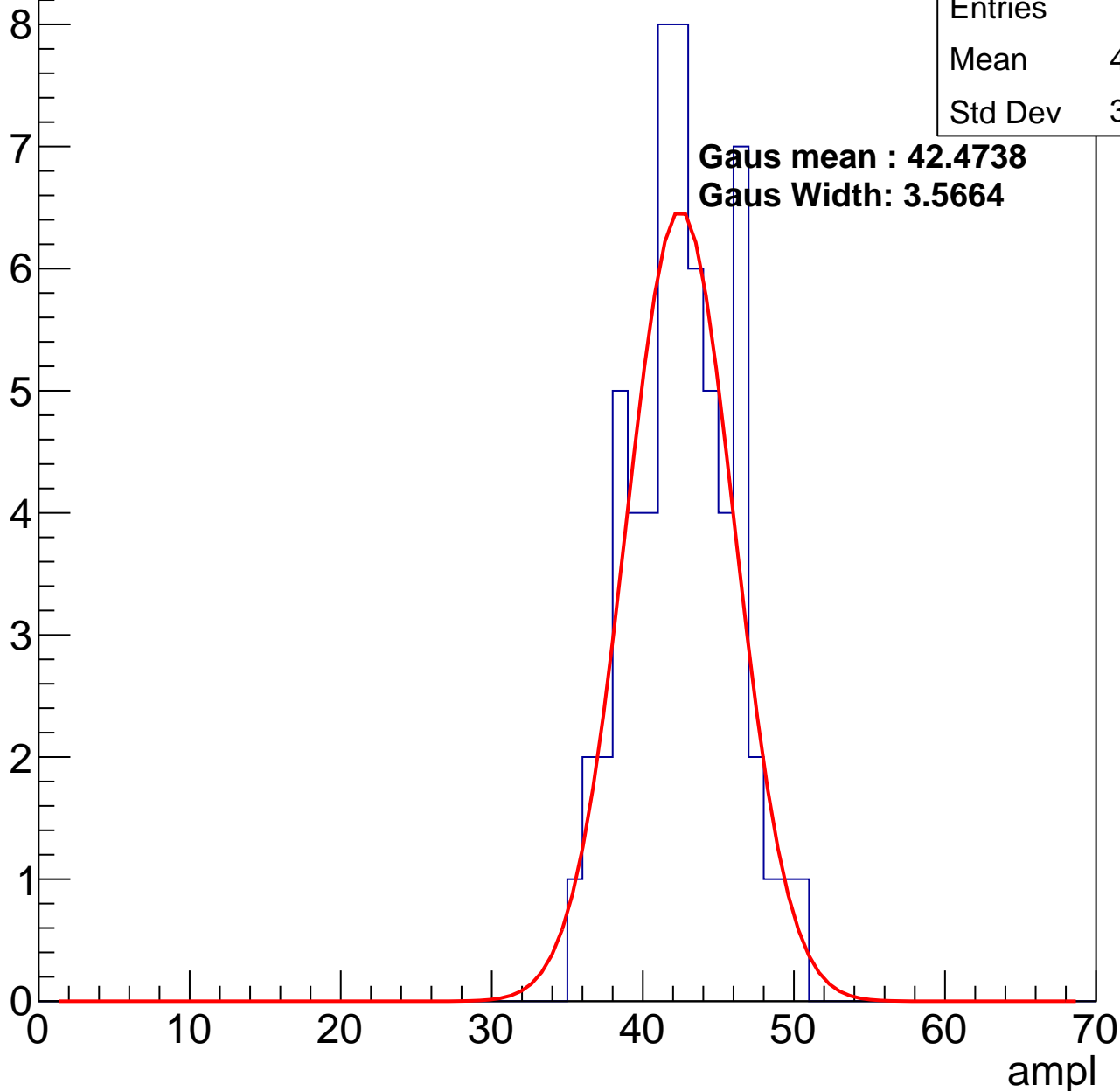
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.16
Std Dev	3.345

Gaus mean : 42.4738

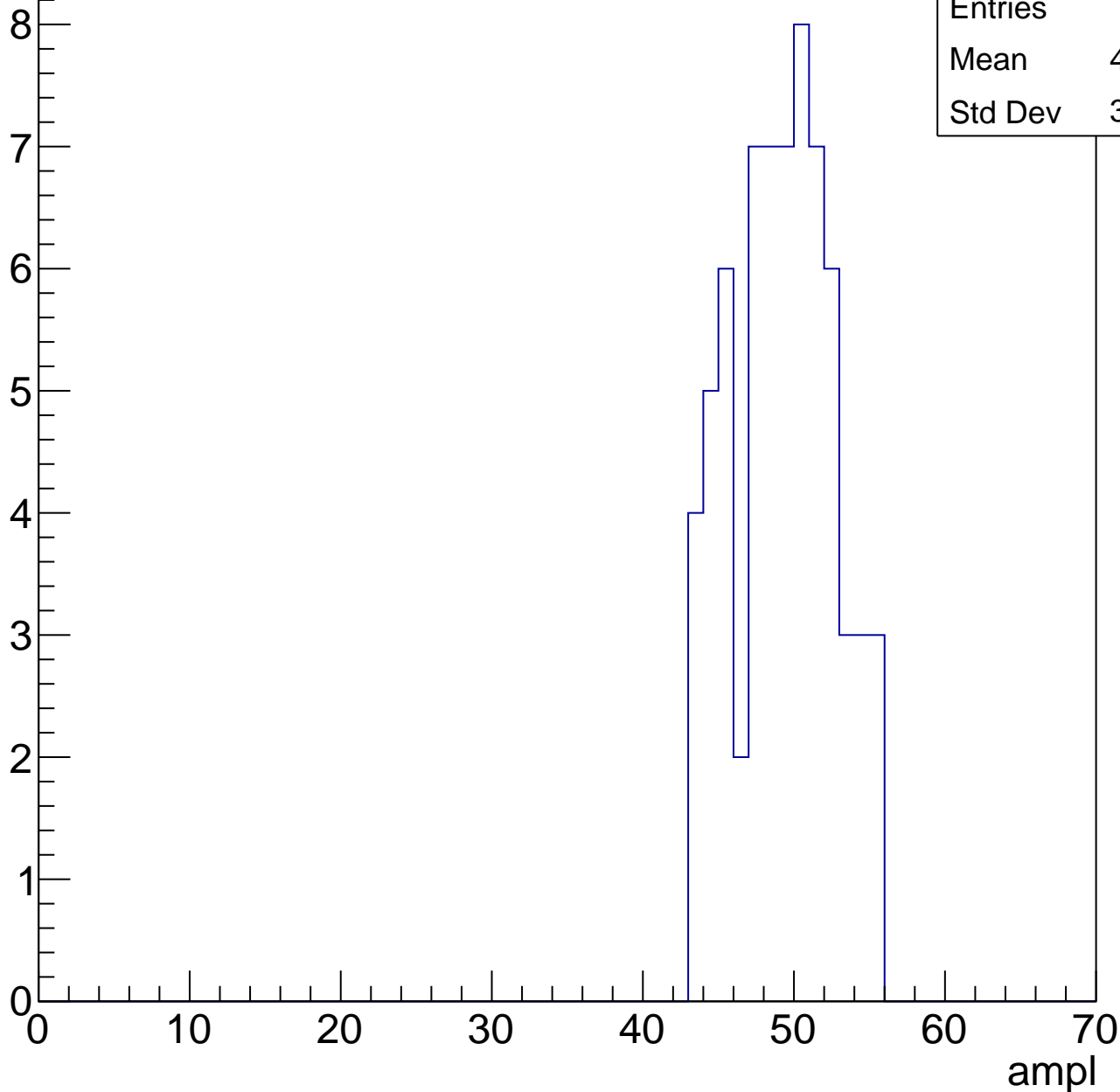
Gaus Width: 3.5664



B1L101S, U6-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



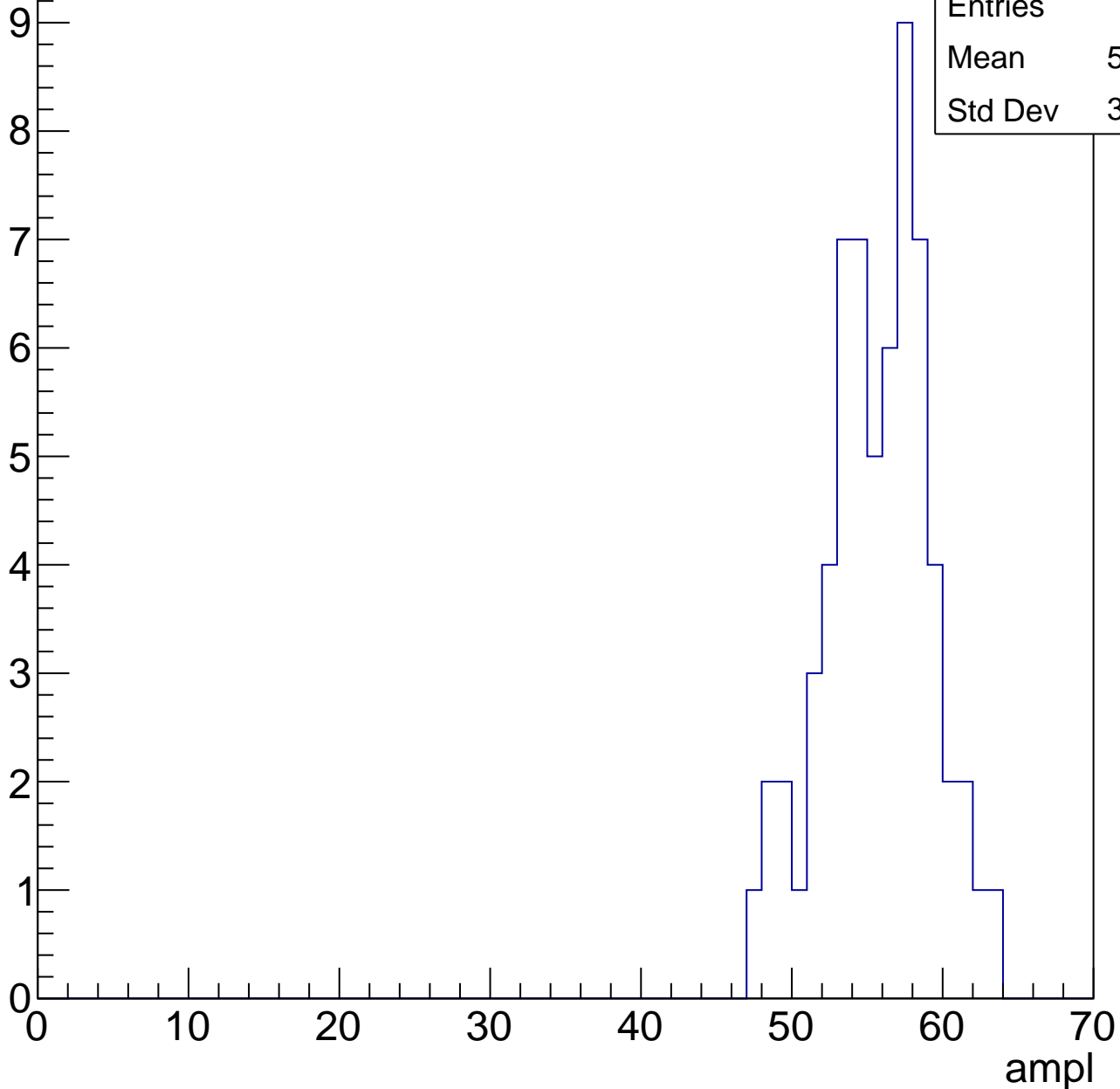
Entries	68
Mean	48.78
Std Dev	3.289

B1L101S, U6-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	55.22
Std Dev	3.489



B1L101S, U6-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	58.21
Std Dev	9.725

ampl

0

10

20

30

40

50

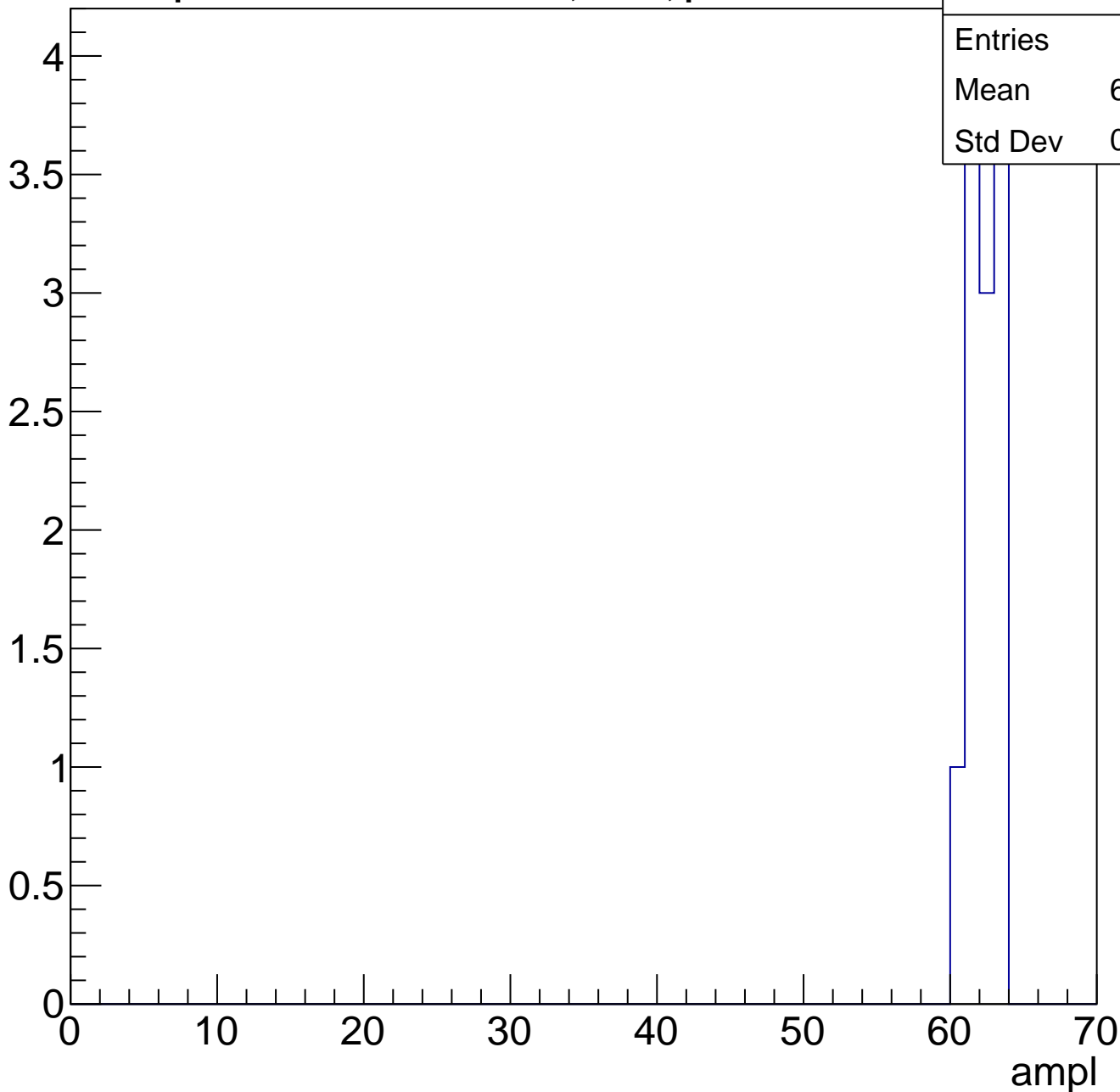
60

70

B1L101S, U6-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch39, adc0

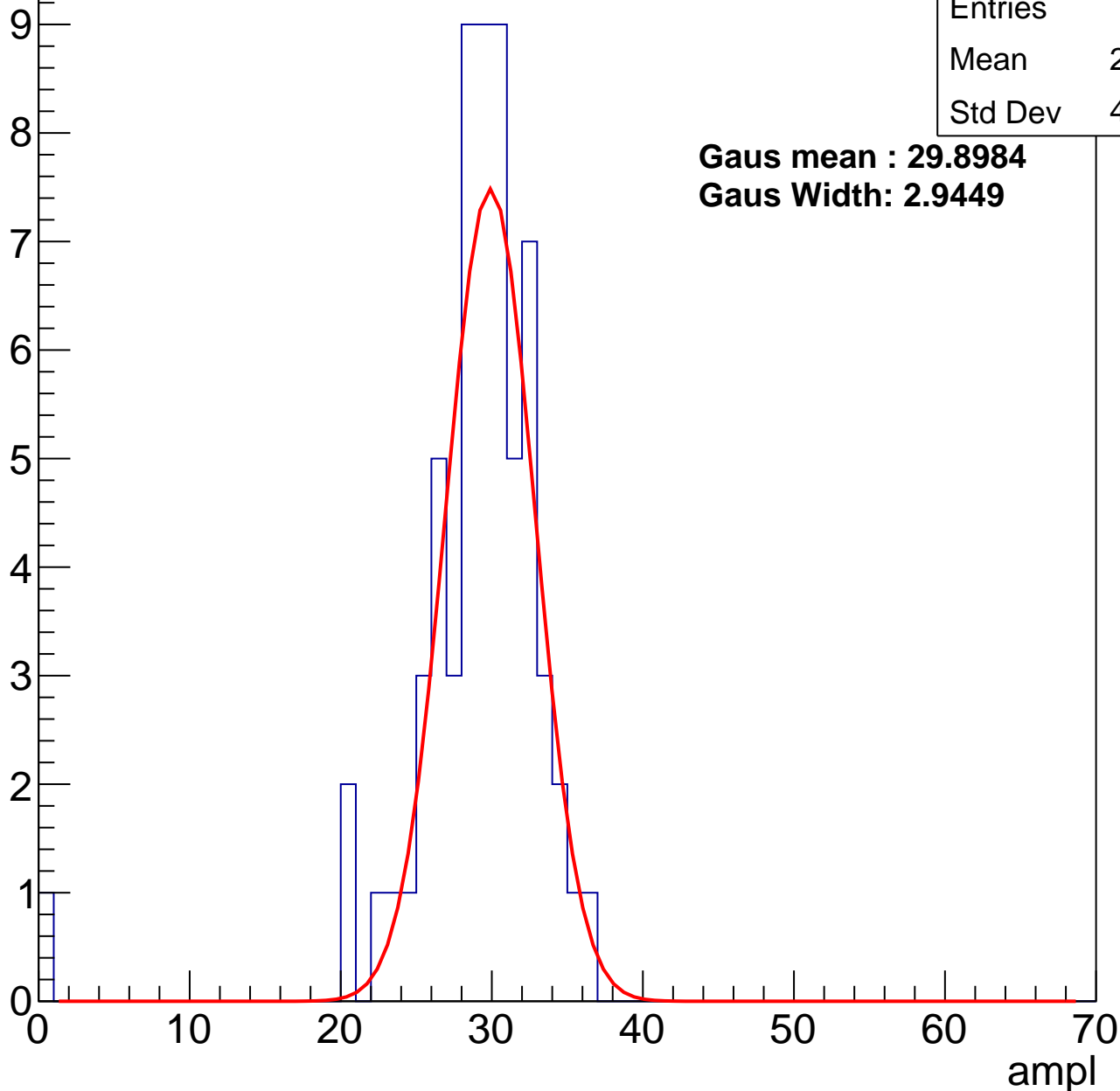
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.49
Std Dev	4.856

Gaus mean : 29.8984

Gaus Width: 2.9449



B1L101S, U6-ch39, adc1

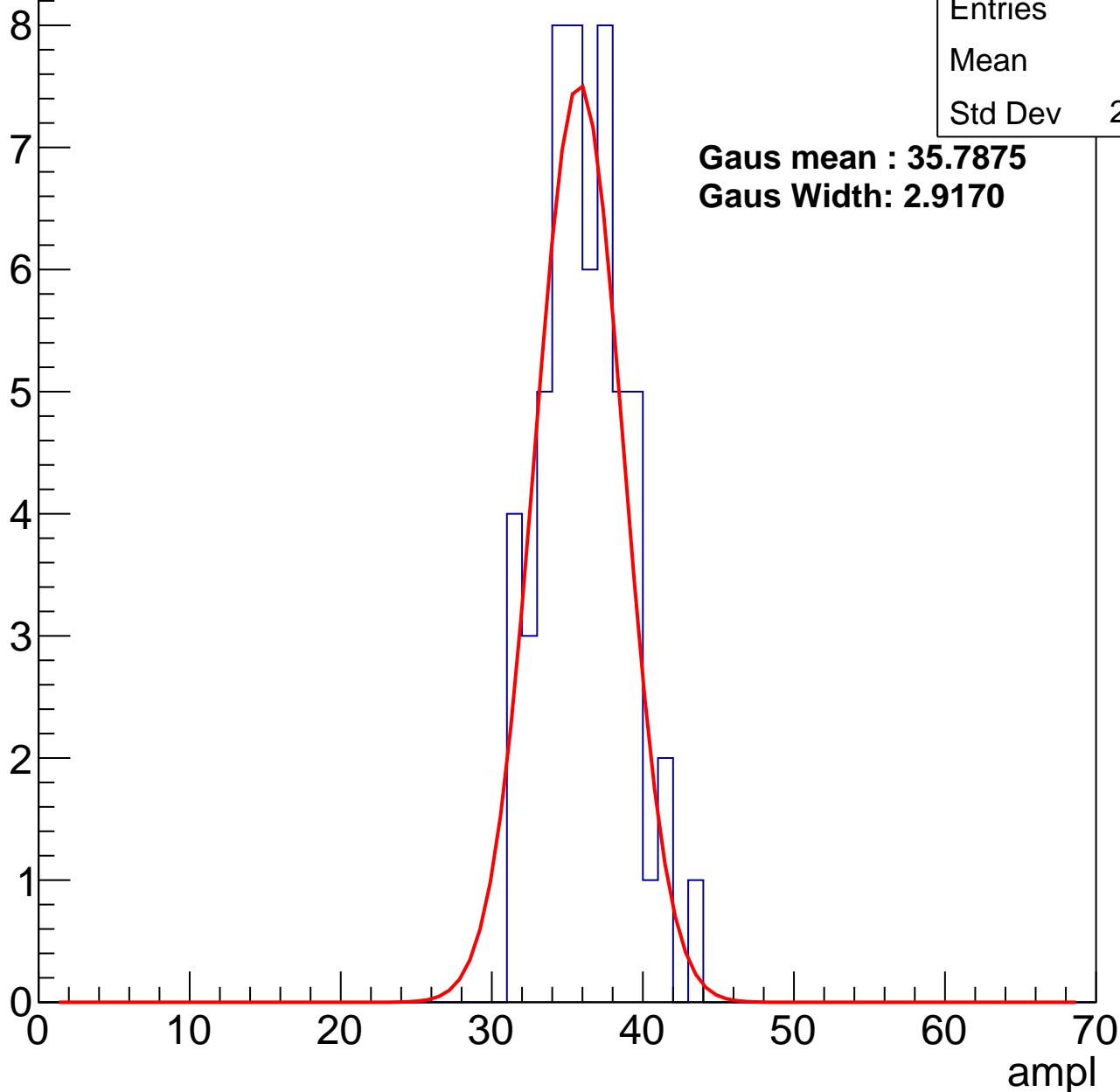
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.7
Std Dev	2.725

Gaus mean : 35.7875

Gaus Width: 2.9170



B1L101S, U6-ch39, adc2

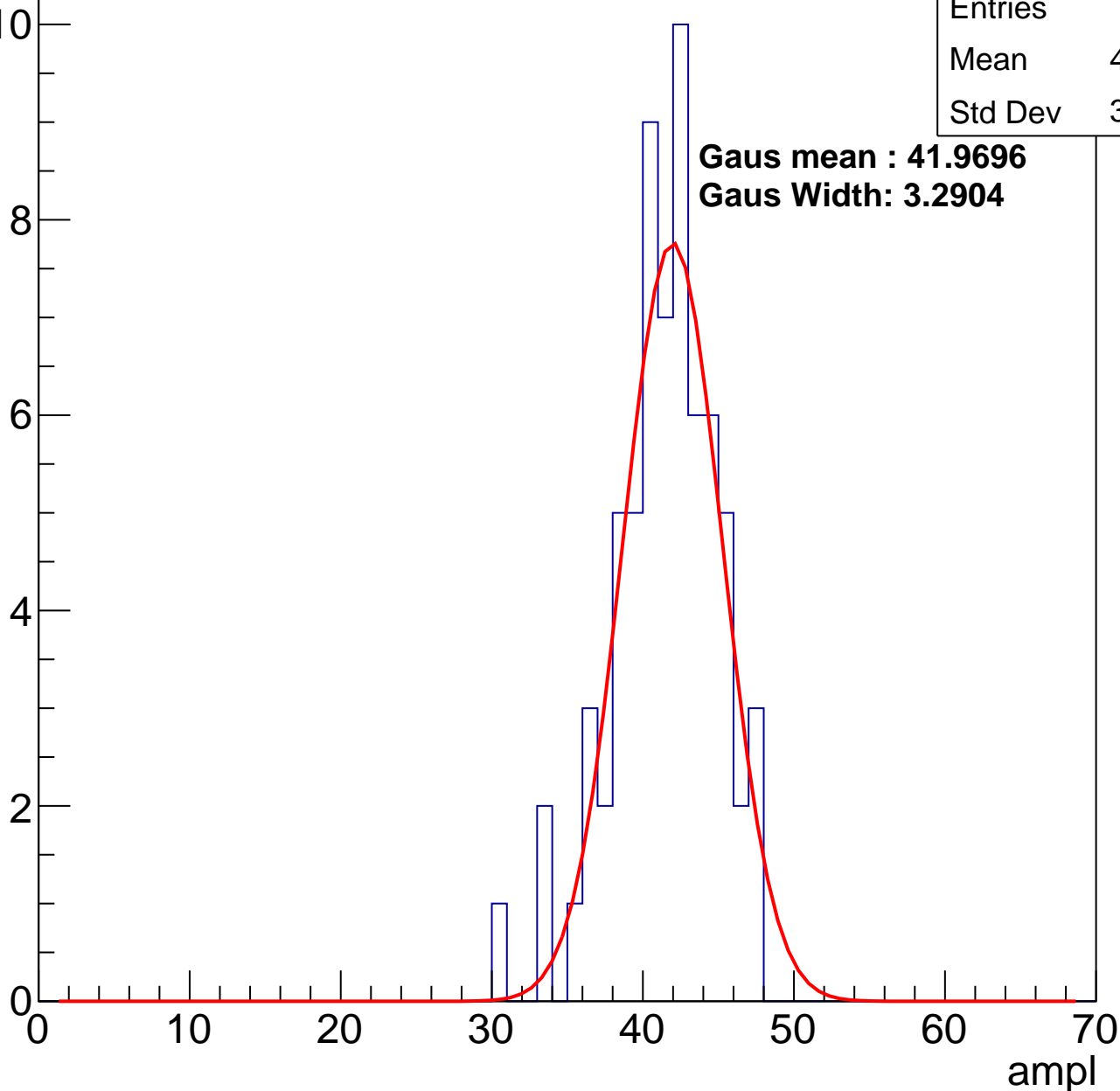
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	40.97
Std Dev	3.438

Gaus mean : 41.9696

Gaus Width: 3.2904

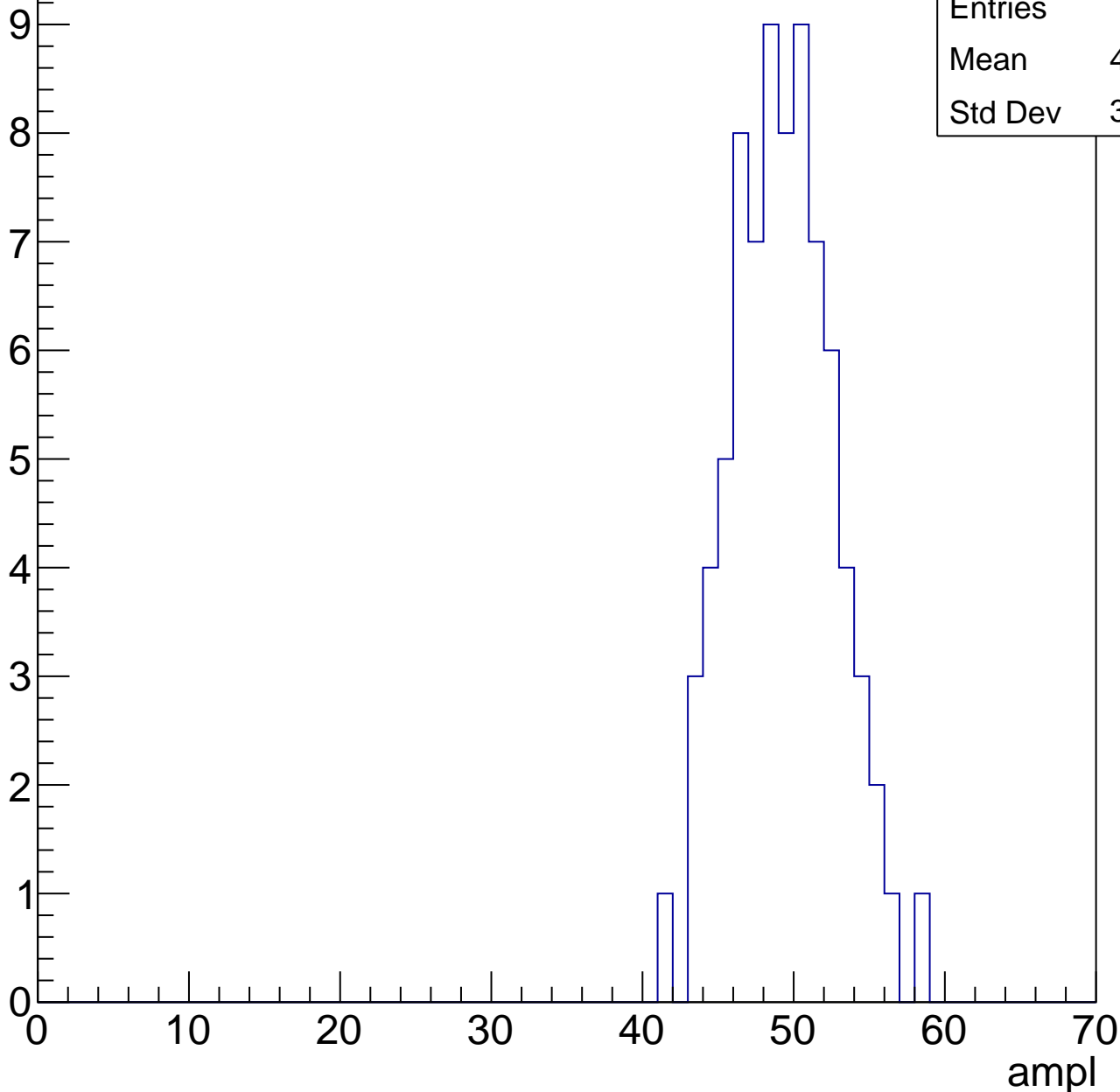


B1L101S, U6-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

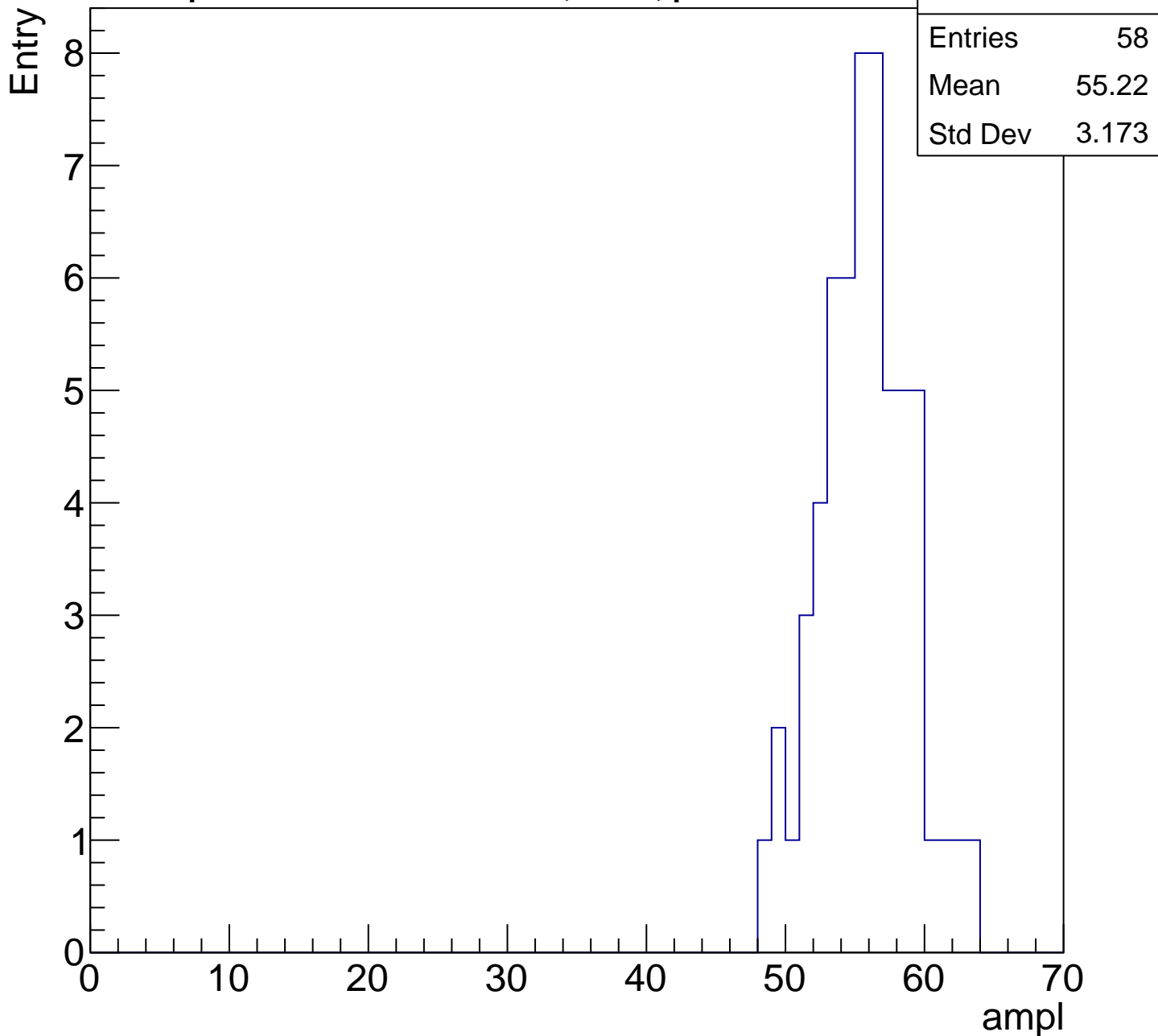
Entry

Entries	78
Mean	48.83
Std Dev	3.379



B1L101S, U6-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch39, adc5

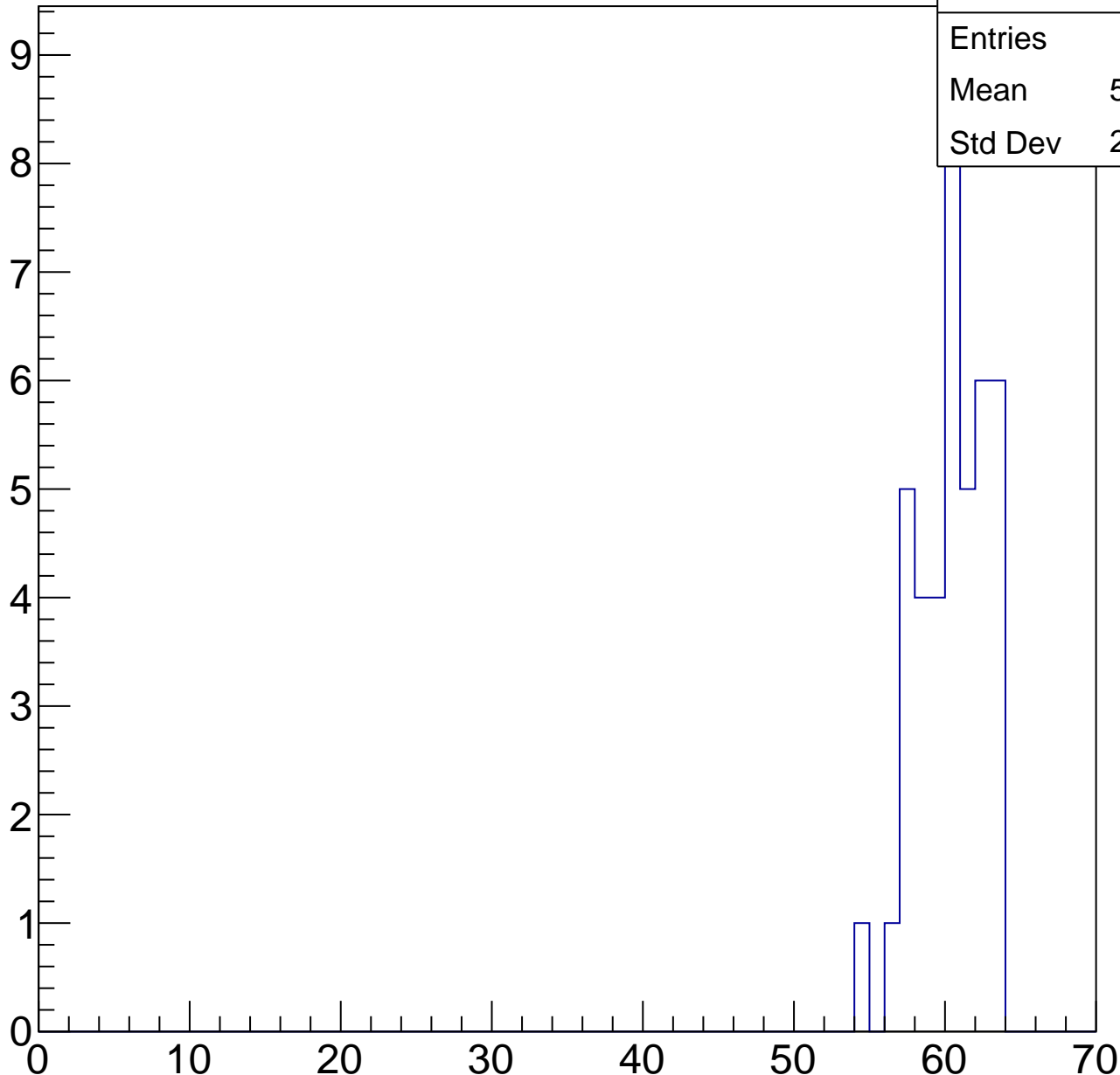
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.95
Std Dev	2.208

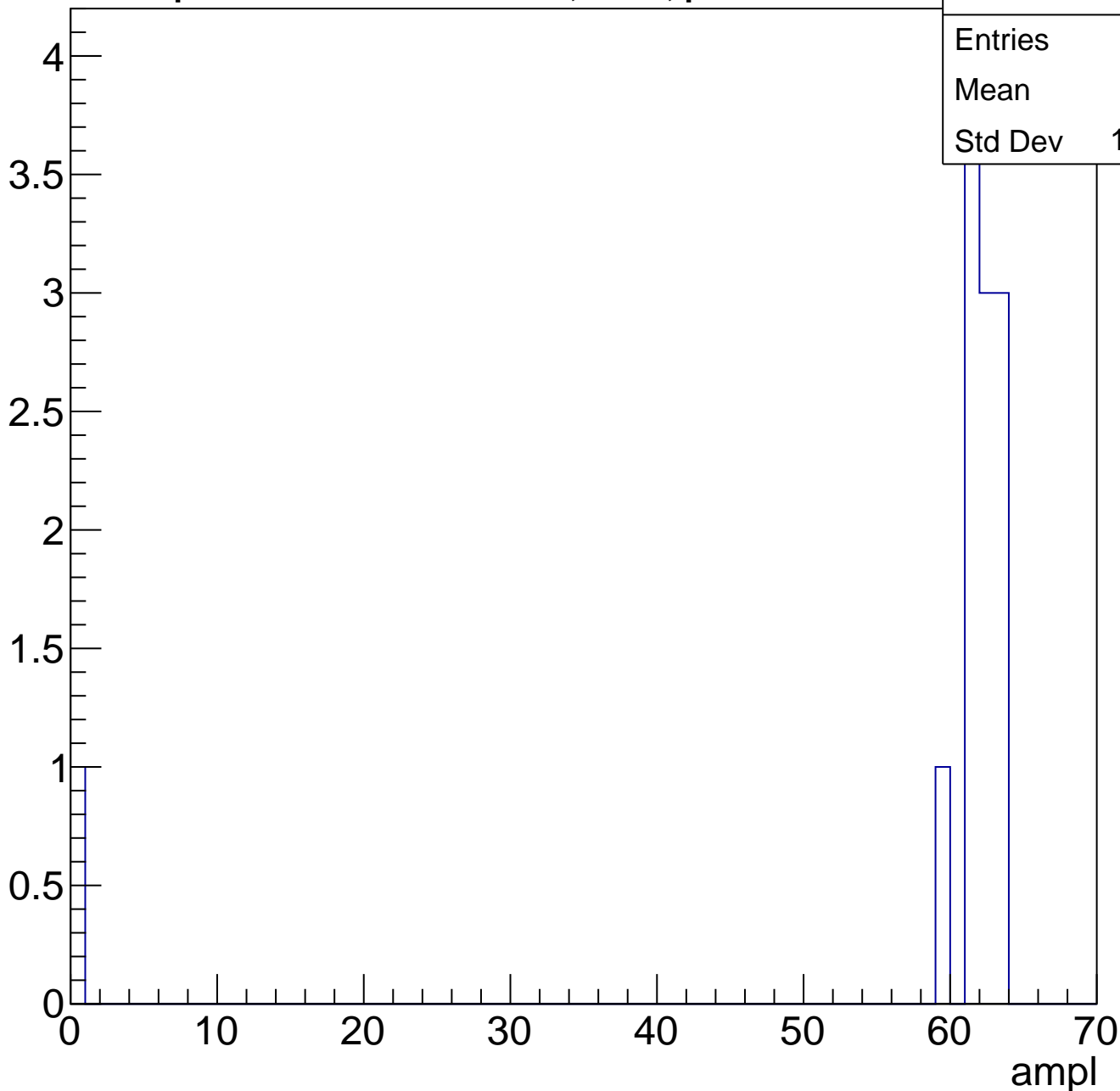
ampl



B1L101S, U6-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	18
Std Dev	0

ampl

B1L101S, U6-ch40, adc0

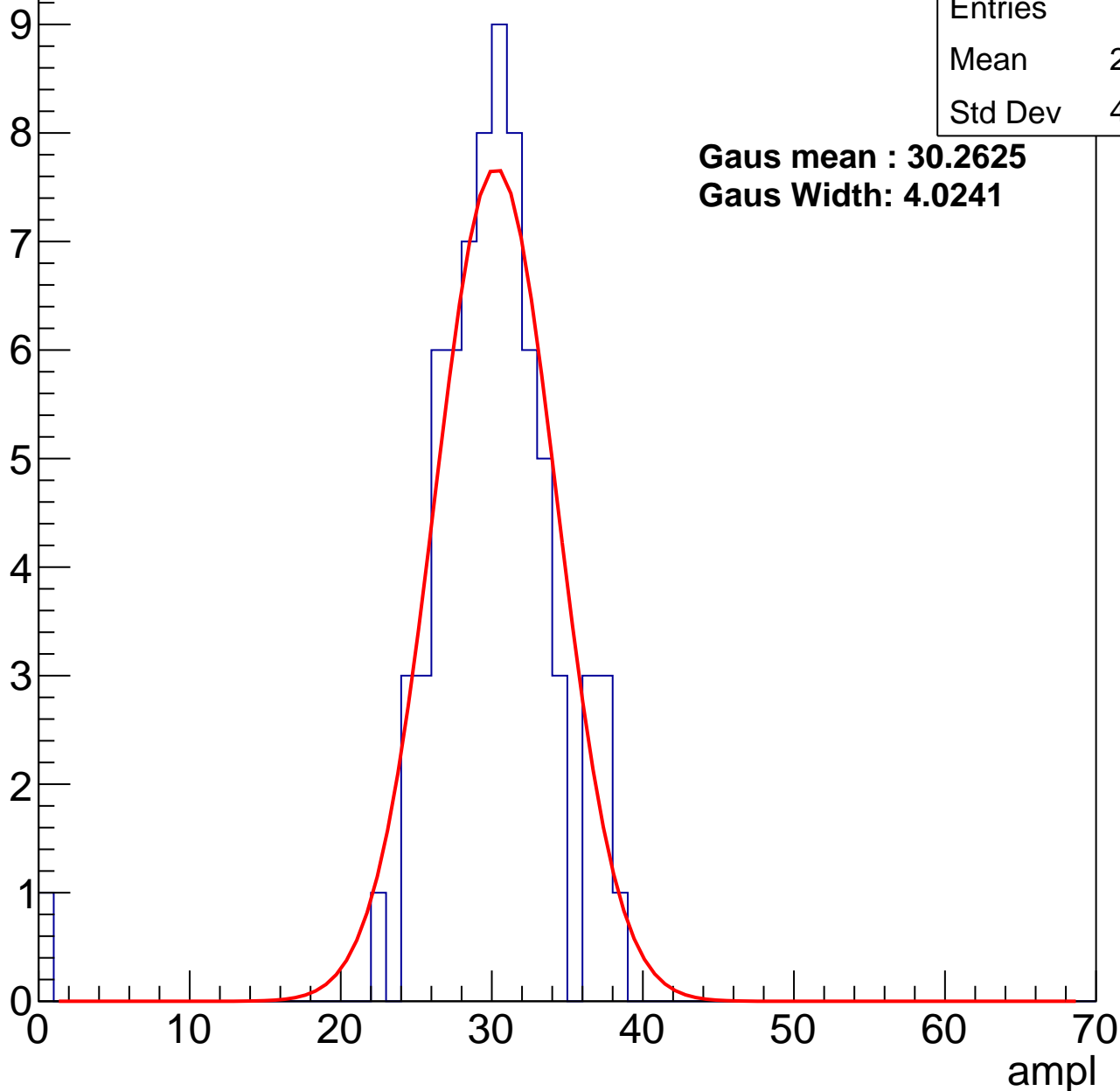
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.44
Std Dev	4.902

Gaus mean : 30.2625

Gaus Width: 4.0241



B1L101S, U6-ch40, adc1

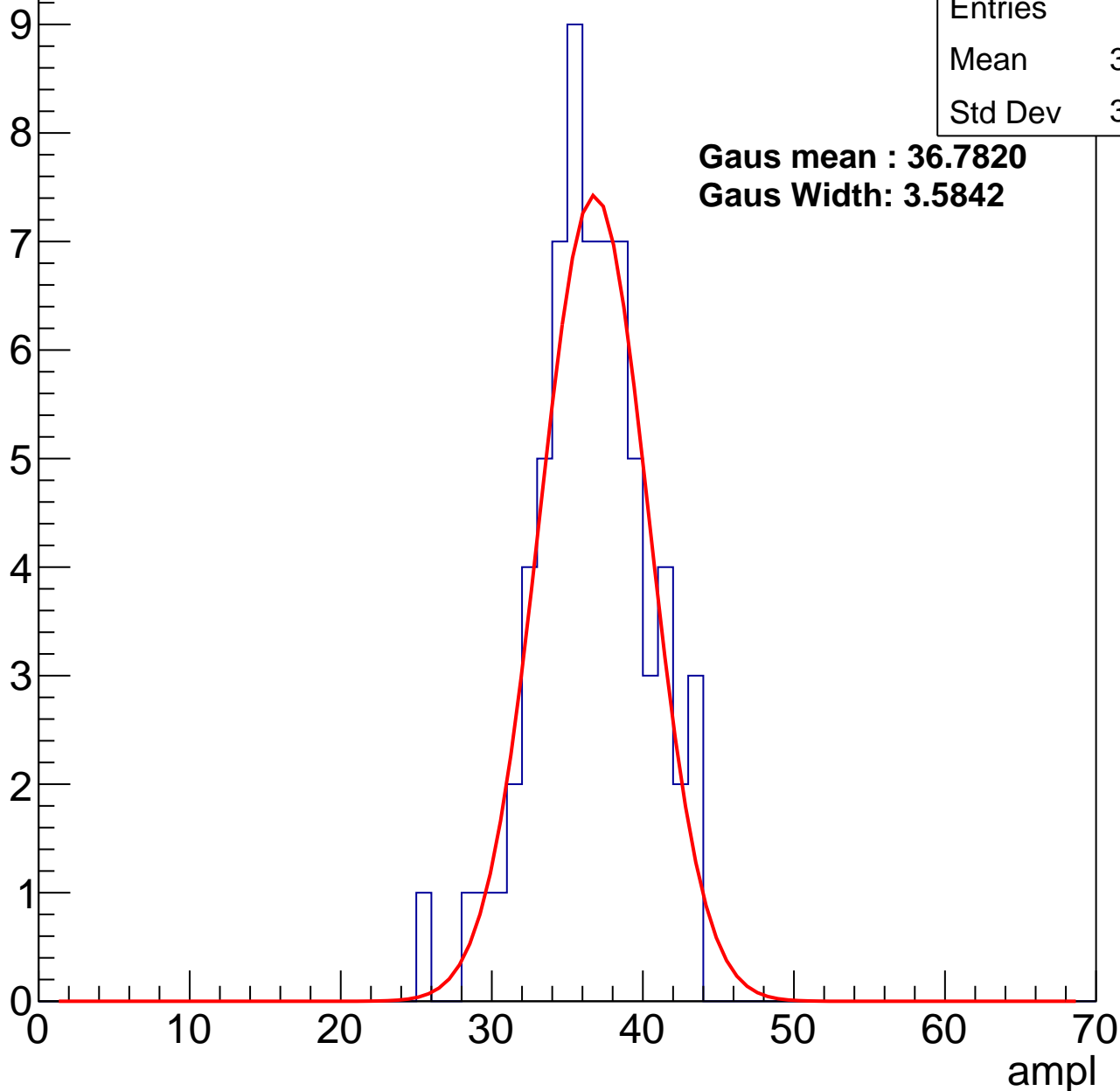
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.07
Std Dev	3.637

Gaus mean : 36.7820

Gaus Width: 3.5842



B1L101S, U6-ch40, adc2

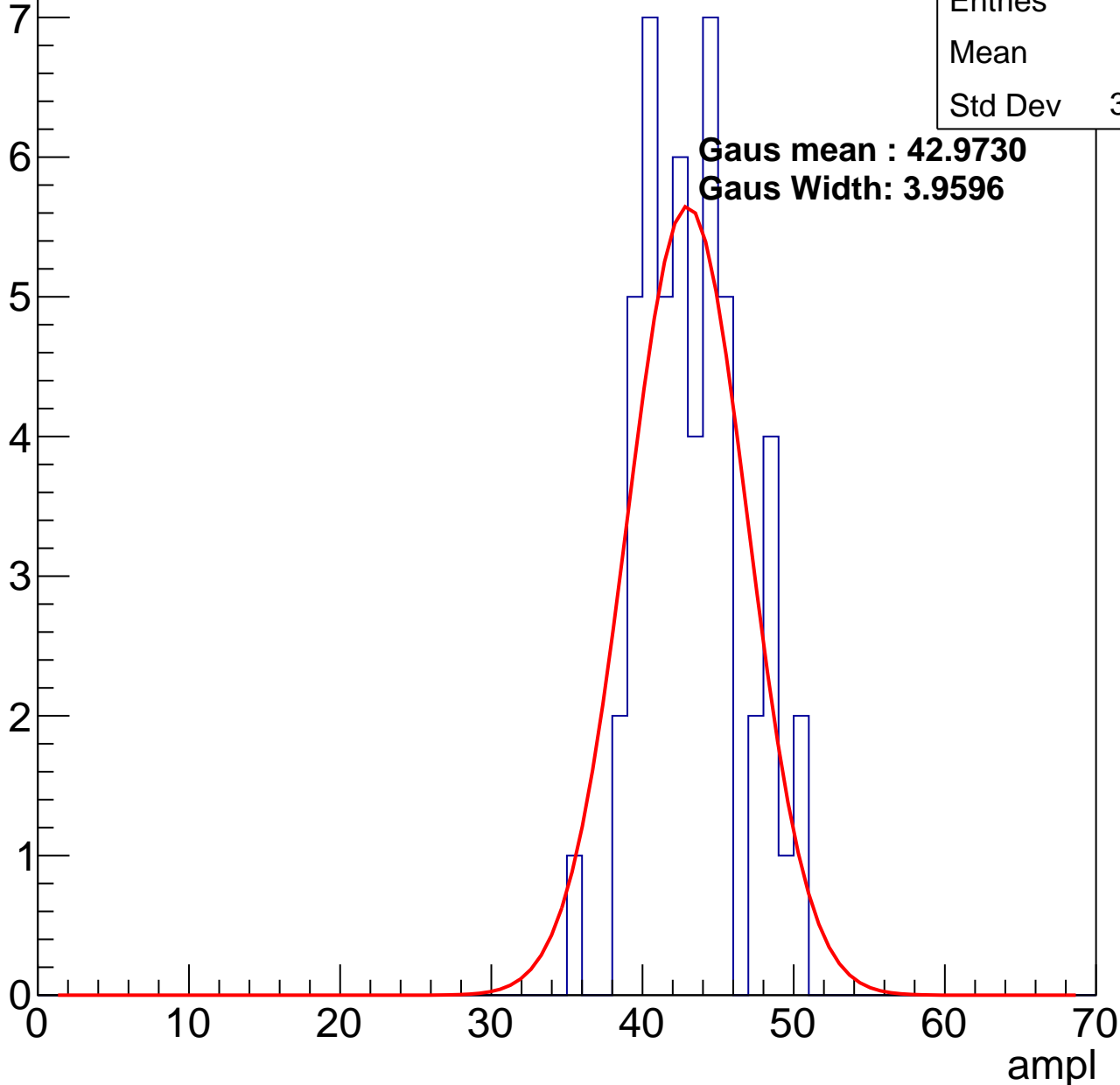
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.8
Std Dev	3.355

Gaus mean : 42.9730

Gaus Width: 3.9596



B1L101S, U6-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

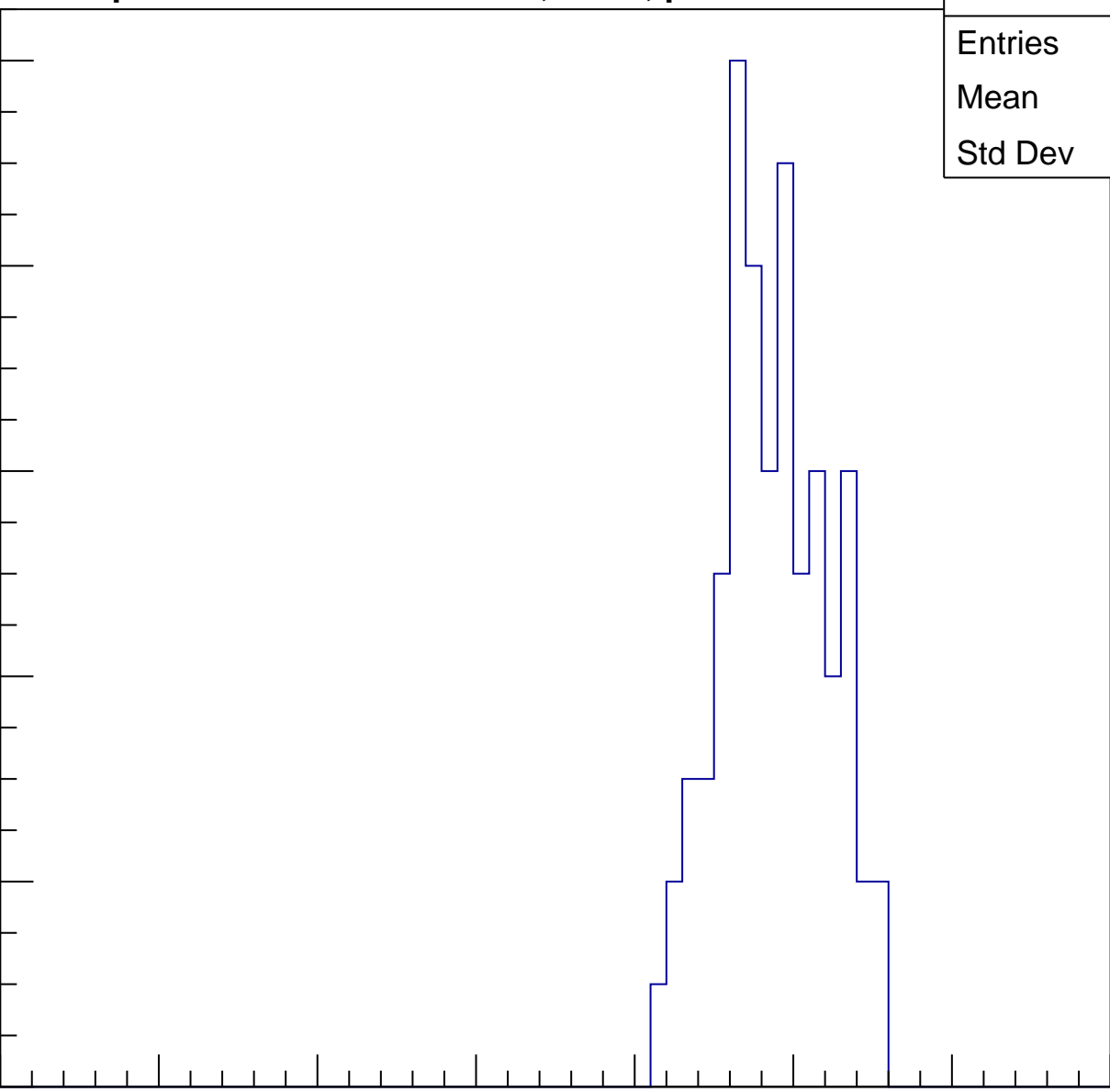
Entries	72
Mean	48.28
Std Dev	3.33

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

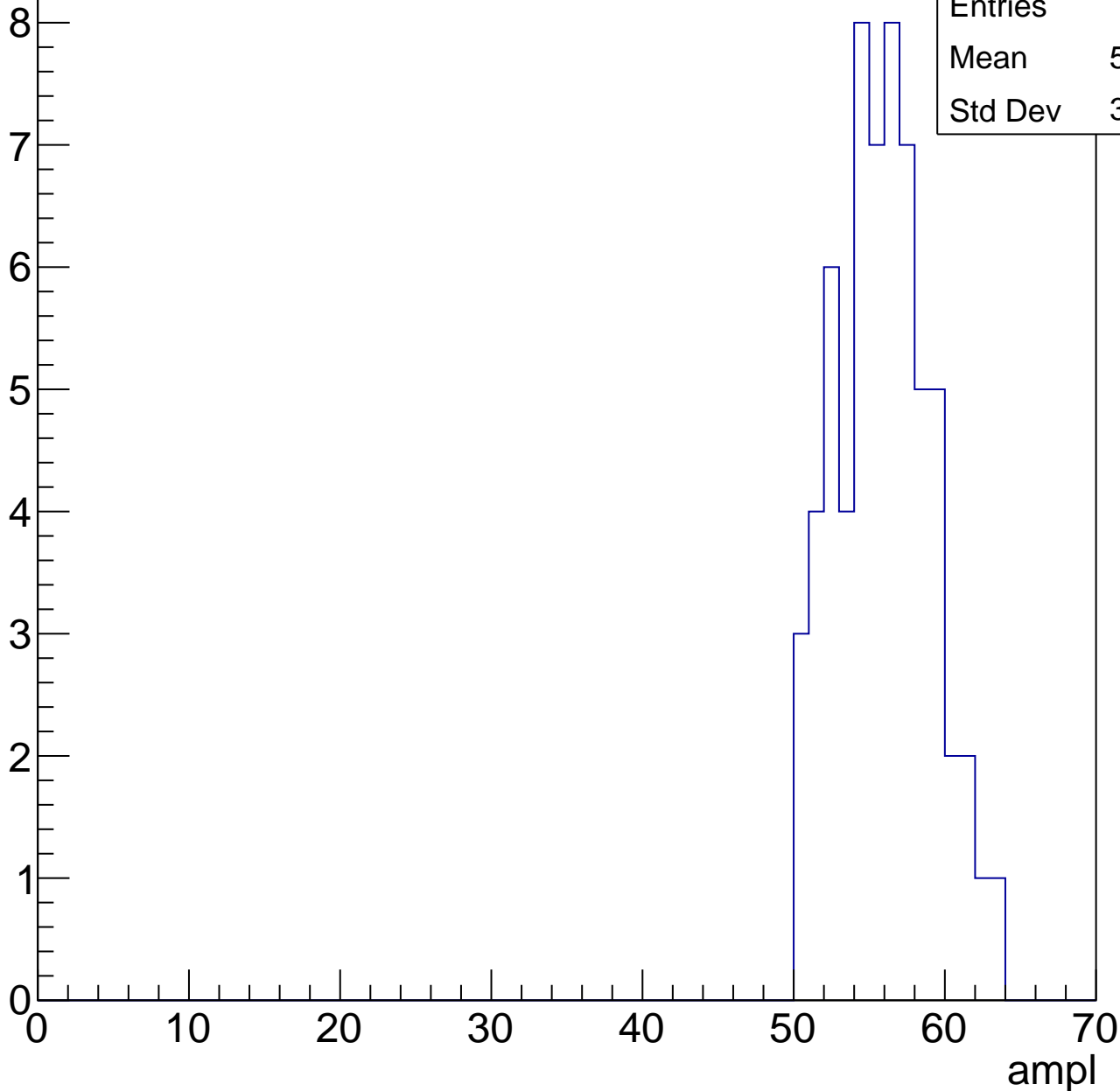


B1L101S, U6-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.46
Std Dev	3.085



B1L101S, U6-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

38

Mean

60.29

Std Dev

2.175

0

ampl

0

10

20

30

40

50

60

70

B1L101S, U6-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

13

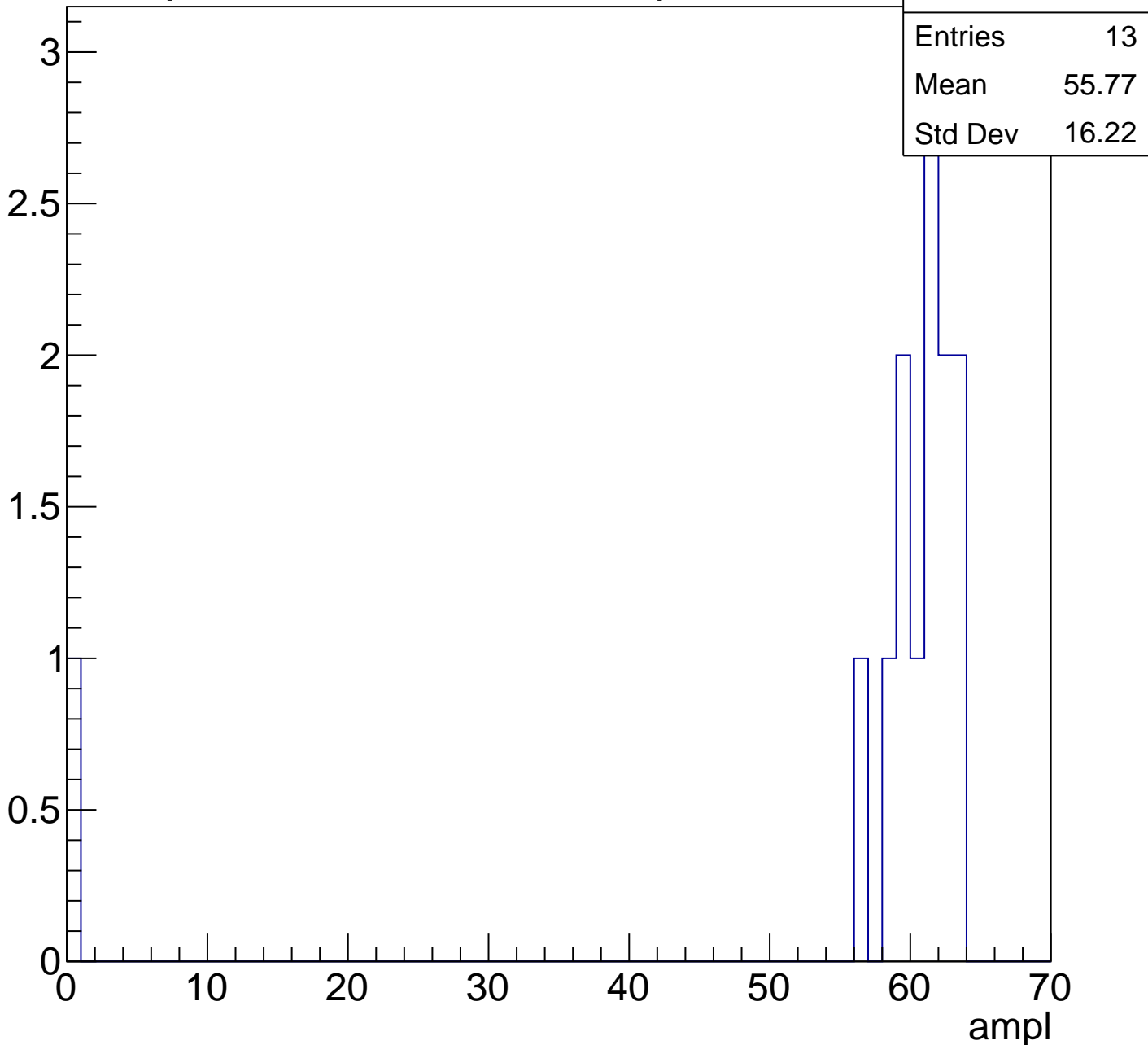
Mean

55.77

Std Dev

16.22

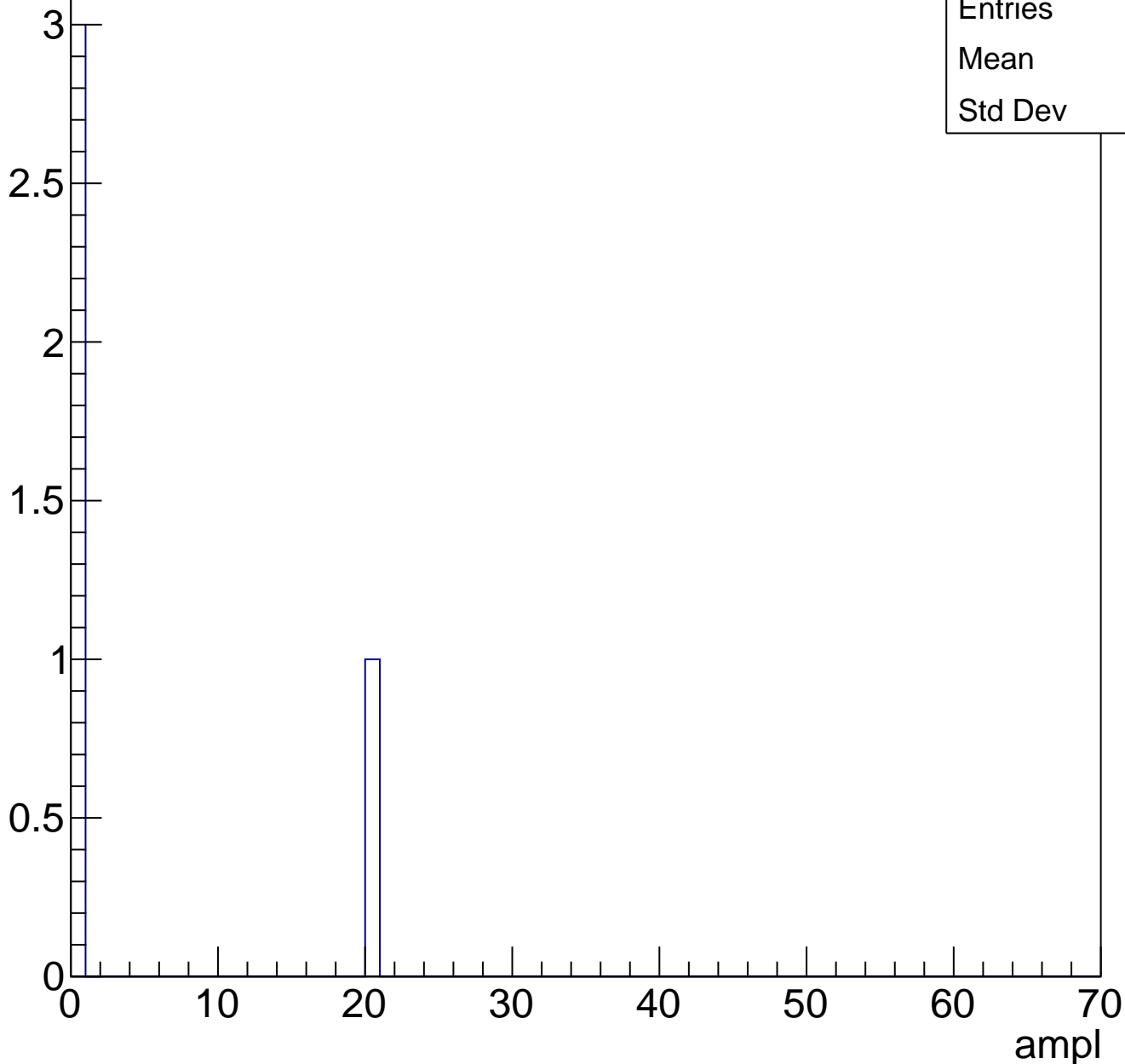
ampl



B1L101S, U6-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	5
Std Dev	8.66

B1L101S, U6-ch41, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	28.55
Std Dev	6.248

Gaus mean : 30.3039

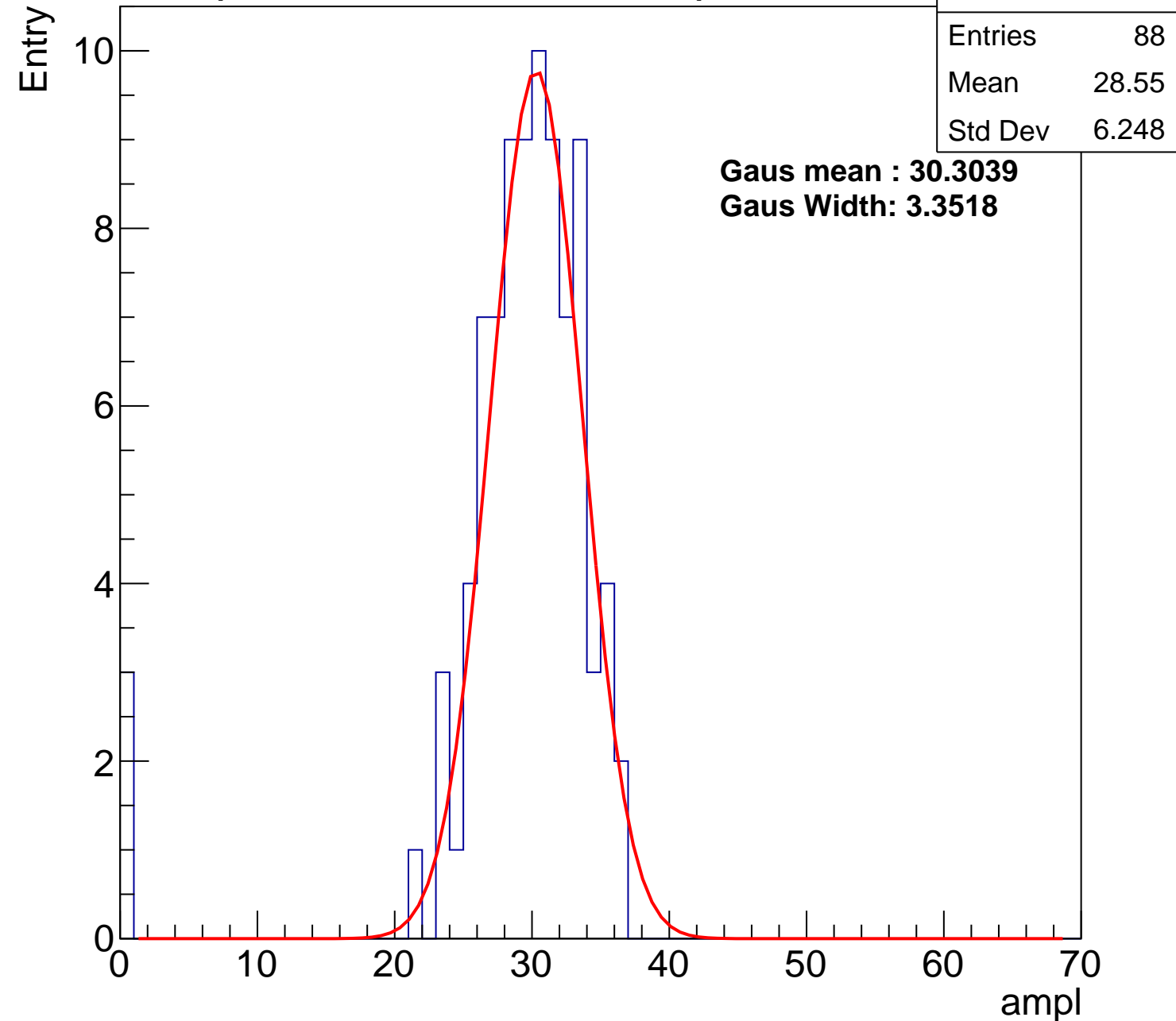
Gaus Width: 3.3518

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch41, adc1

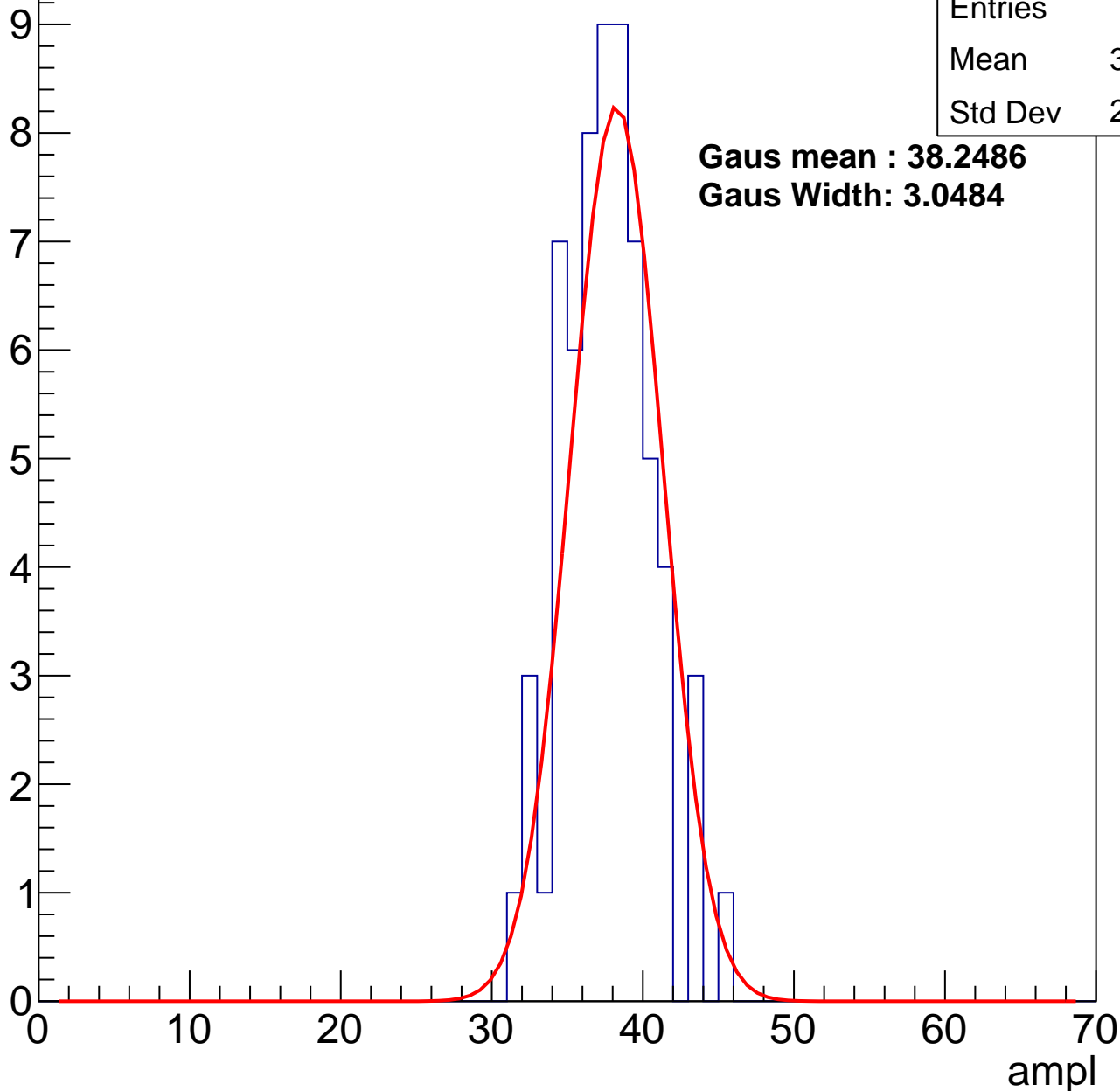
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.22
Std Dev	2.896

Gaus mean : 38.2486

Gaus Width: 3.0484



B1L101S, U6-ch41, adc2

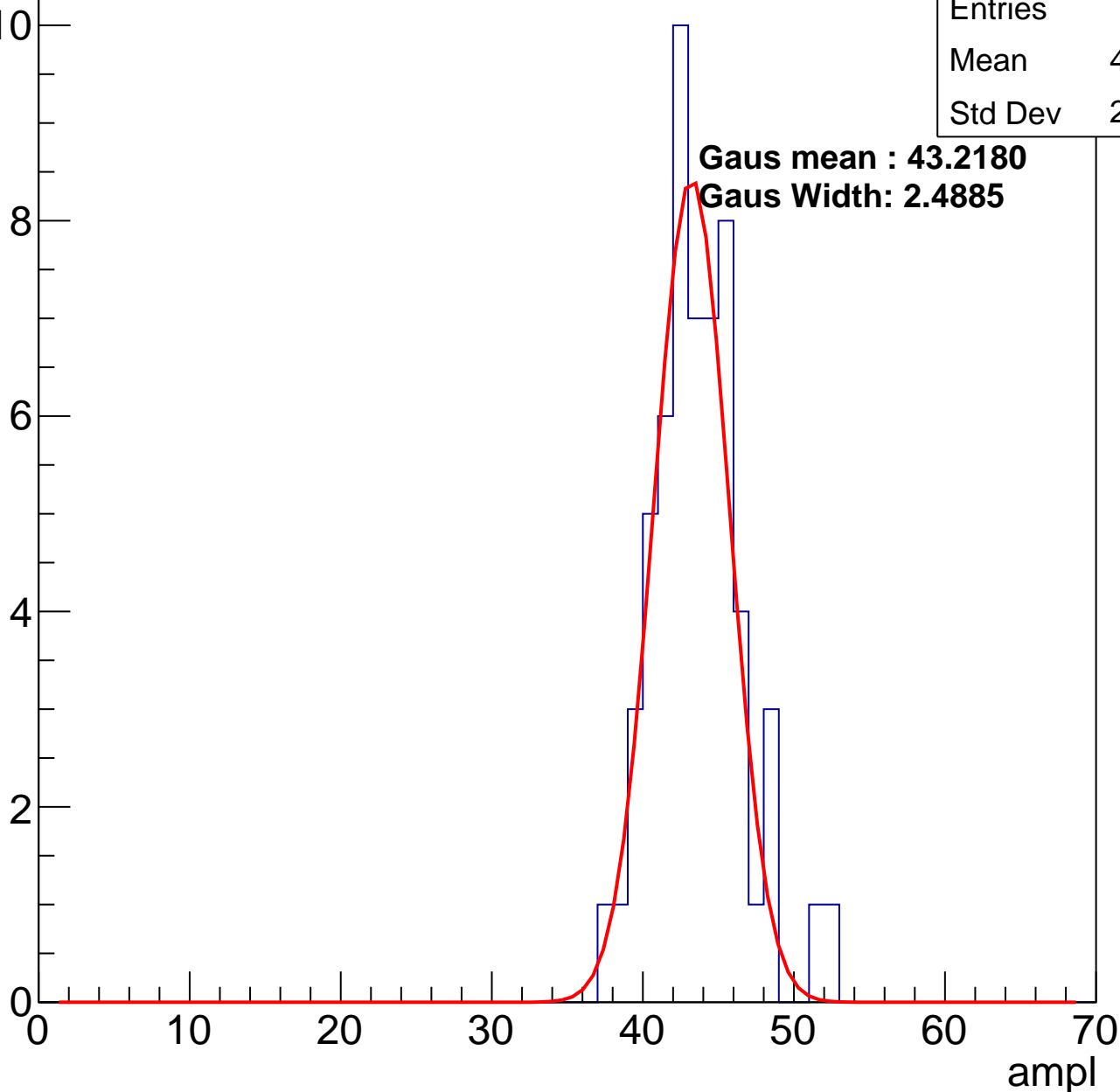
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.19
Std Dev	2.927

Gaus mean : 43.2180

Gaus Width: 2.4885



B1L101S, U6-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

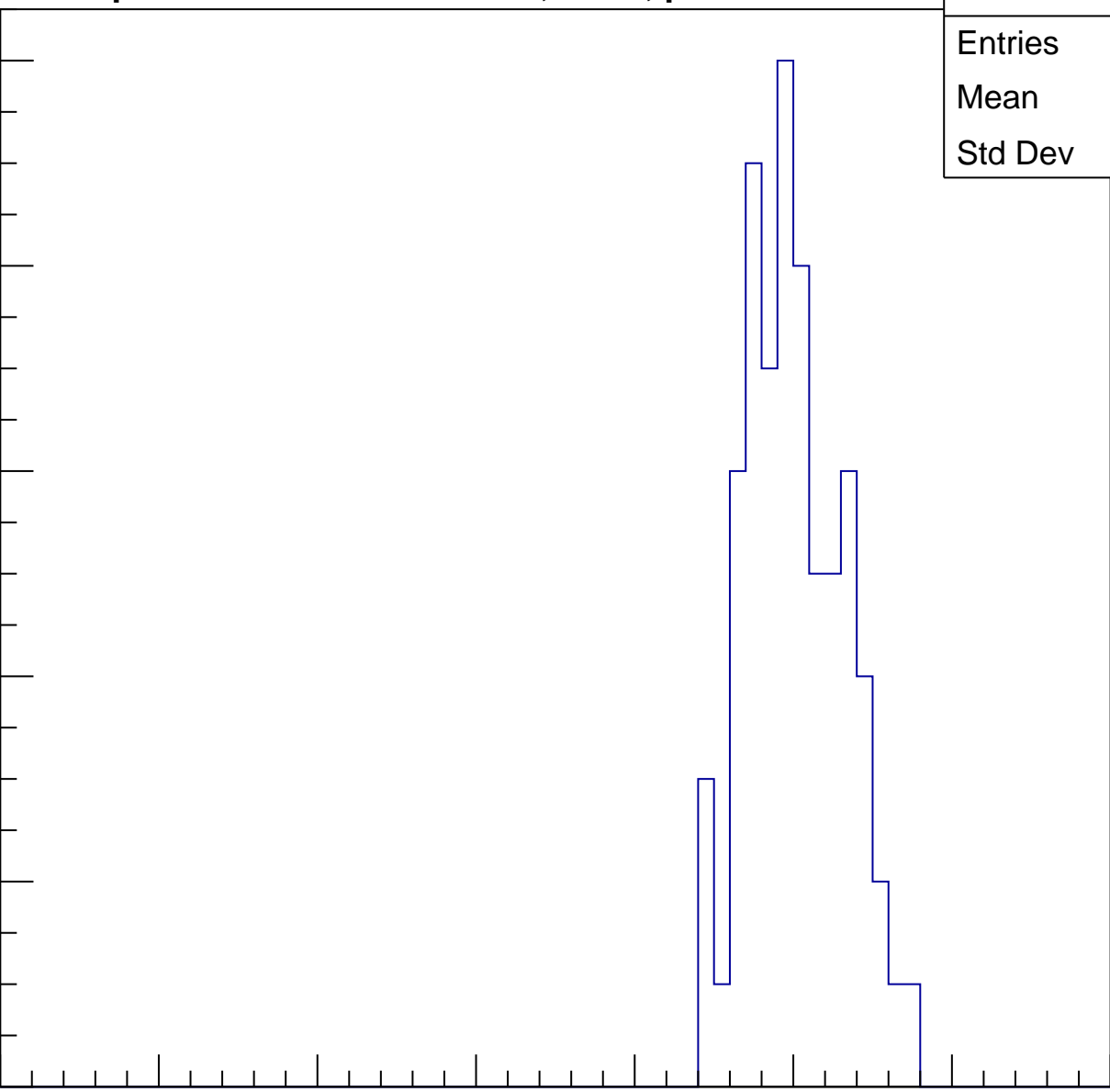
Entries	68
Mean	49.62
Std Dev	3.01

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

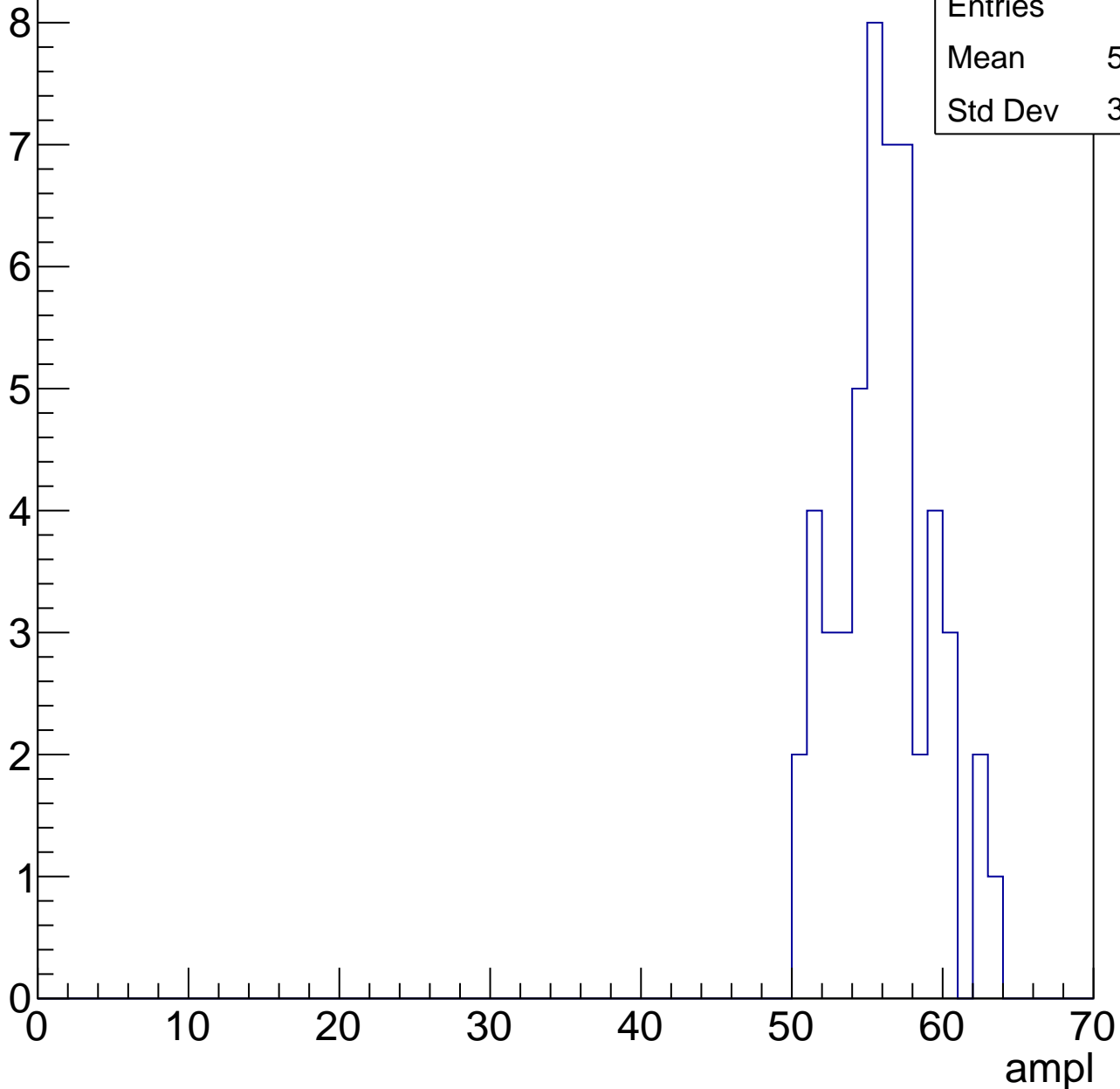


B1L101S, U6-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.67
Std Dev	3.098

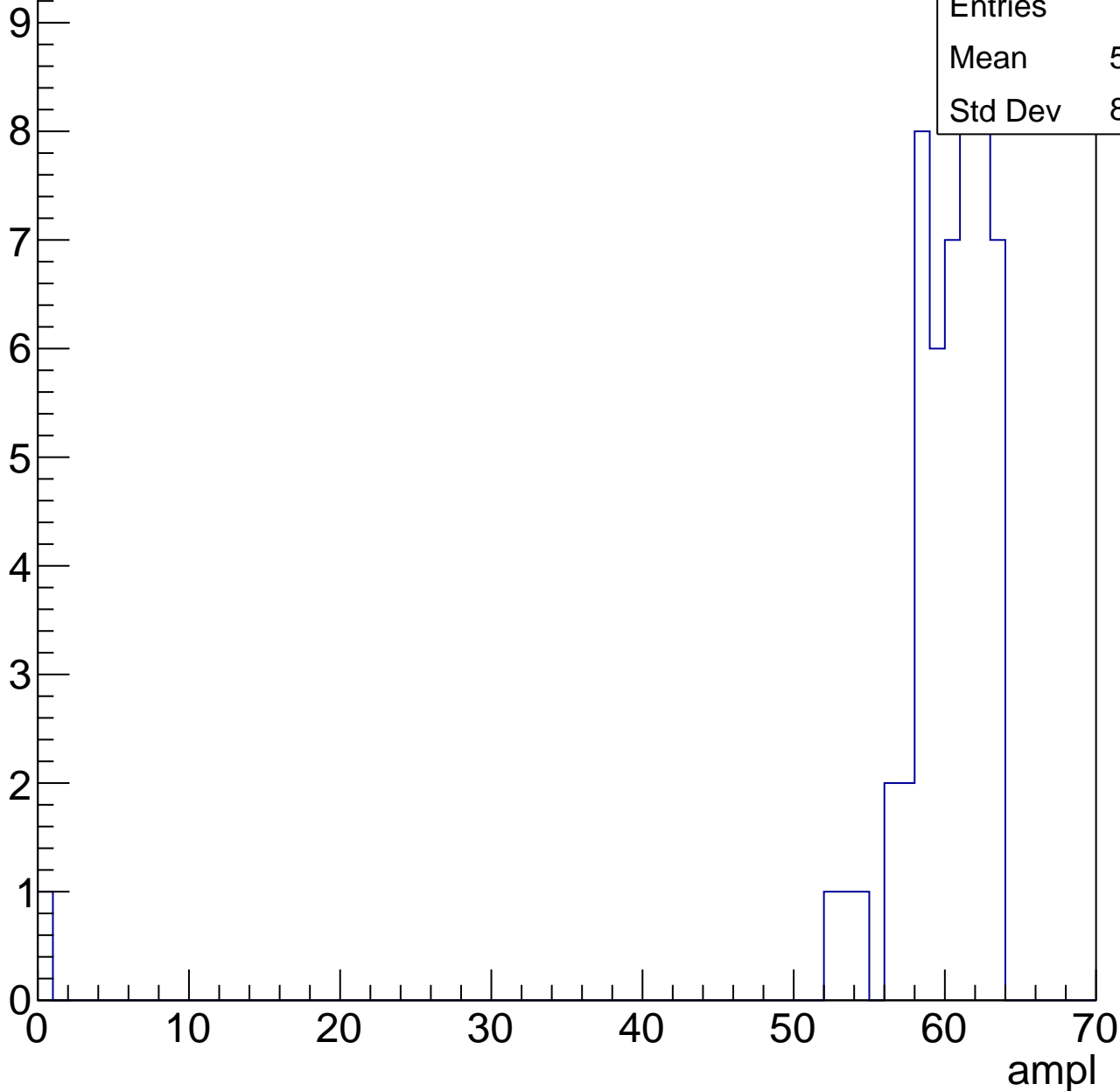


B1L101S, U6-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.66
Std Dev	8.518



B1L101S, U6-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch42, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.16
Std Dev	4.79

Gaus mean : 29.8956

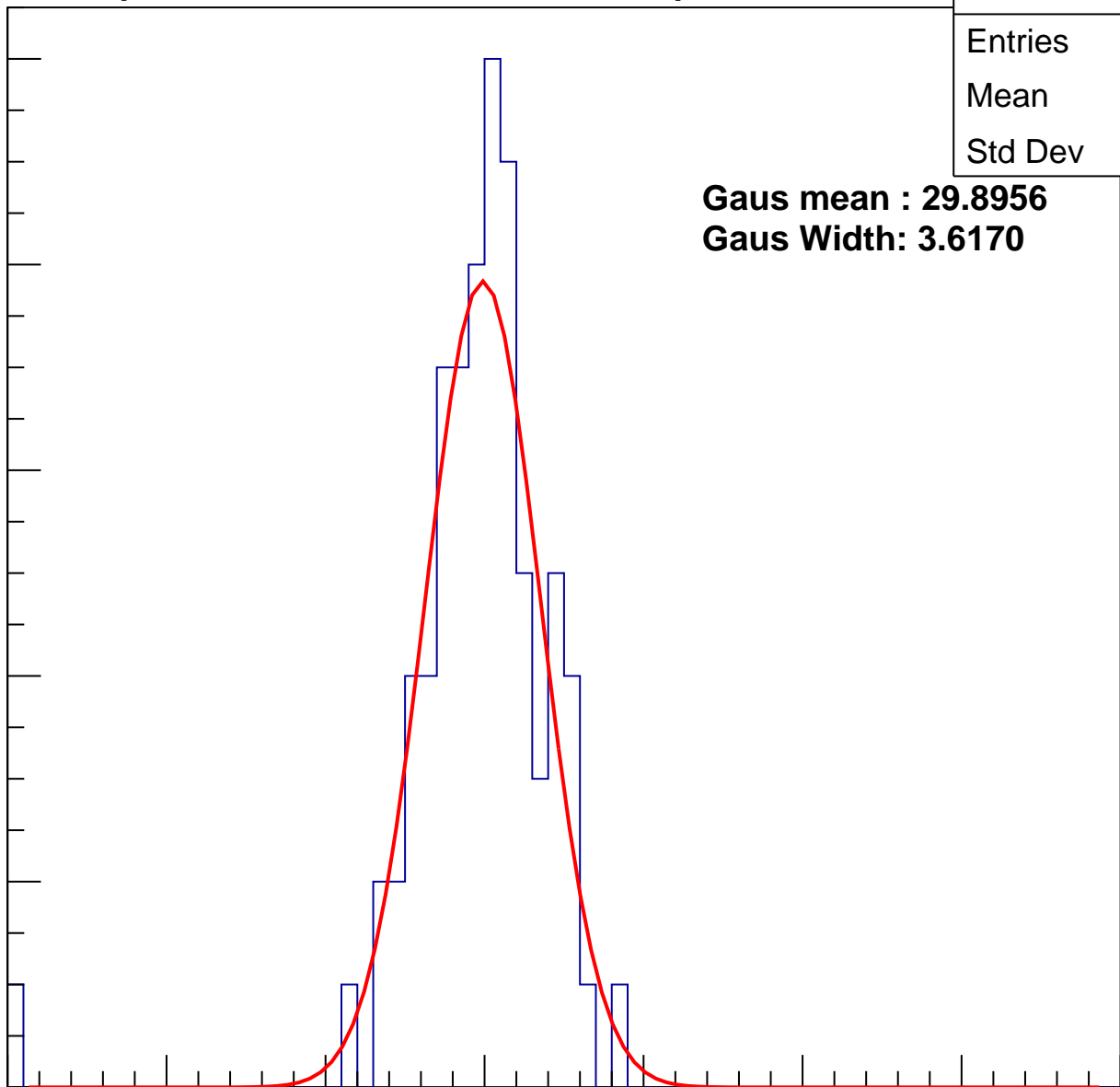
Gaus Width: 3.6170

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch42, adc1

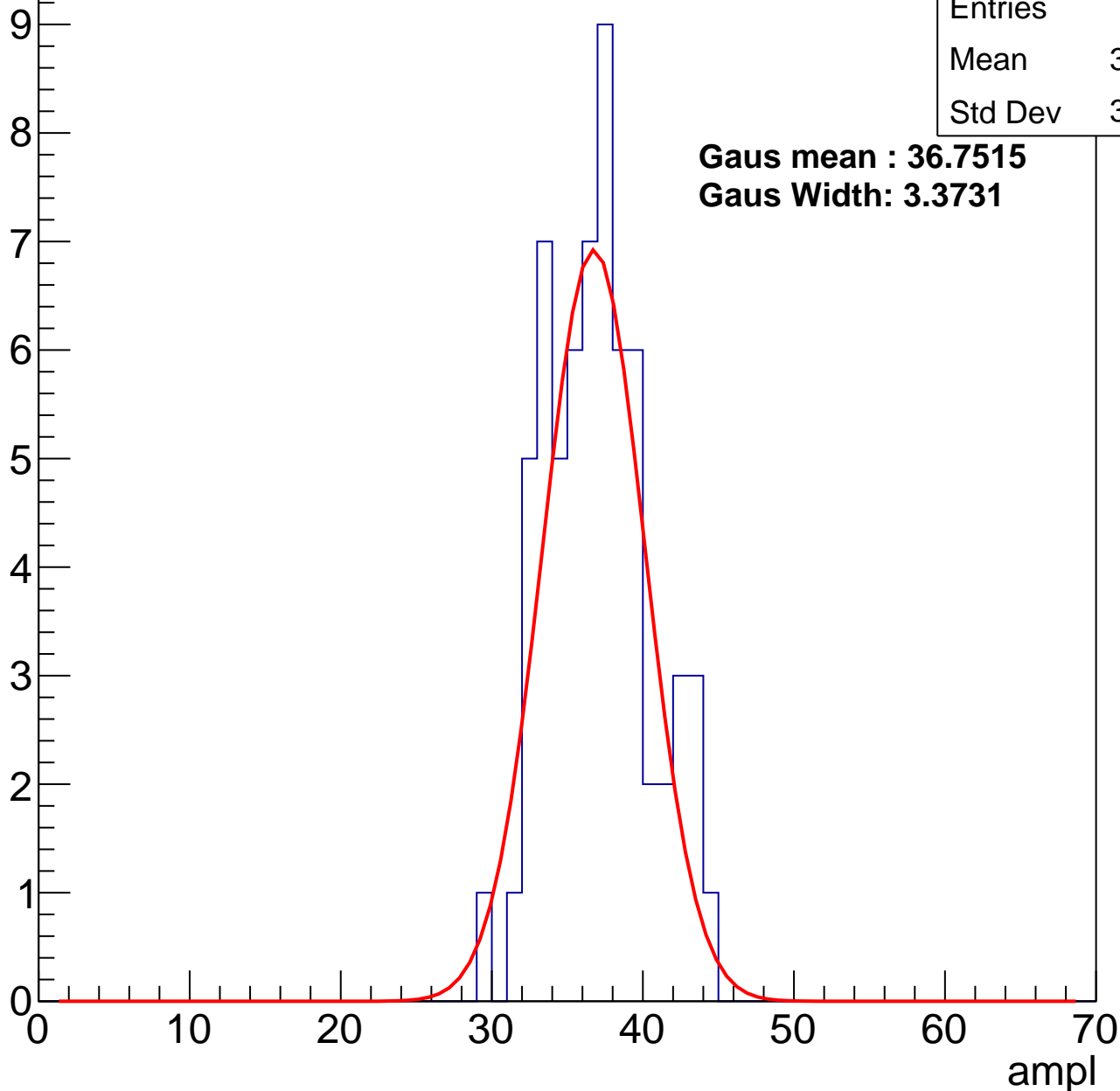
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.55
Std Dev	3.335

Gaus mean : 36.7515

Gaus Width: 3.3731



B1L101S, U6-ch42, adc2

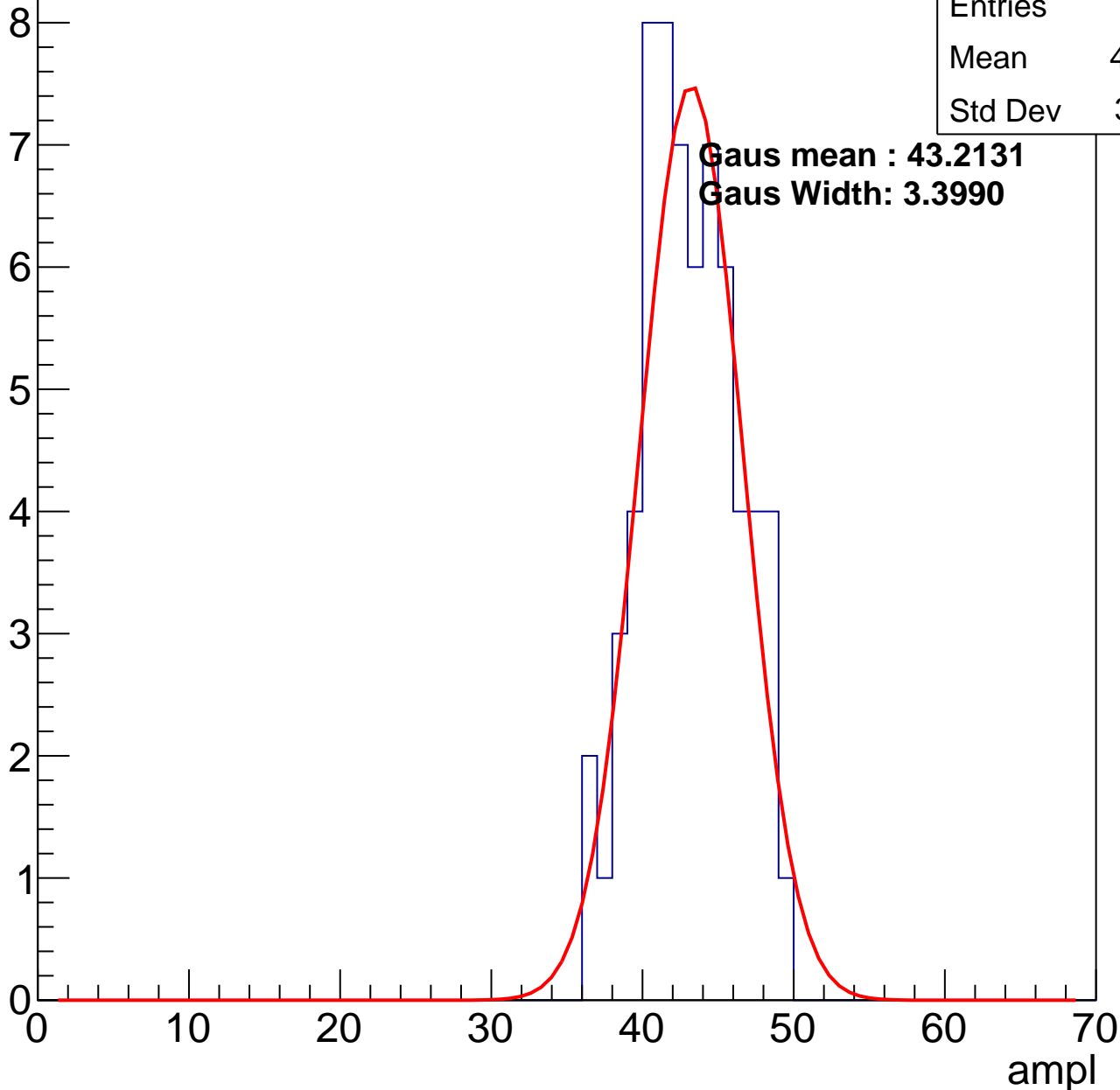
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.62
Std Dev	3.141

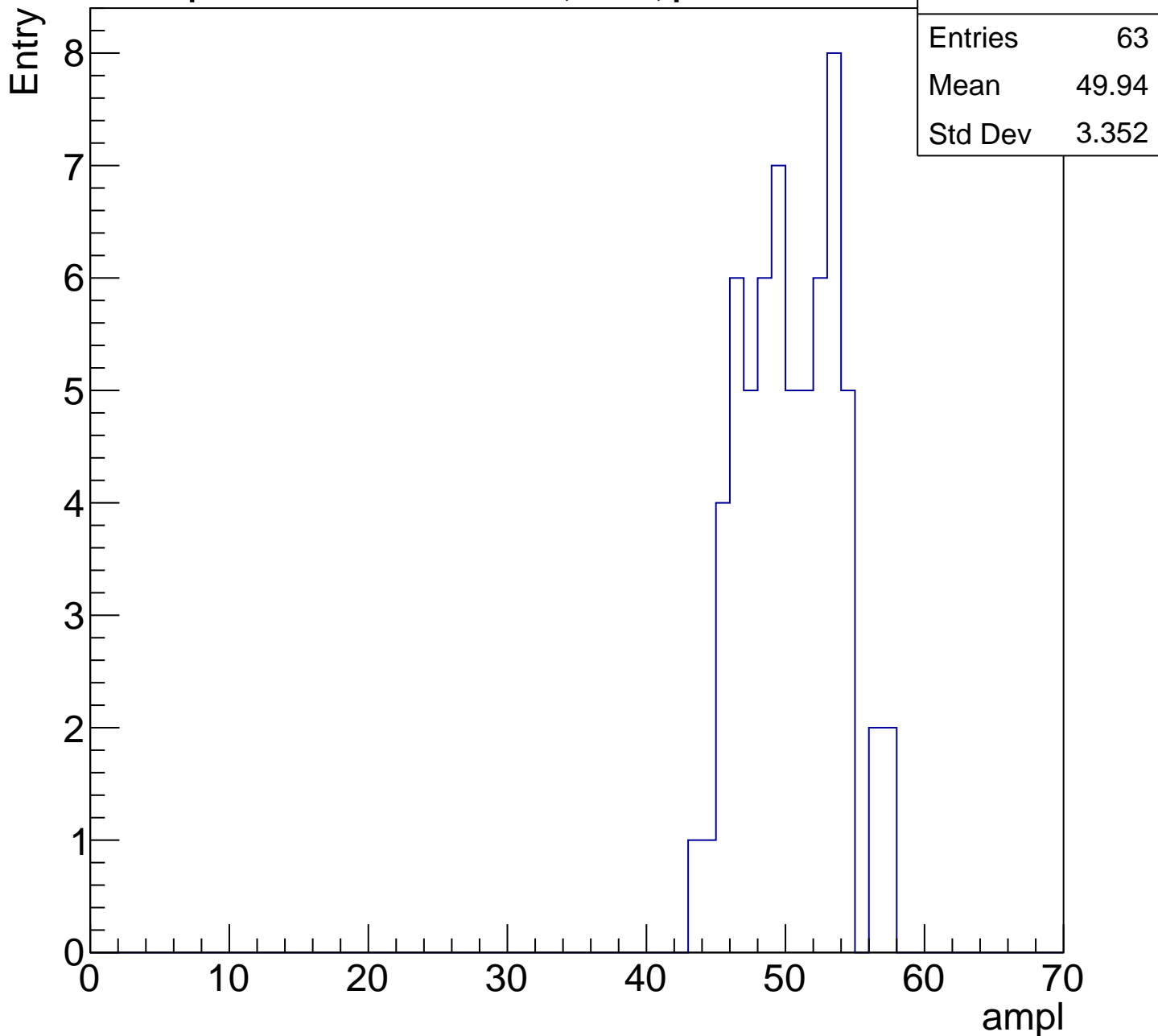
Gaus mean : 43.2131

Gaus Width: 3.3990



B1L101S, U6-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

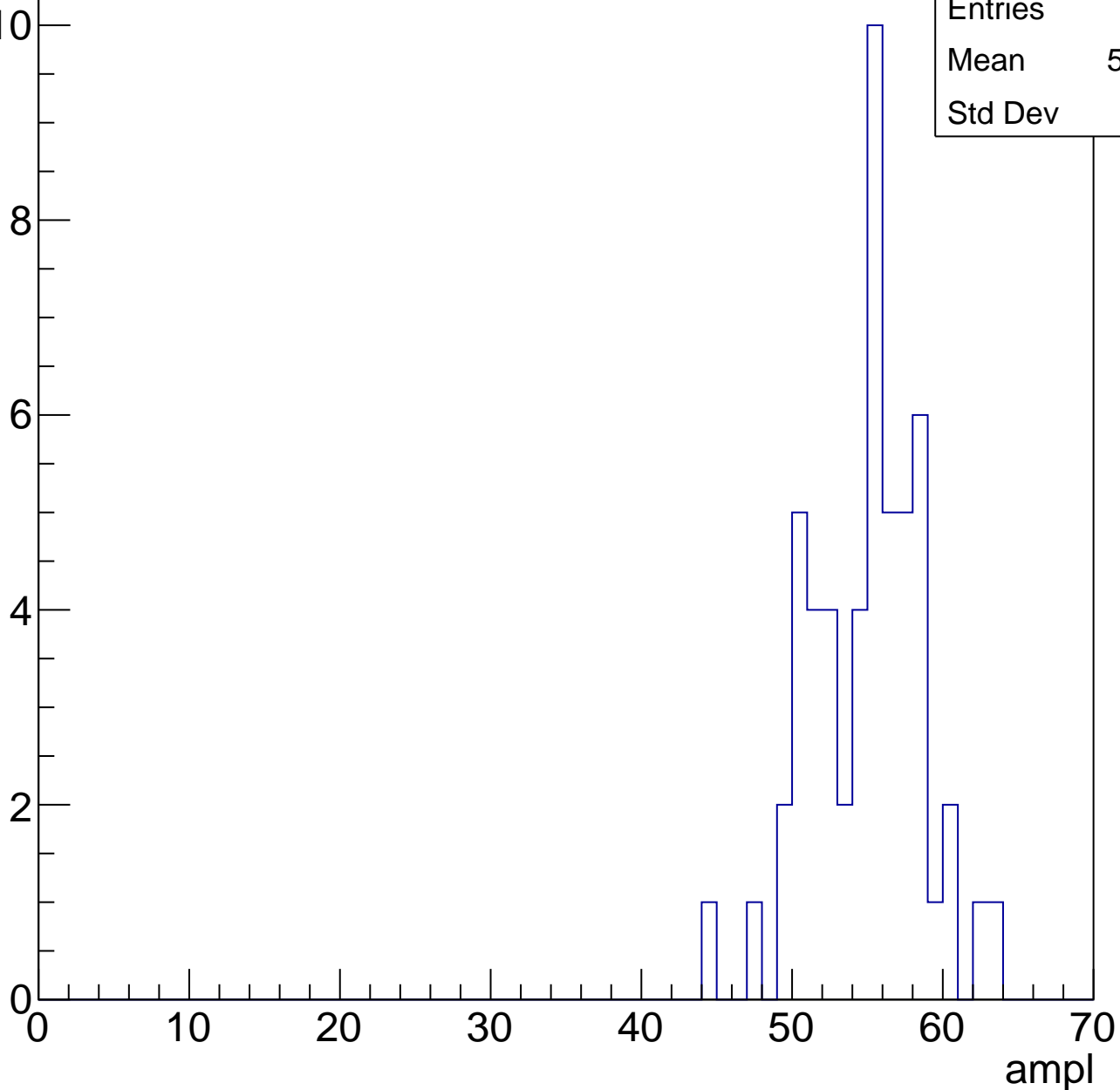


B1L101S, U6-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.44
Std Dev	3.69



B1L101S, U6-ch42, adc5

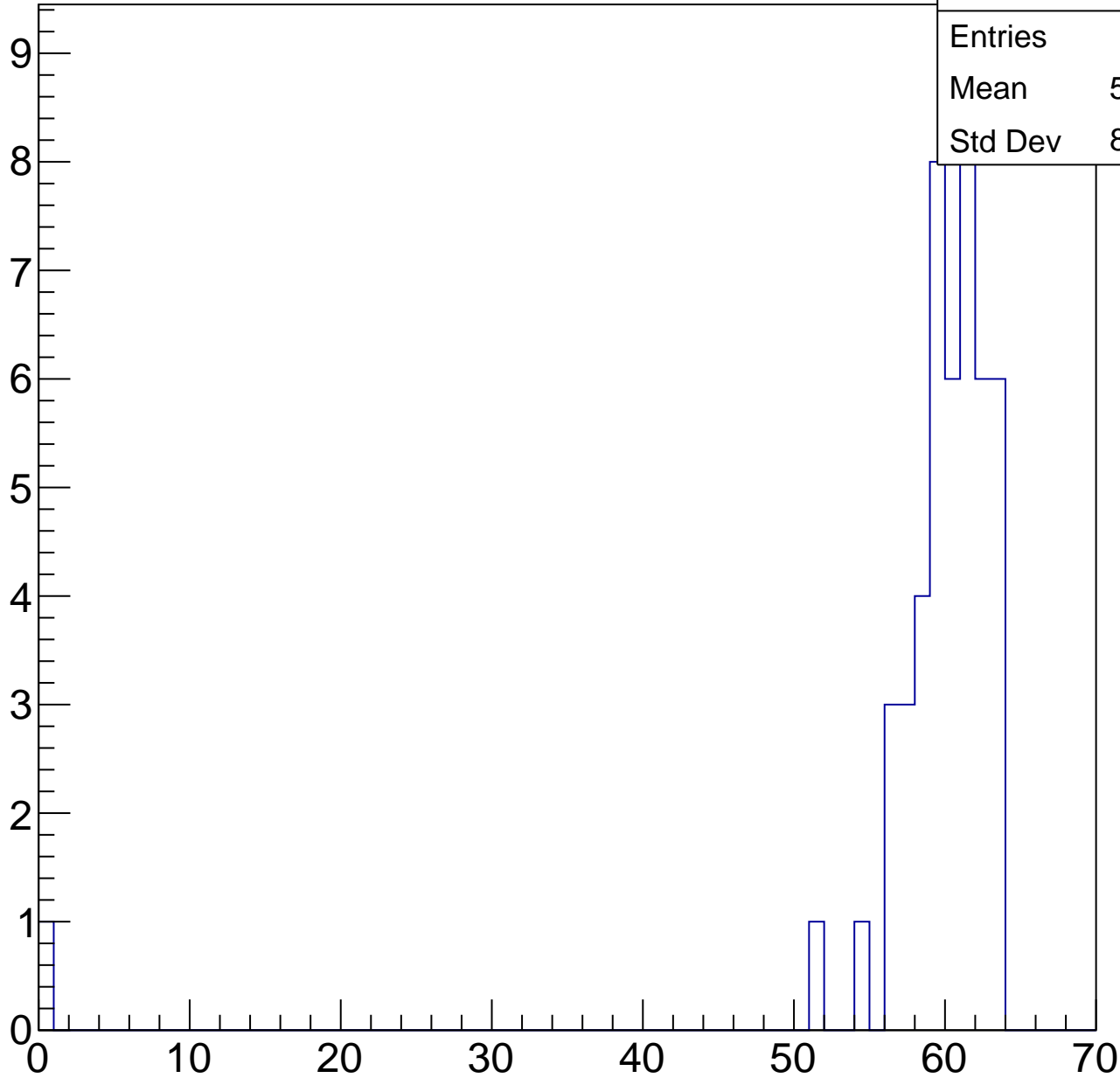
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.48
Std Dev	8.888

ampl



B1L101S, U6-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

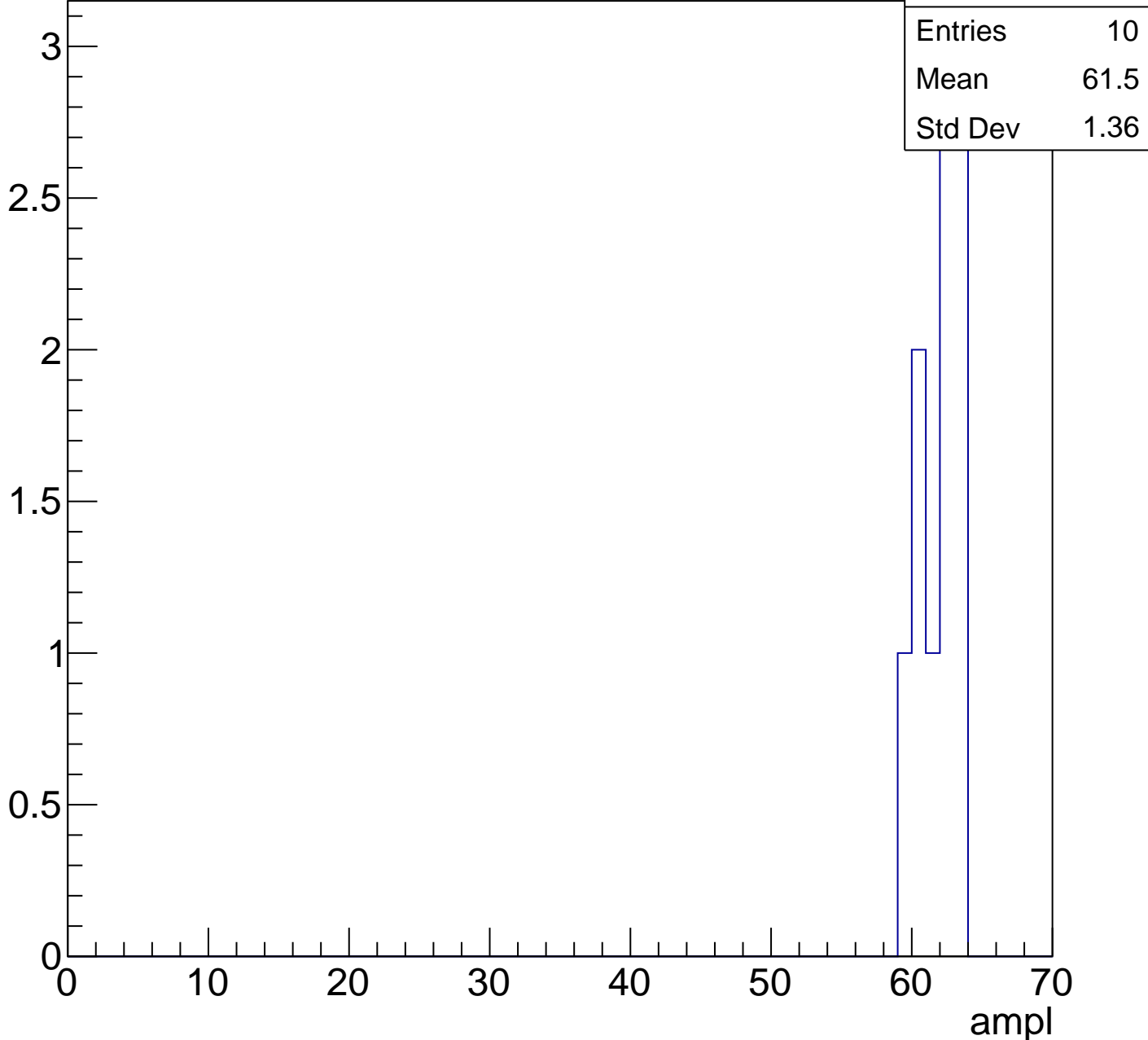
10

Mean

61.5

Std Dev

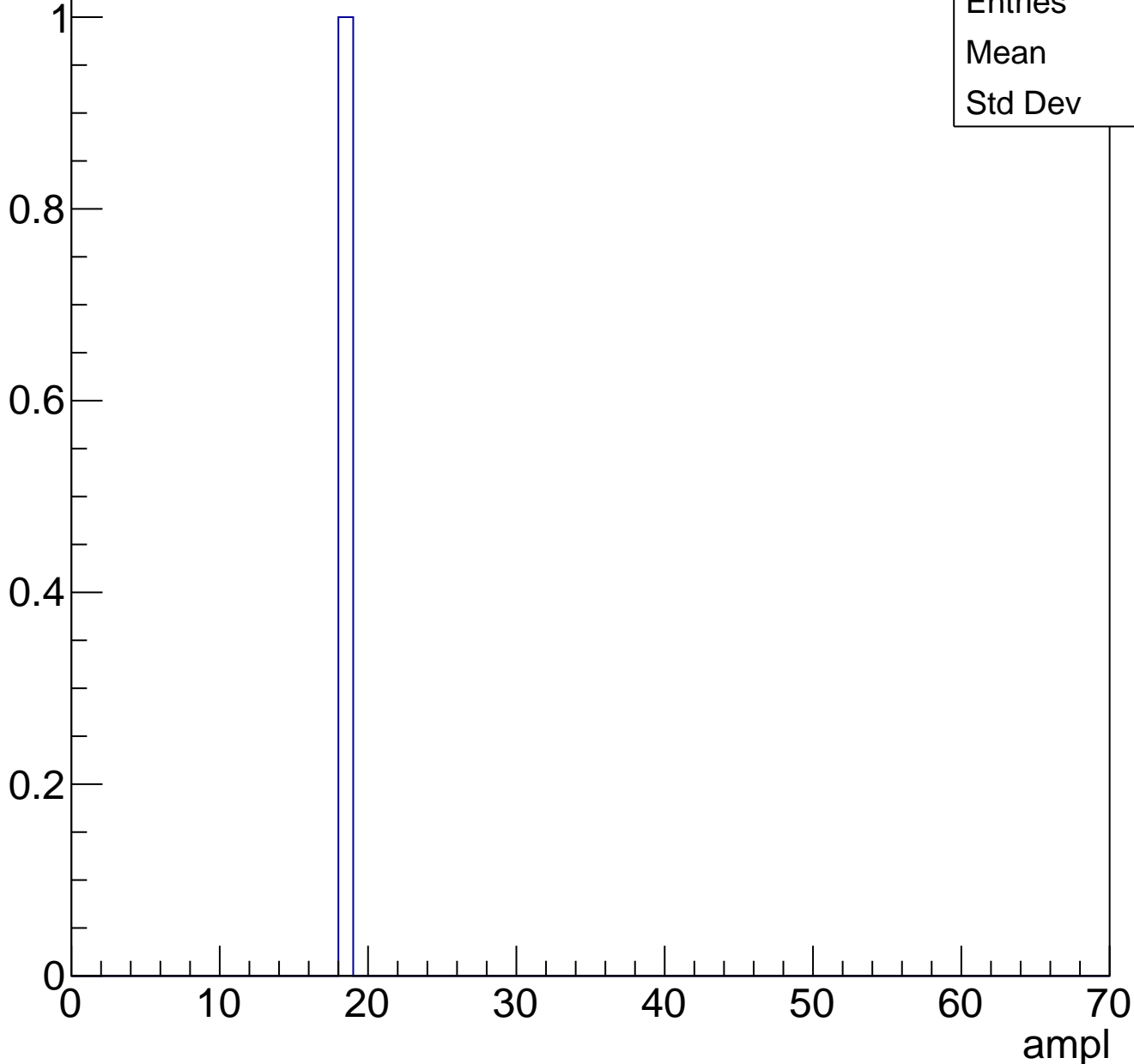
1.36



B1L101S, U6-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch43, adc0

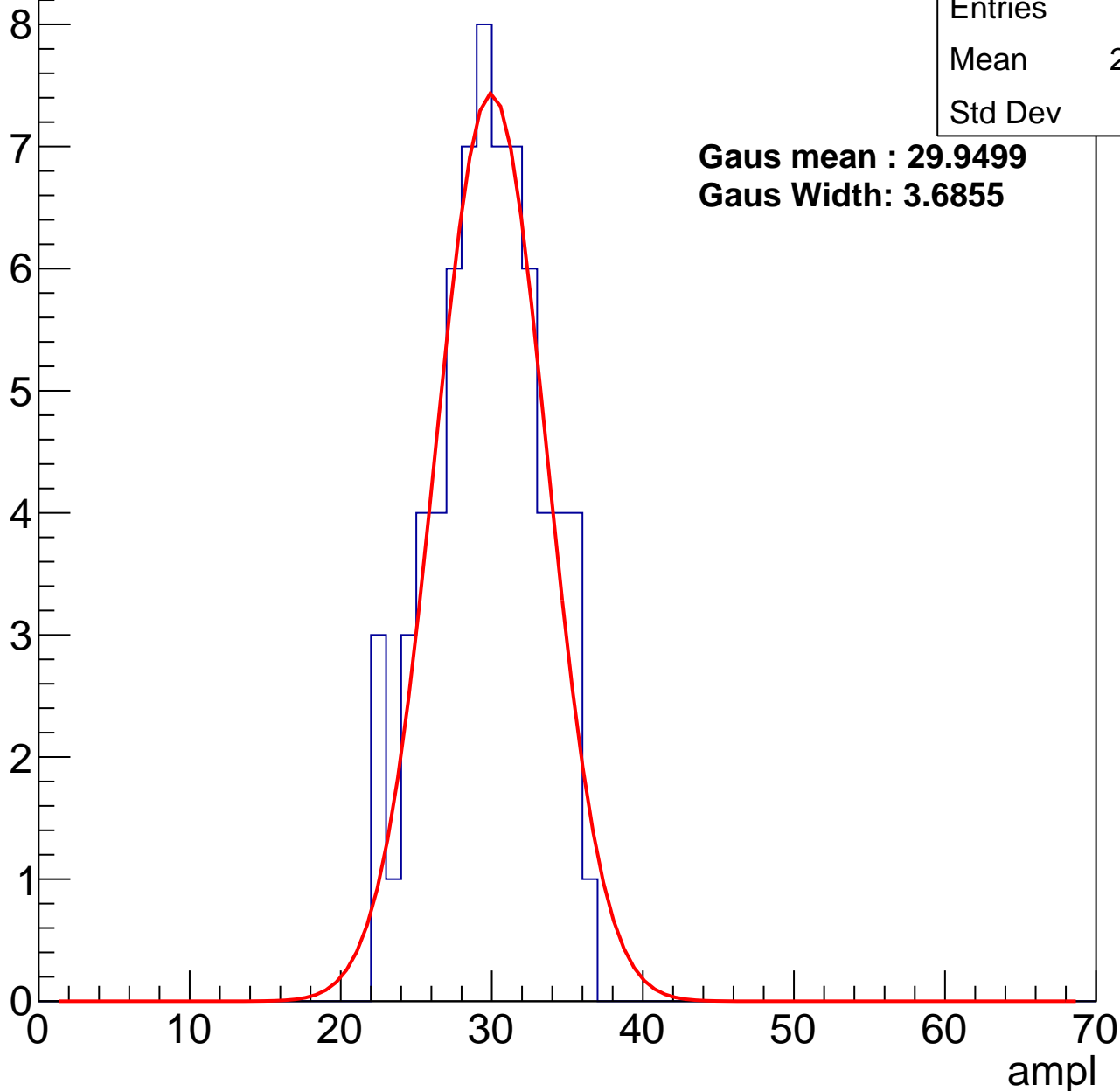
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.25
Std Dev	3.47

Gaus mean : 29.9499

Gaus Width: 3.6855



B1L101S, U6-ch43, adc1

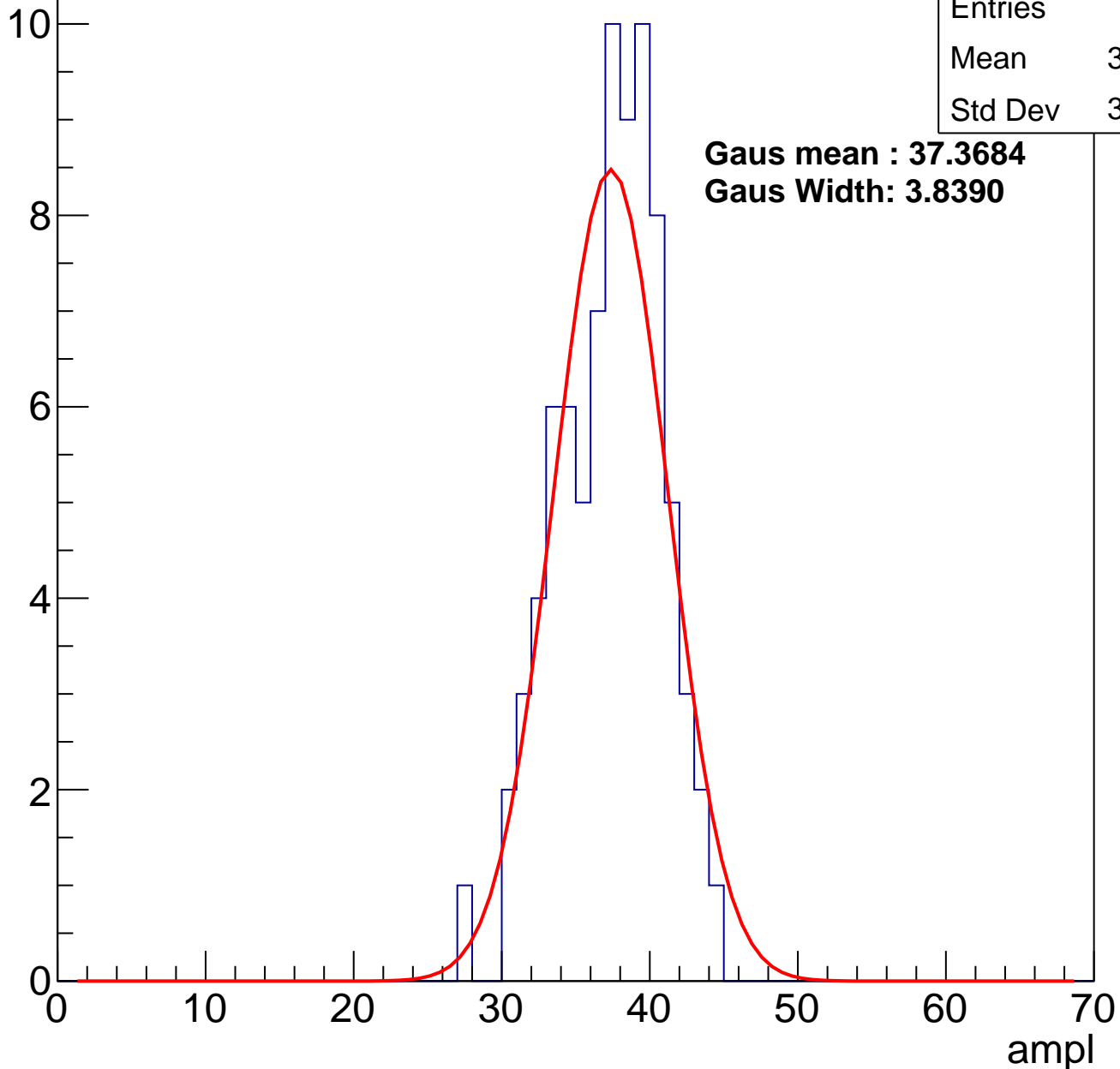
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	36.83
Std Dev	3.453

Gaus mean : 37.3684

Gaus Width: 3.8390

Entry

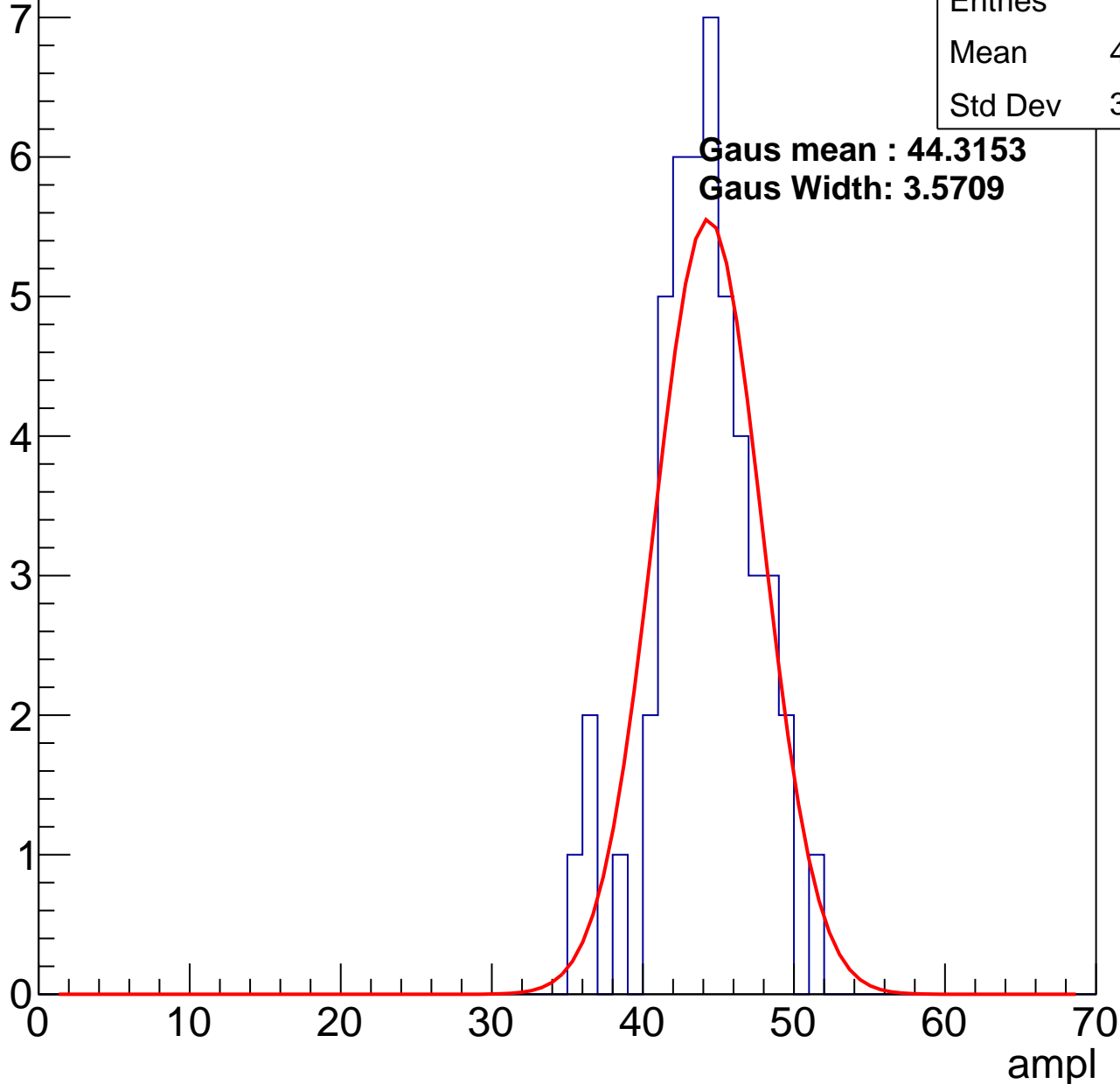


B1L101S, U6-ch43, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	43.56
Std Dev	3.347

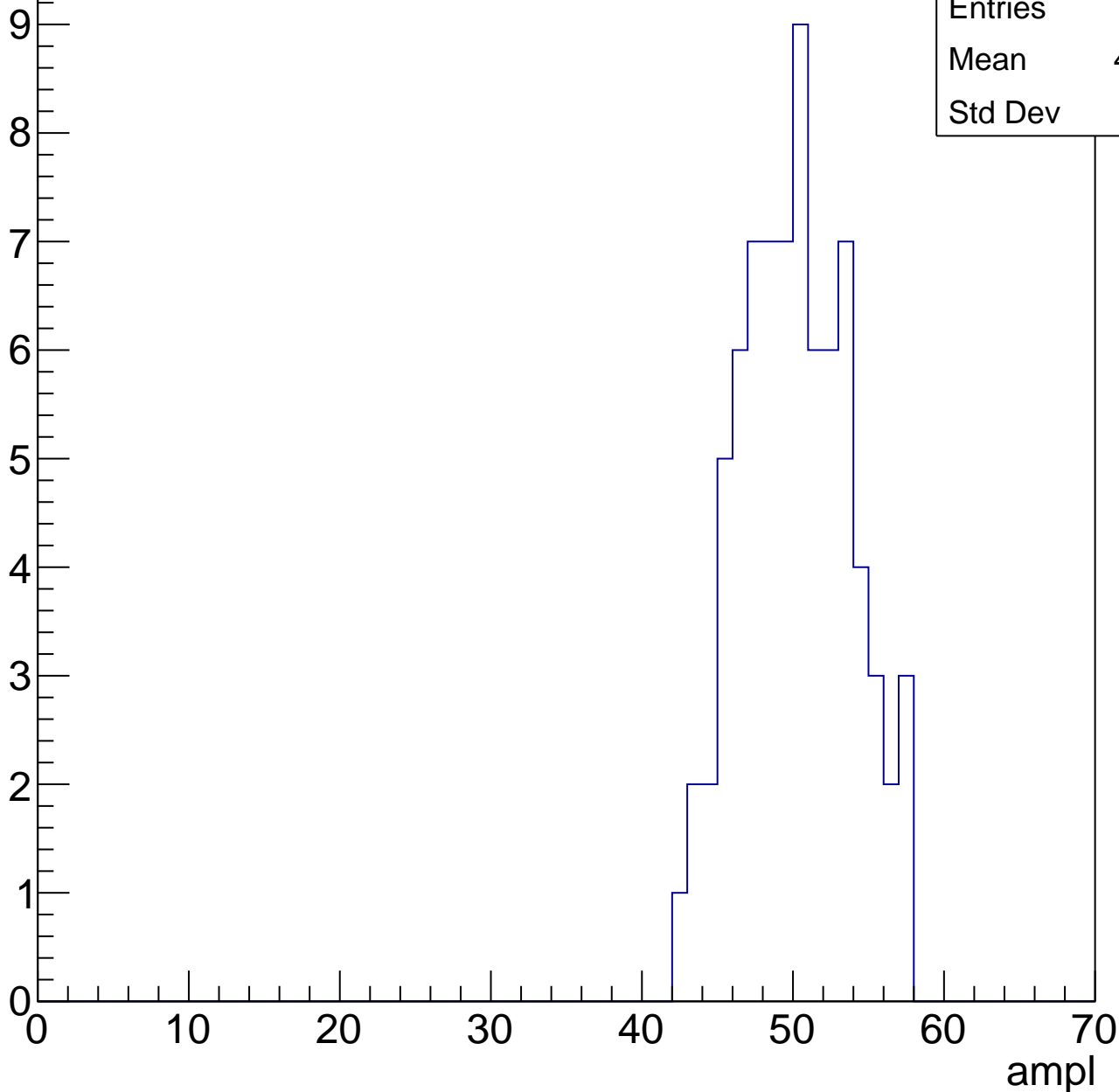


B1L101S, U6-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	49.71
Std Dev	3.6

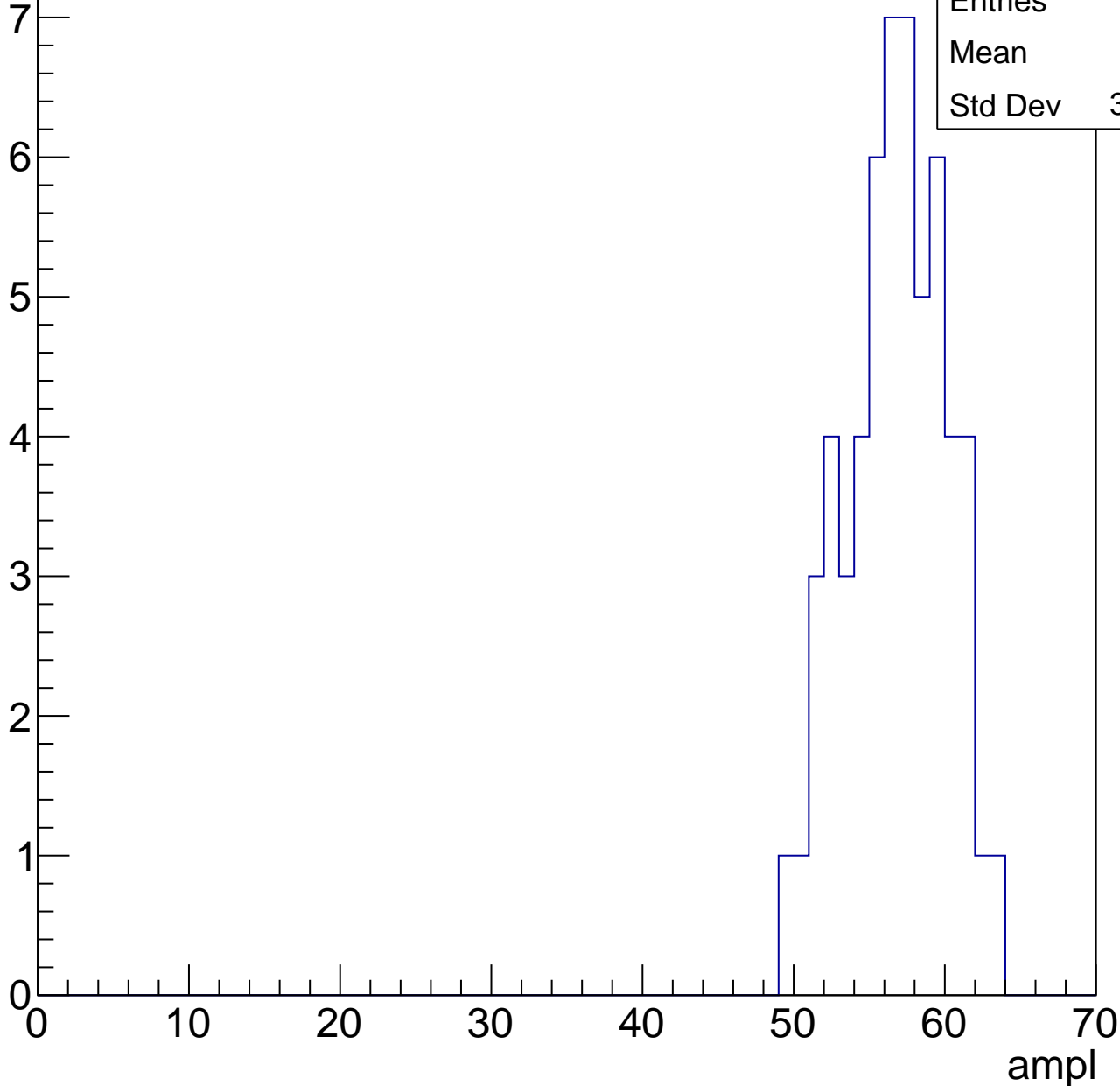


B1L101S, U6-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.3
Std Dev	3.239

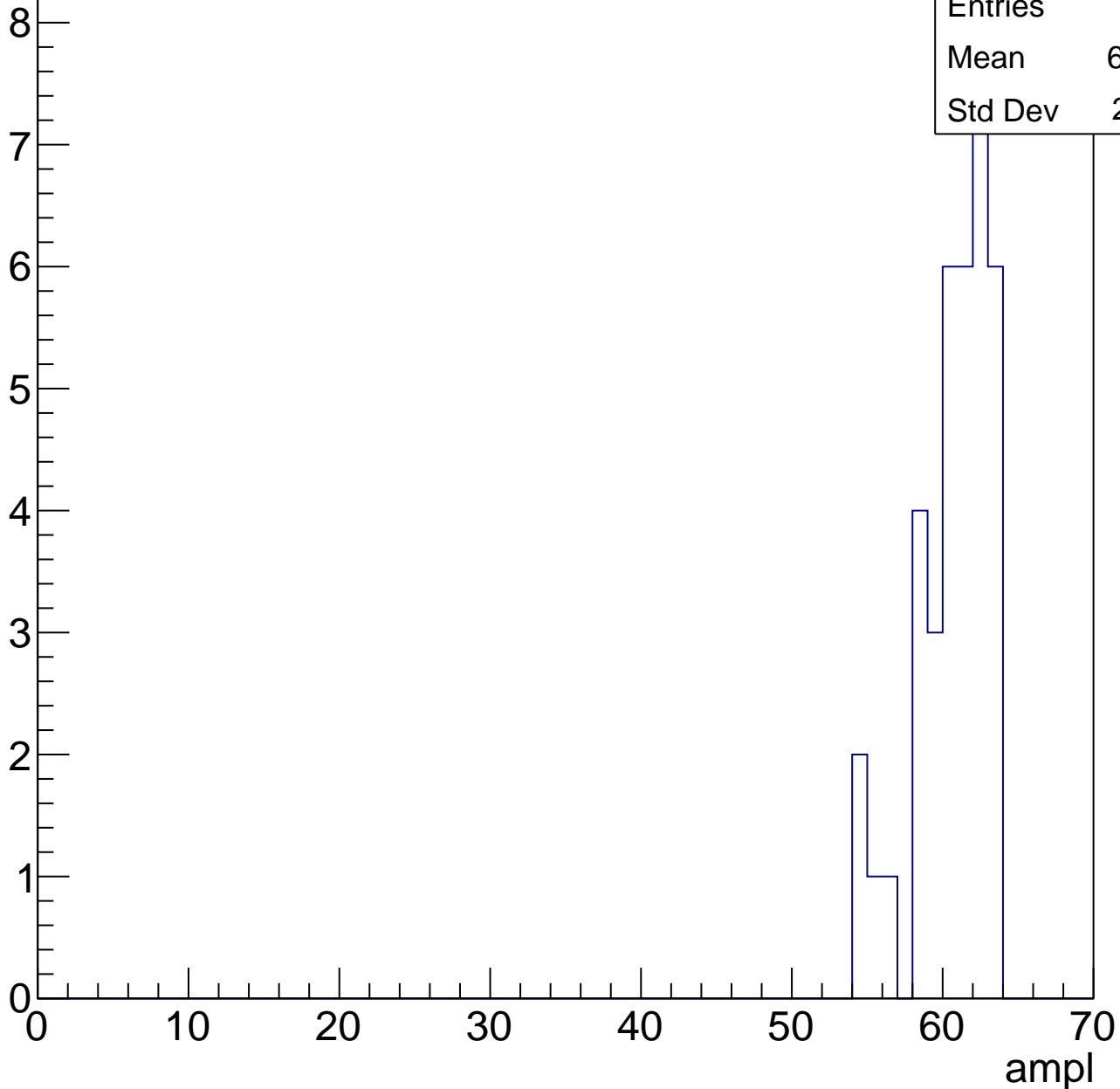


B1L101S, U6-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

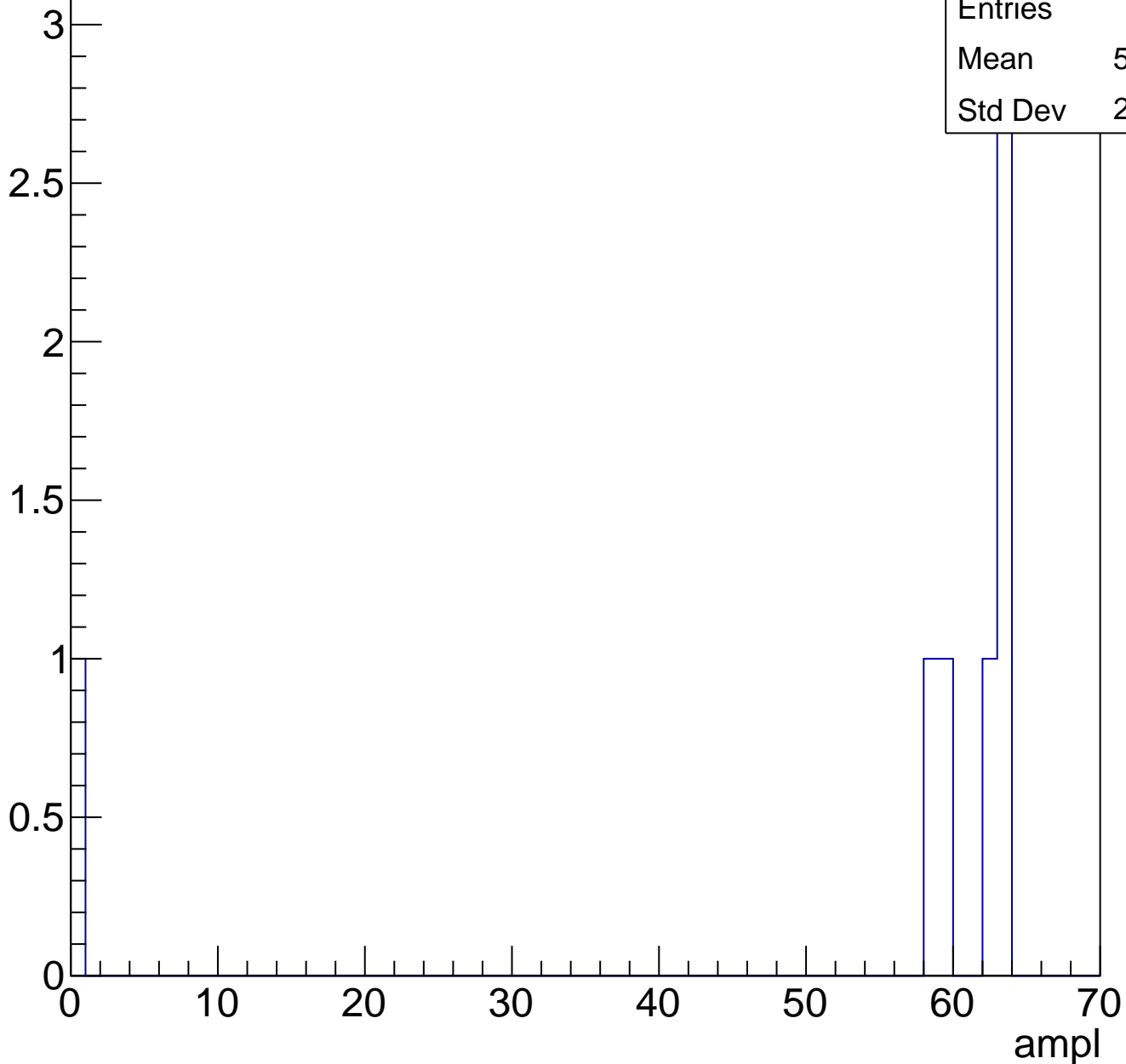
Entries	37
Mean	60.22
Std Dev	2.451



B1L101S, U6-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch44, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	29.72
Std Dev	4.987

Gaus mean : 30.7778

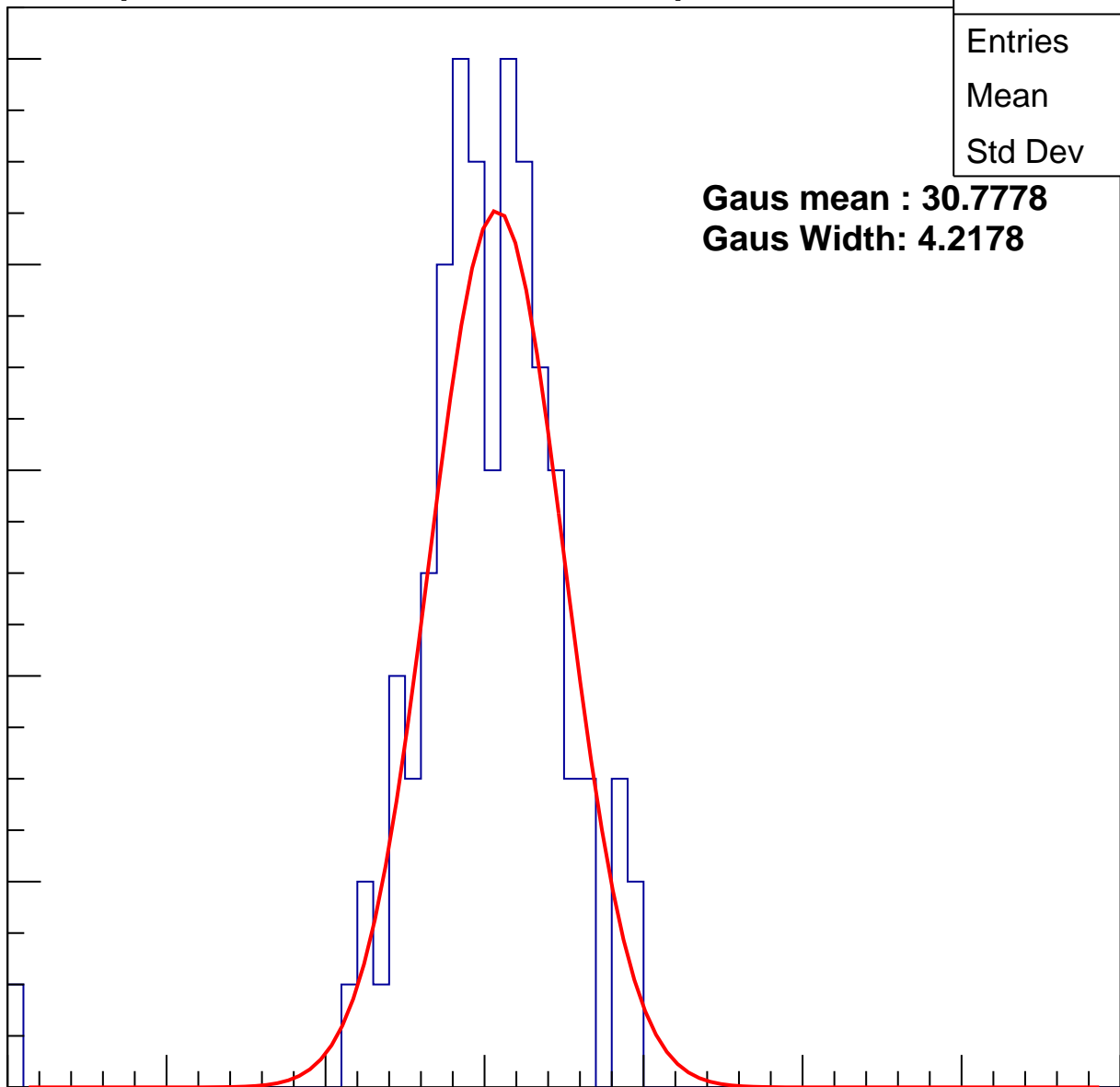
Gaus Width: 4.2178

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch44, adc1

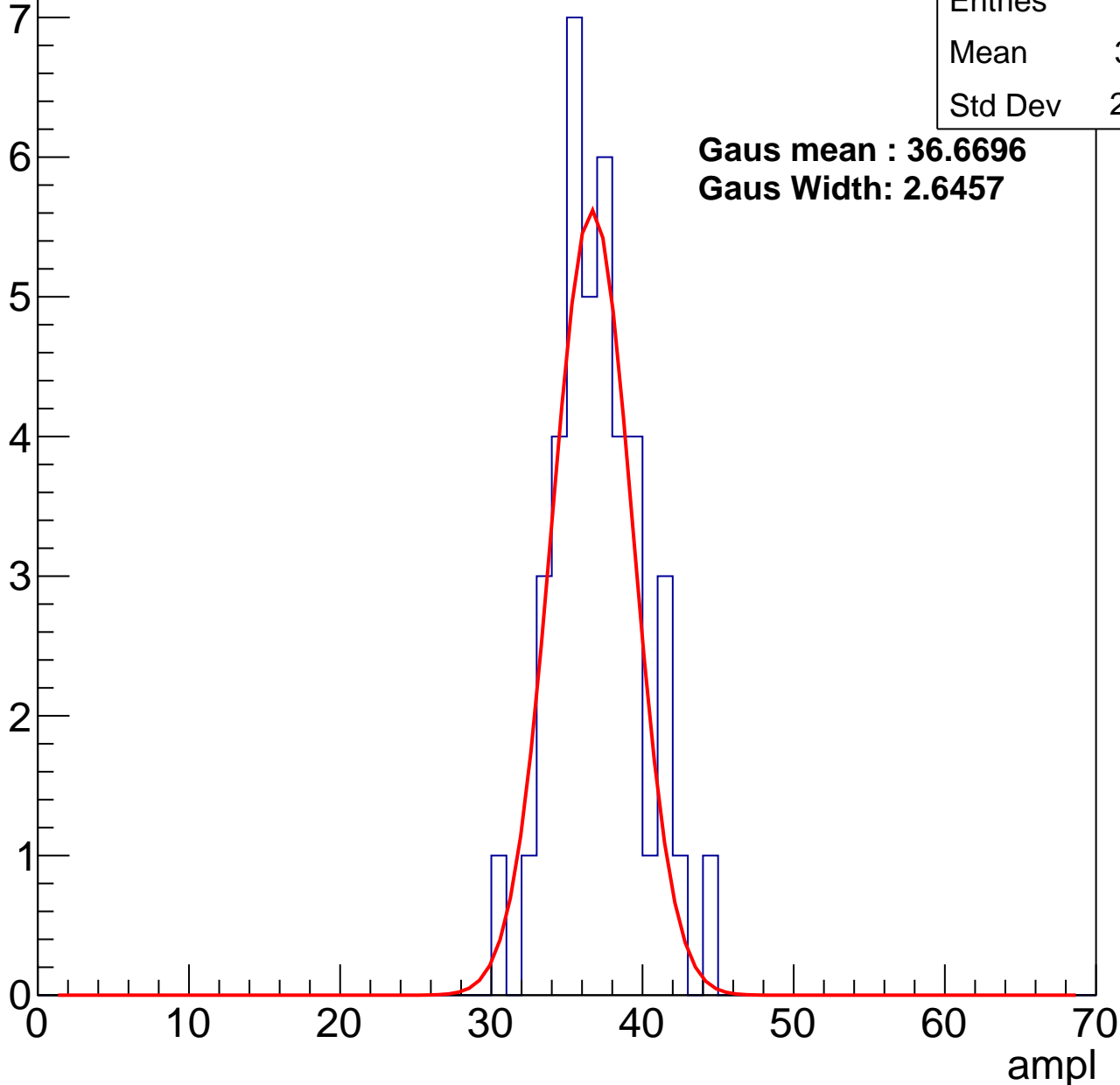
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	36.61
Std Dev	2.862

Gaus mean : 36.6696

Gaus Width: 2.6457



B1L101S, U6-ch44, adc2

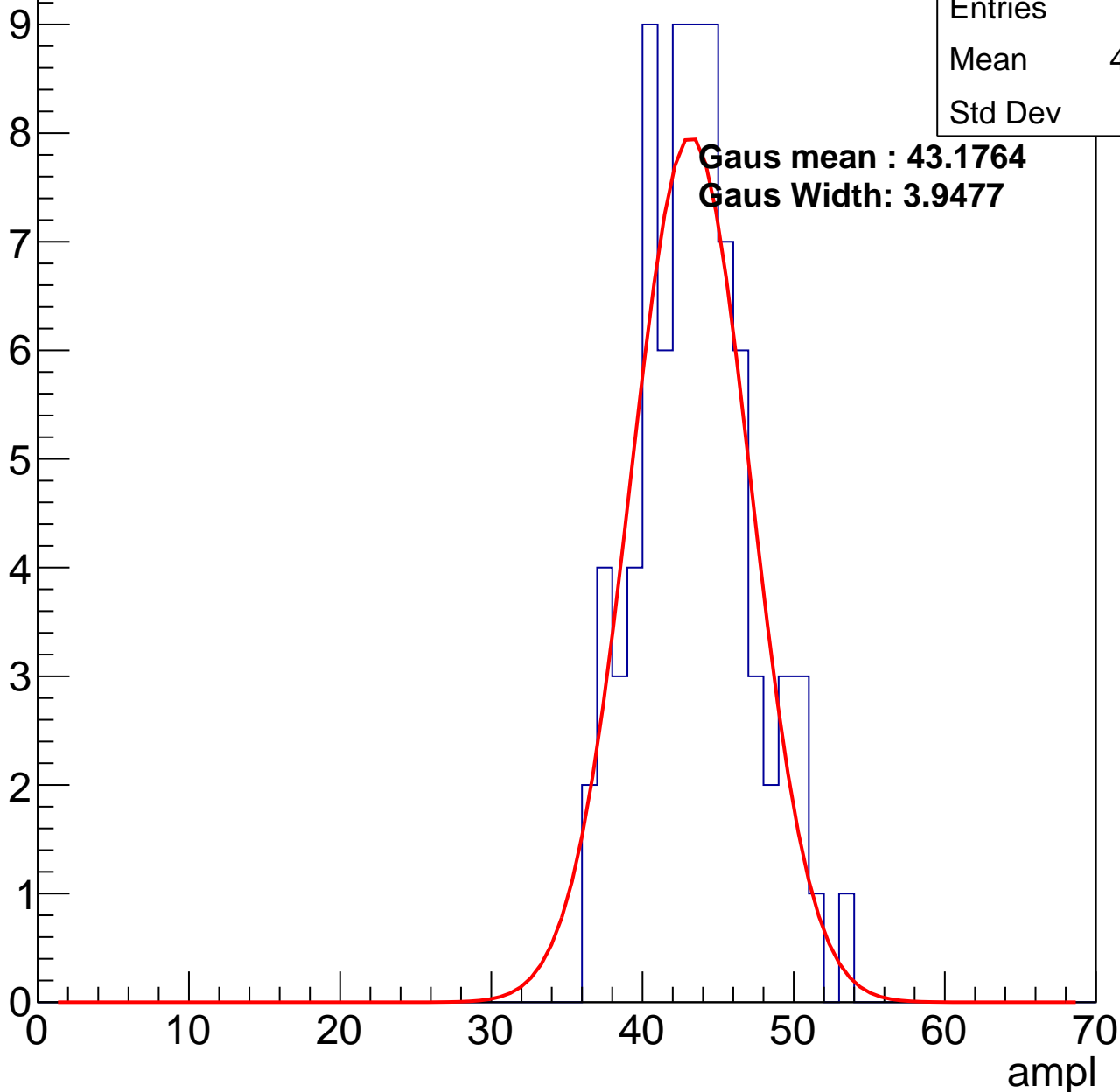
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	43.04
Std Dev	3.69

Gaus mean : 43.1764

Gaus Width: 3.9477

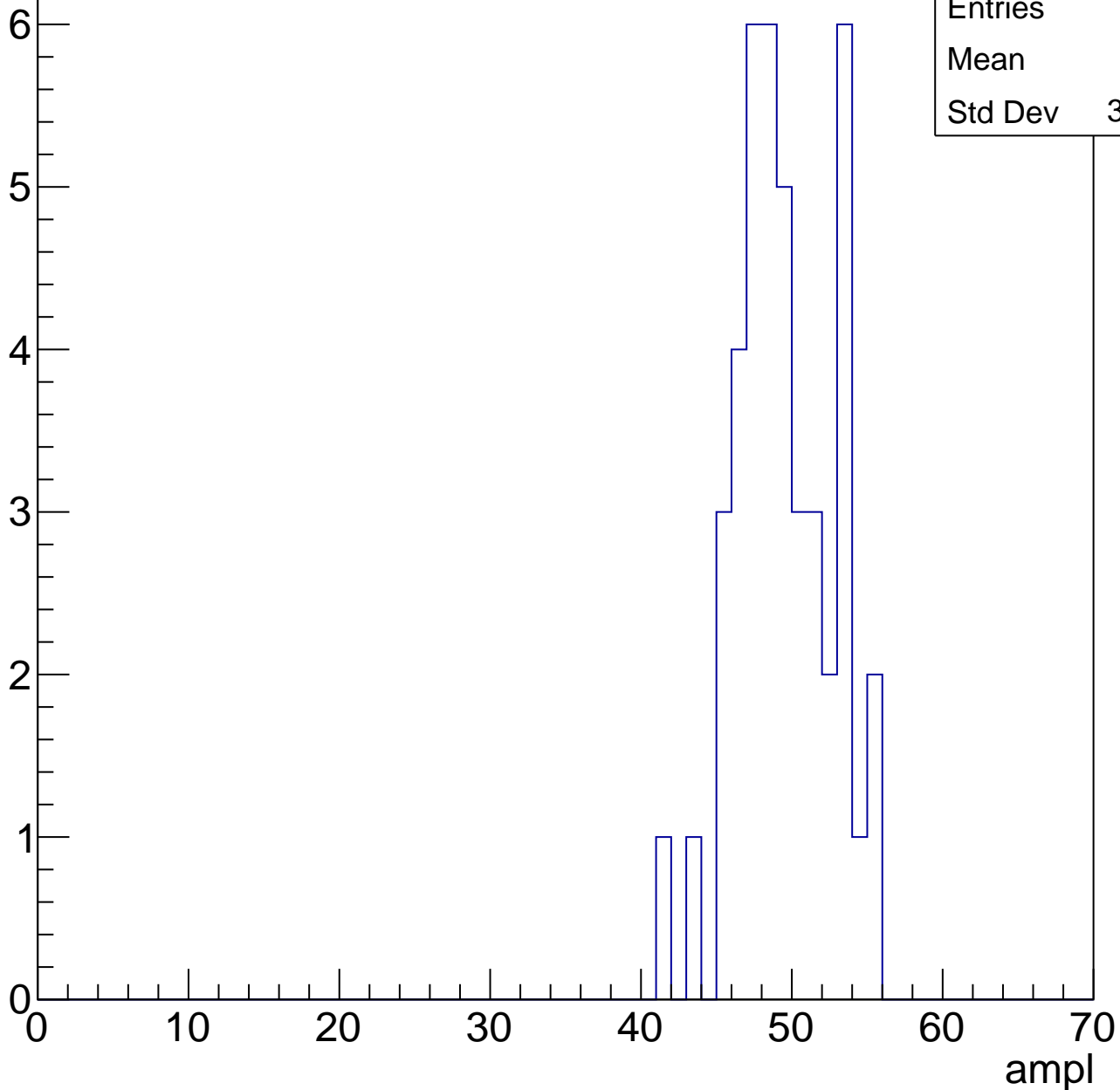


B1L101S, U6-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

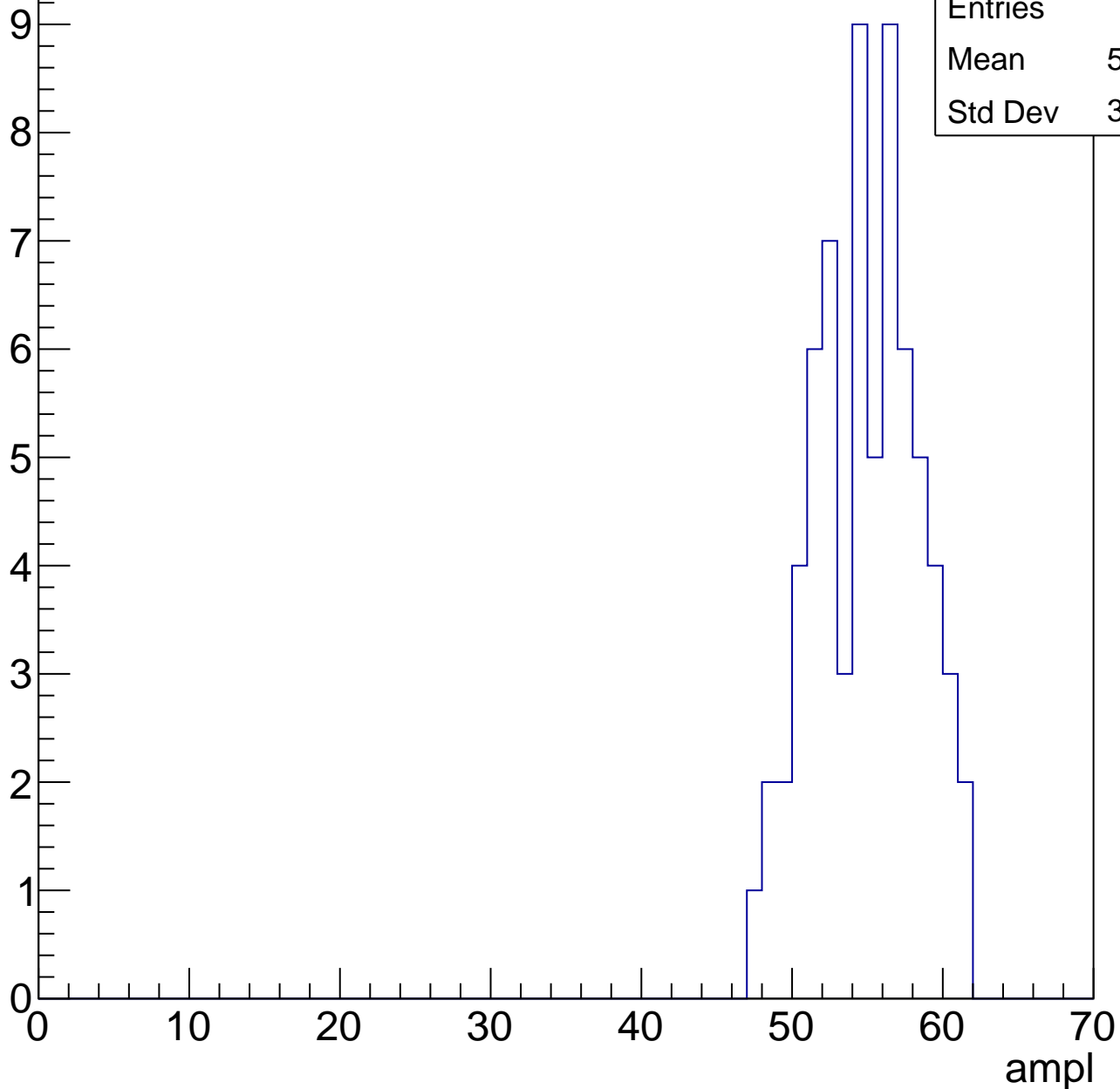
Entries	43
Mean	49
Std Dev	3.199



B1L101S, U6-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

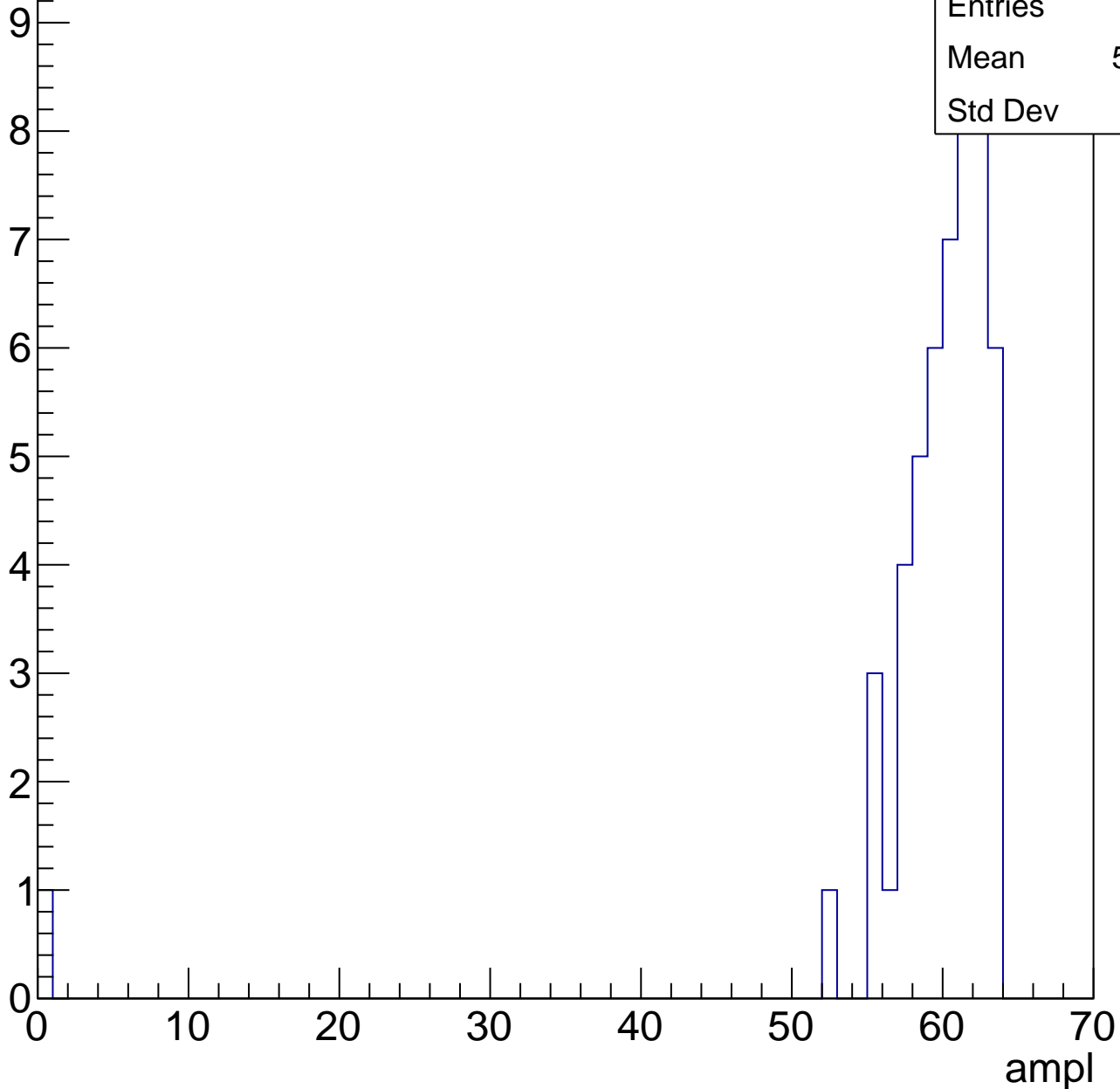


B1L101S, U6-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.61
Std Dev	8.65



B1L101S, U6-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch45, adc0

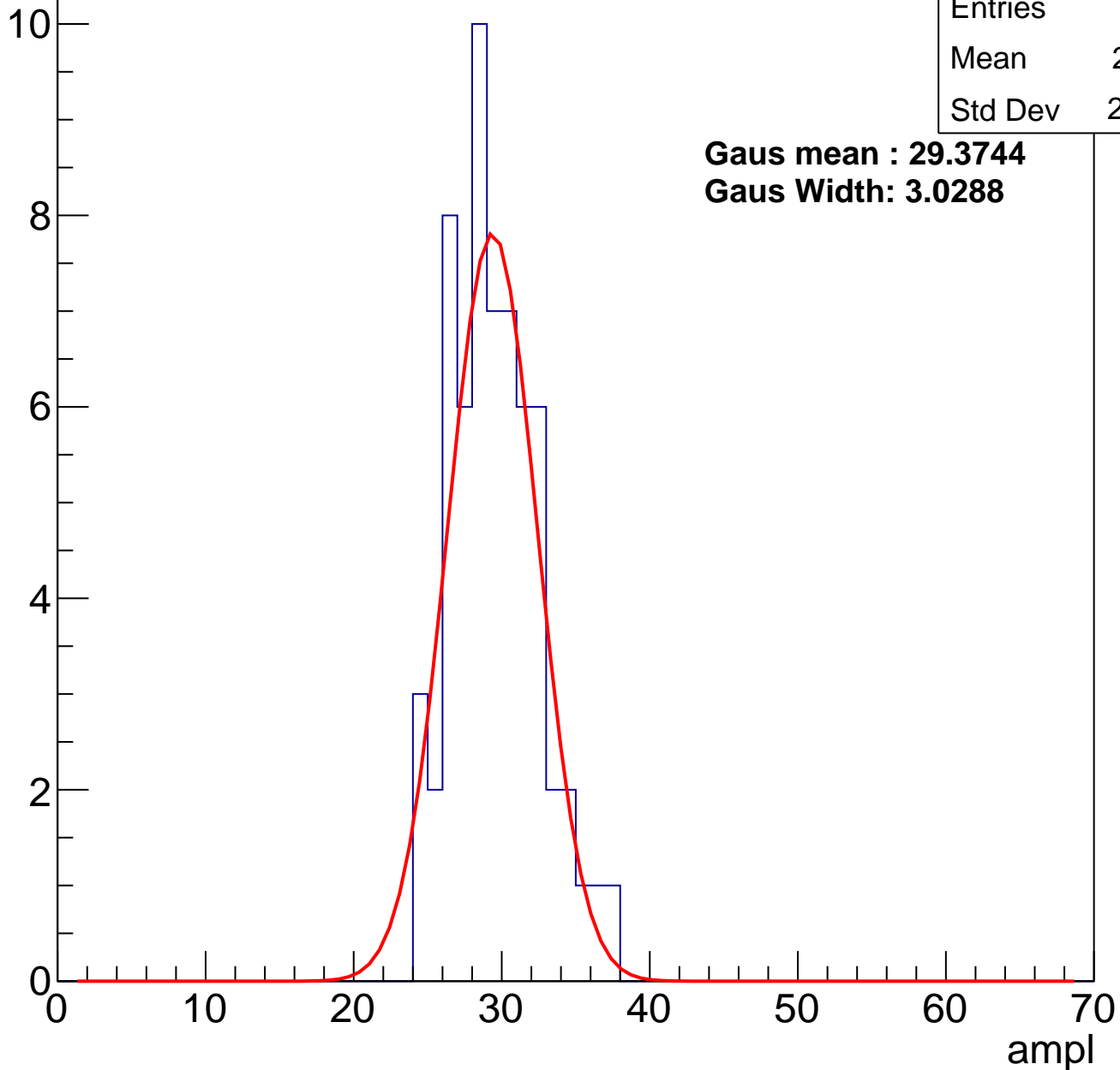
calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	29.11
Std Dev	2.919

Gaus mean : 29.3744

Gaus Width: 3.0288

Entry



B1L101S, U6-ch45, adc1

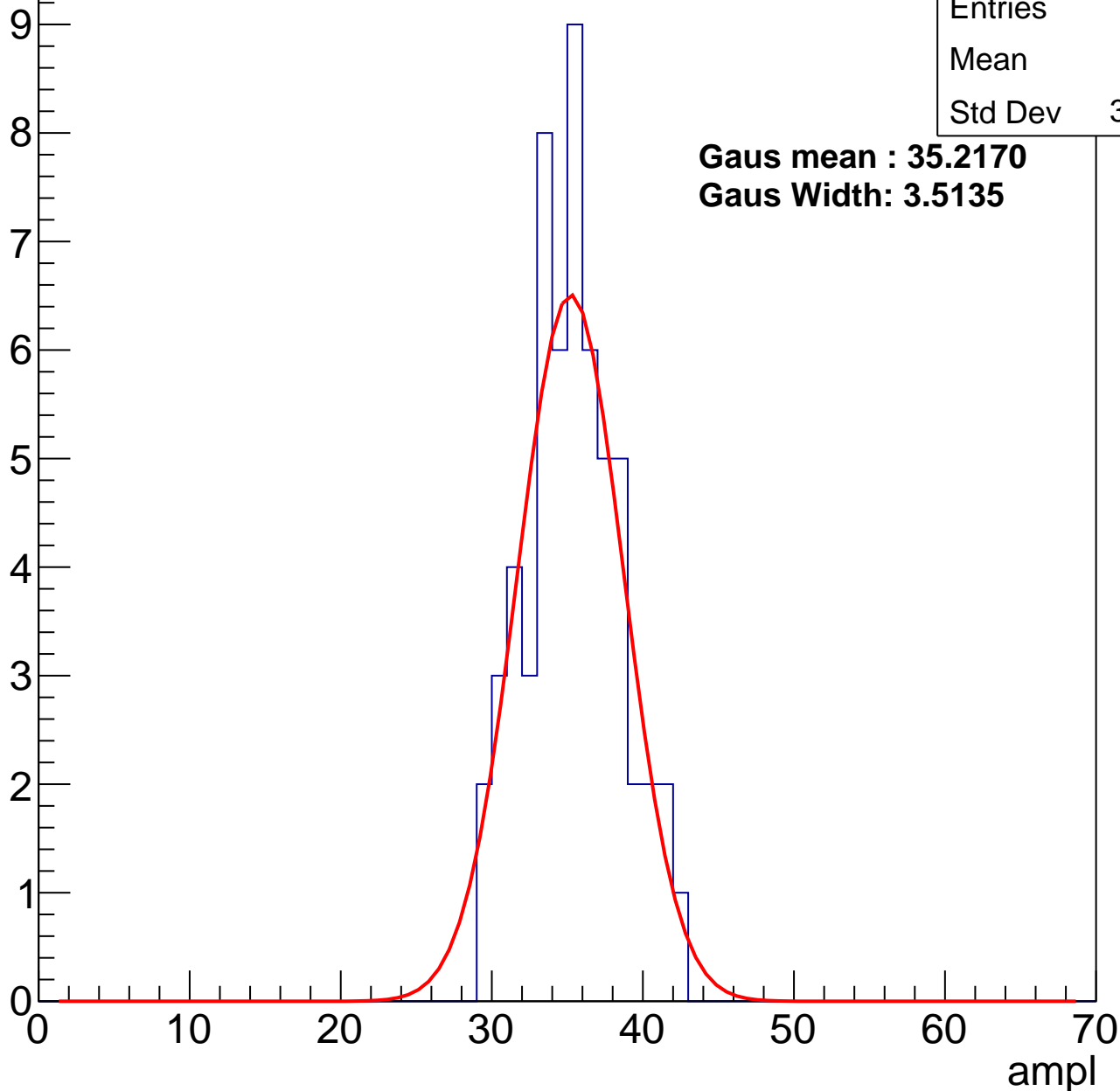
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	34.9
Std Dev	3.078

Gaus mean : 35.2170

Gaus Width: 3.5135



B1L101S, U6-ch45, adc2

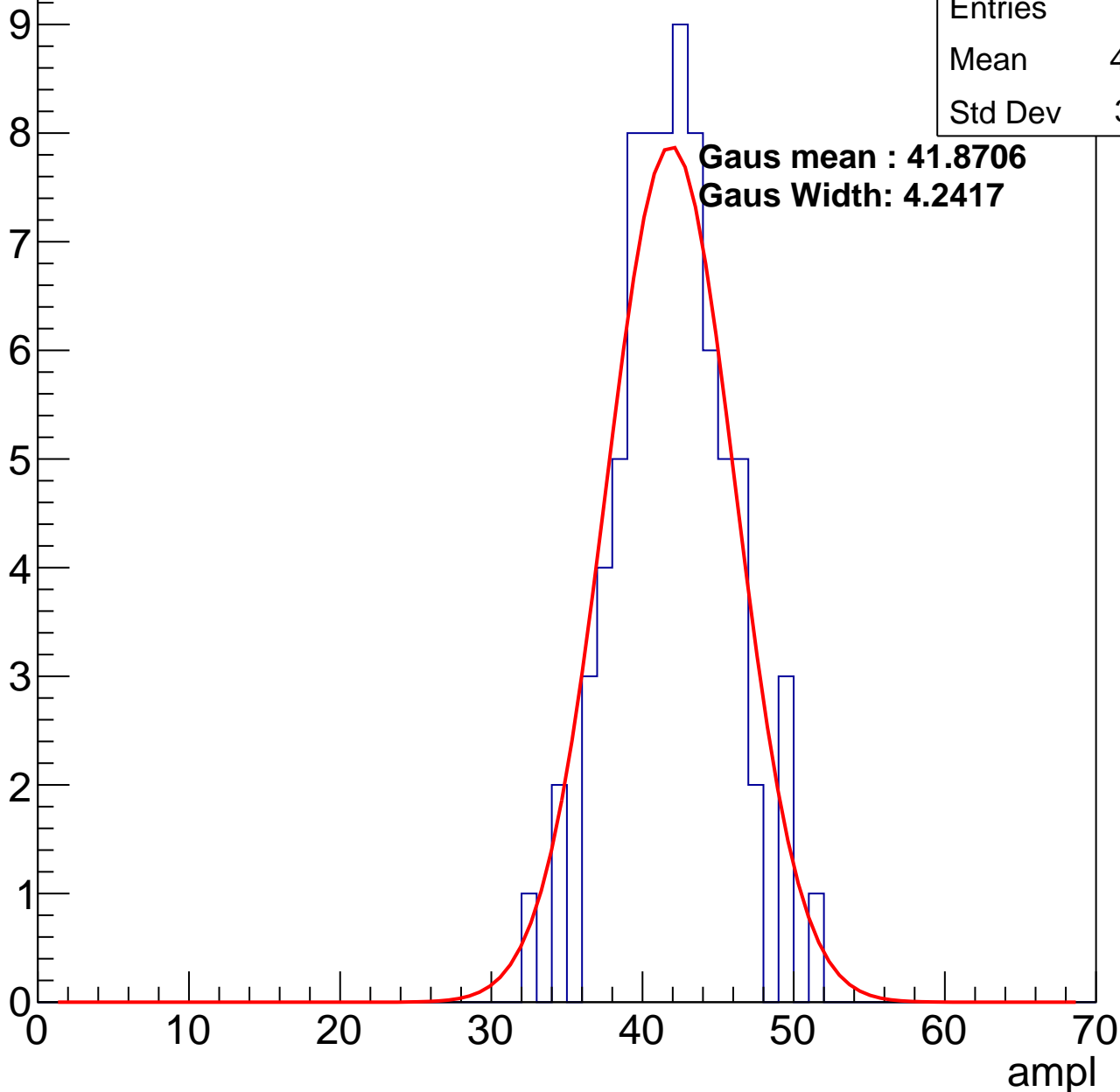
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	41.53
Std Dev	3.661

Gaus mean : 41.8706

Gaus Width: 4.2417

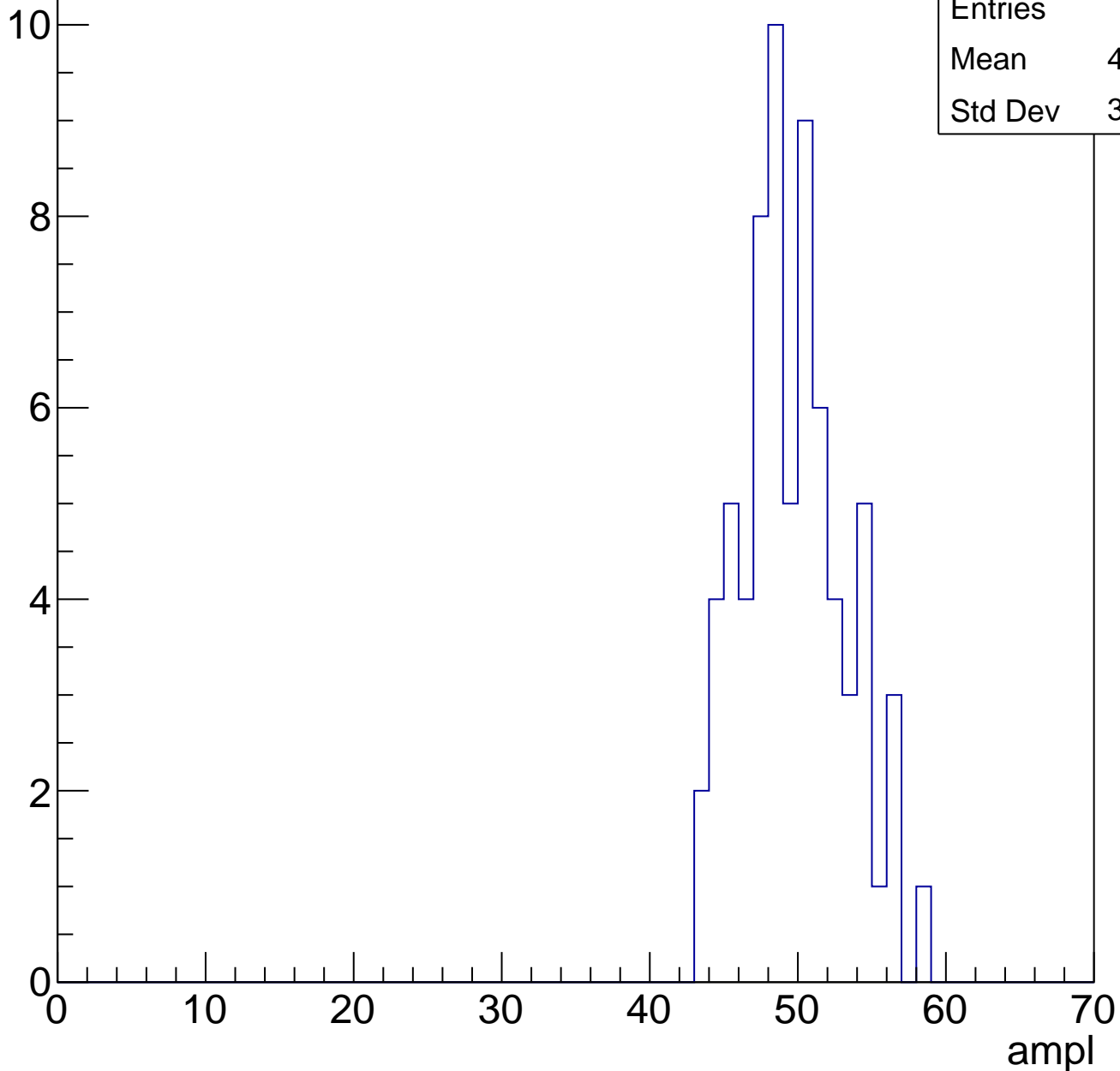


B1L101S, U6-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	49.23
Std Dev	3.448

Entry

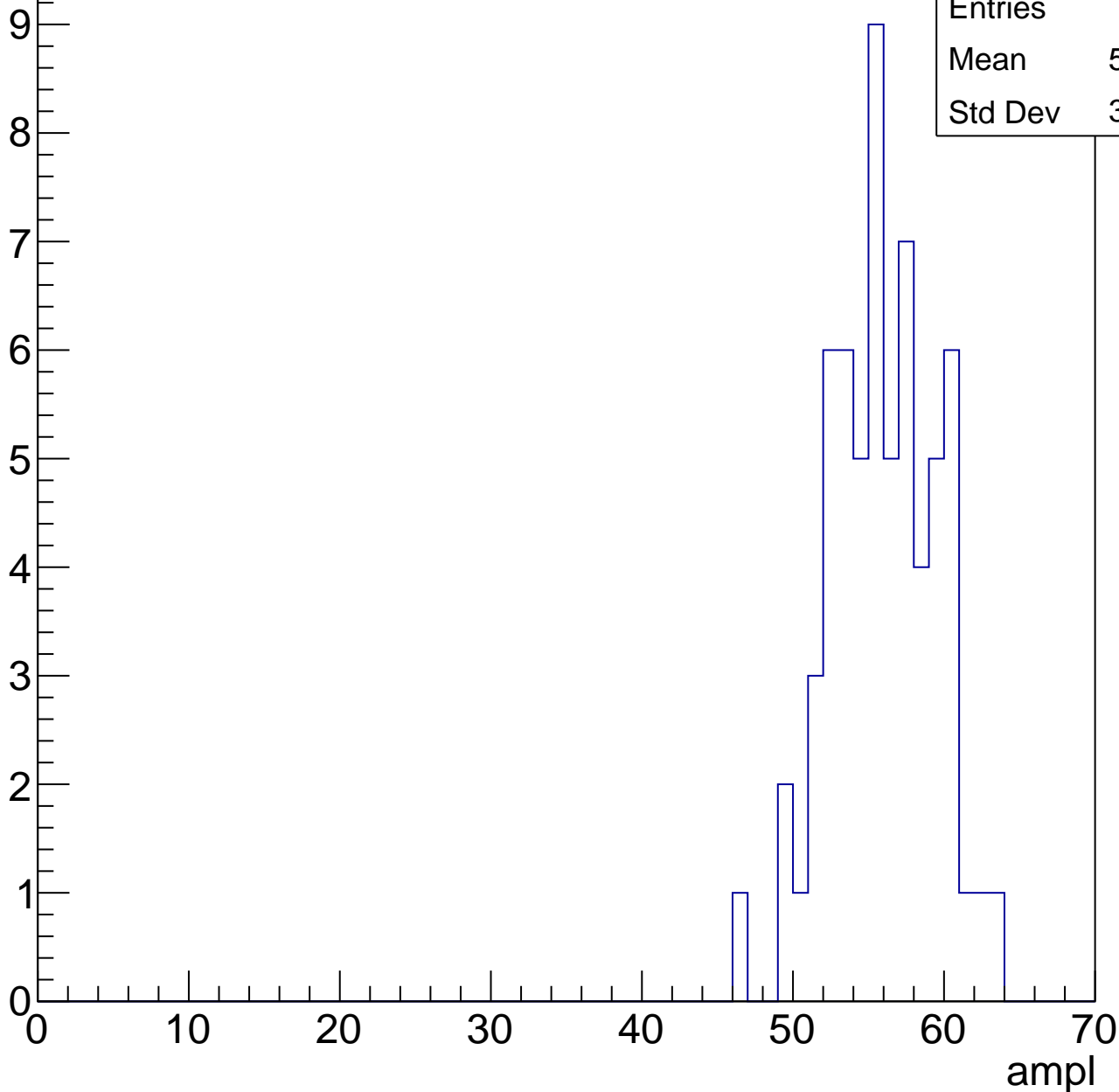


B1L101S, U6-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.46
Std Dev	3.436

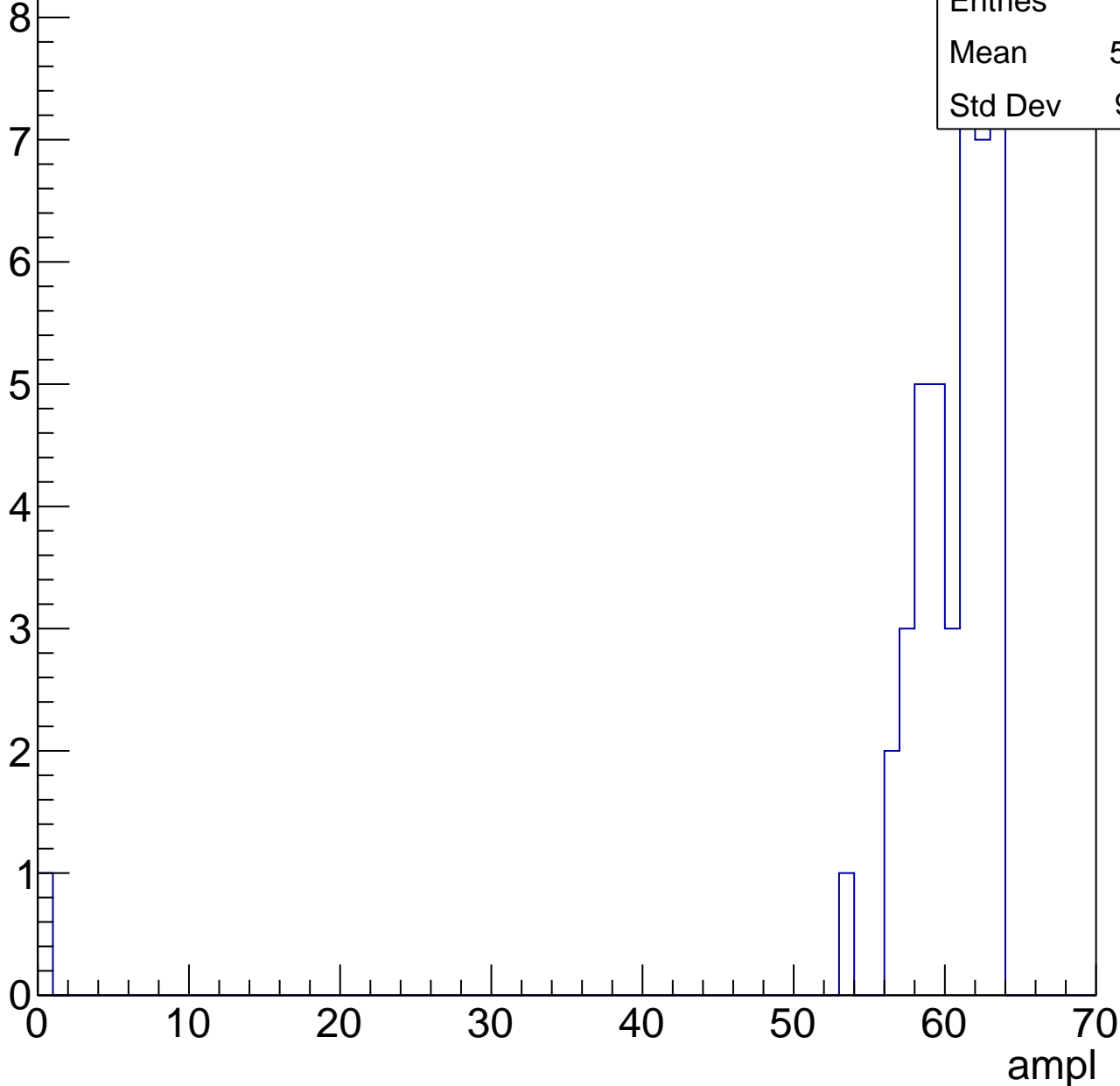


B1L101S, U6-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.77
Std Dev	9.371



B1L101S, U6-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U6-ch46, adc0

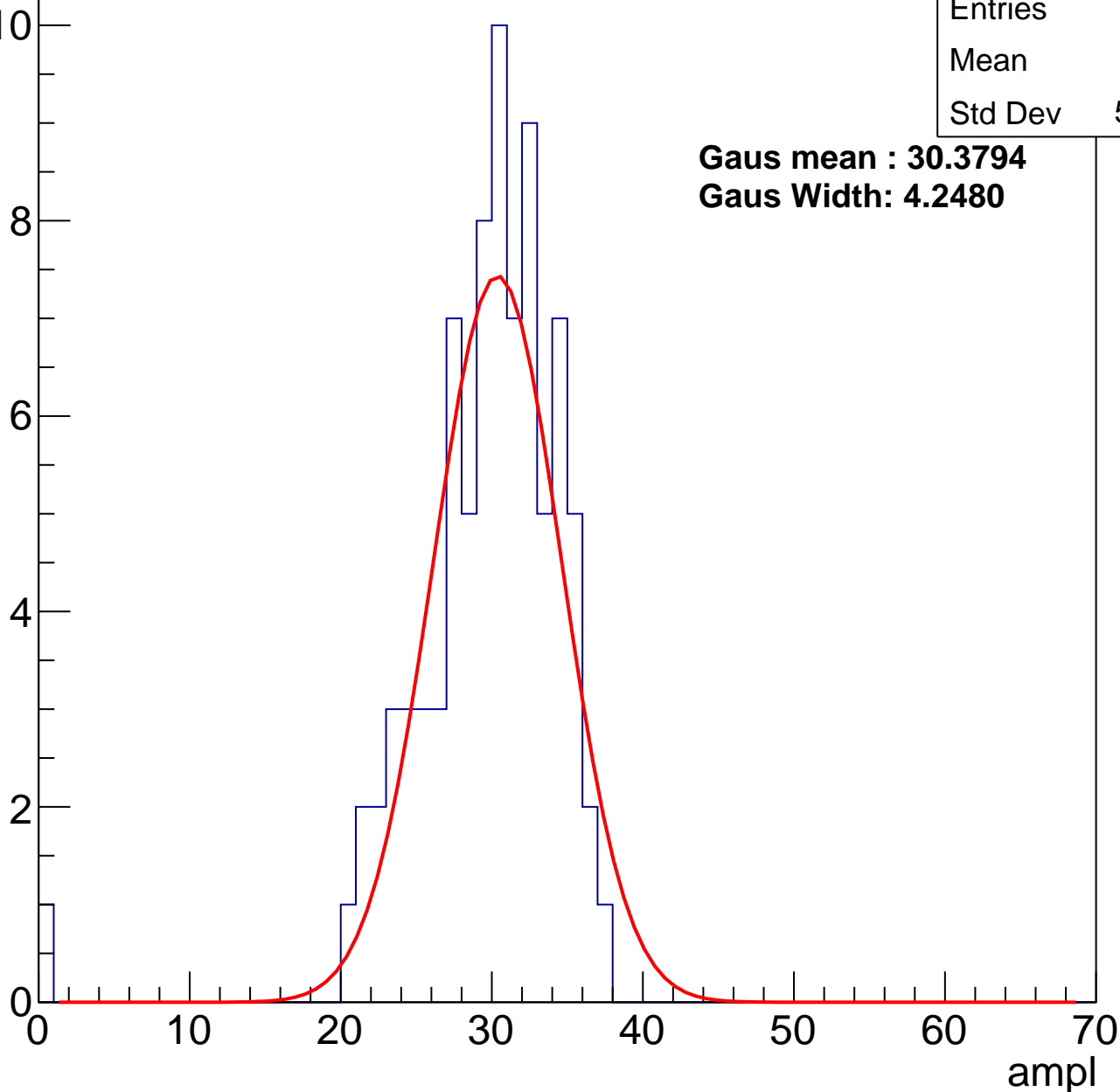
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.2
Std Dev	5.061

Gaus mean : 30.3794

Gaus Width: 4.2480



B1L101S, U6-ch46, adc1

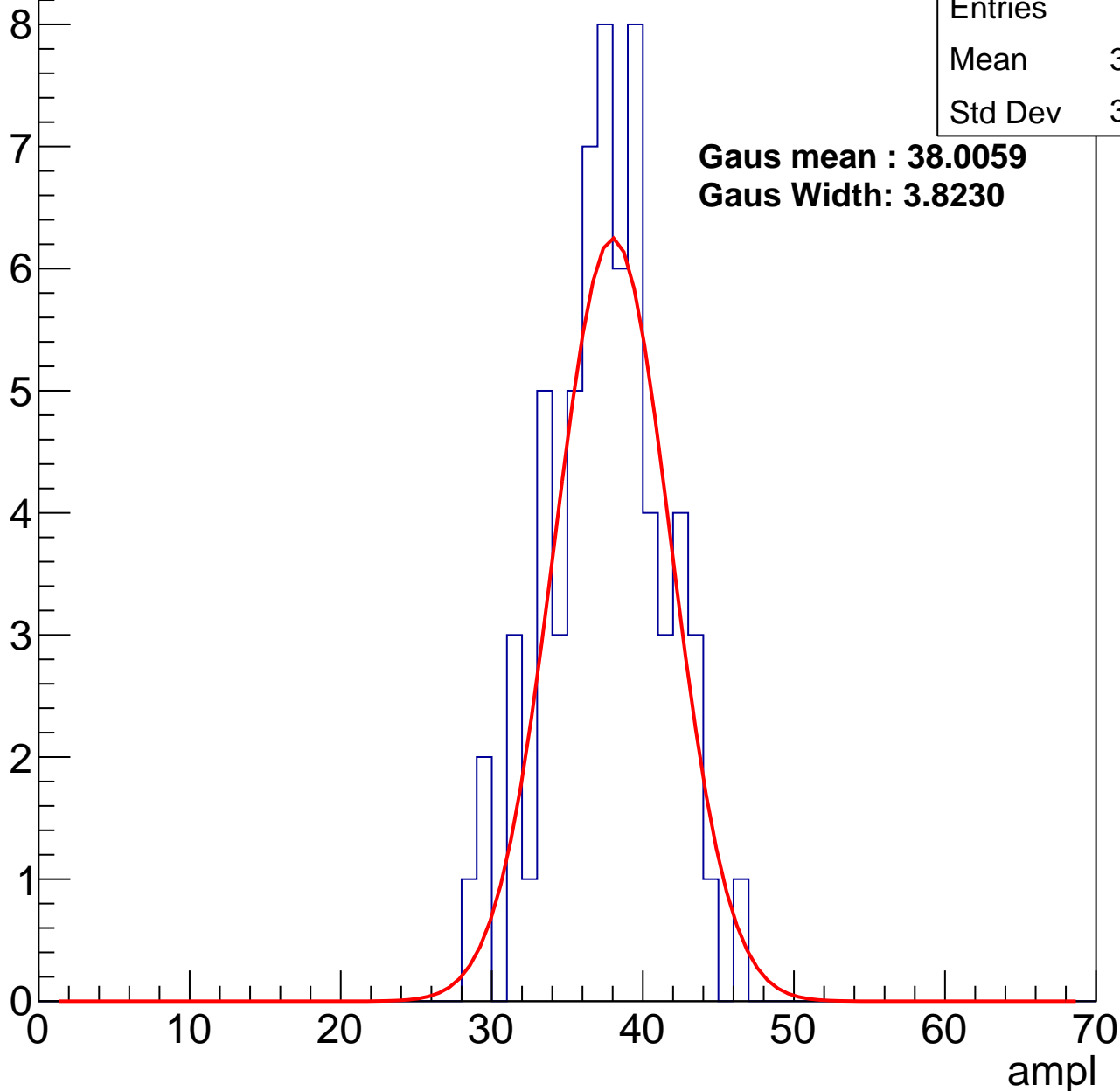
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.09
Std Dev	3.794

Gaus mean : 38.0059

Gaus Width: 3.8230



B1L101S, U6-ch46, adc2

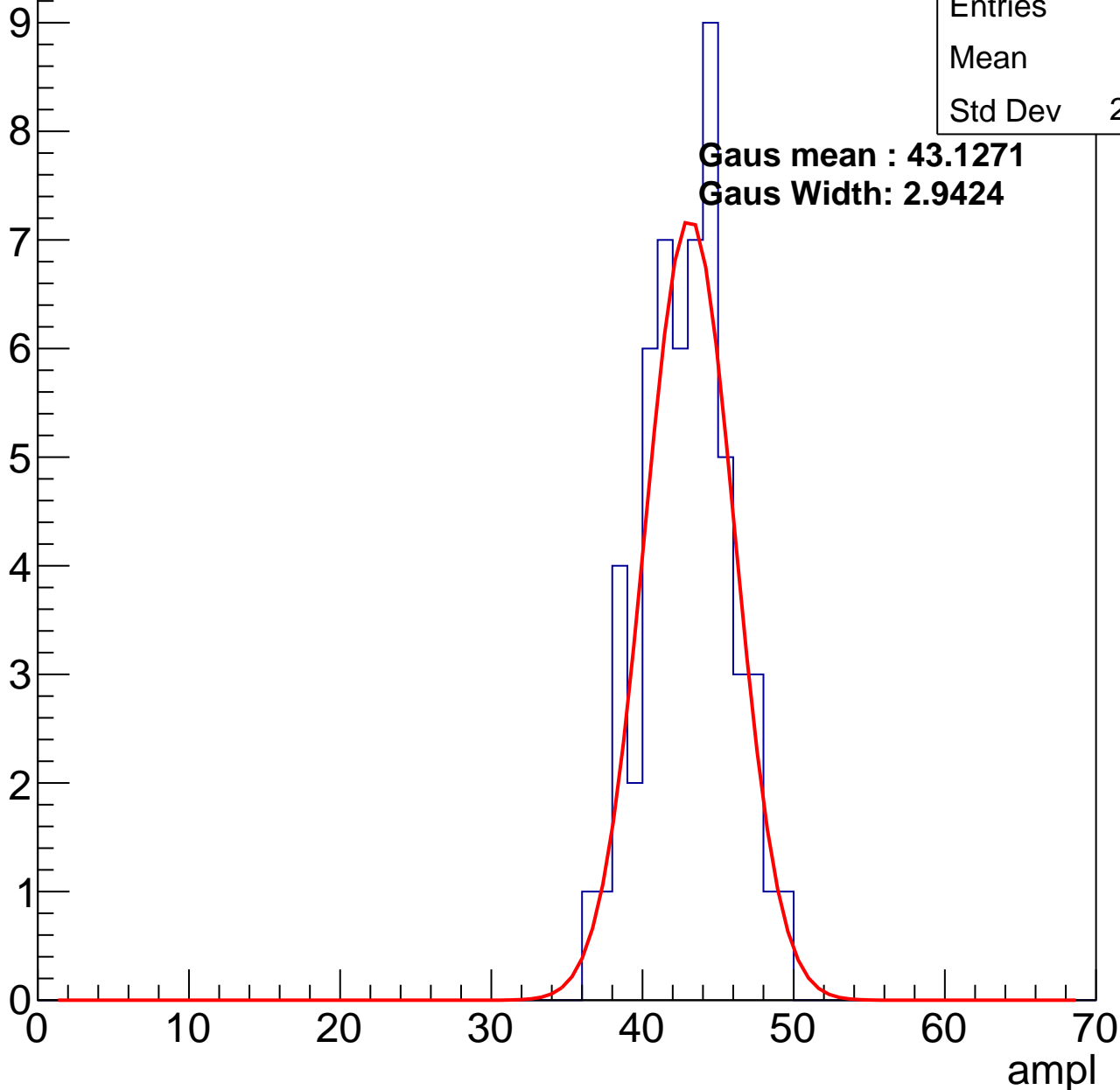
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.5
Std Dev	2.854

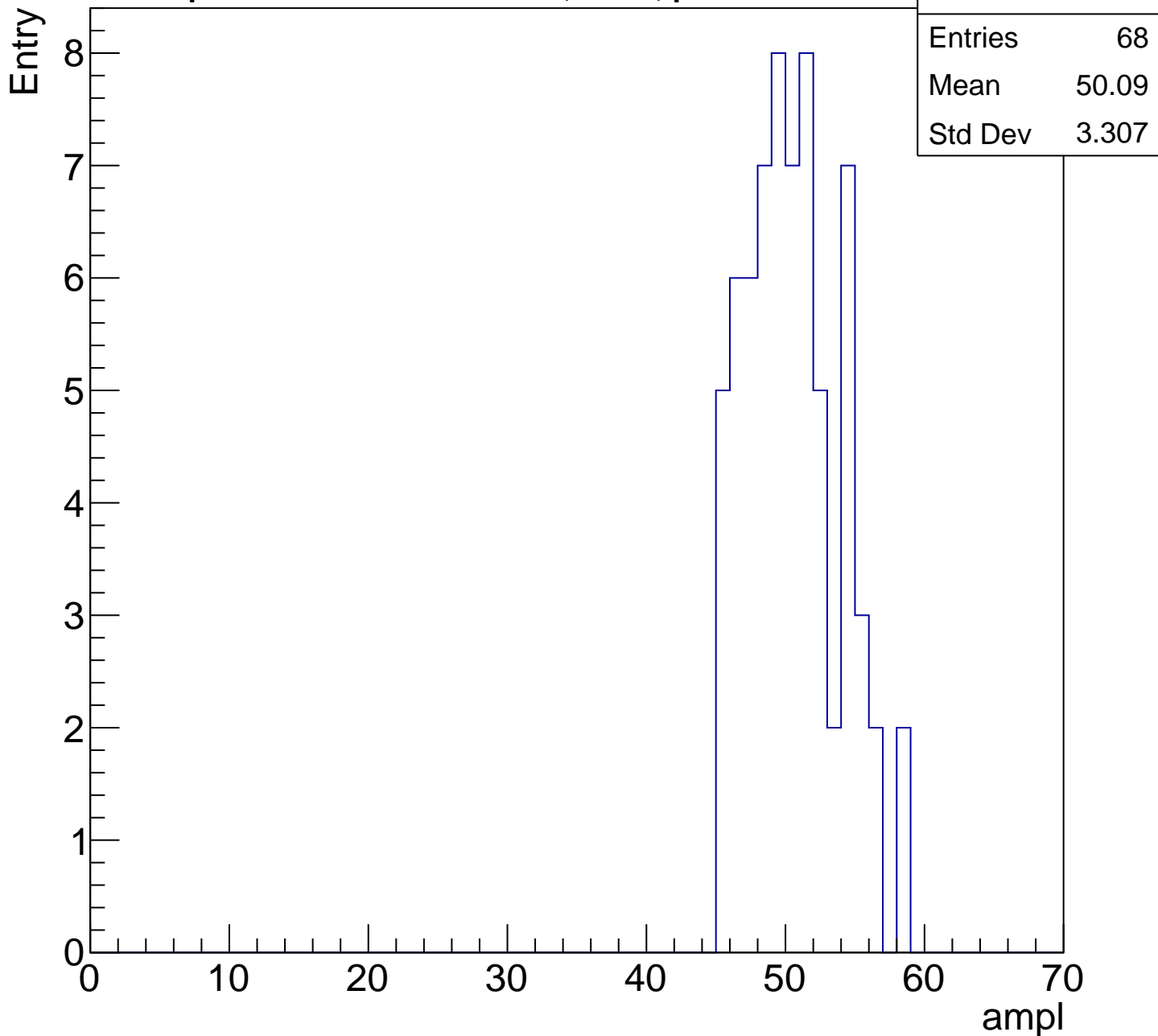
Gaus mean : 43.1271

Gaus Width: 2.9424



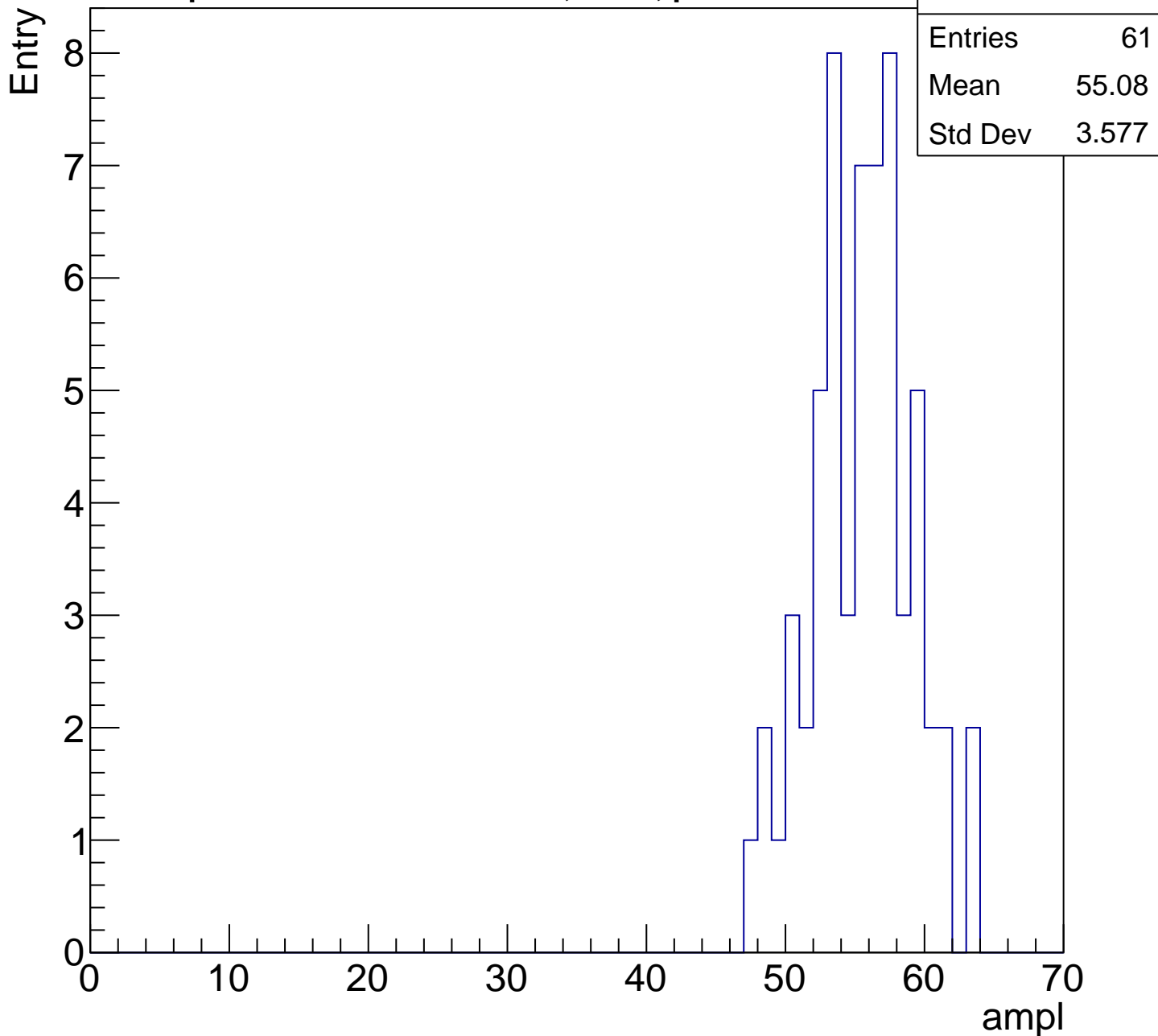
B1L101S, U6-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

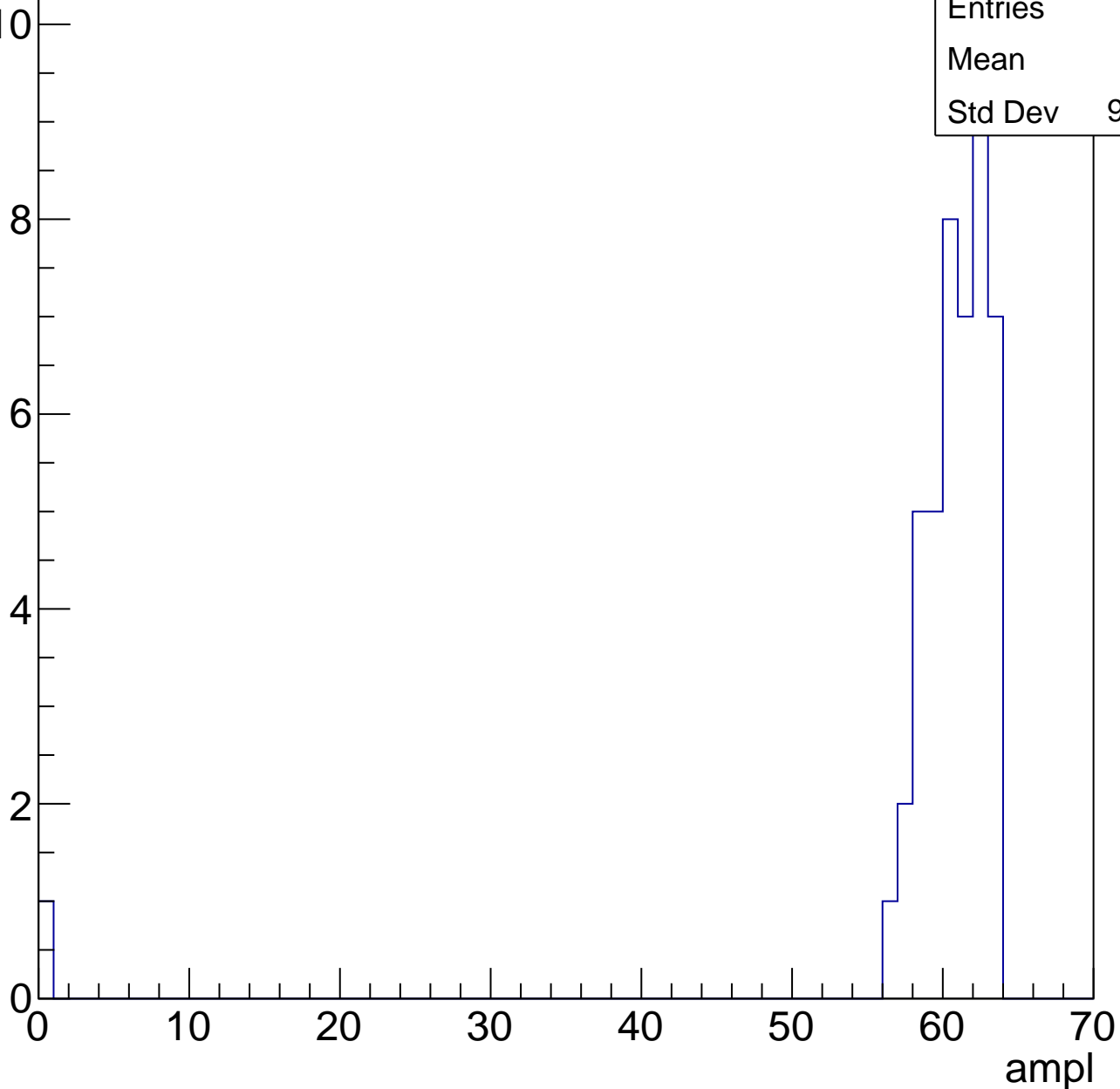


B1L101S, U6-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

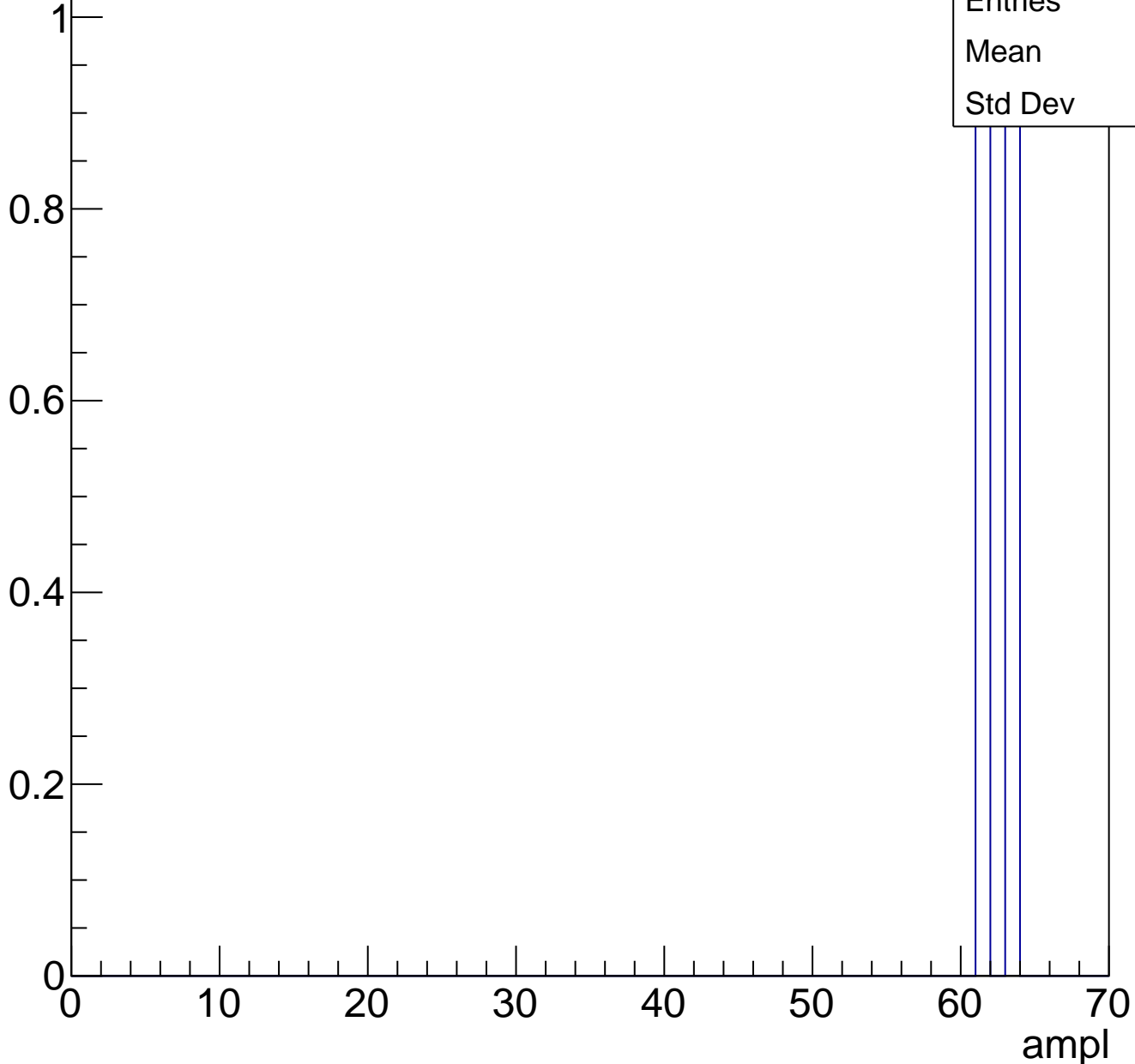
Entries	46
Mean	59.2
Std Dev	9.016



B1L101S, U6-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch47, adc0

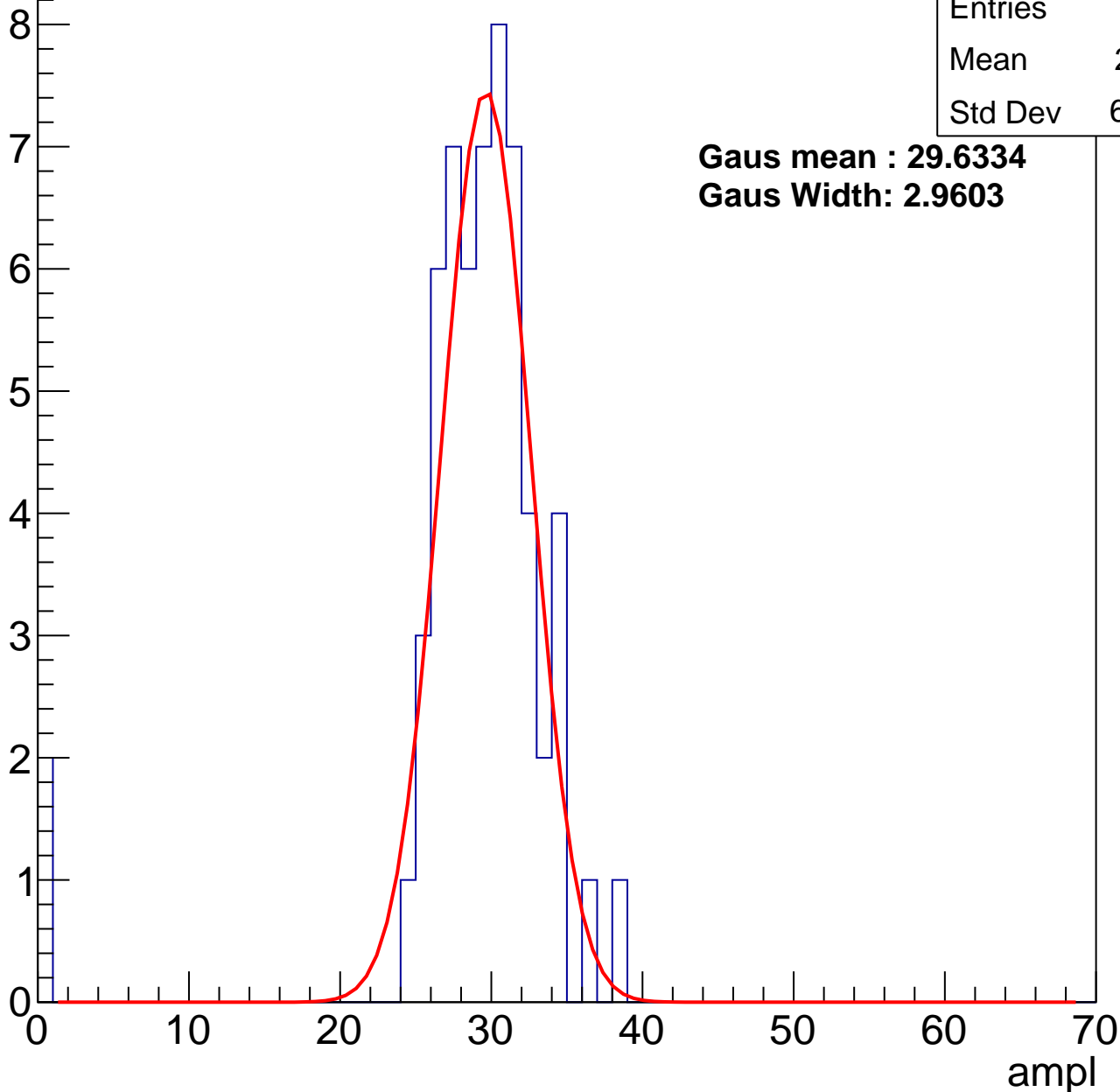
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.41
Std Dev	6.045

Gaus mean : 29.6334

Gaus Width: 2.9603



B1L101S, U6-ch47, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	35.85
Std Dev	3.89

Gaus mean : 35.8689

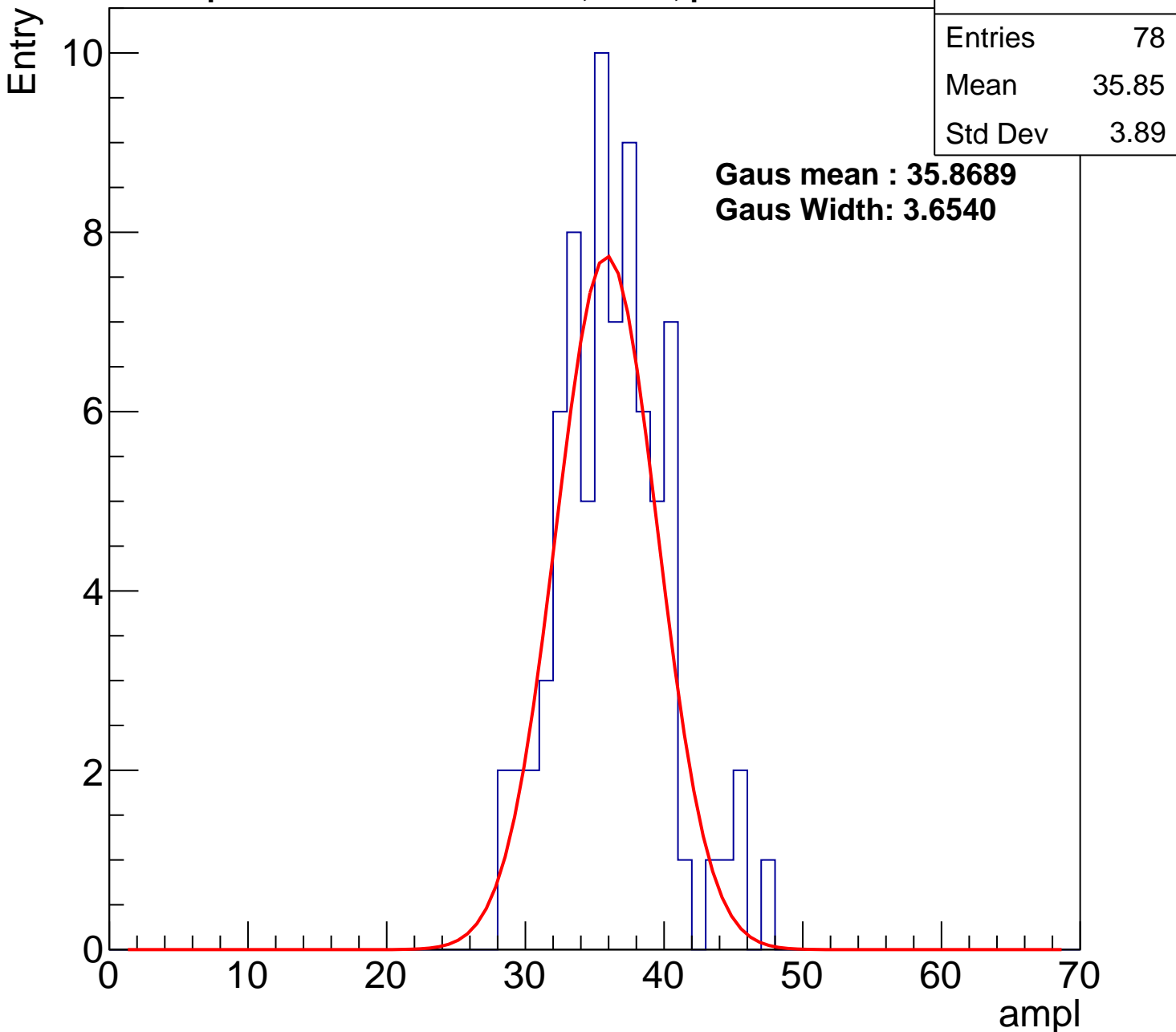
Gaus Width: 3.6540

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch47, adc2

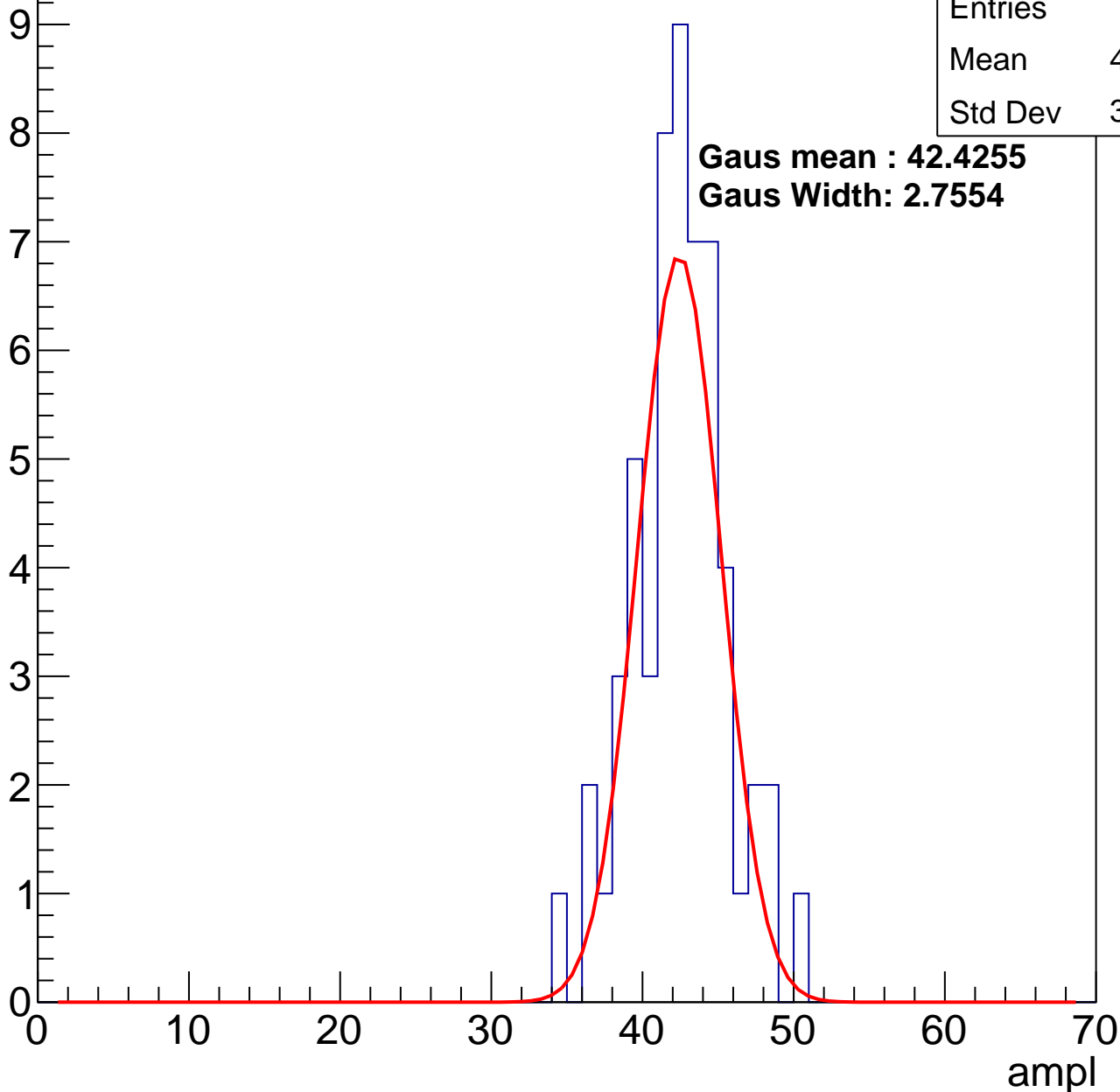
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.02
Std Dev	3.125

Gaus mean : 42.4255

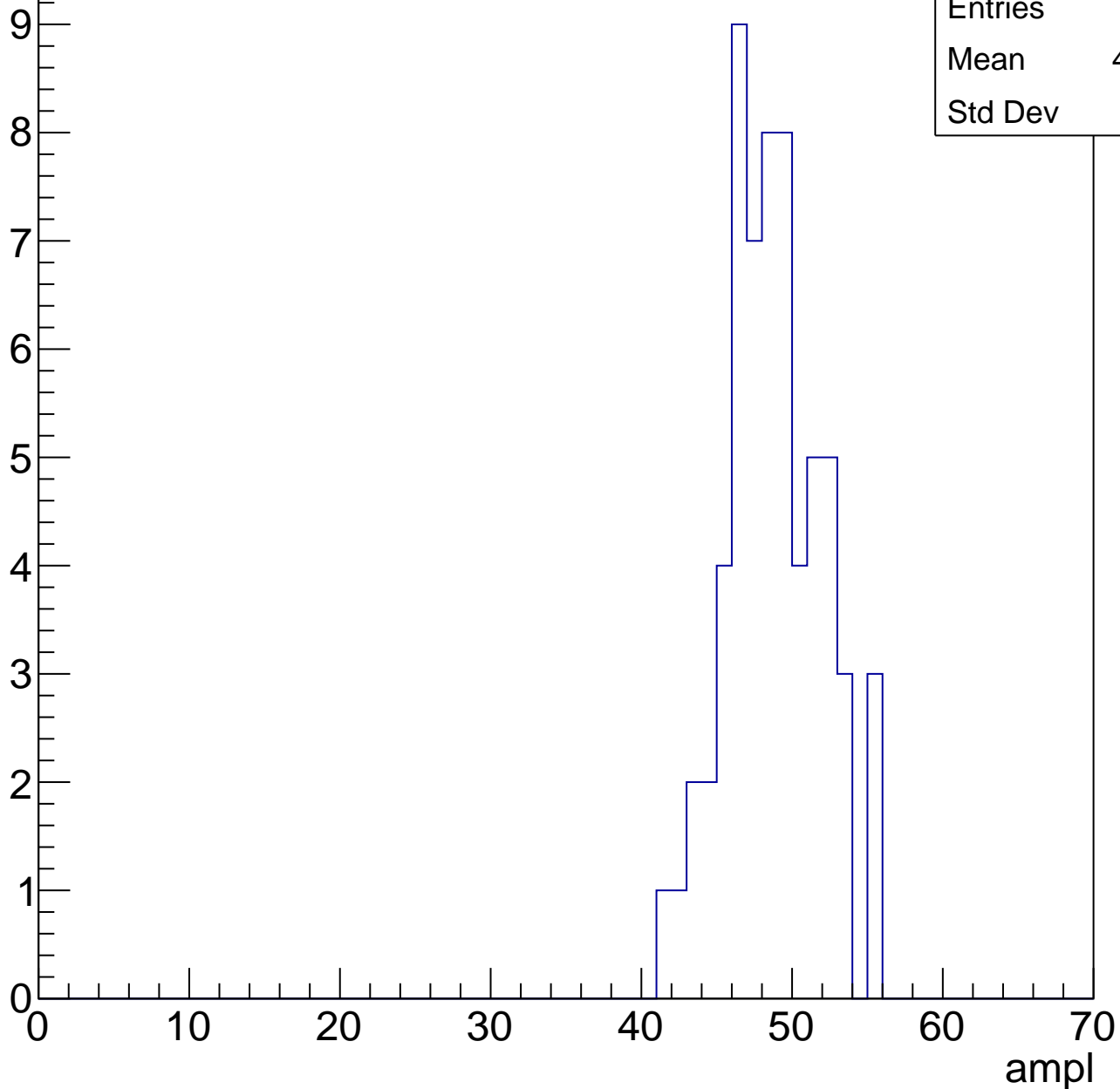
Gaus Width: 2.7554



B1L101S, U6-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	62
Mean	48.31
Std Dev	3.14

B1L101S, U6-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

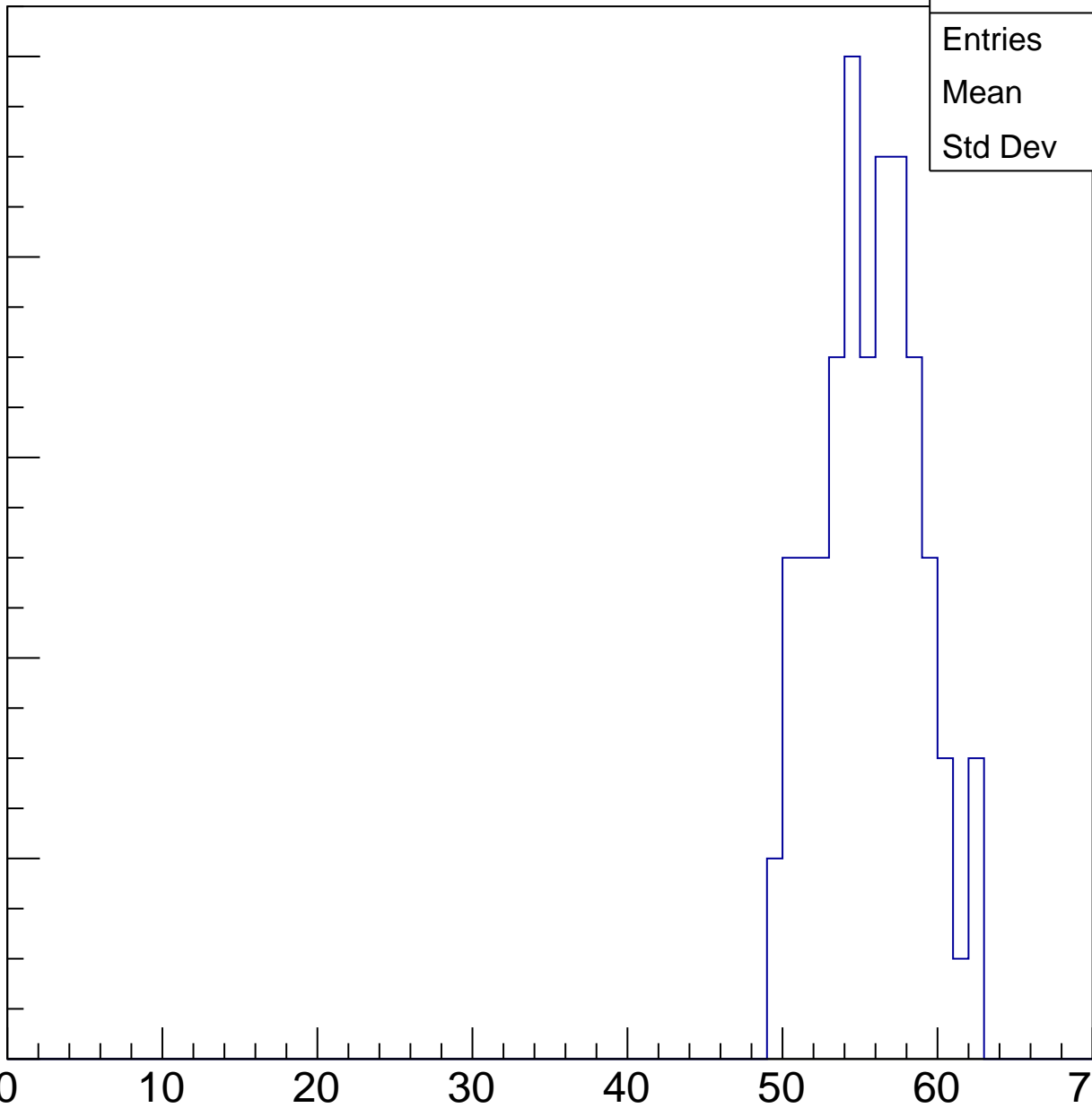
Entries	78
Mean	55.18
Std Dev	3.21

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

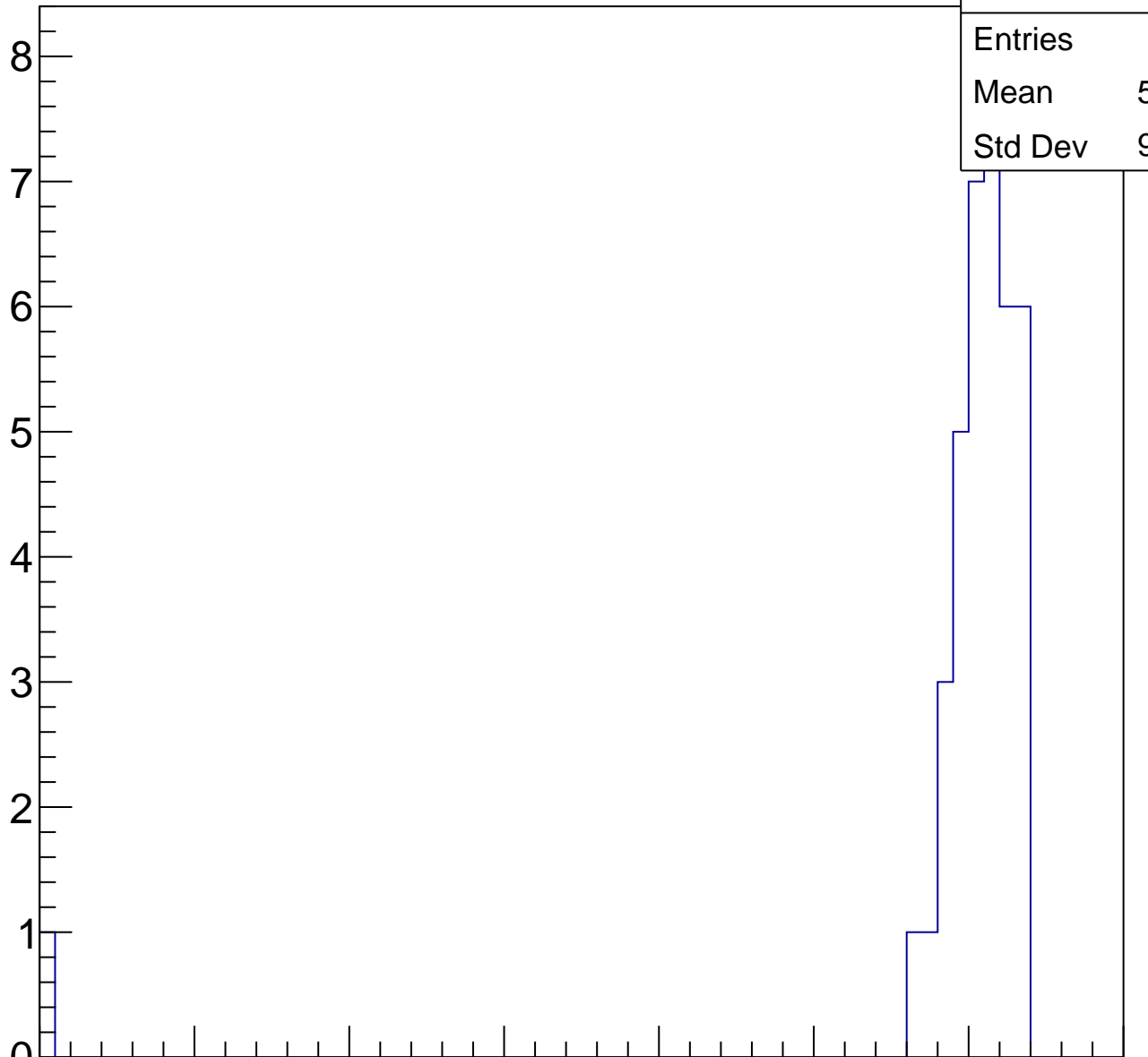
Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	58.95
Std Dev	9.849

ampl

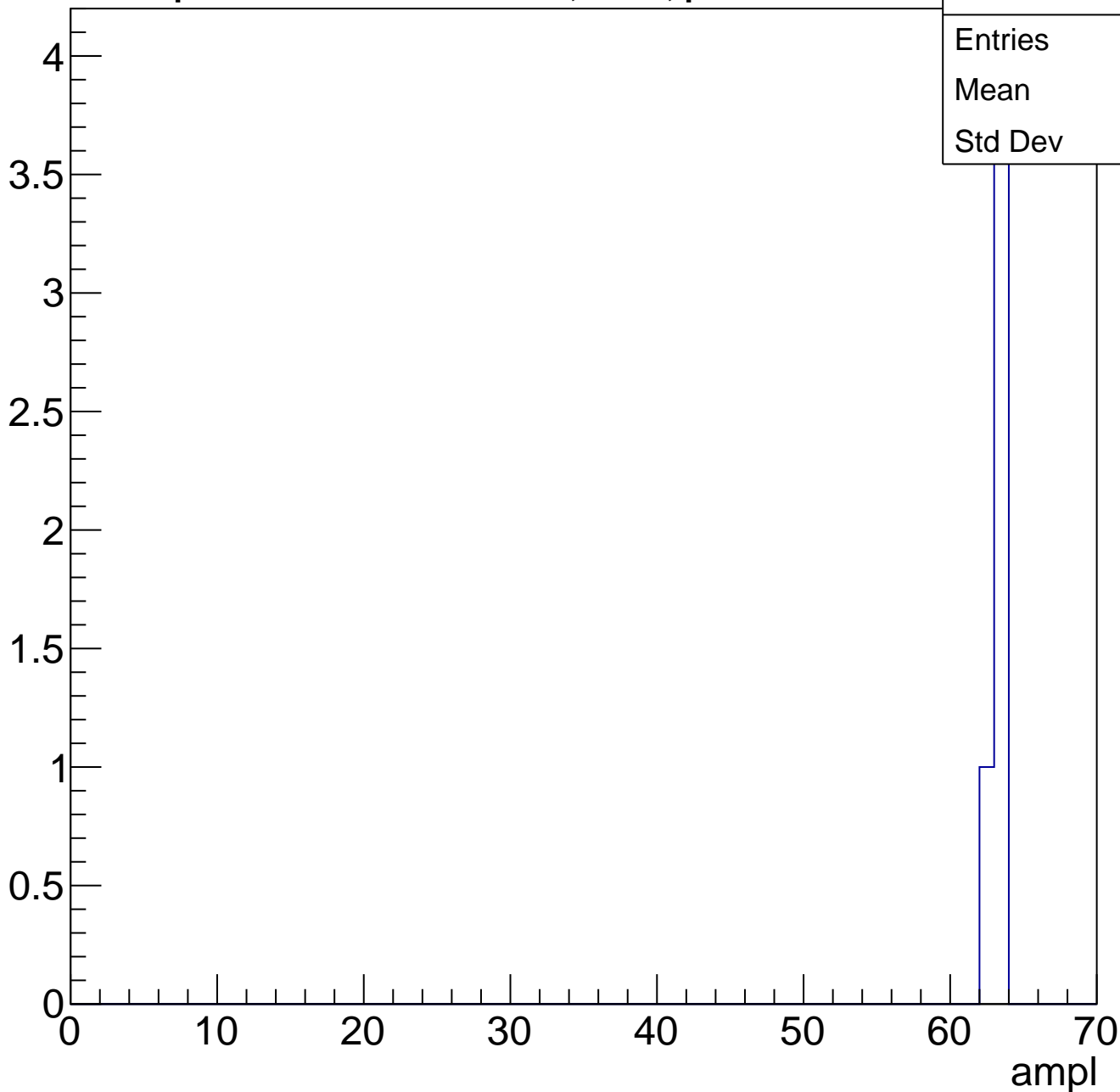
0 10 20 30 40 50 60 70



B1L101S, U6-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

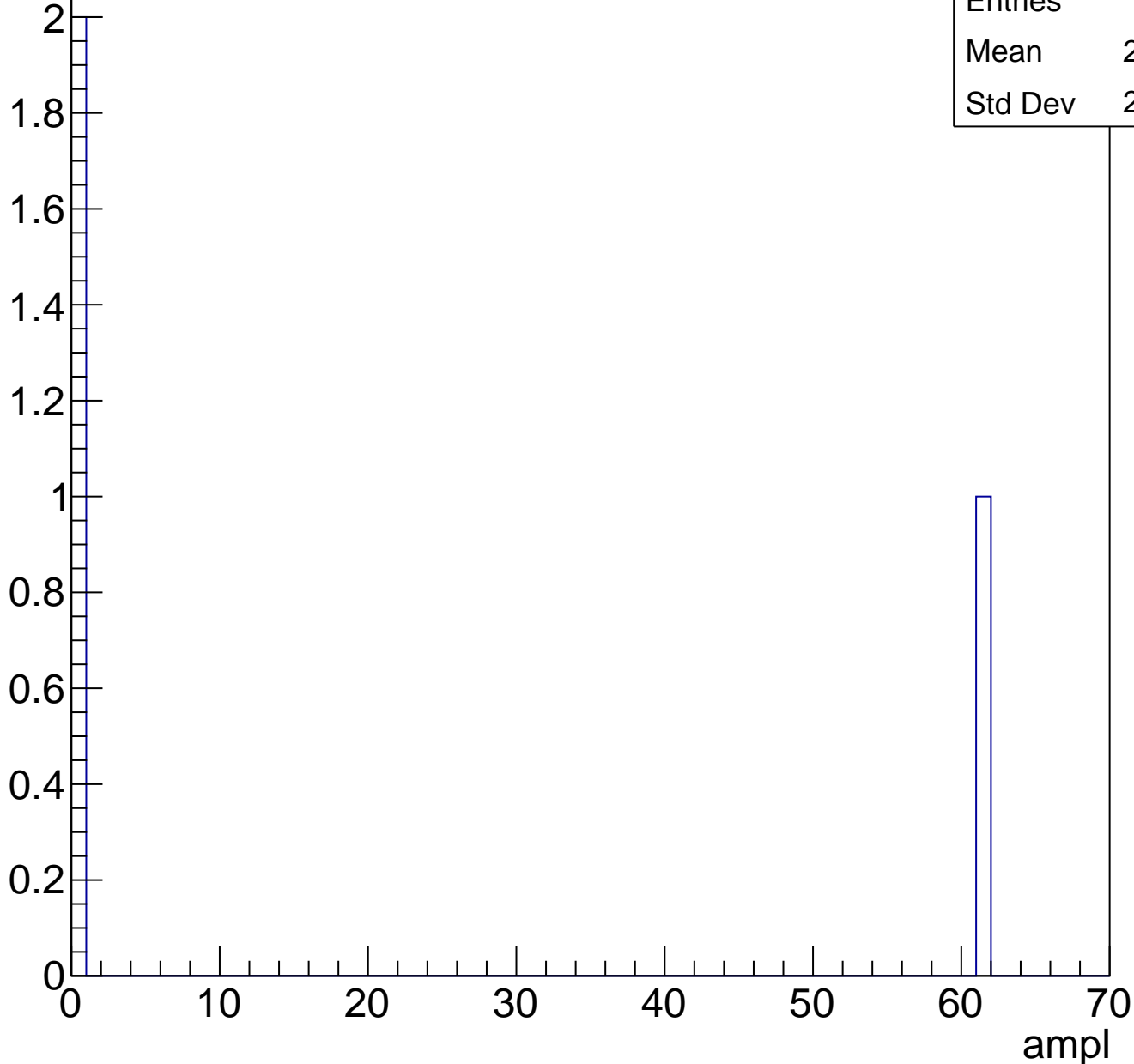
Entry



B1L101S, U6-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch48, adc0

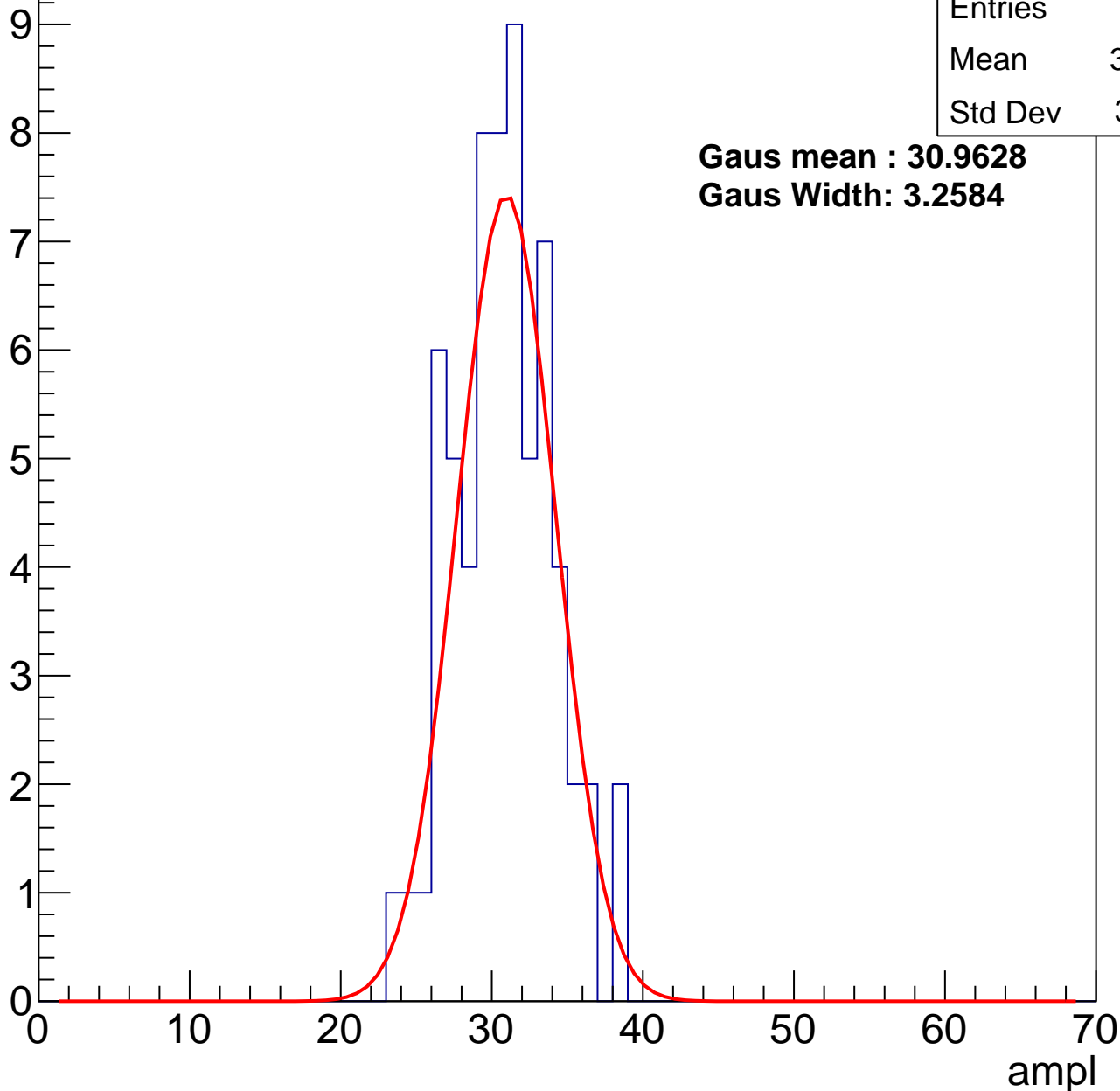
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.32
Std Dev	3.221

Gaus mean : 30.9628

Gaus Width: 3.2584



B1L101S, U6-ch48, adc1

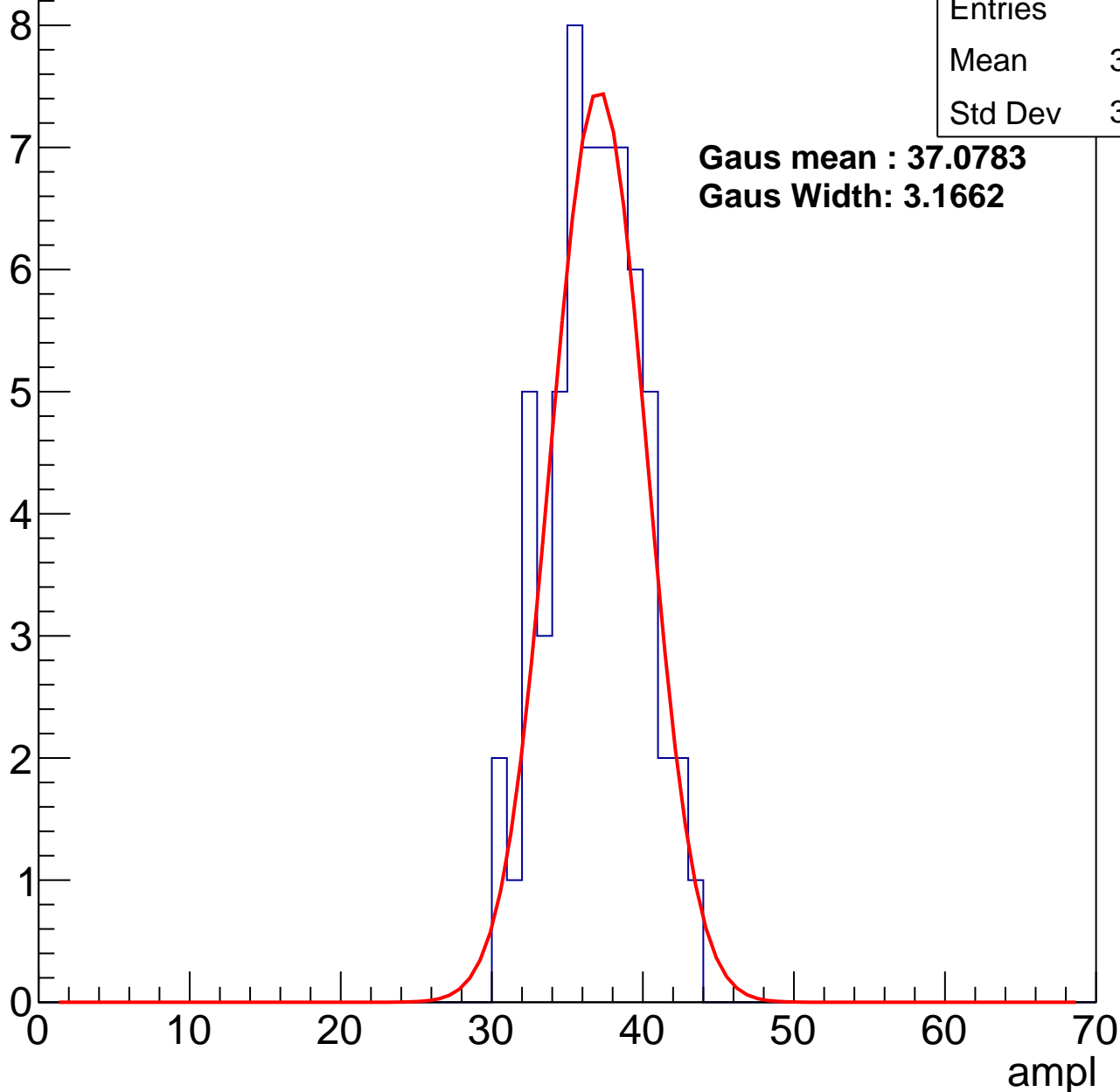
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.39
Std Dev	3.037

Gaus mean : 37.0783

Gaus Width: 3.1662



B1L101S, U6-ch48, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

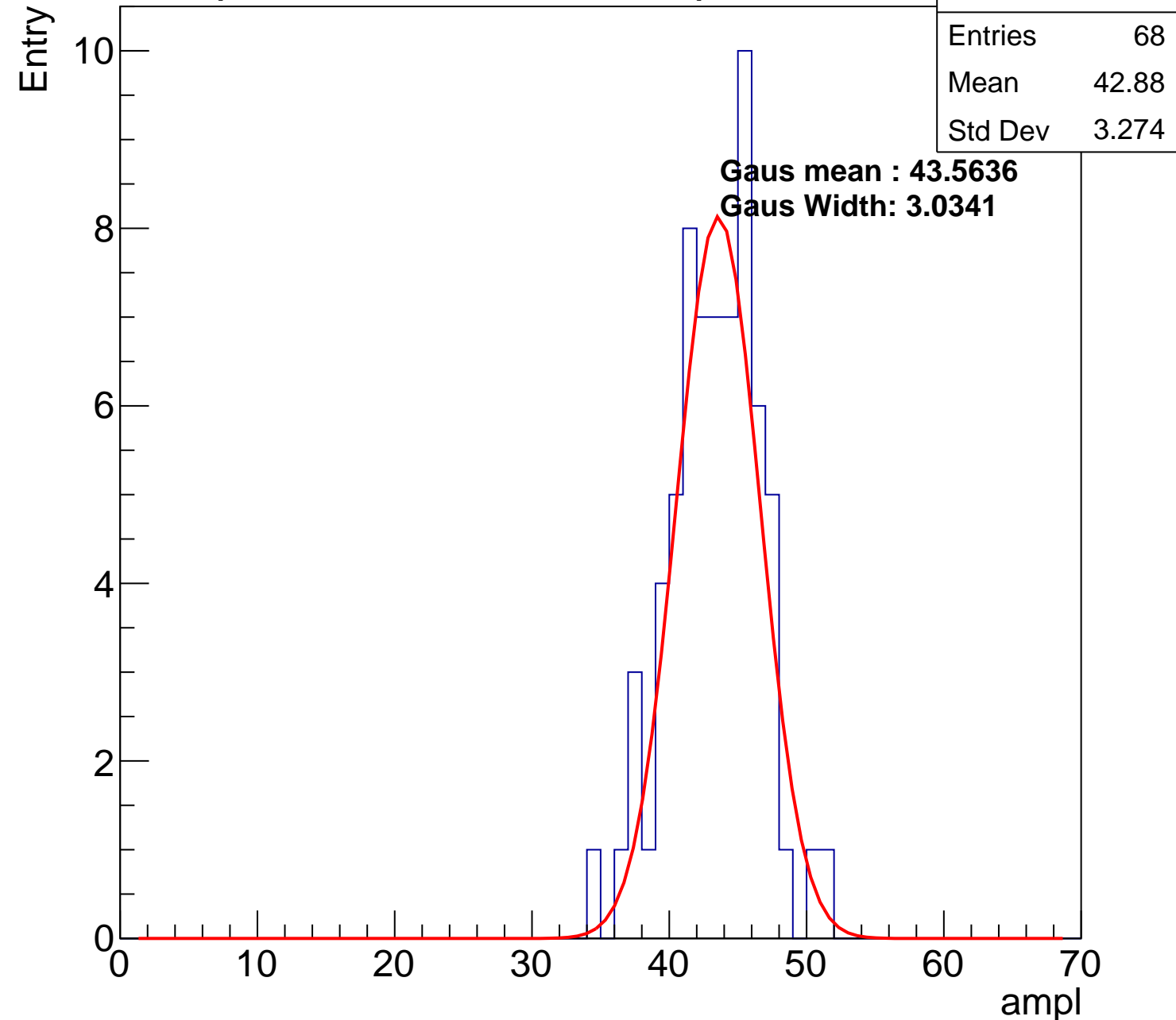
Entries	68
Mean	42.88
Std Dev	3.274

Gaus mean : 43.5636

Gaus Width: 3.0341

10
8
6
4
2
0

ampl

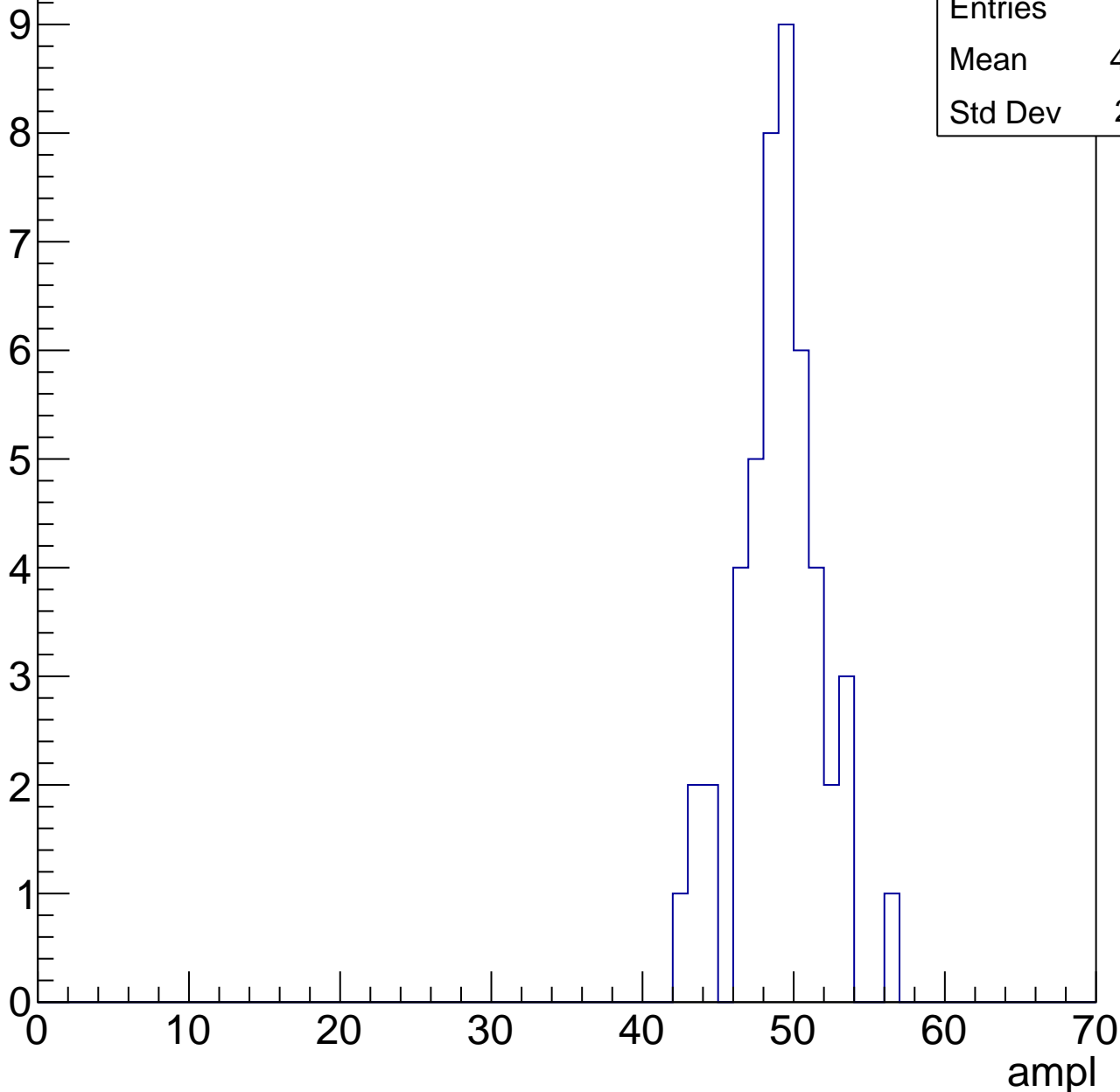


B1L101S, U6-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	48.57
Std Dev	2.781

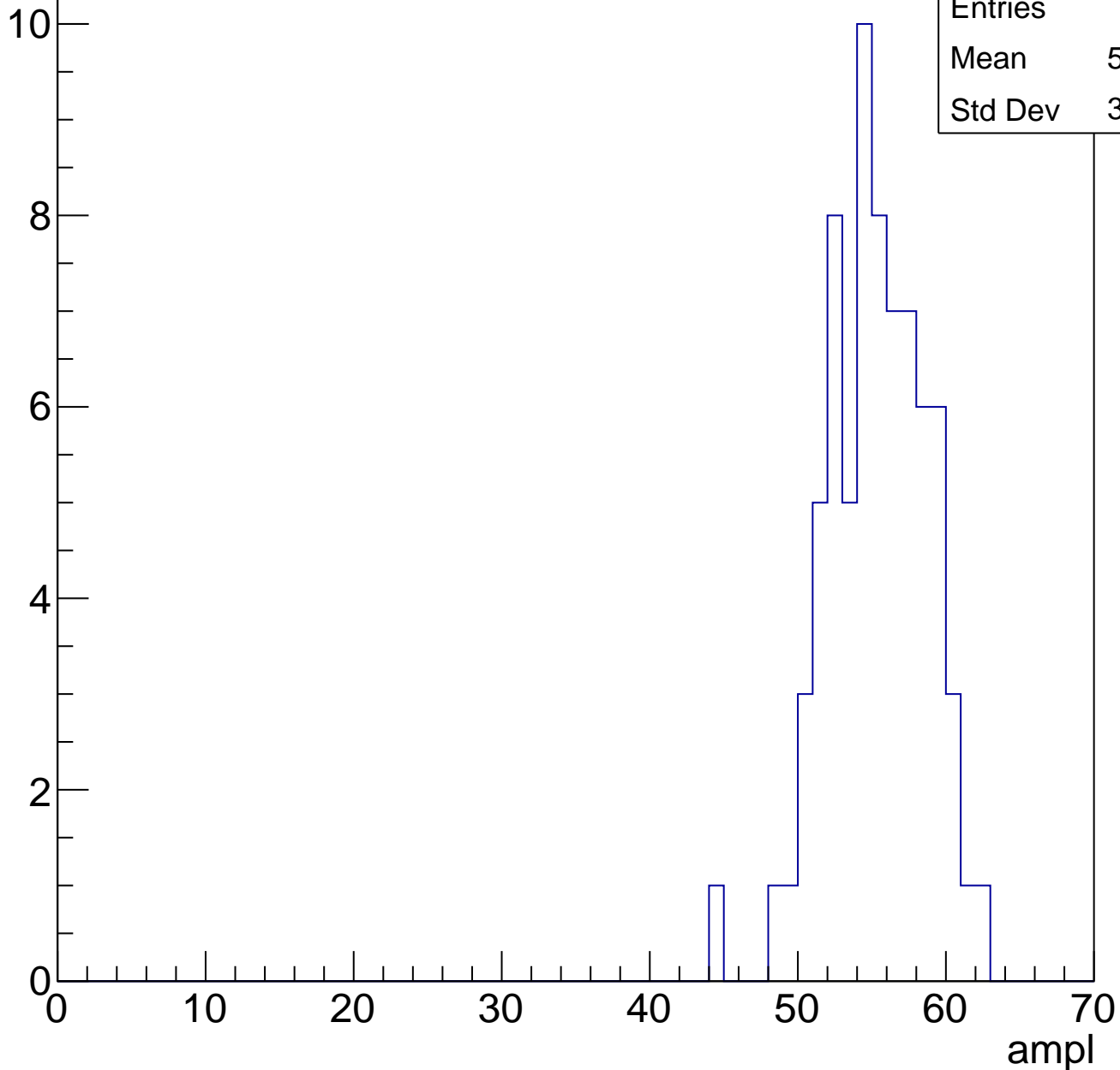


B1L101S, U6-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	54.84
Std Dev	3.323

Entry

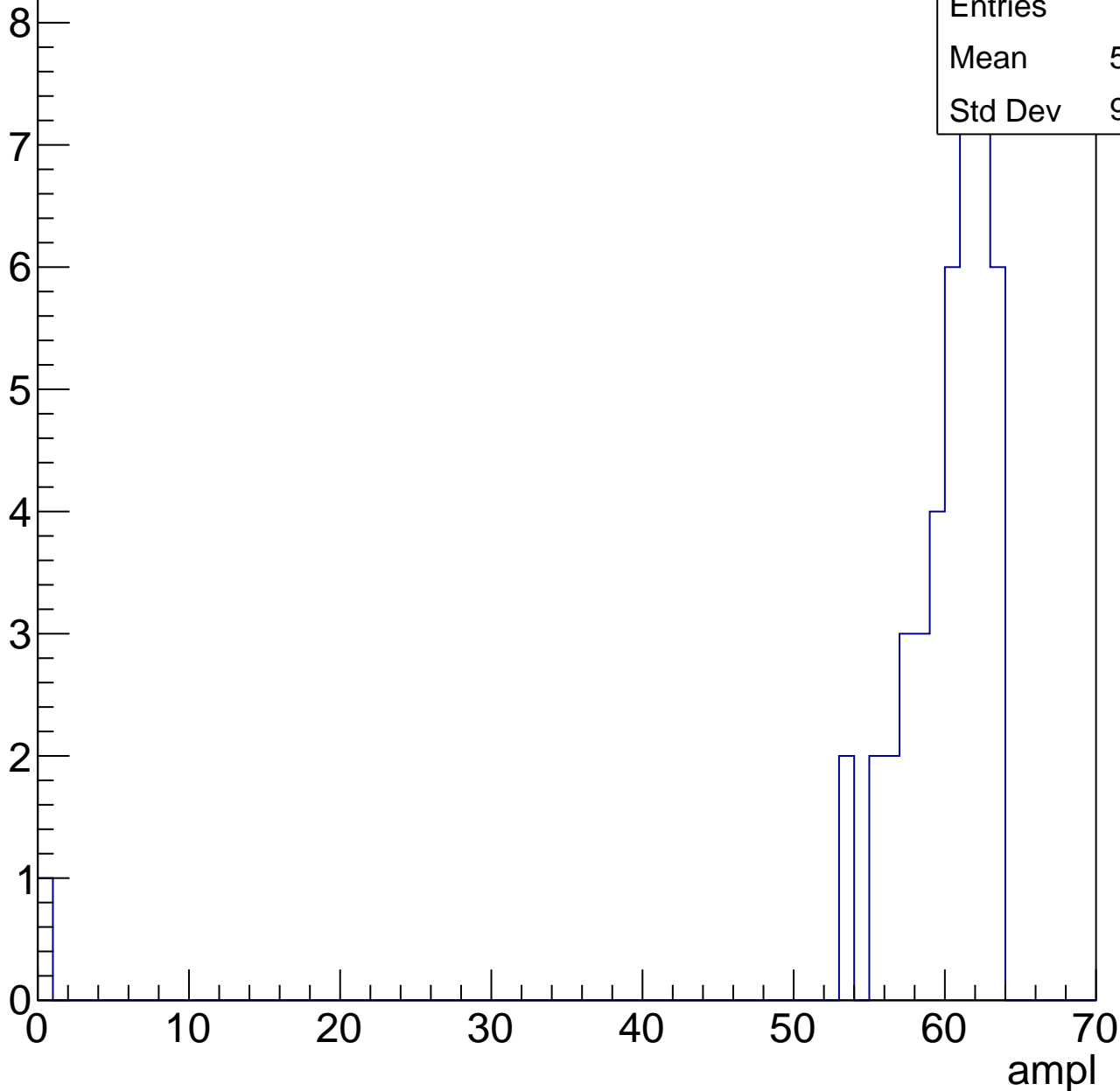


B1L101S, U6-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

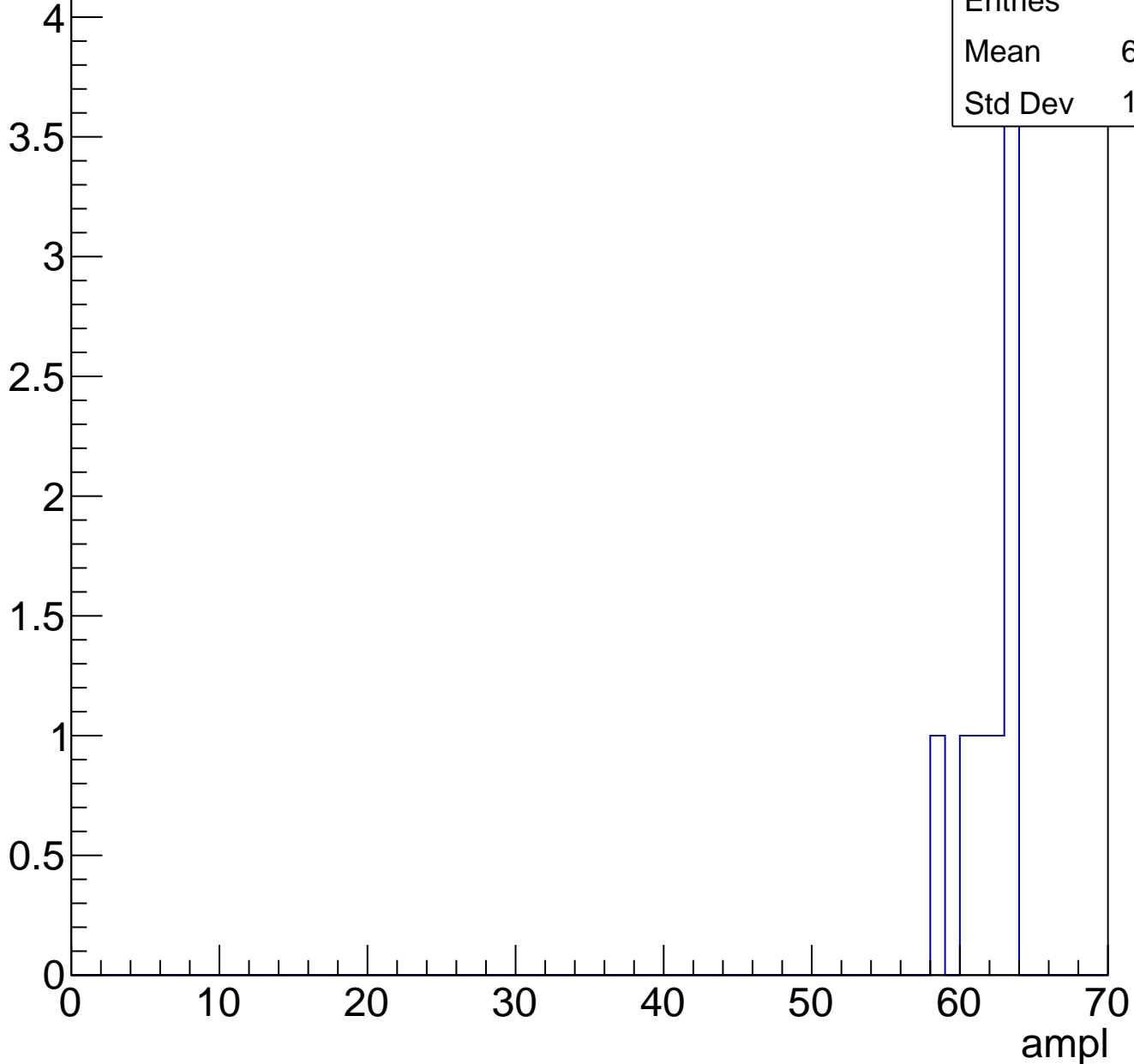
Entries	45
Mean	58.47
Std Dev	9.203



B1L101S, U6-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch49, adc0

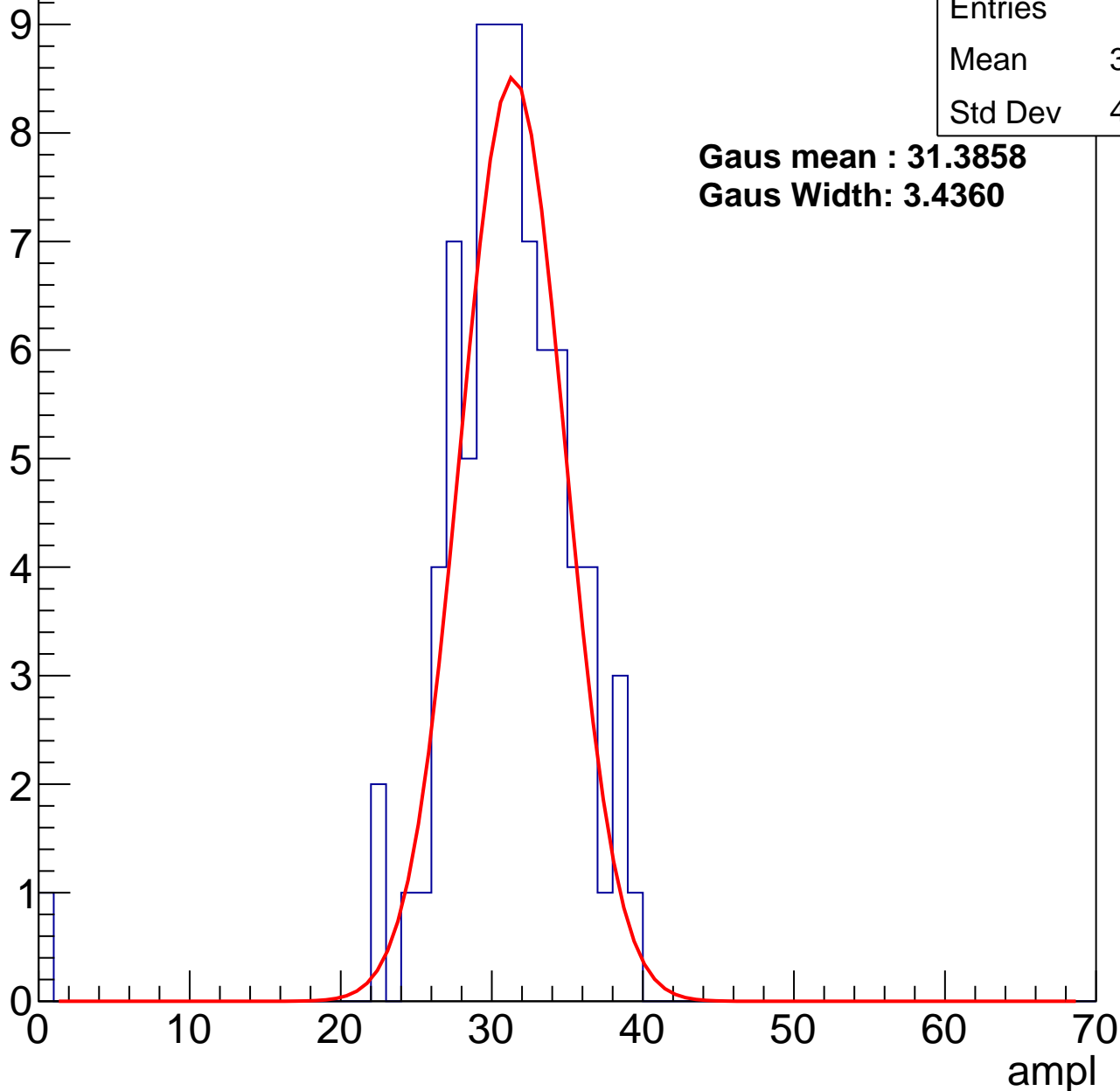
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.45
Std Dev	4.977

Gaus mean : 31.3858

Gaus Width: 3.4360



B1L101S, U6-ch49, adc1

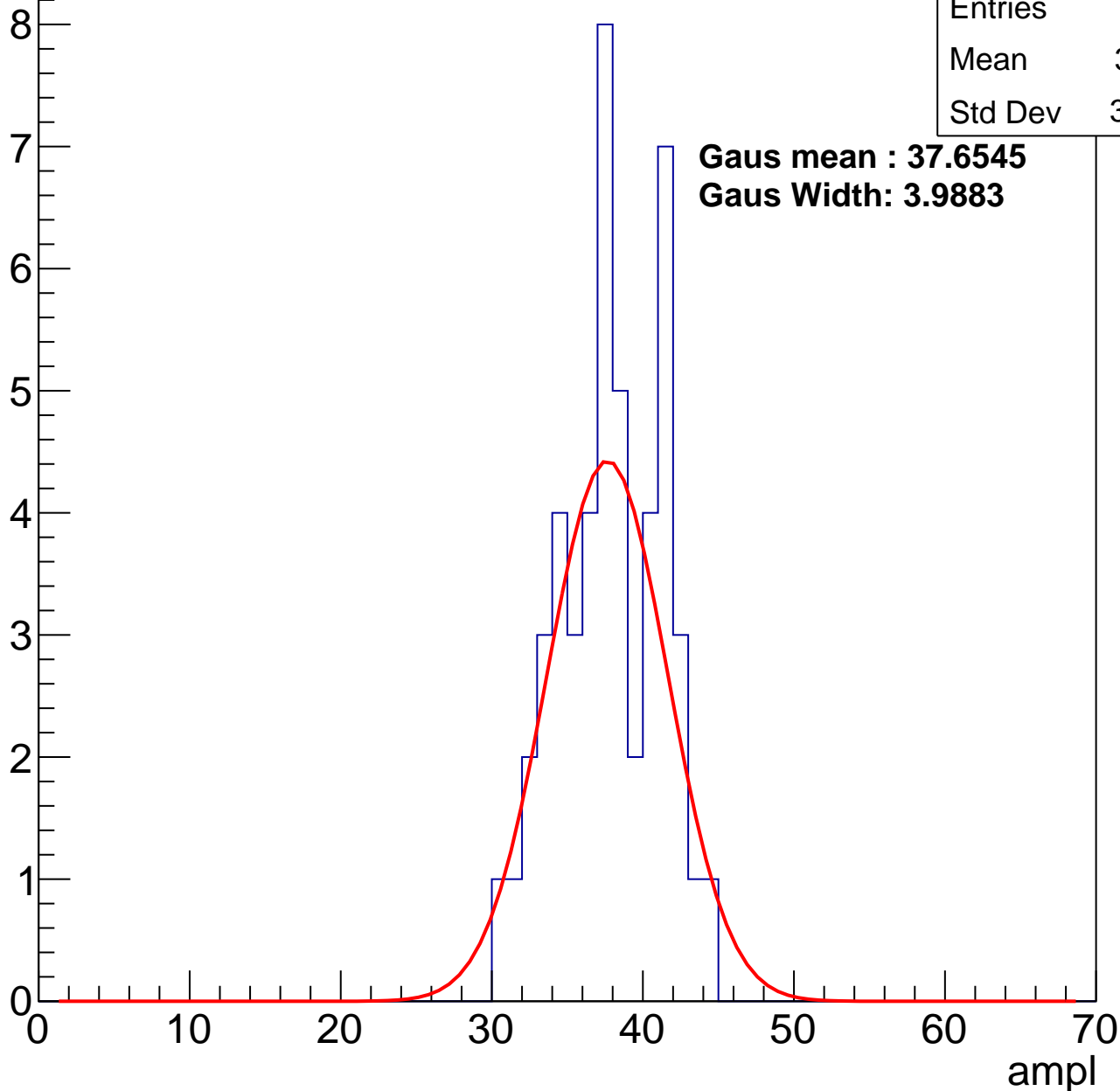
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	37.41
Std Dev	3.344

Gaus mean : 37.6545

Gaus Width: 3.9883



B1L101S, U6-ch49, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.41
Std Dev	3.625

Gaus mean : 43.4298

Gaus Width: 3.5395

10

8

6

4

2

0

0

10

20

30

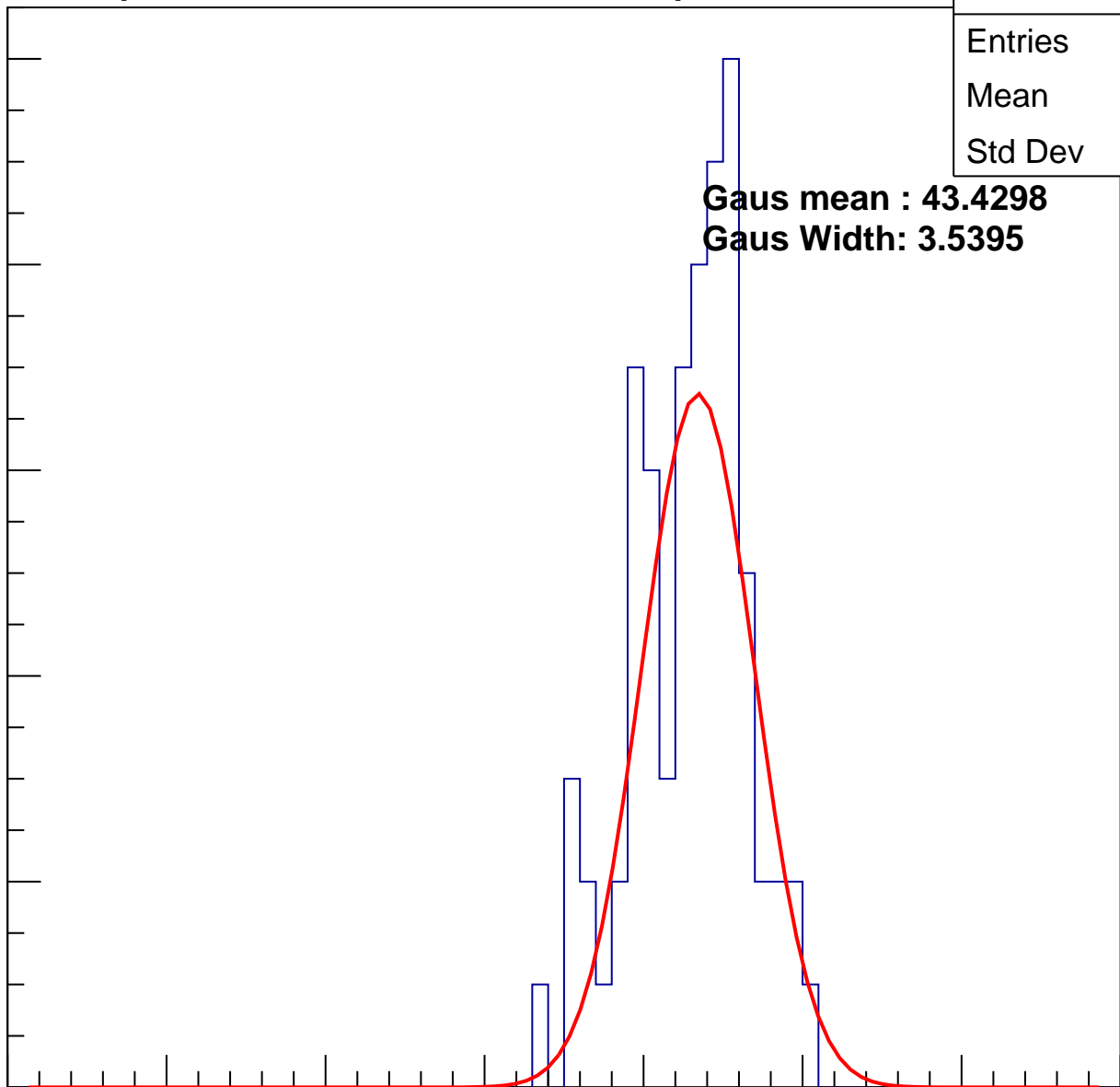
40

50

60

70

ampl

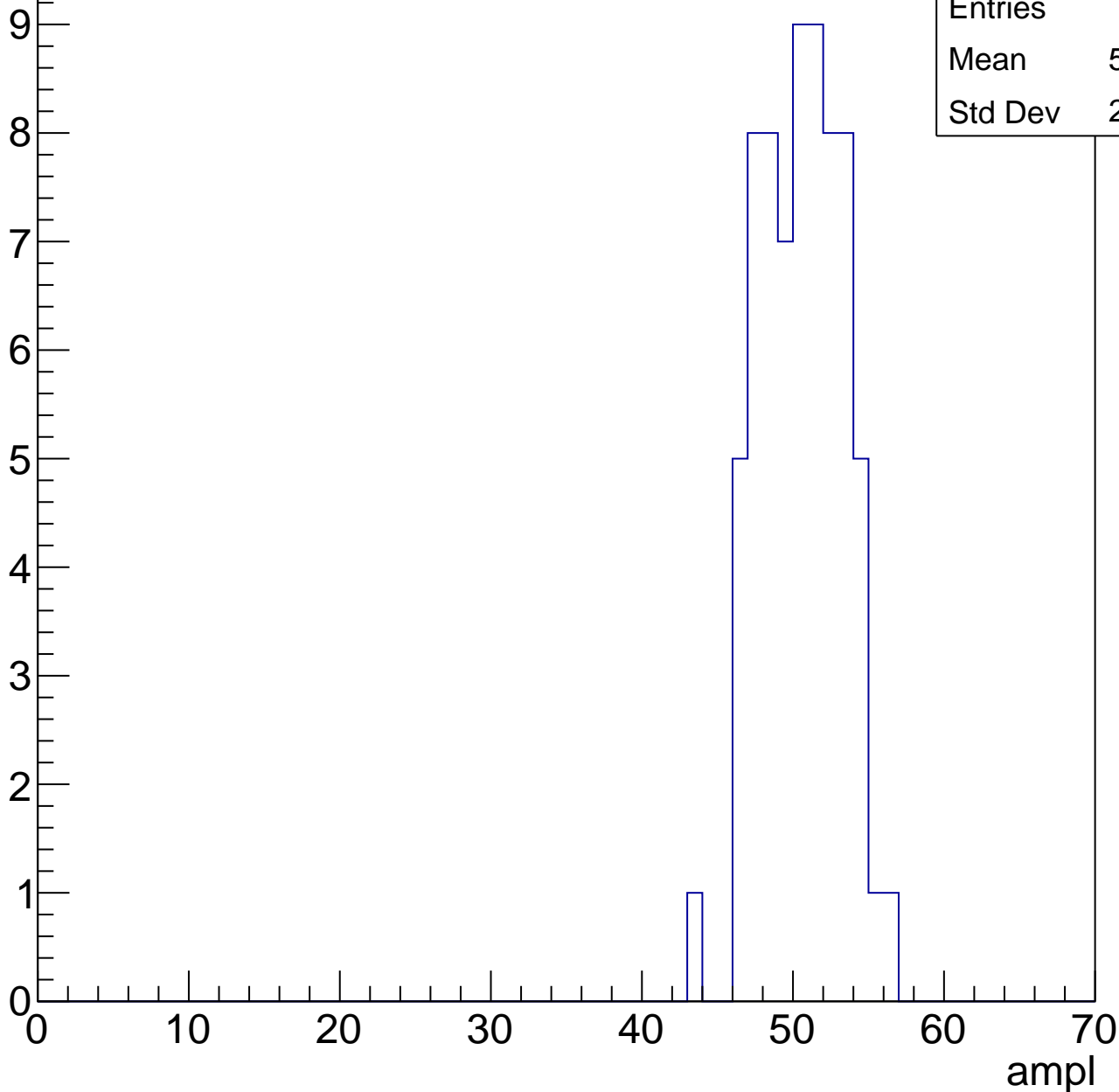


B1L101S, U6-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.09
Std Dev	2.655

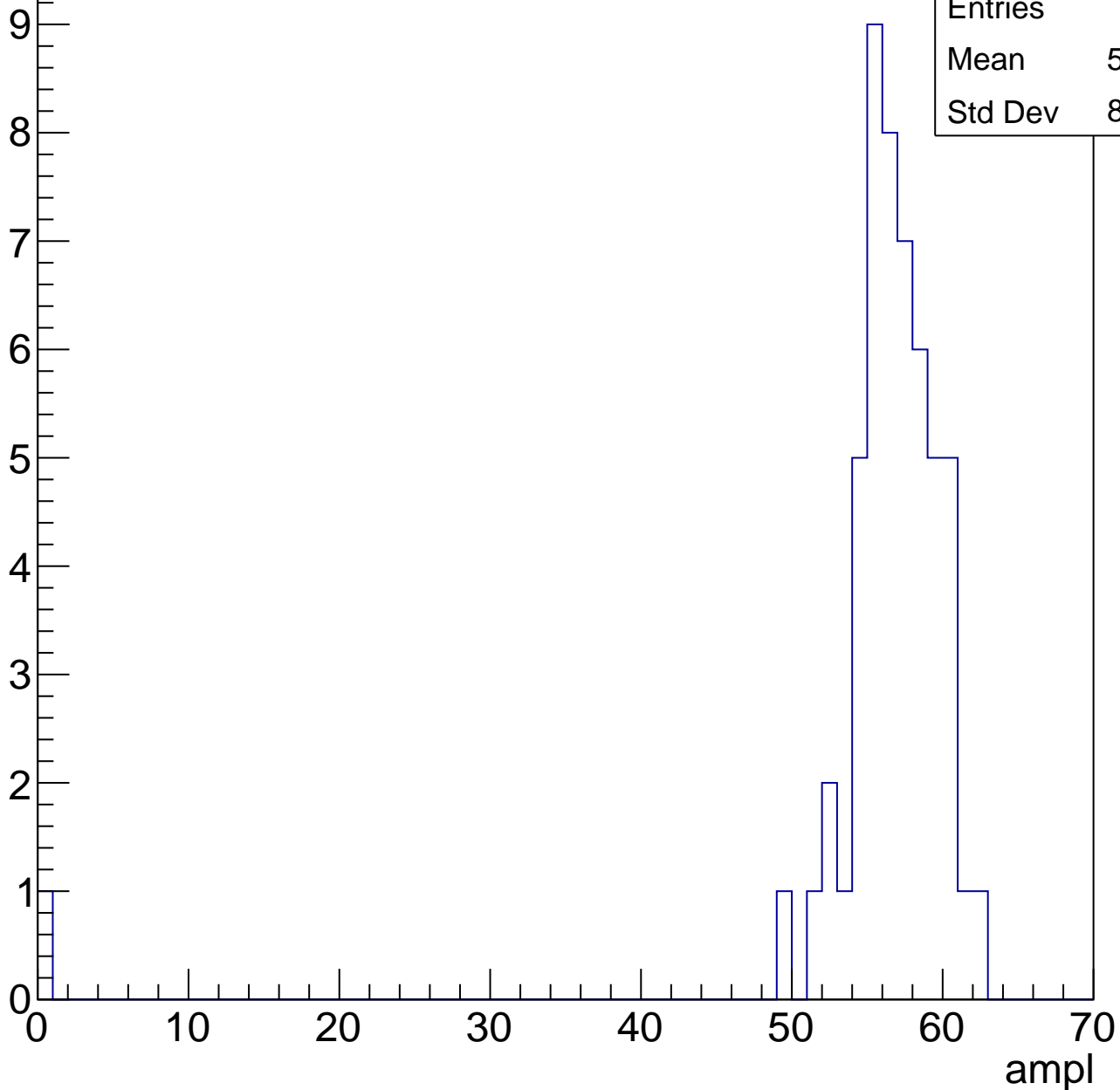


B1L101S, U6-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.38
Std Dev	8.099

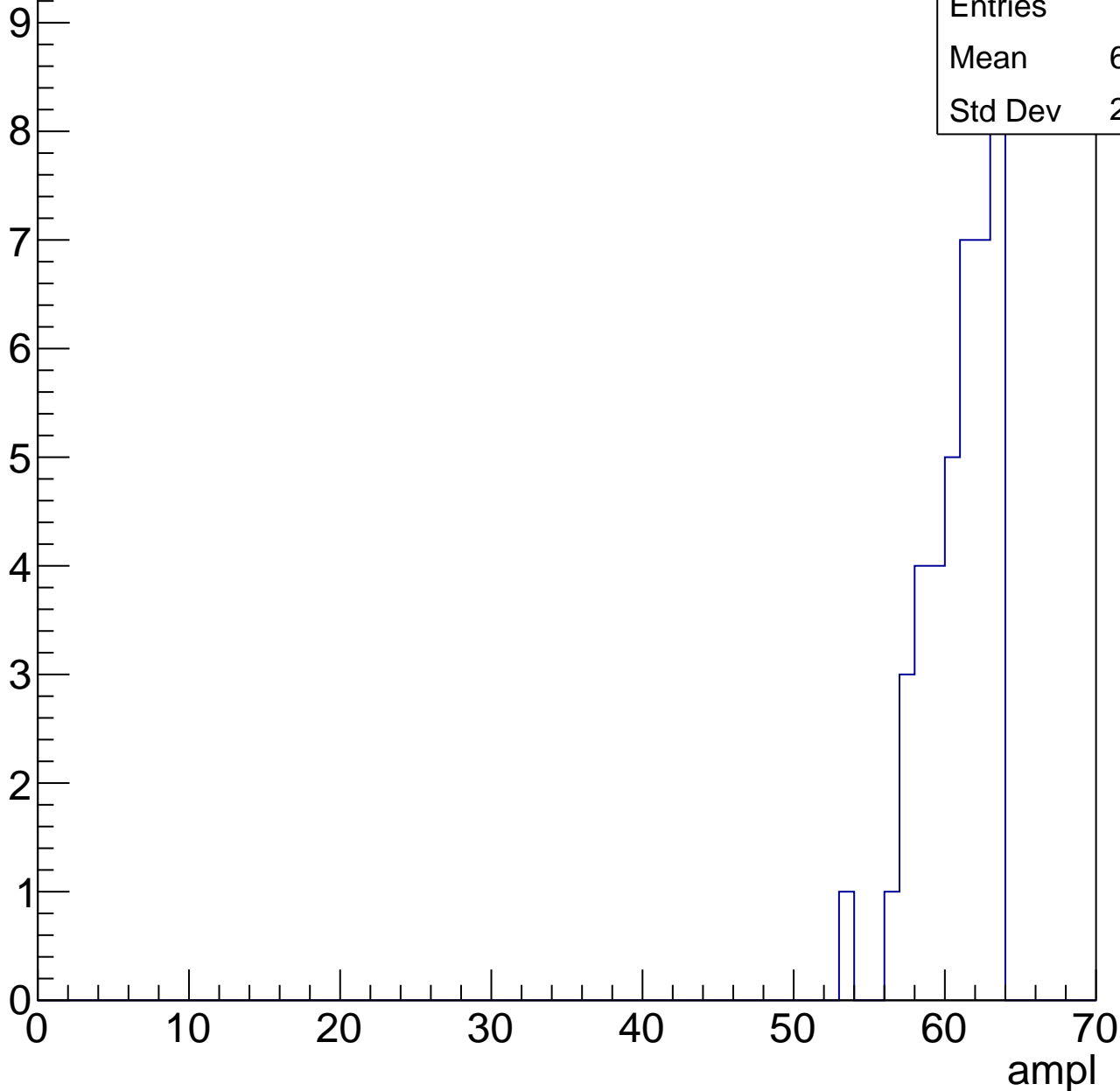


B1L101S, U6-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	60.39
Std Dev	2.326



B1L101S, U6-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.247

ampl

0 10 20 30 40 50 60 70

B1L101S, U6-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch50, adc0

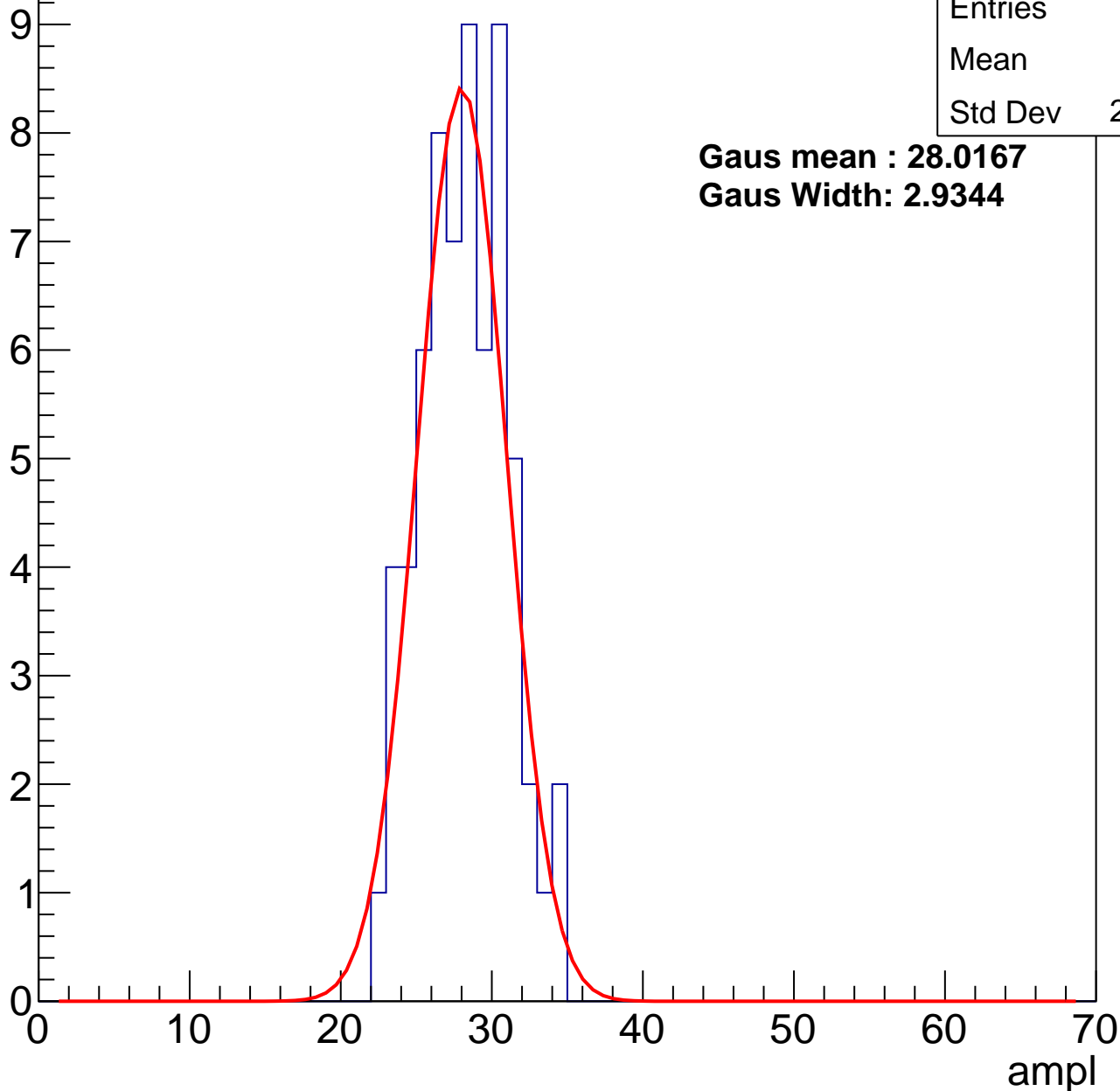
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.7
Std Dev	2.804

Gaus mean : 28.0167

Gaus Width: 2.9344



B1L101S, U6-ch50, adc1

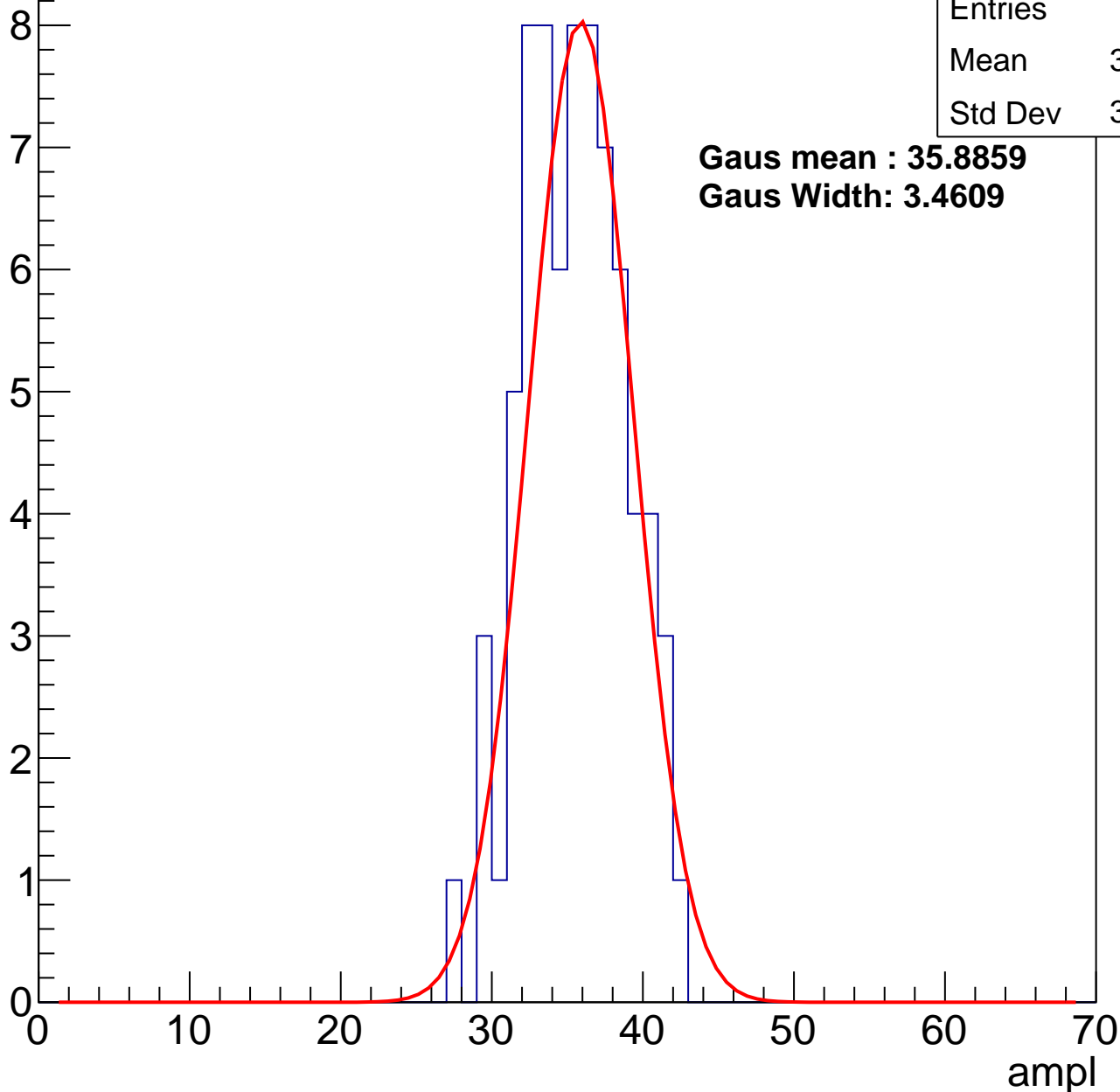
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.05
Std Dev	3.306

Gaus mean : 35.8859

Gaus Width: 3.4609



B1L101S, U6-ch50, adc2

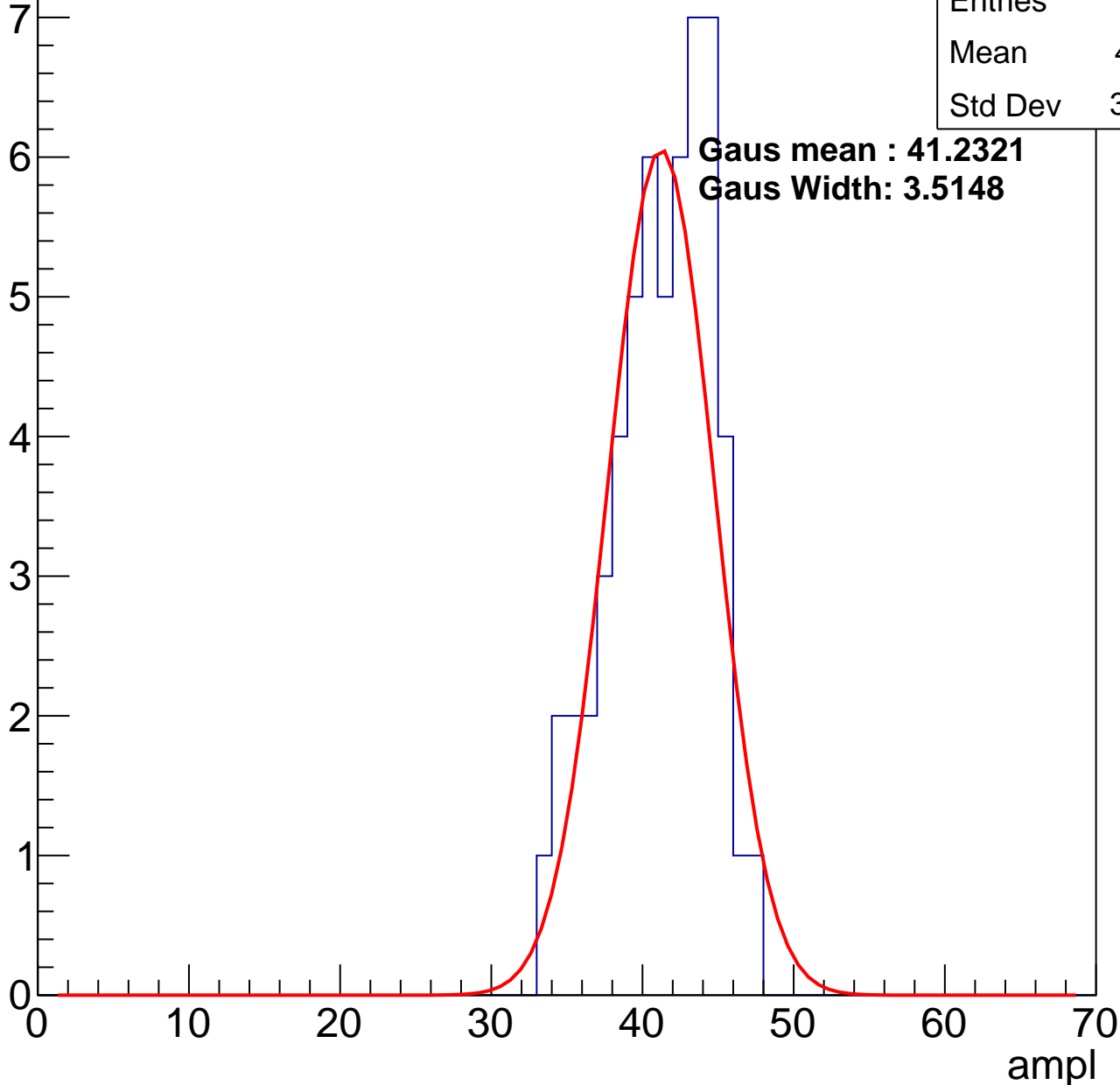
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	40.71
Std Dev	3.304

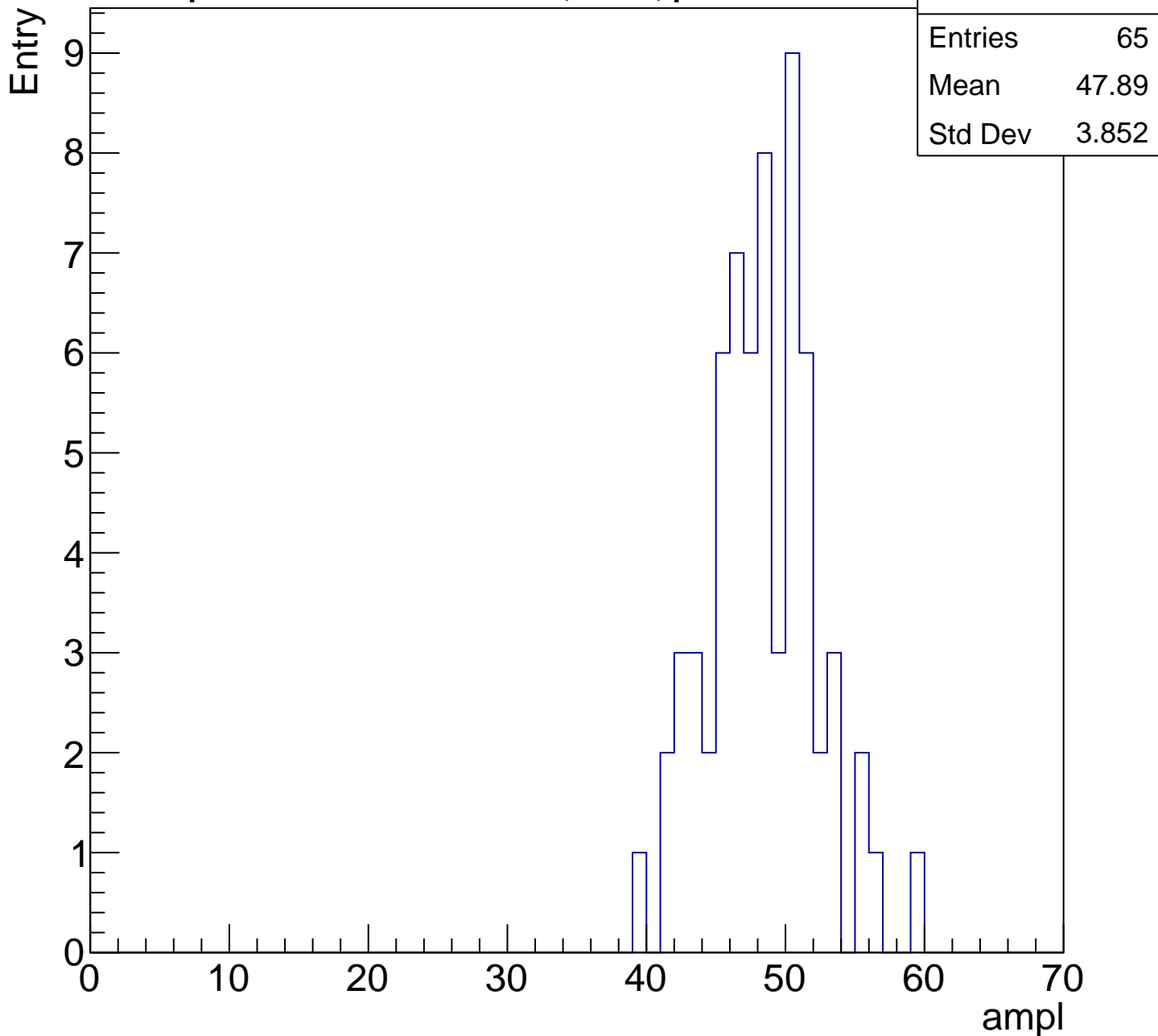
Gaus mean : 41.2321

Gaus Width: 3.5148



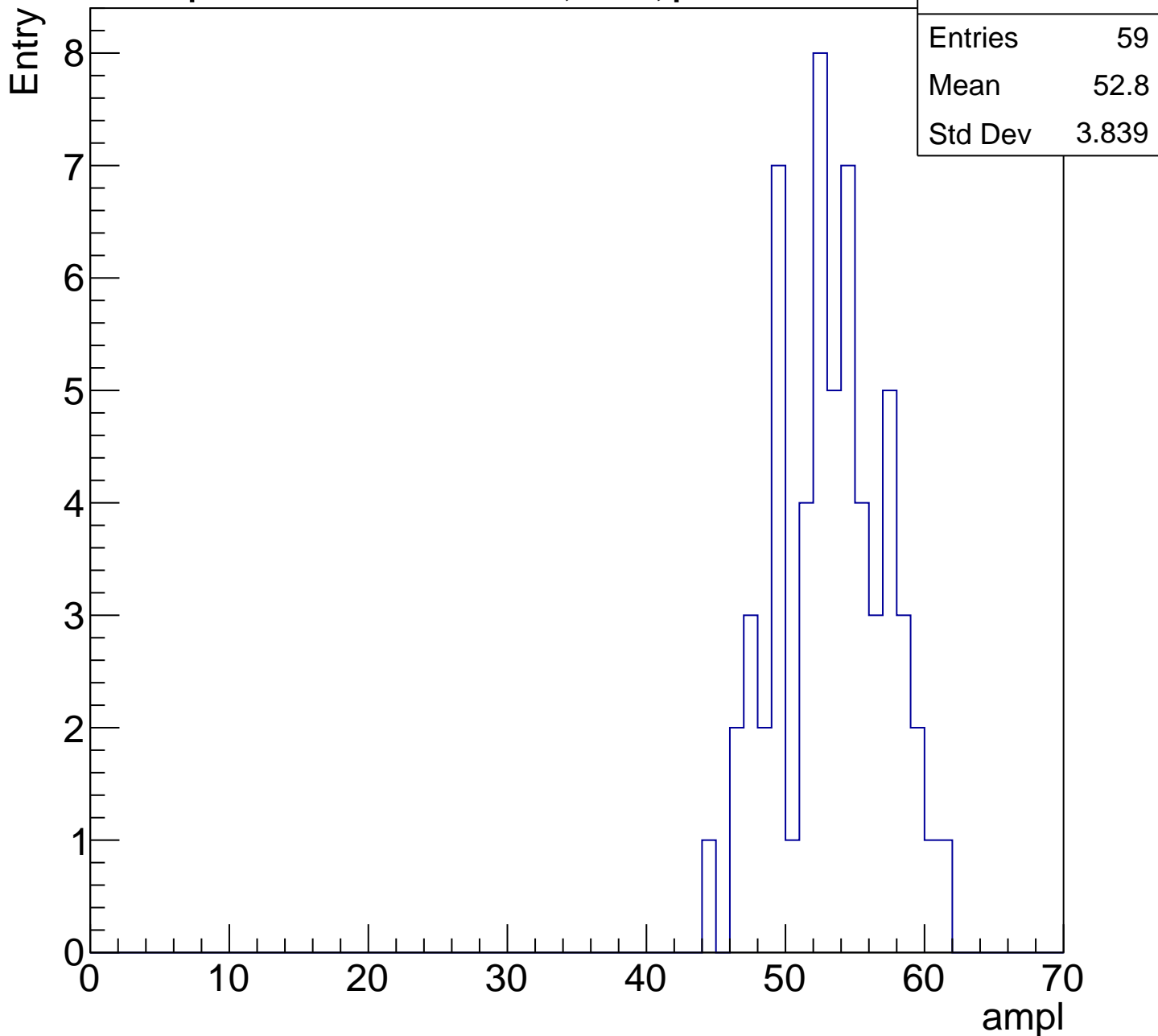
B1L101S, U6-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

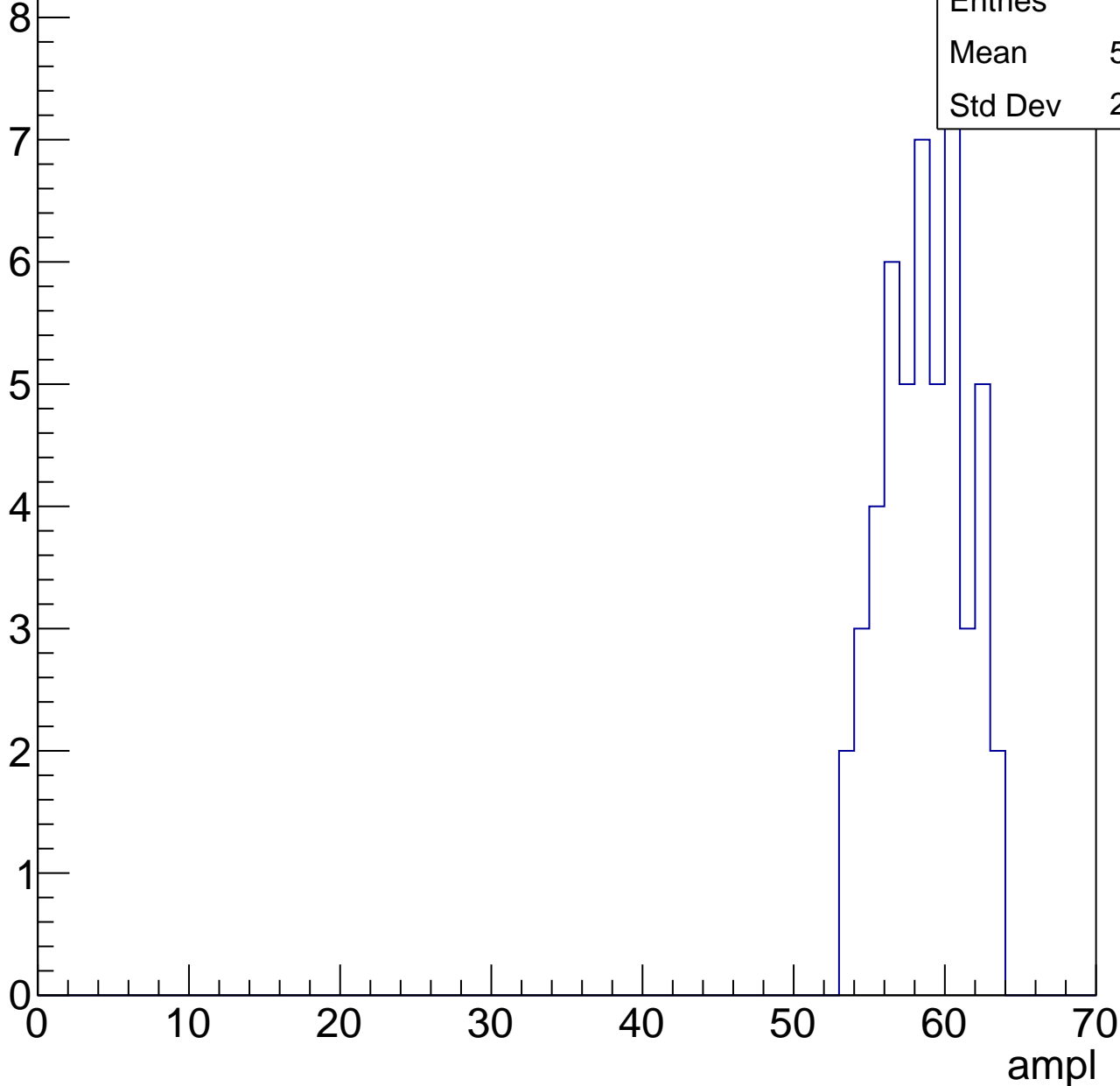


B1L101S, U6-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.18
Std Dev	2.666

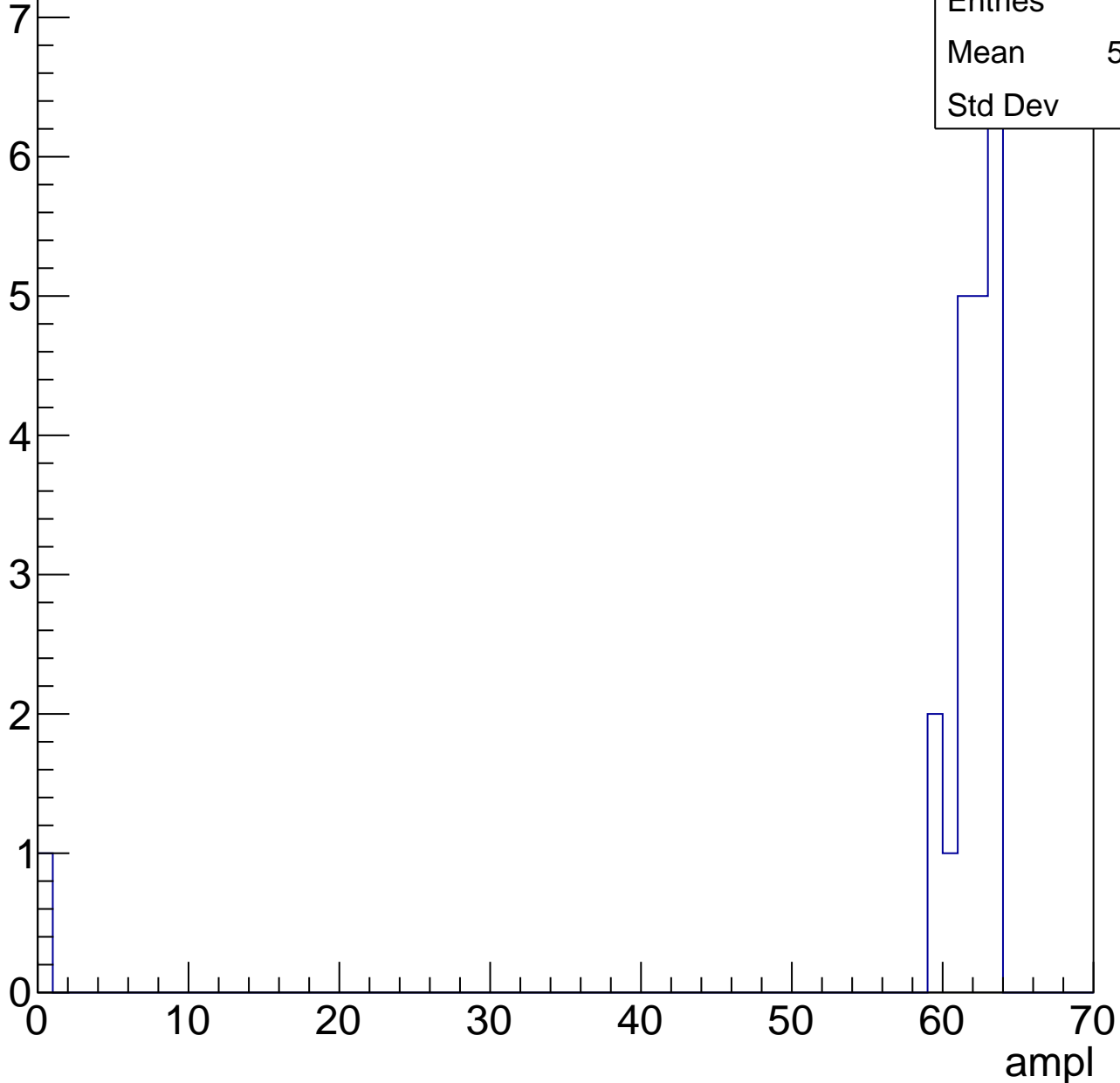


B1L101S, U6-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

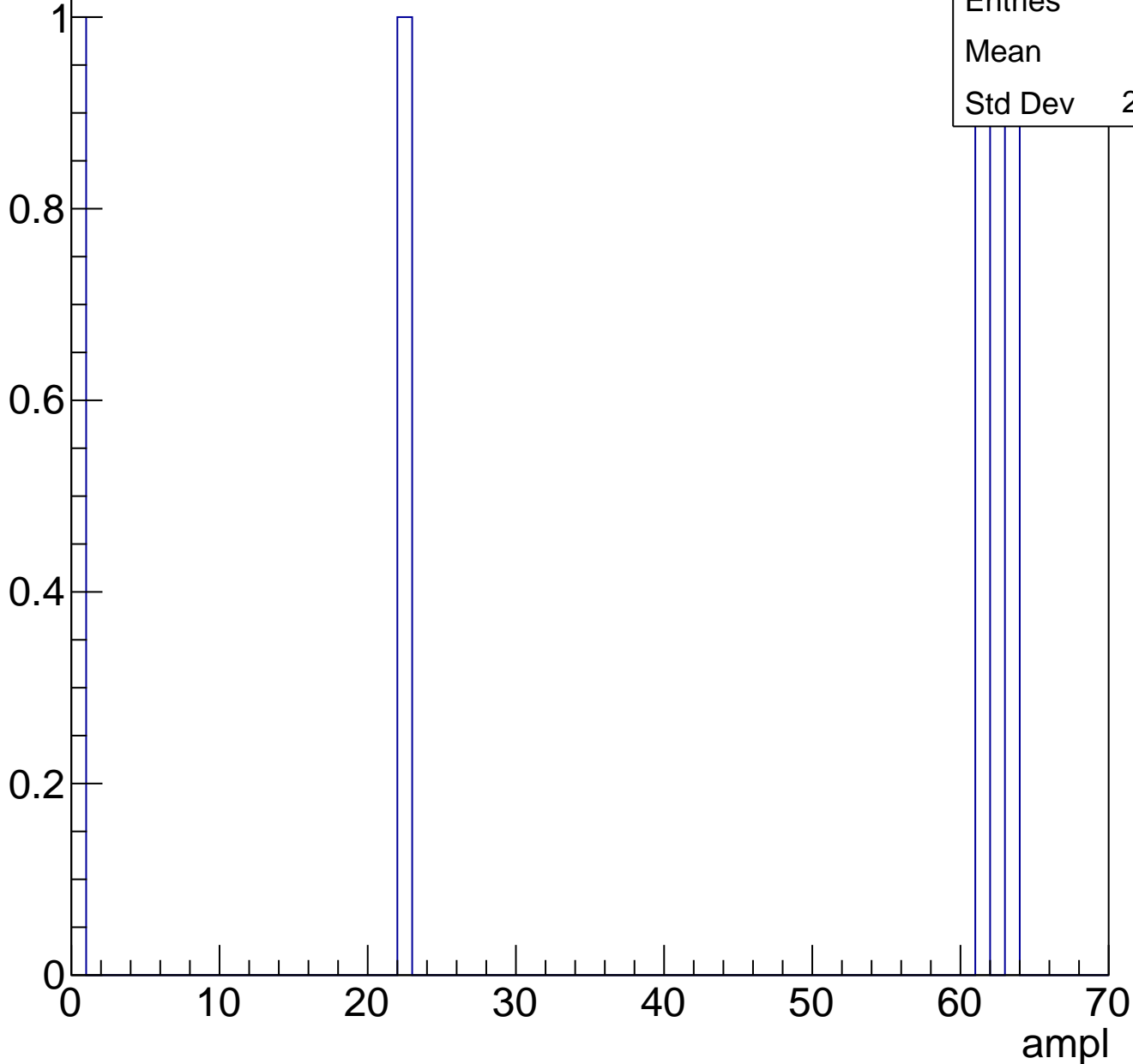
Entries	21
Mean	58.76
Std Dev	13.2



B1L101S, U6-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	36.5
Std Dev	26.67

B1L101S, U6-ch51, adc0

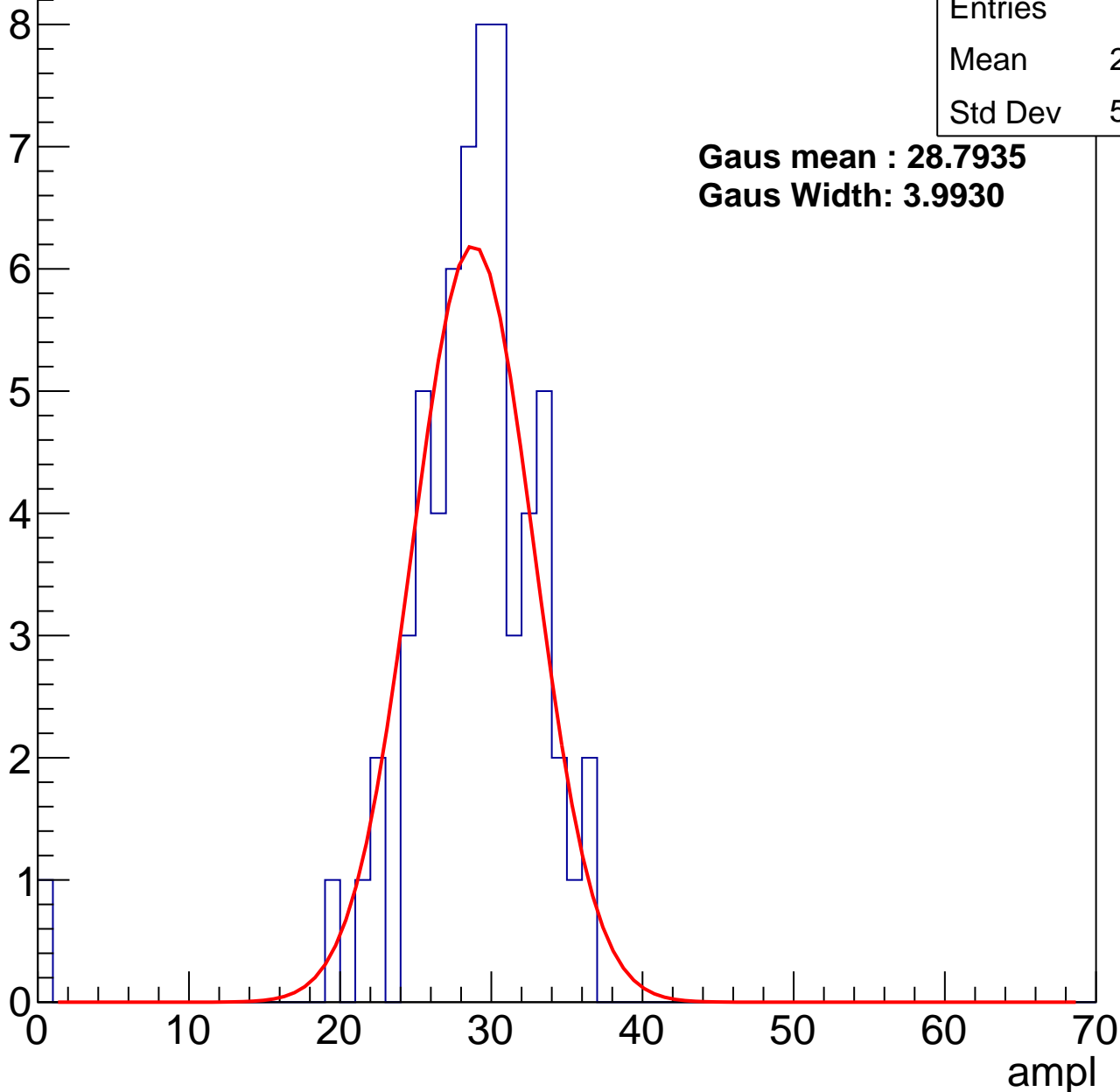
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.19
Std Dev	5.045

Gaus mean : 28.7935

Gaus Width: 3.9930



B1L101S, U6-ch51, adc1

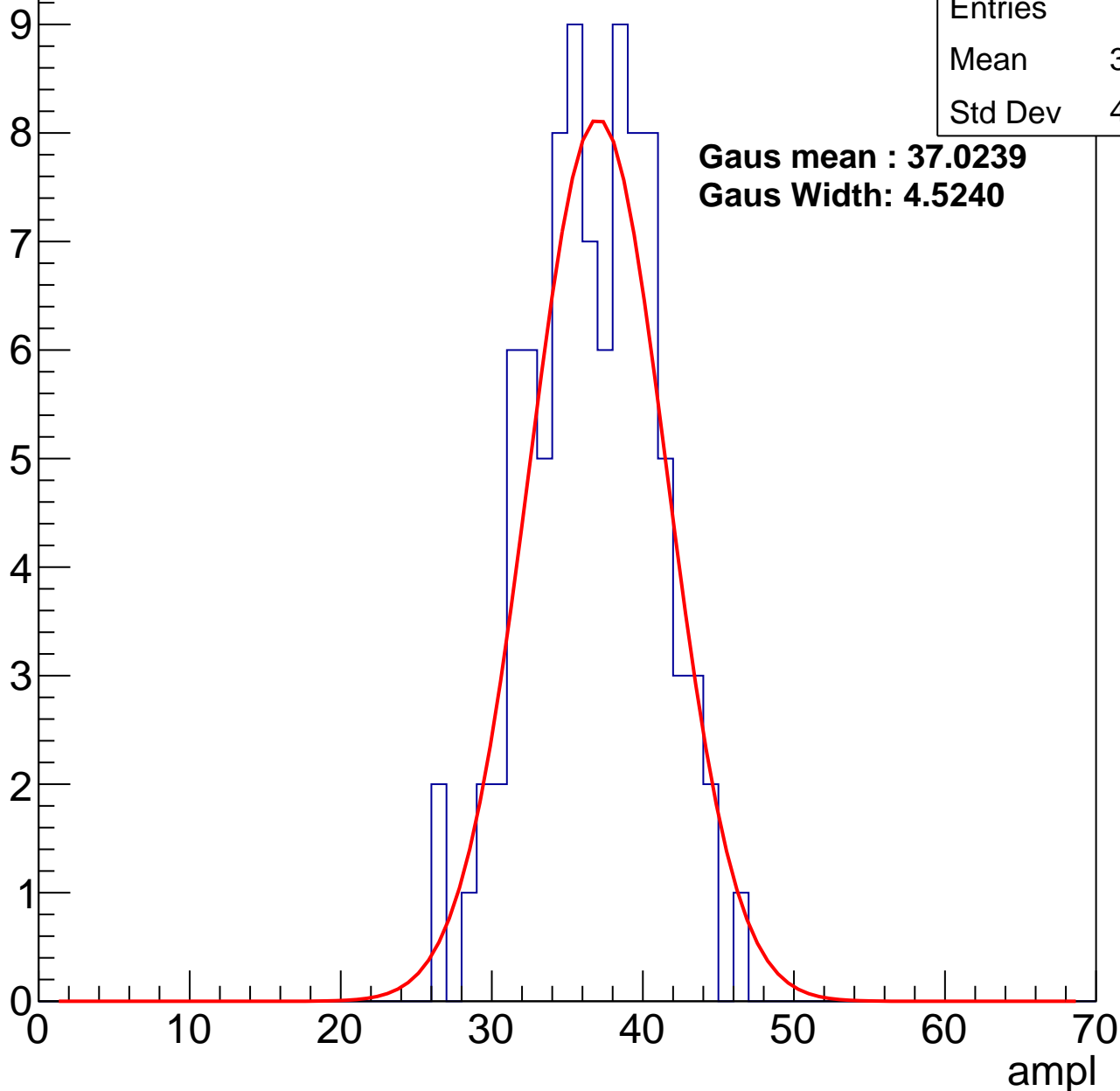
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	36.24
Std Dev	4.162

Gaus mean : 37.0239

Gaus Width: 4.5240



B1L101S, U6-ch51, adc2

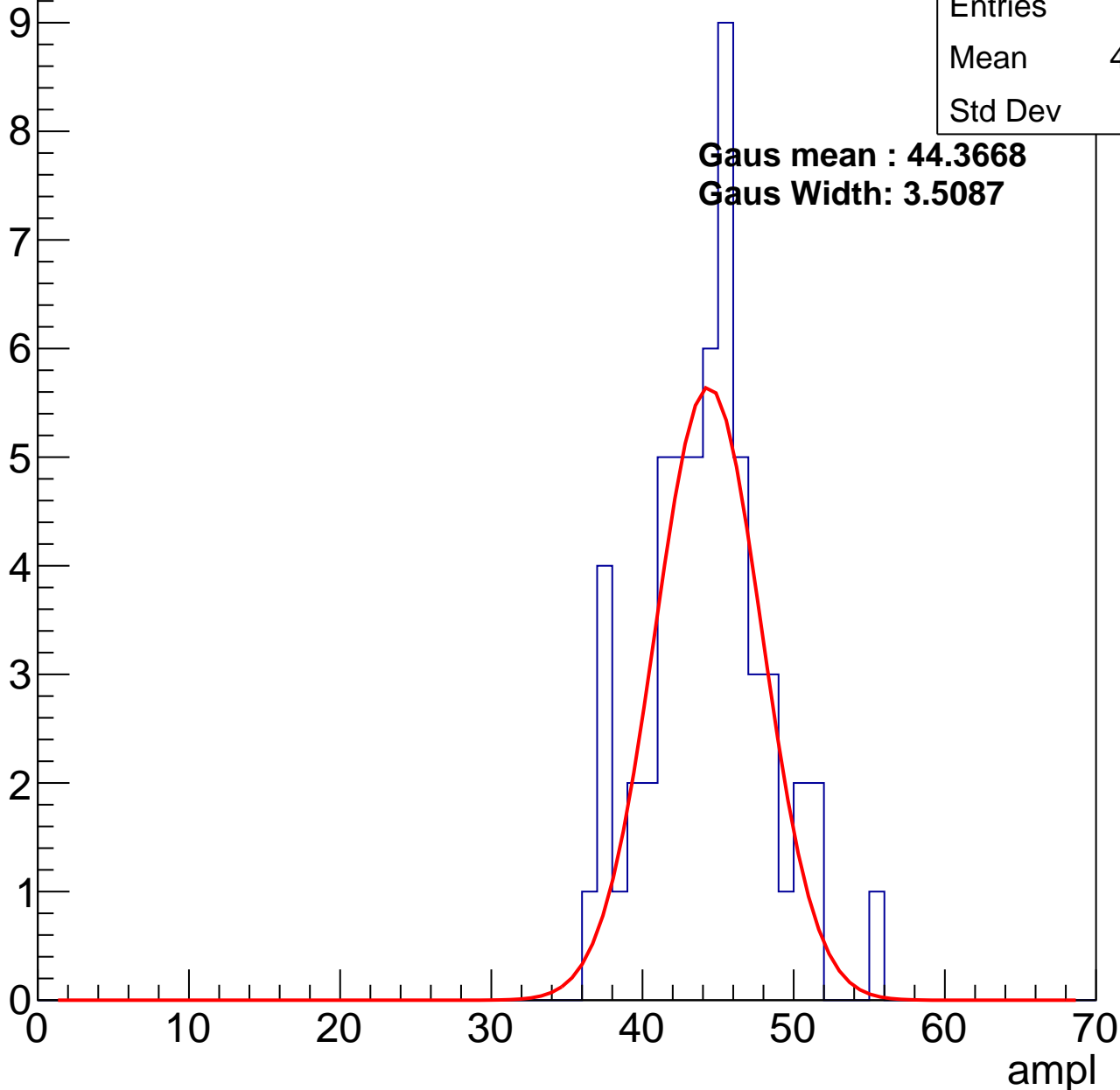
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.86
Std Dev	3.9

Gaus mean : 44.3668

Gaus Width: 3.5087

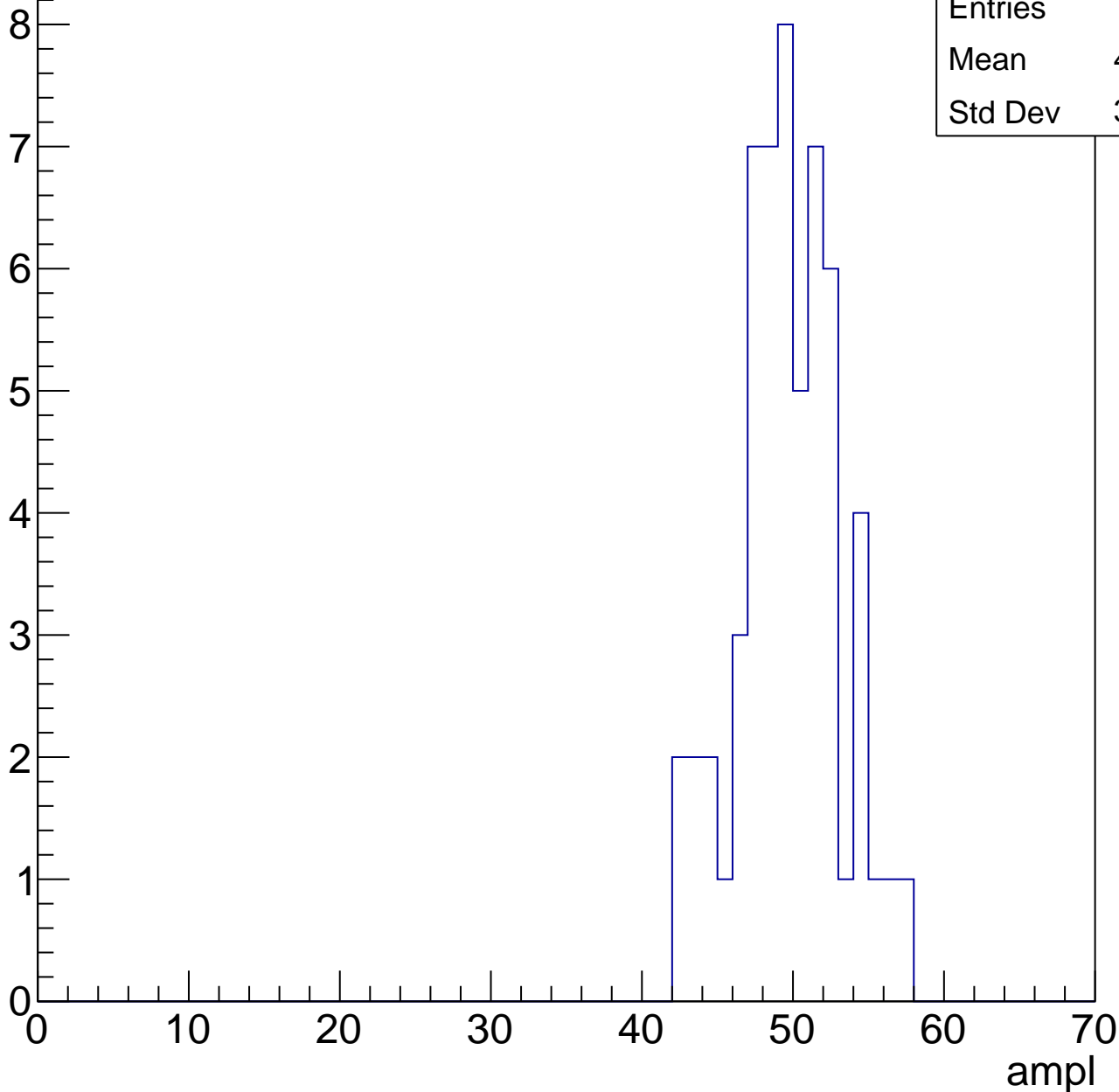


B1L101S, U6-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

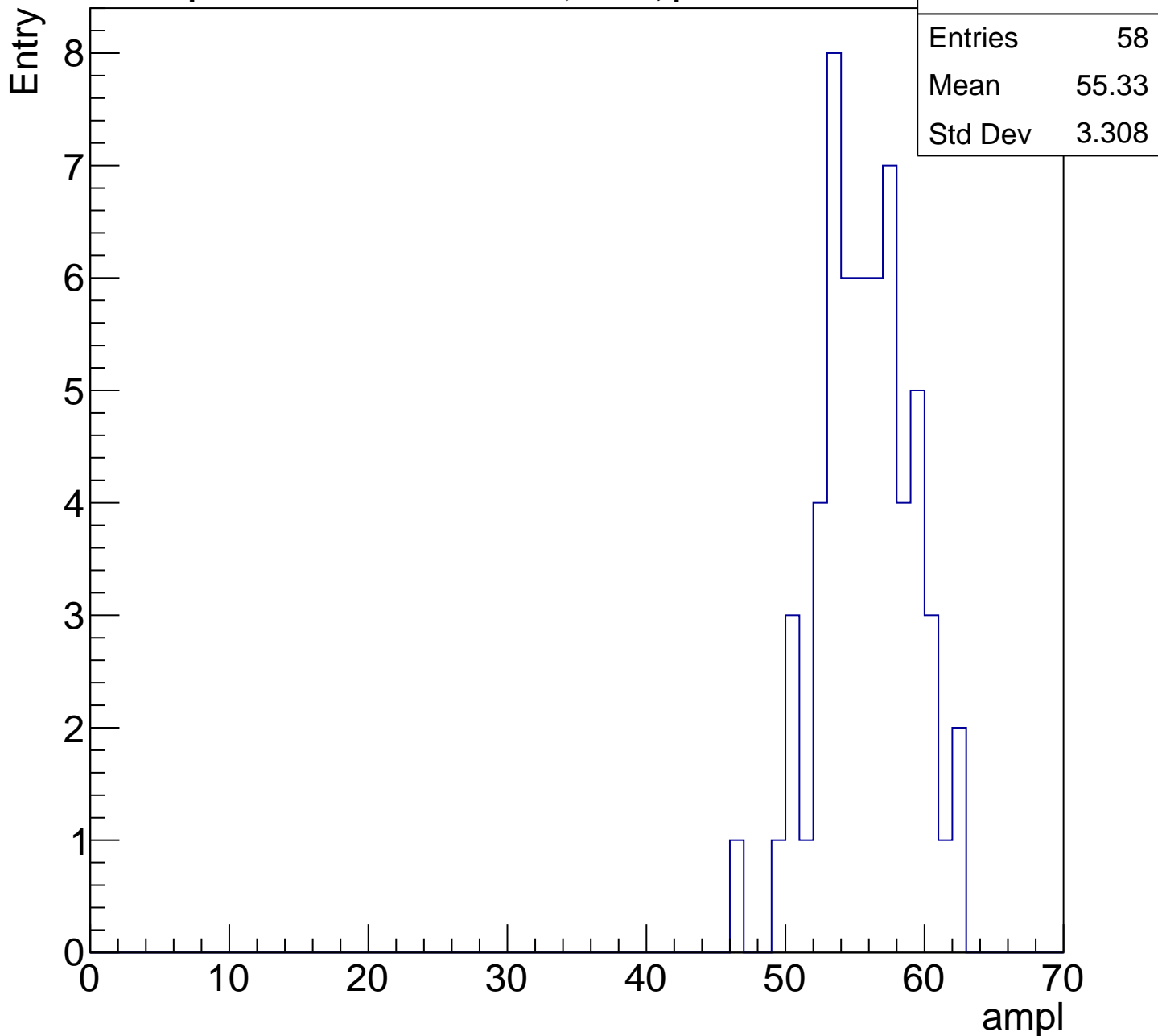
Entry

Entries	58
Mean	49.21
Std Dev	3.341



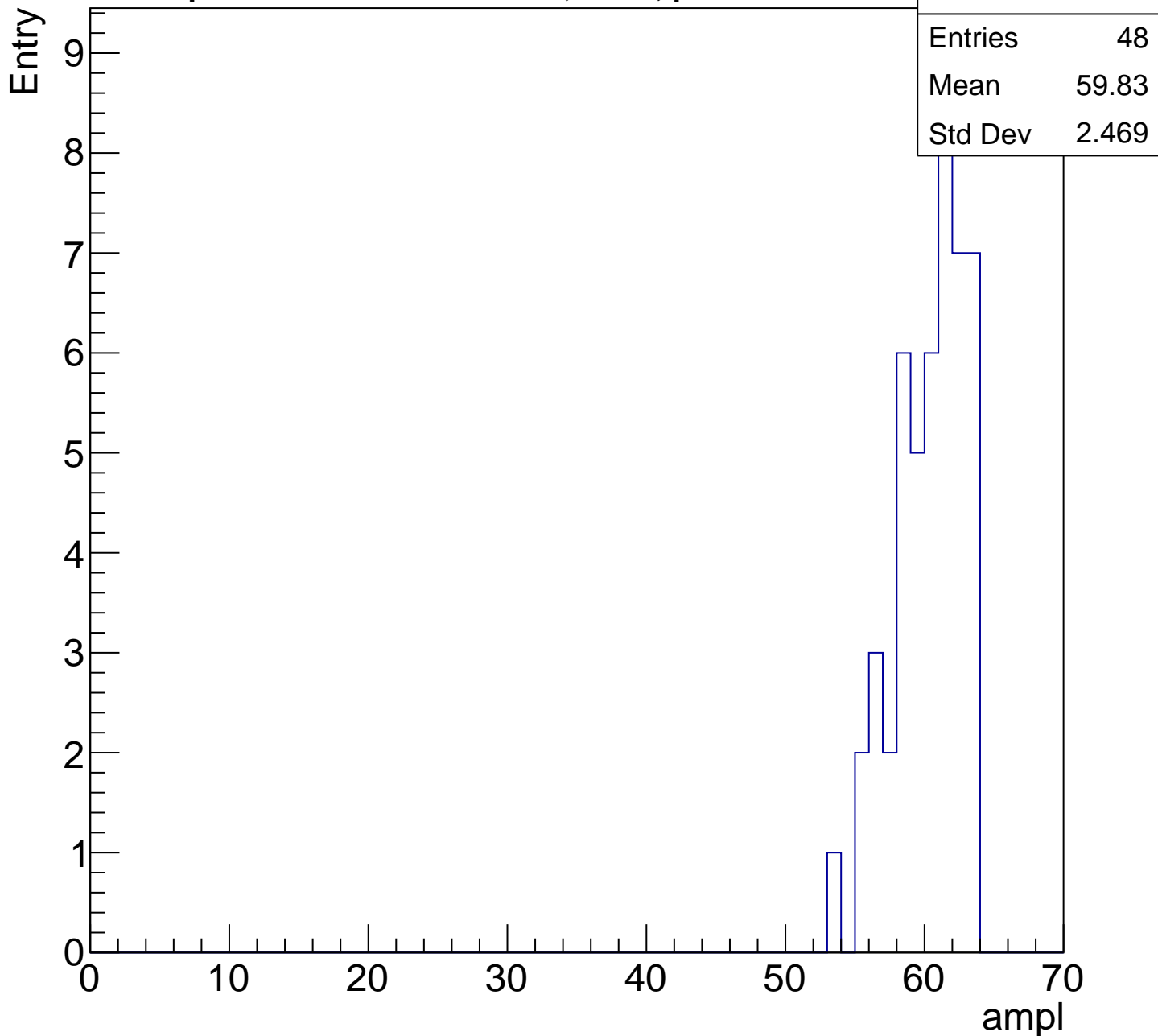
B1L101S, U6-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch51, adc5

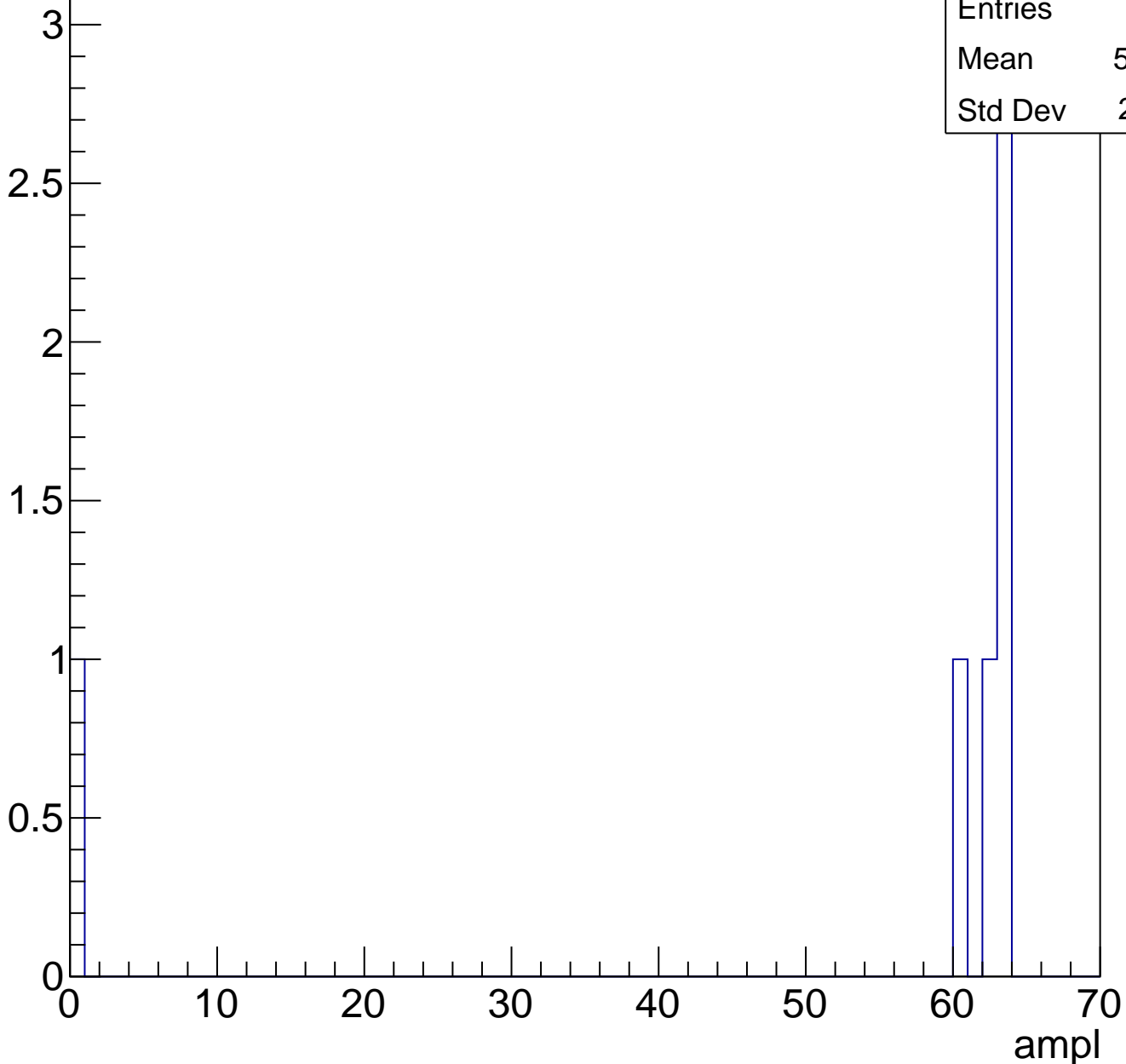
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch52, adc0

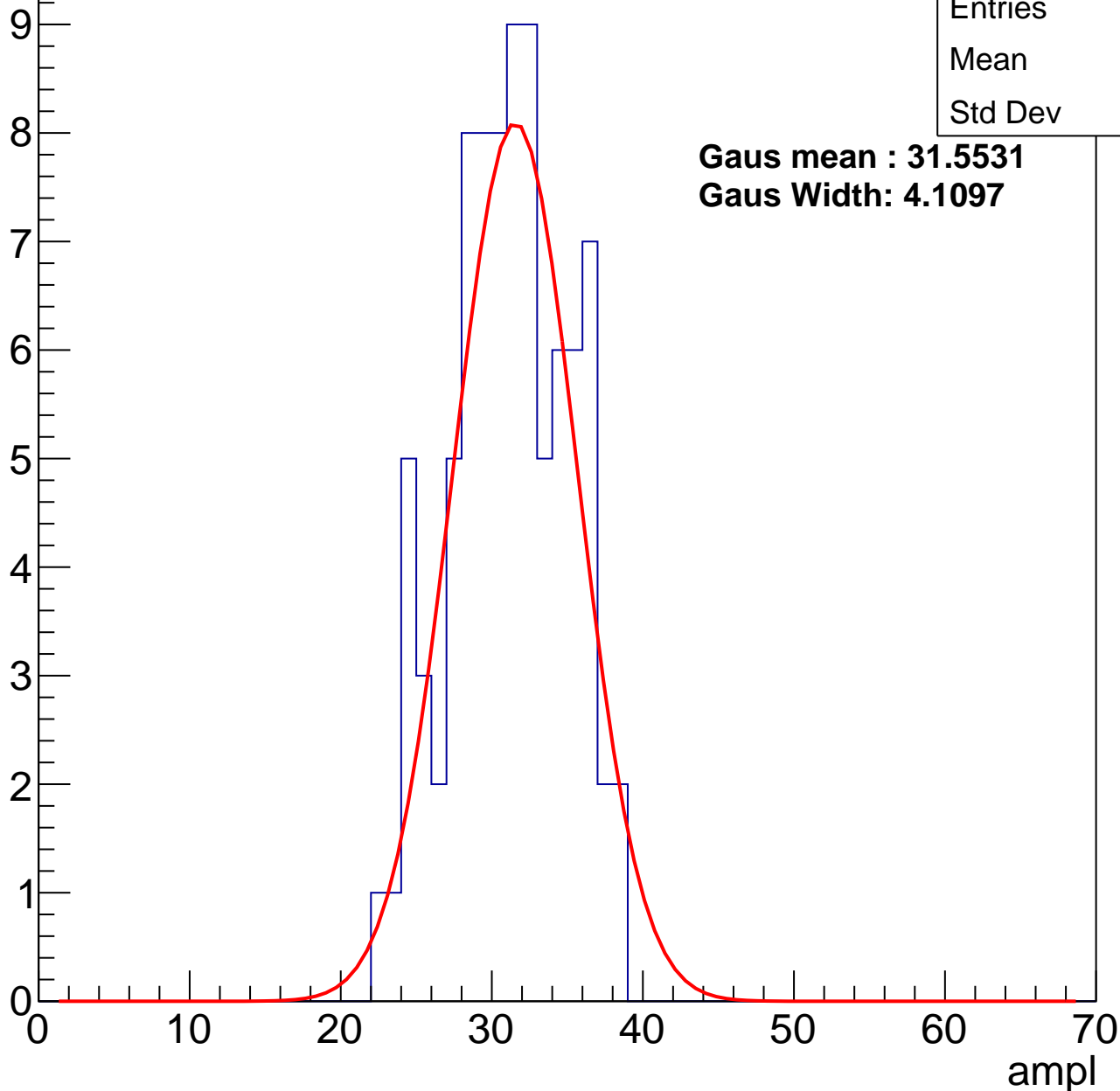
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	30.7
Std Dev	3.8

Gaus mean : 31.5531

Gaus Width: 4.1097



B1L101S, U6-ch52, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.64
Std Dev	3.221

Gaus mean : 39.5498

Gaus Width: 3.6444

10

8

6

4

2

0

0

10

20

30

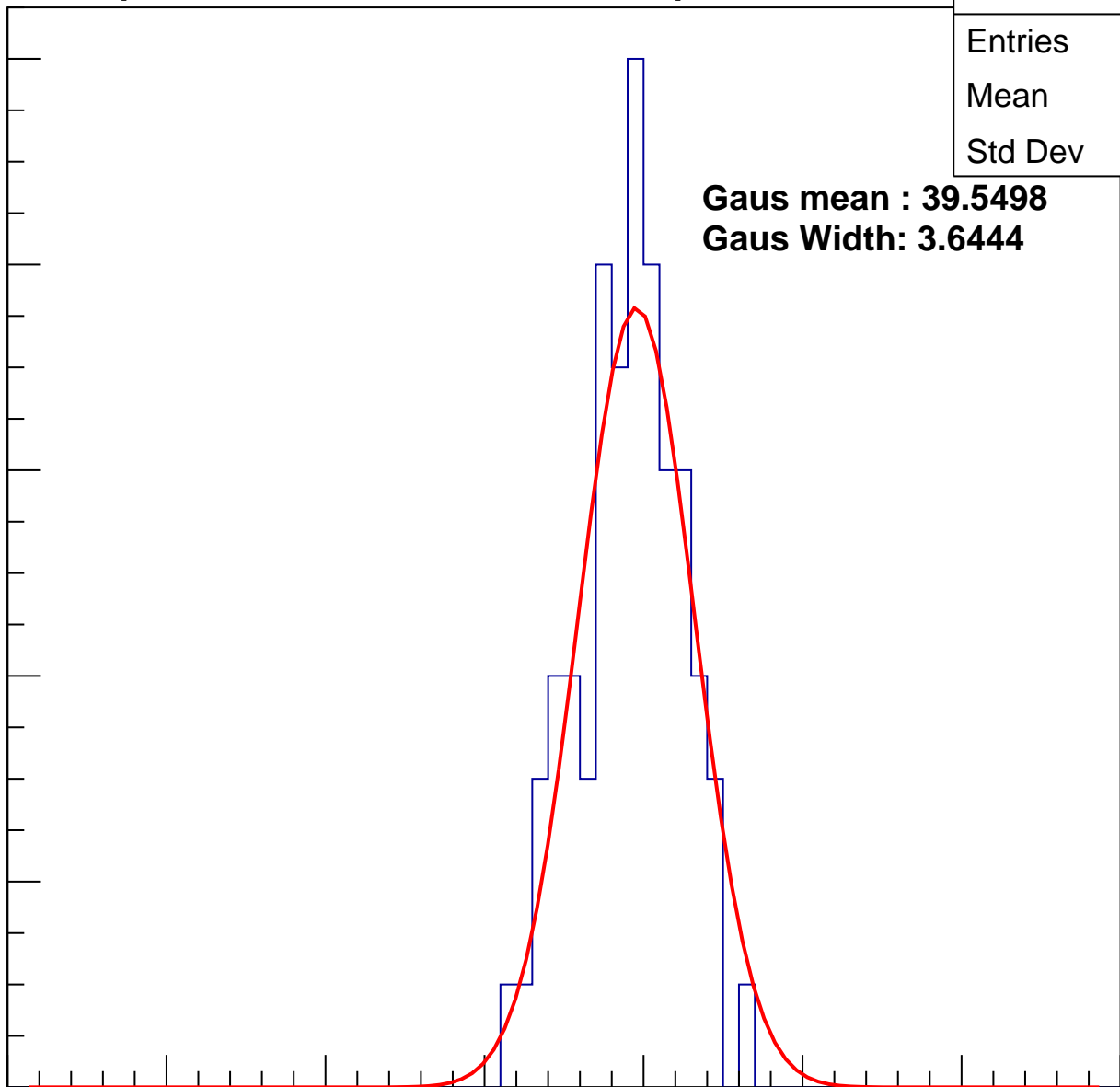
40

50

60

70

ampl



B1L101S, U6-ch52, adc2

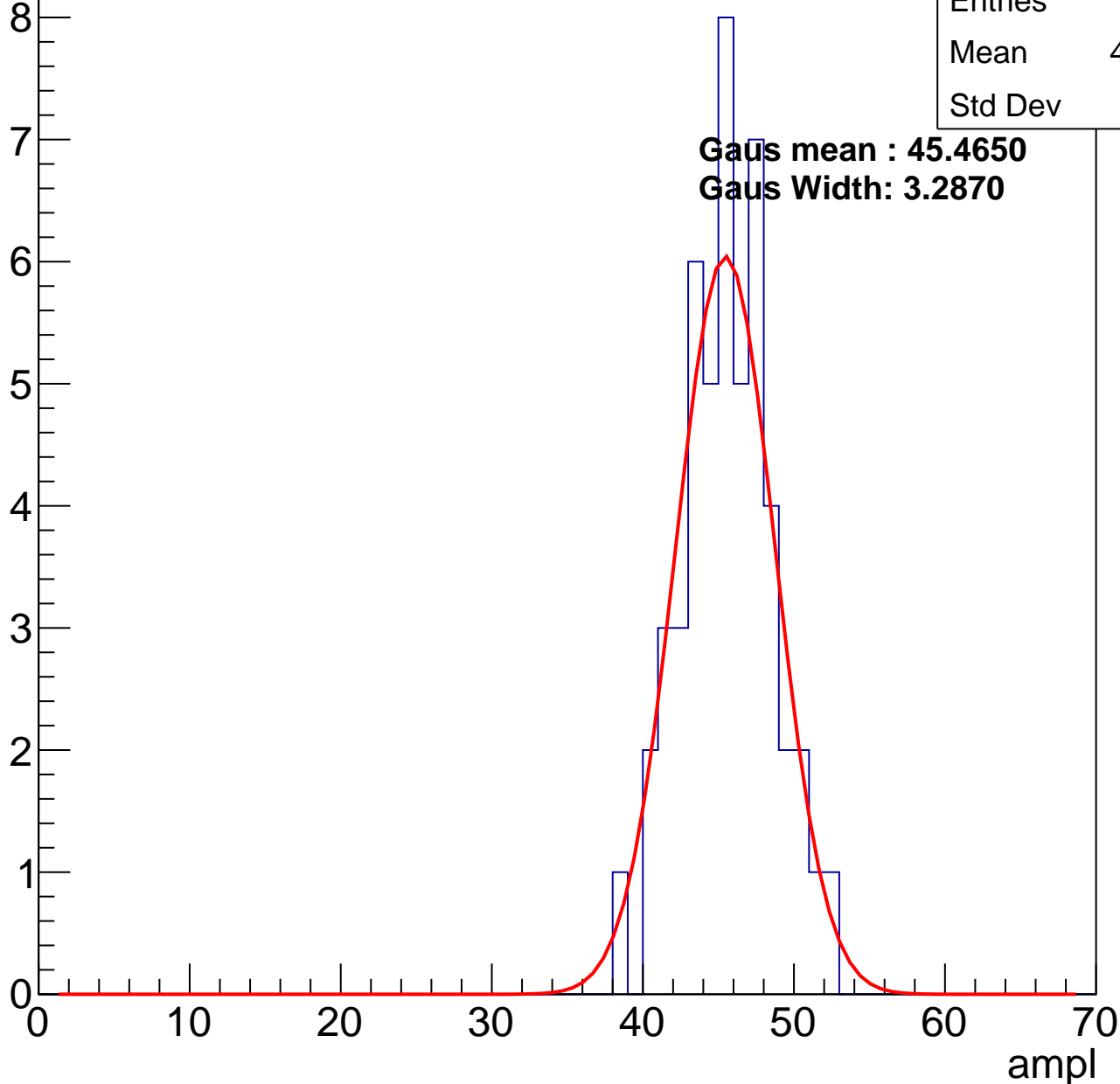
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	45.14
Std Dev	2.96

Gaus mean : 45.4650

Gaus Width: 3.2870

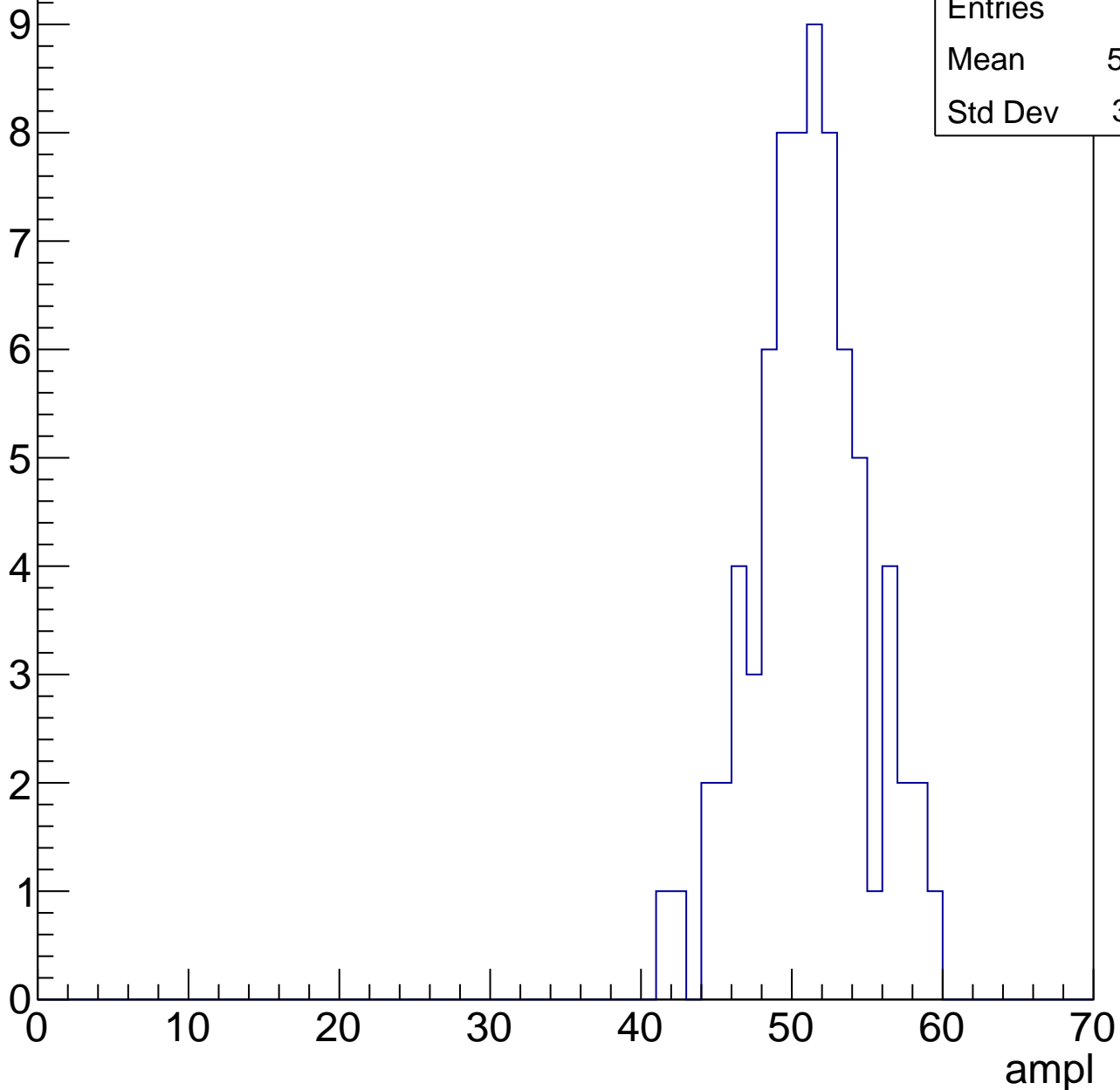


B1L101S, U6-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	50.64
Std Dev	3.721

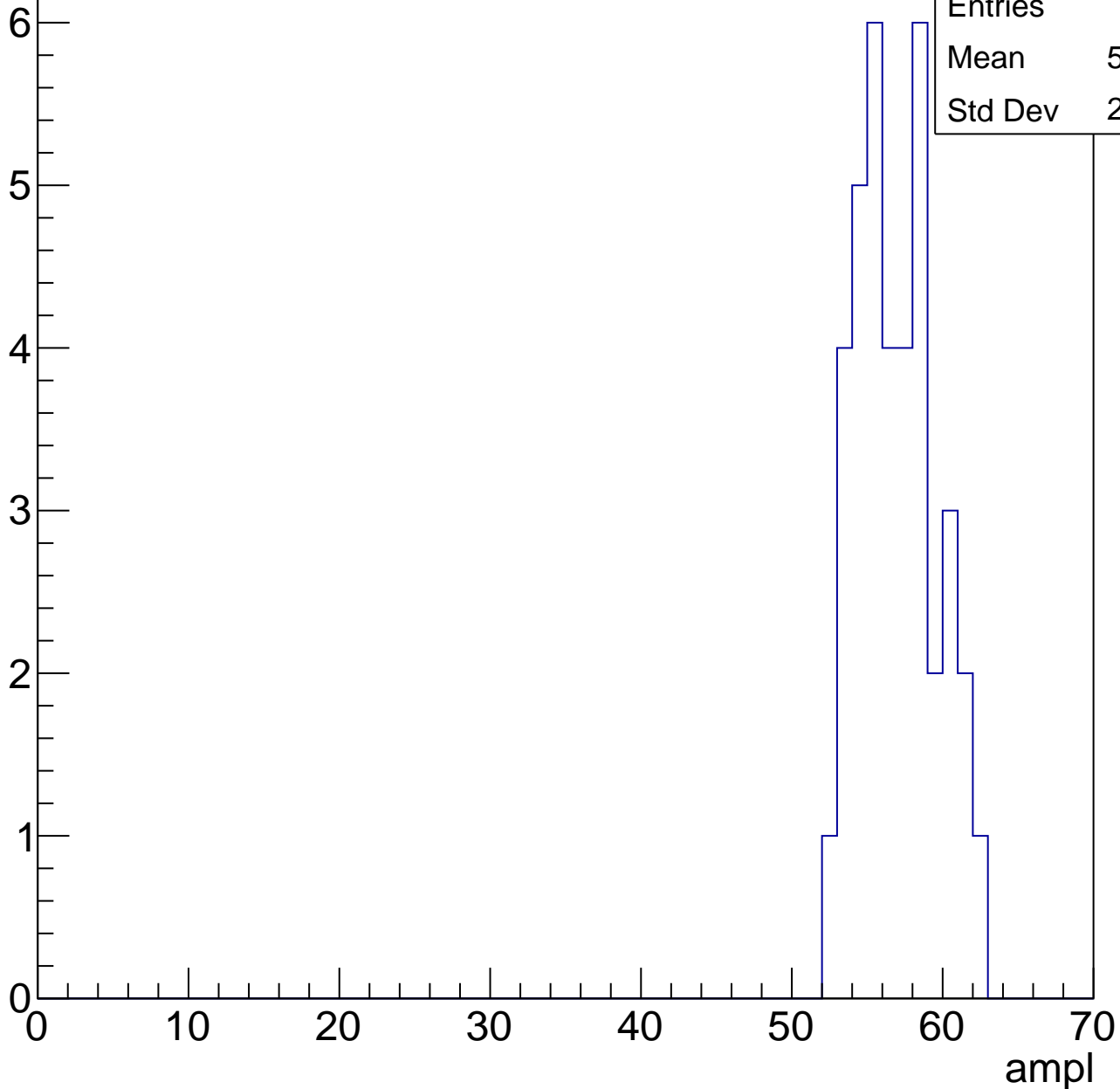


B1L101S, U6-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	56.47
Std Dev	2.562

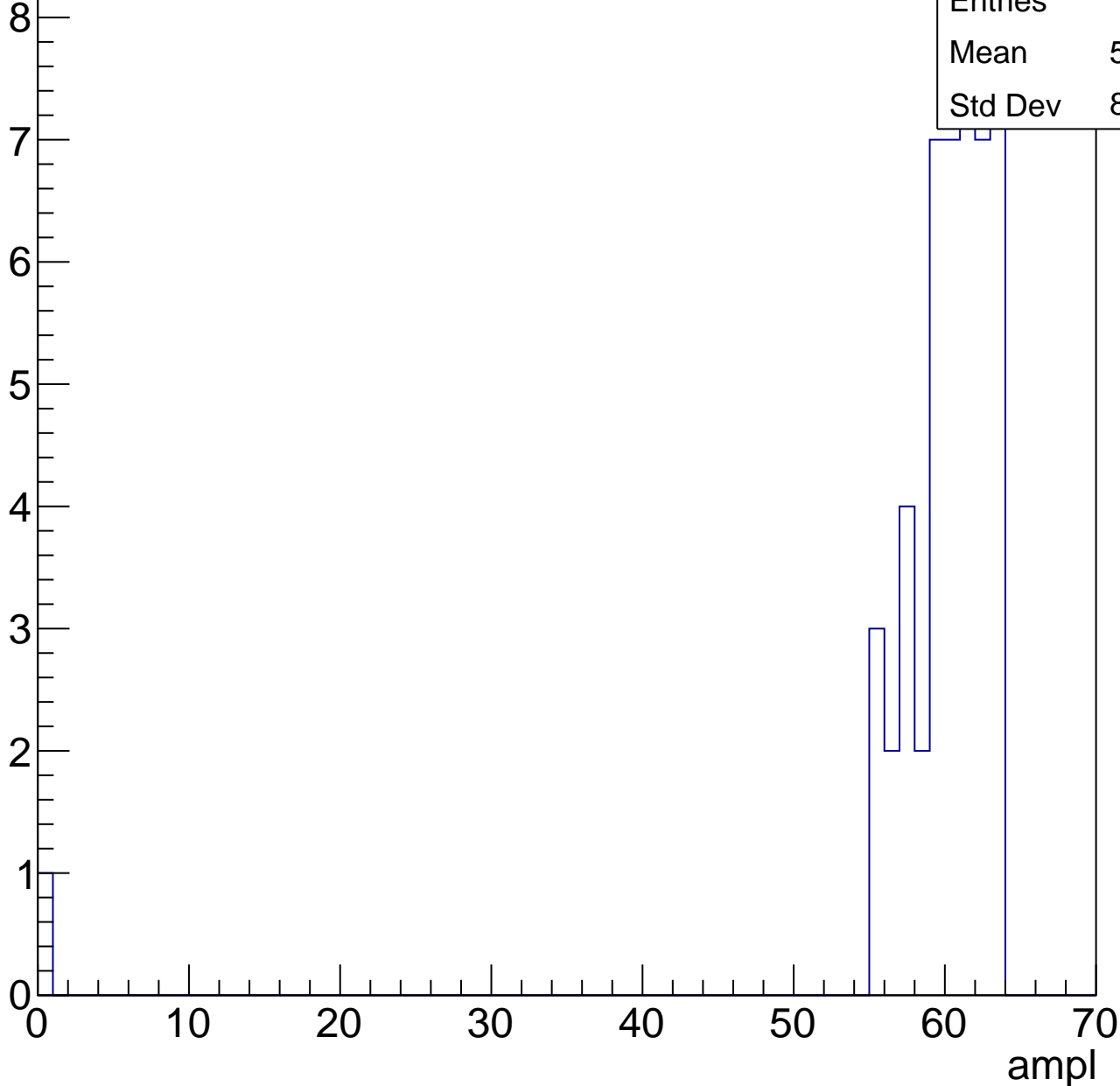


B1L101S, U6-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.78
Std Dev	8.798



B1L101S, U6-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch53, adc0

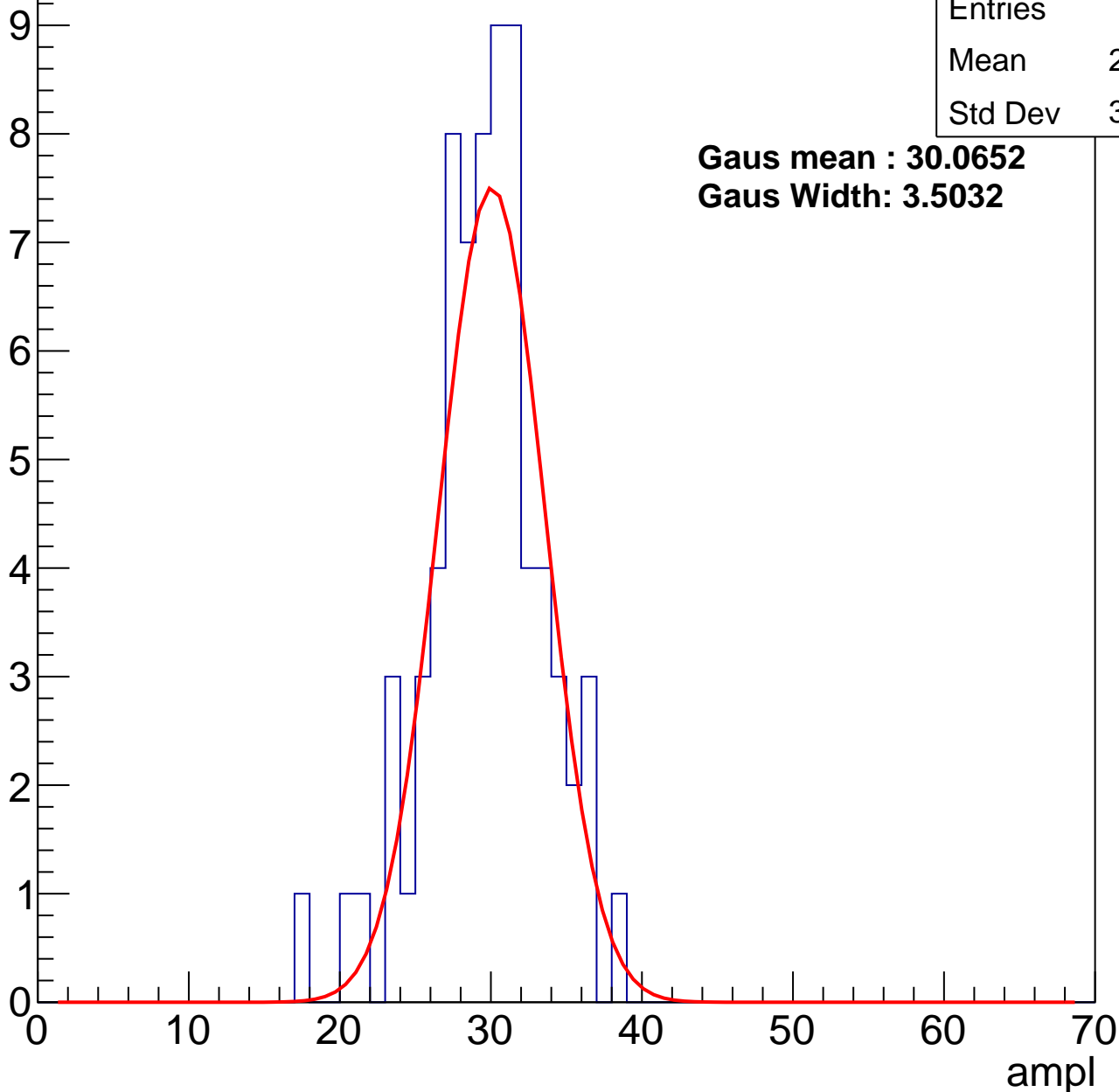
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.18
Std Dev	3.842

Gaus mean : 30.0652

Gaus Width: 3.5032



B1L101S, U6-ch53, adc1

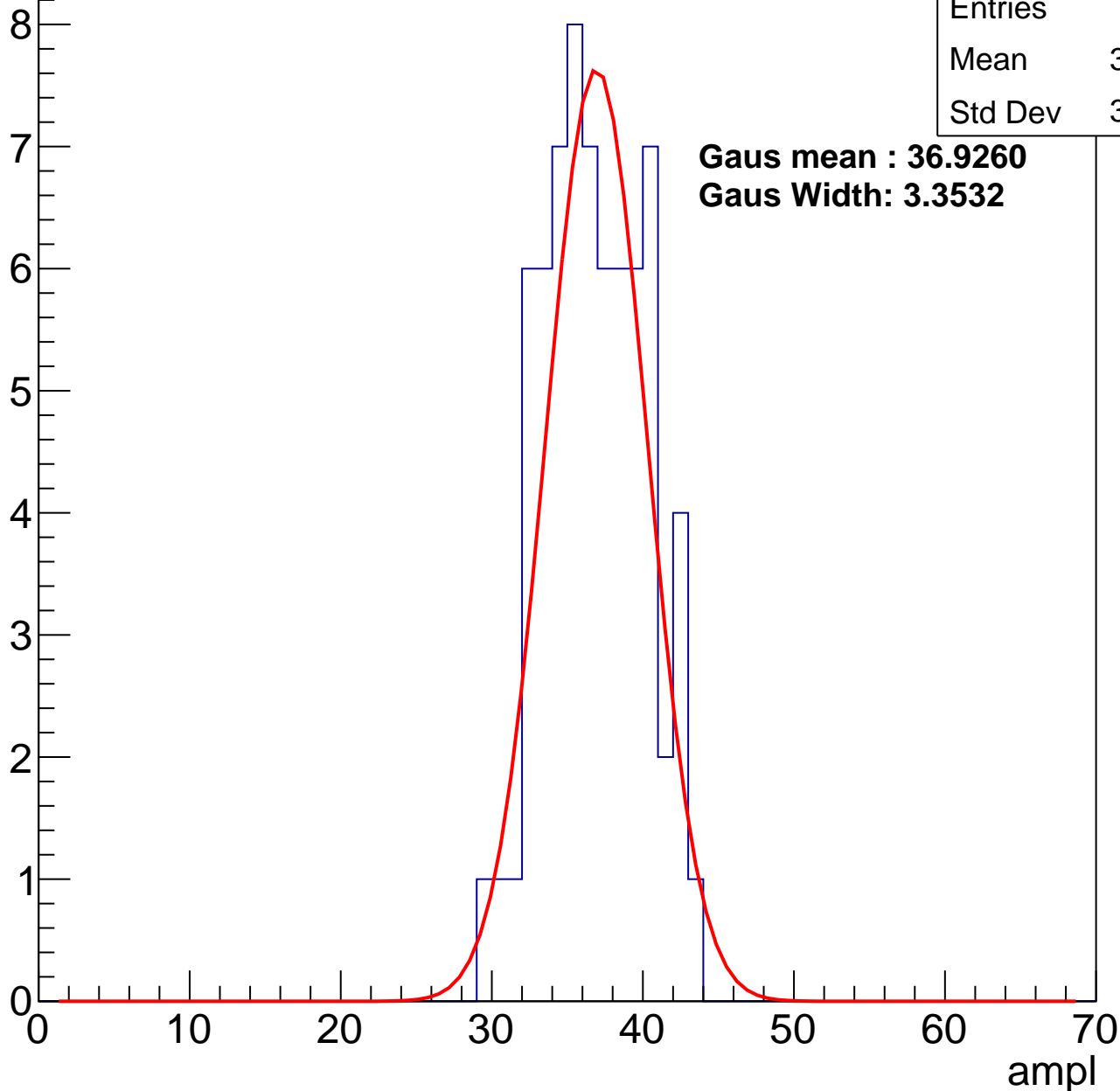
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.33
Std Dev	3.247

Gaus mean : 36.9260

Gaus Width: 3.3532



B1L101S, U6-ch53, adc2

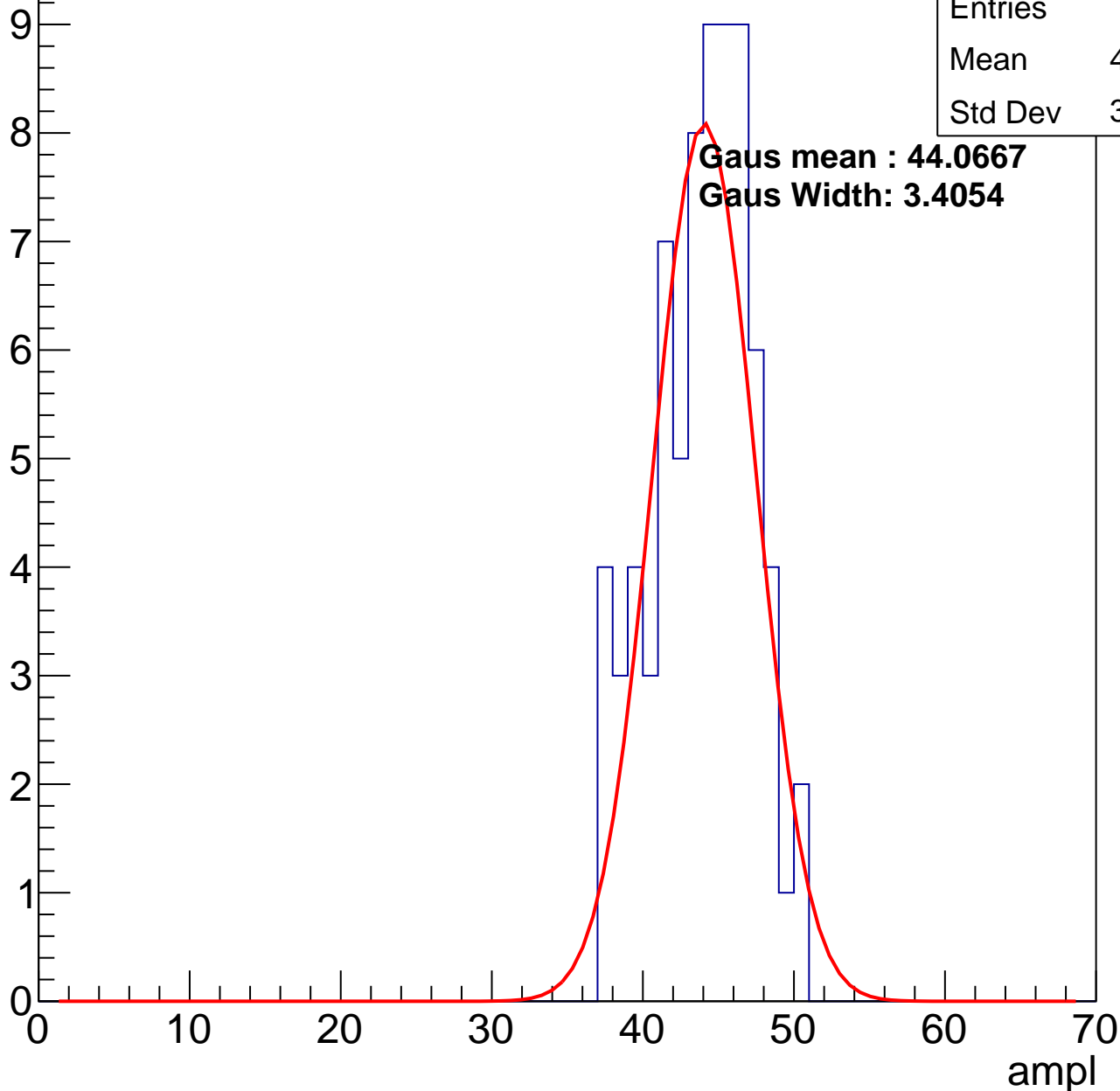
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.47
Std Dev	3.252

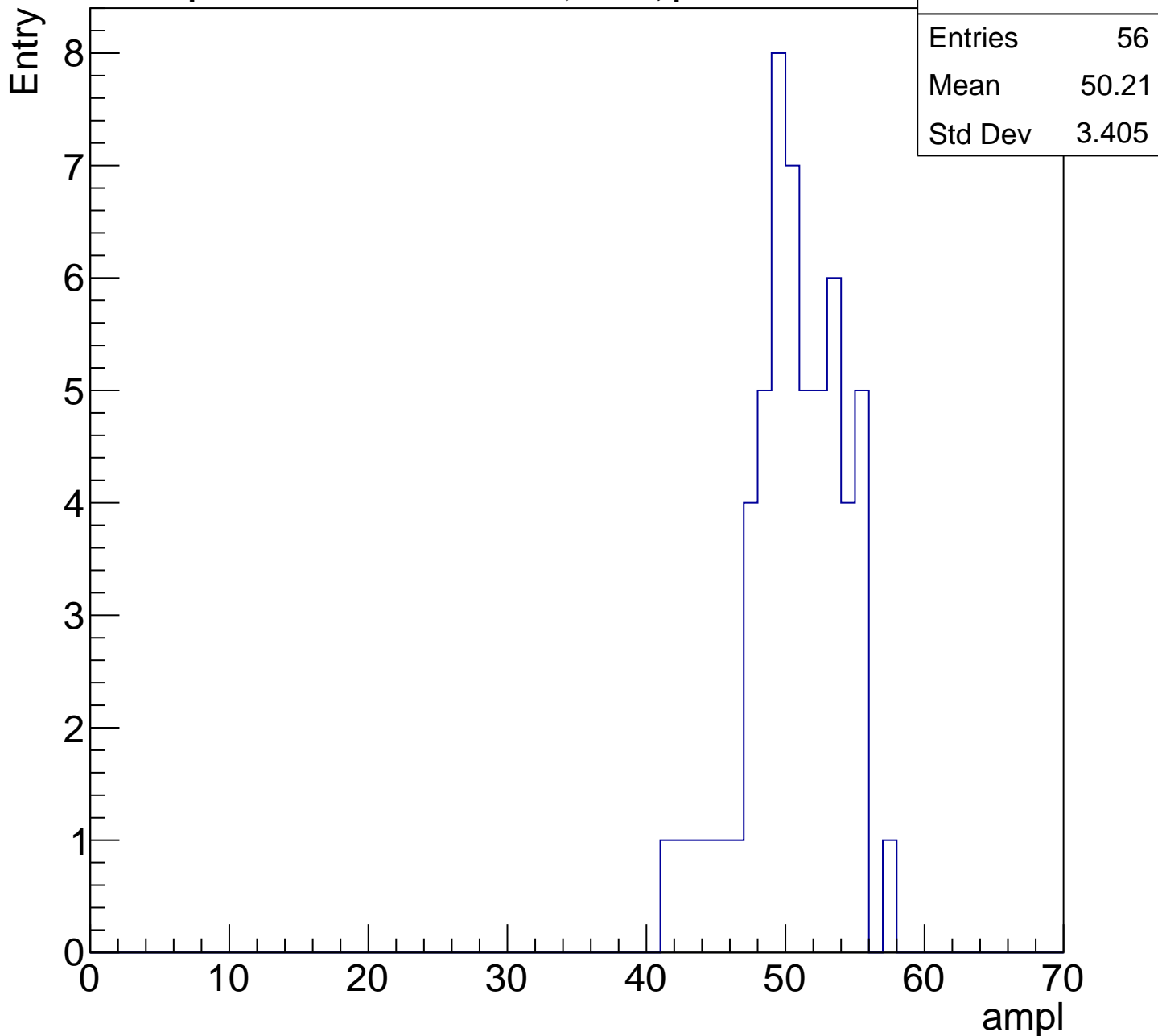
Gaus mean : 44.0667

Gaus Width: 3.4054



B1L101S, U6-ch53, adc3

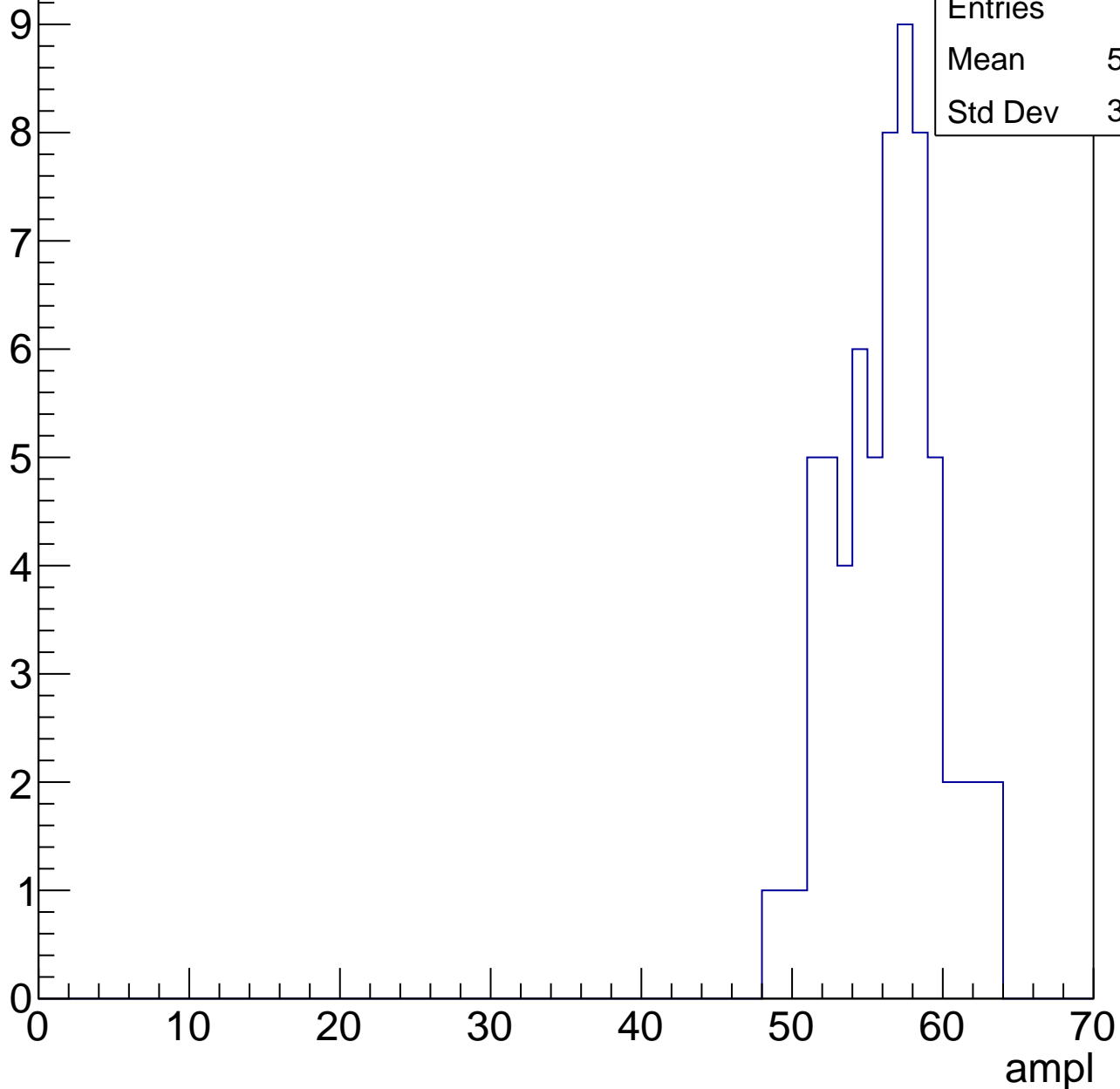
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

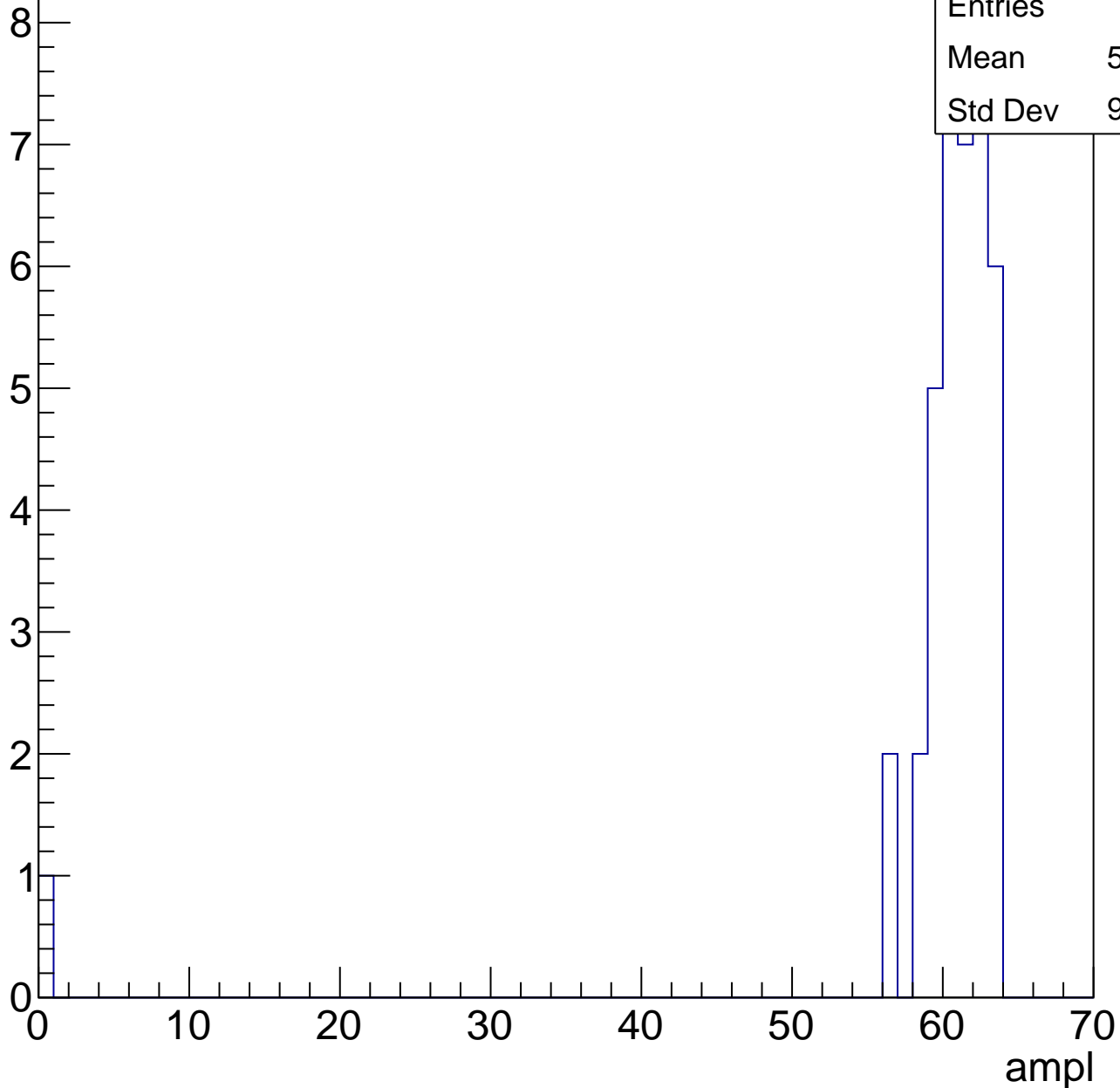
Entry



B1L101S, U6-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch54, adc0

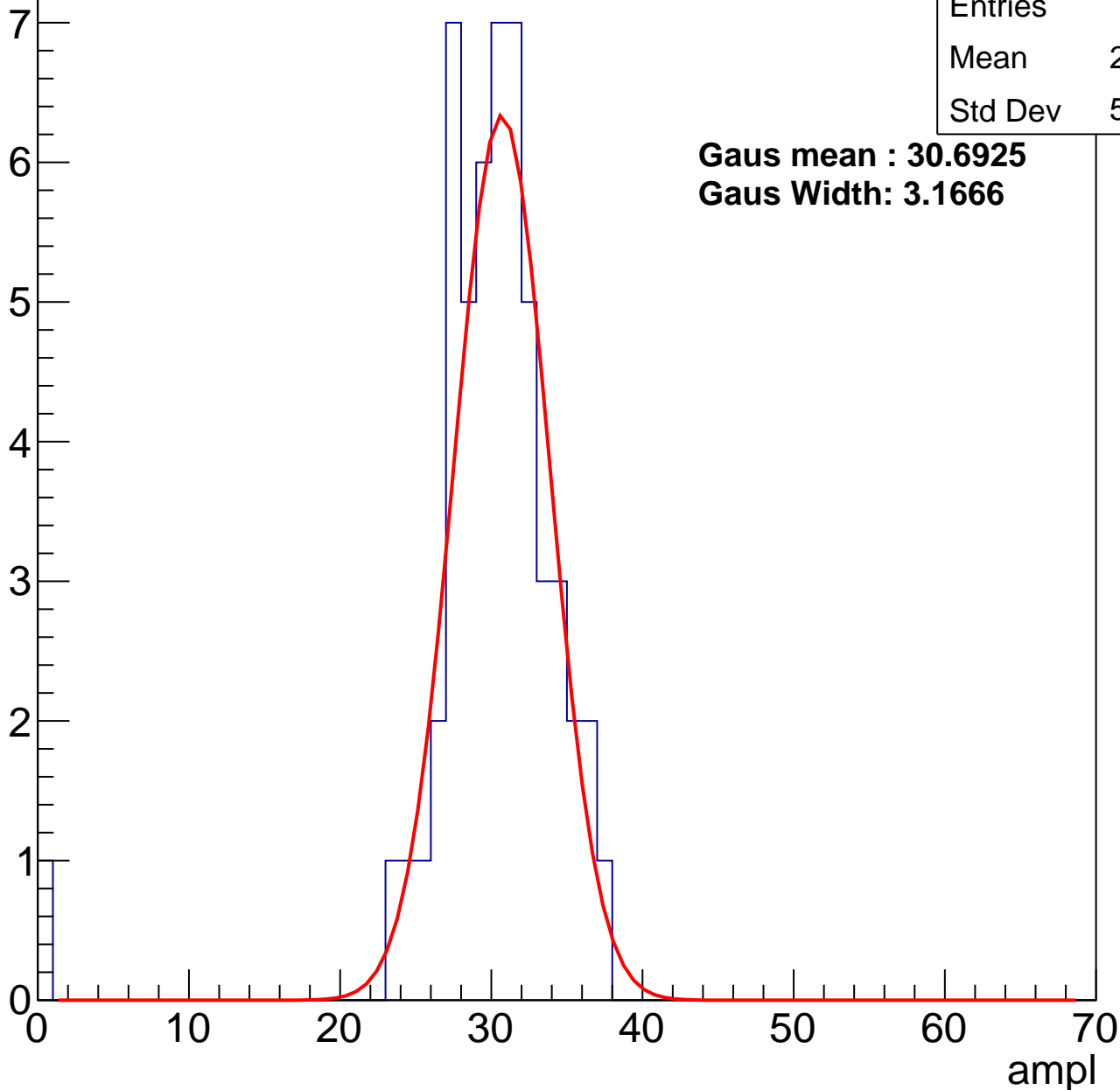
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	29.52
Std Dev	5.076

Gaus mean : 30.6925

Gaus Width: 3.1666



B1L101S, U6-ch54, adc1

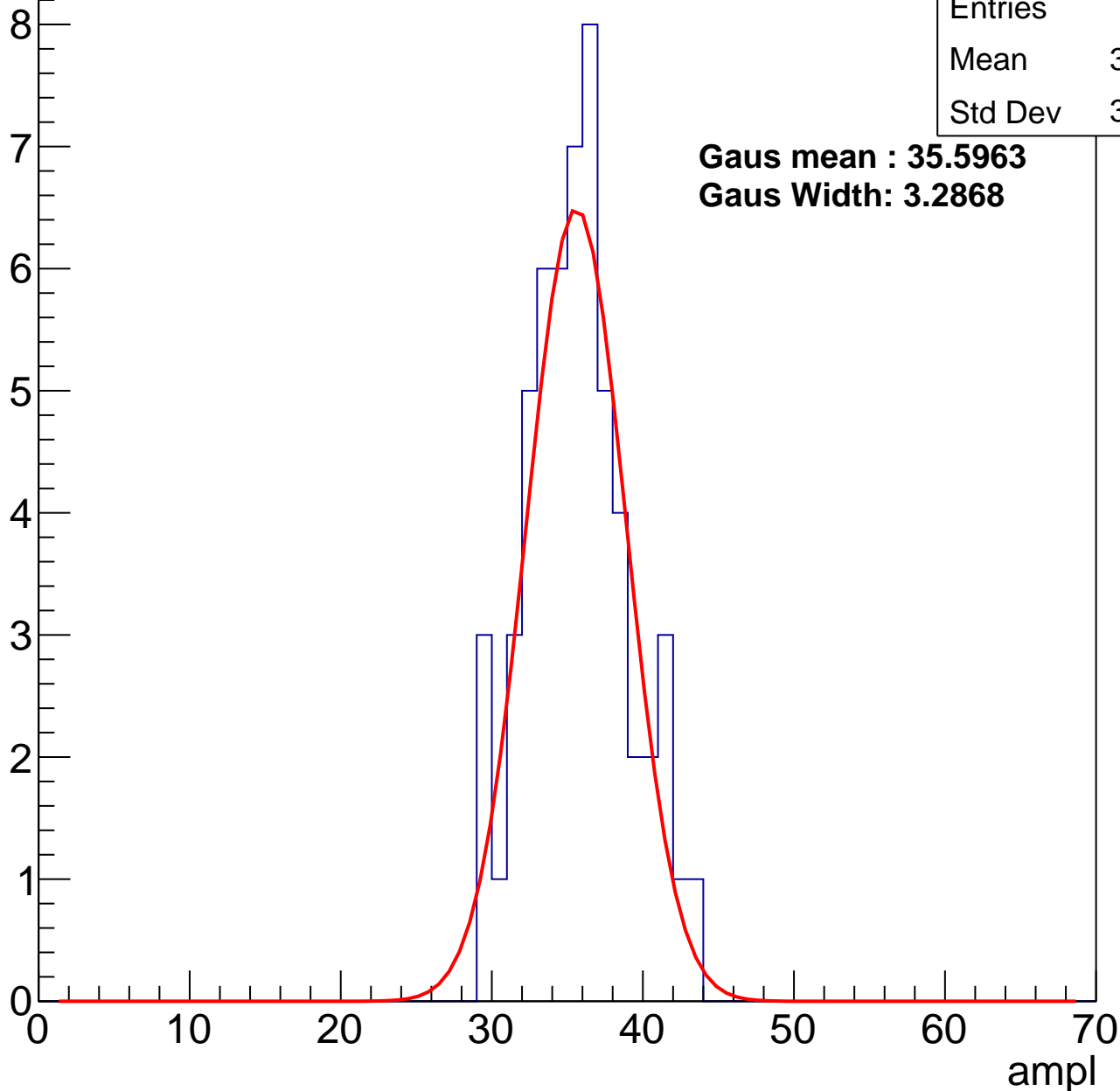
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.23
Std Dev	3.298

Gaus mean : 35.5963

Gaus Width: 3.2868



B1L101S, U6-ch54, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	100
Mean	43.19
Std Dev	4.154

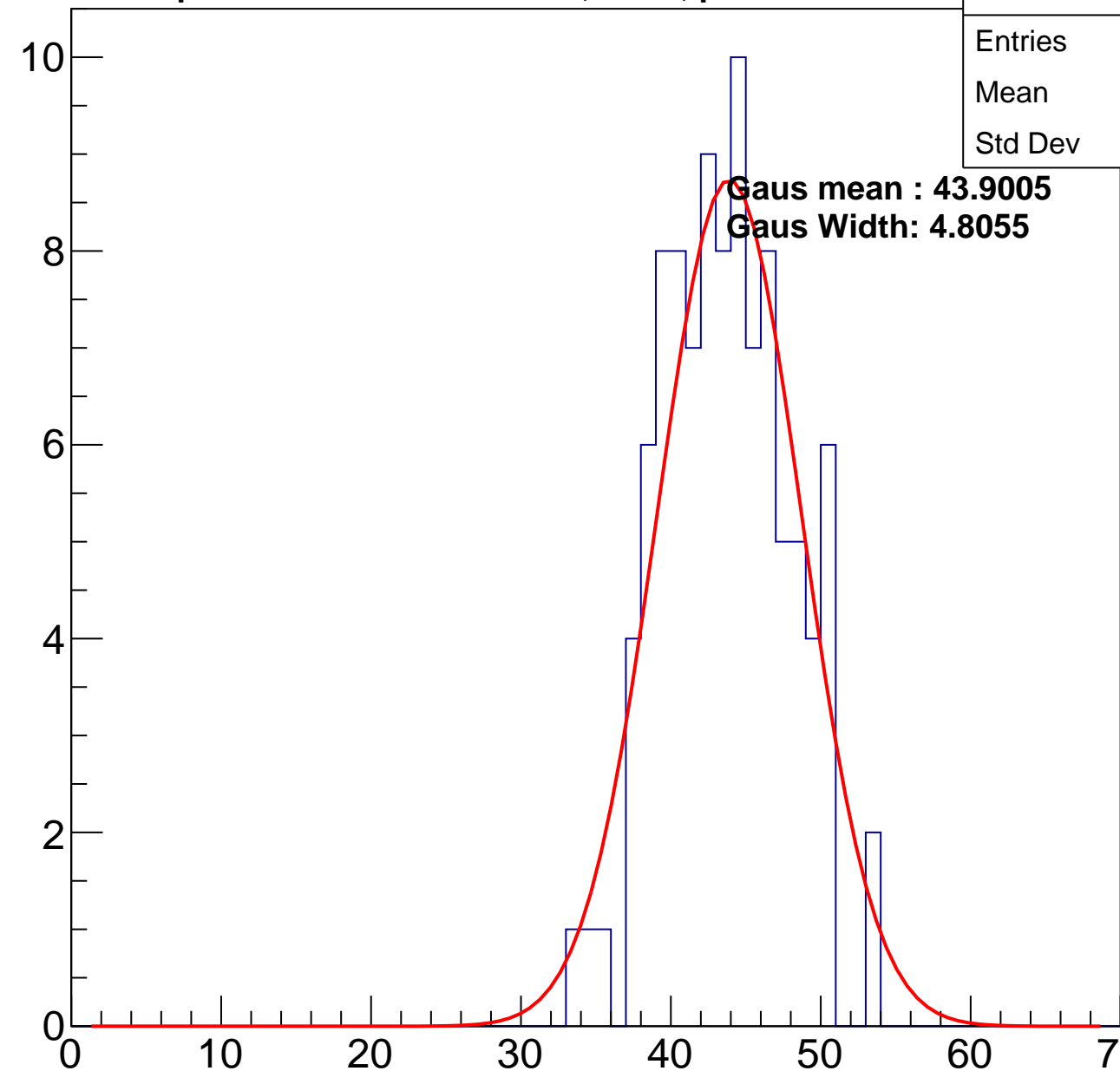
Gaus mean : 43.9005
Gaus Width: 4.8055

Entry

10
8
6
4
2
0

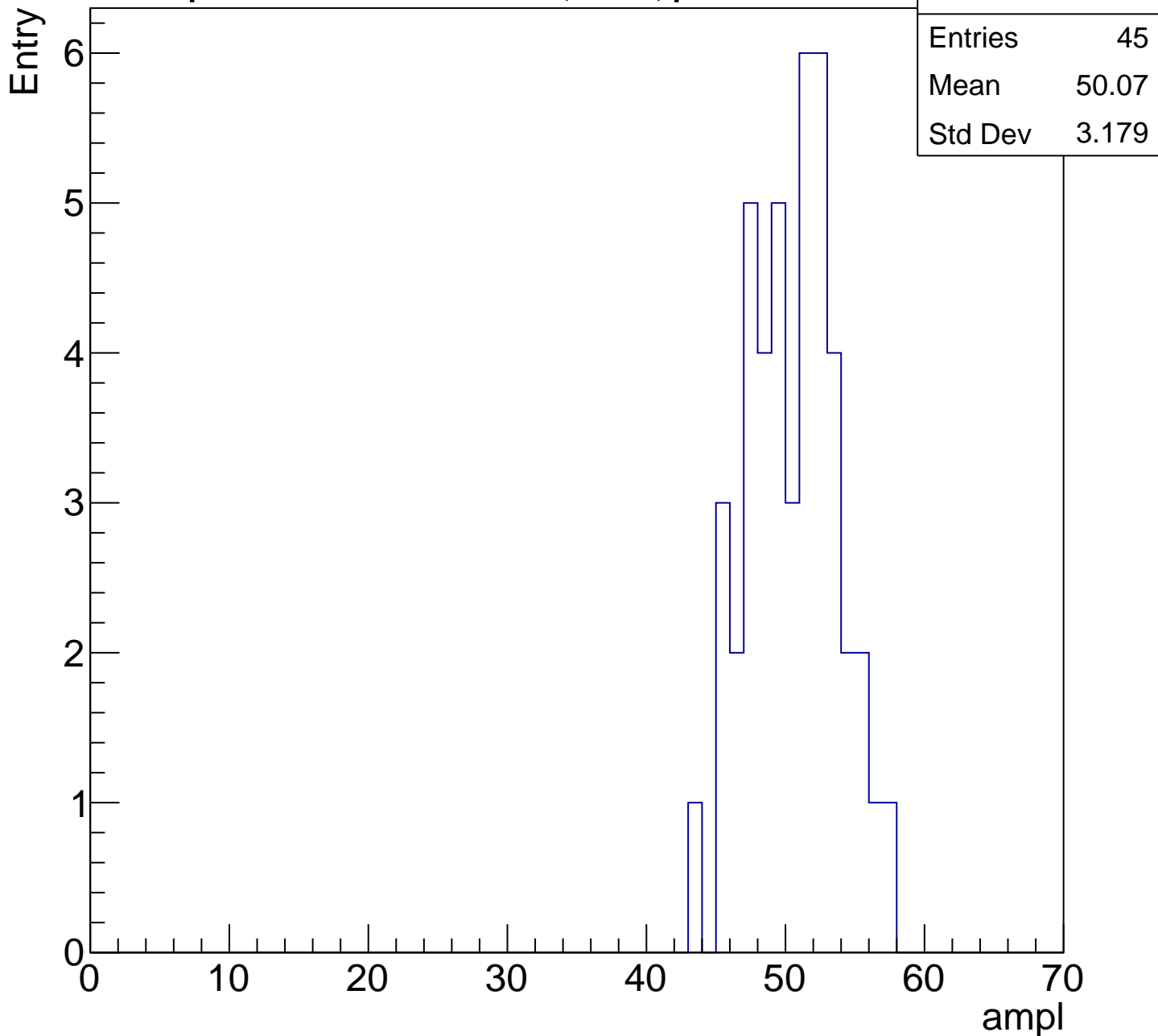
ampl

0 10 20 30 40 50 60 70



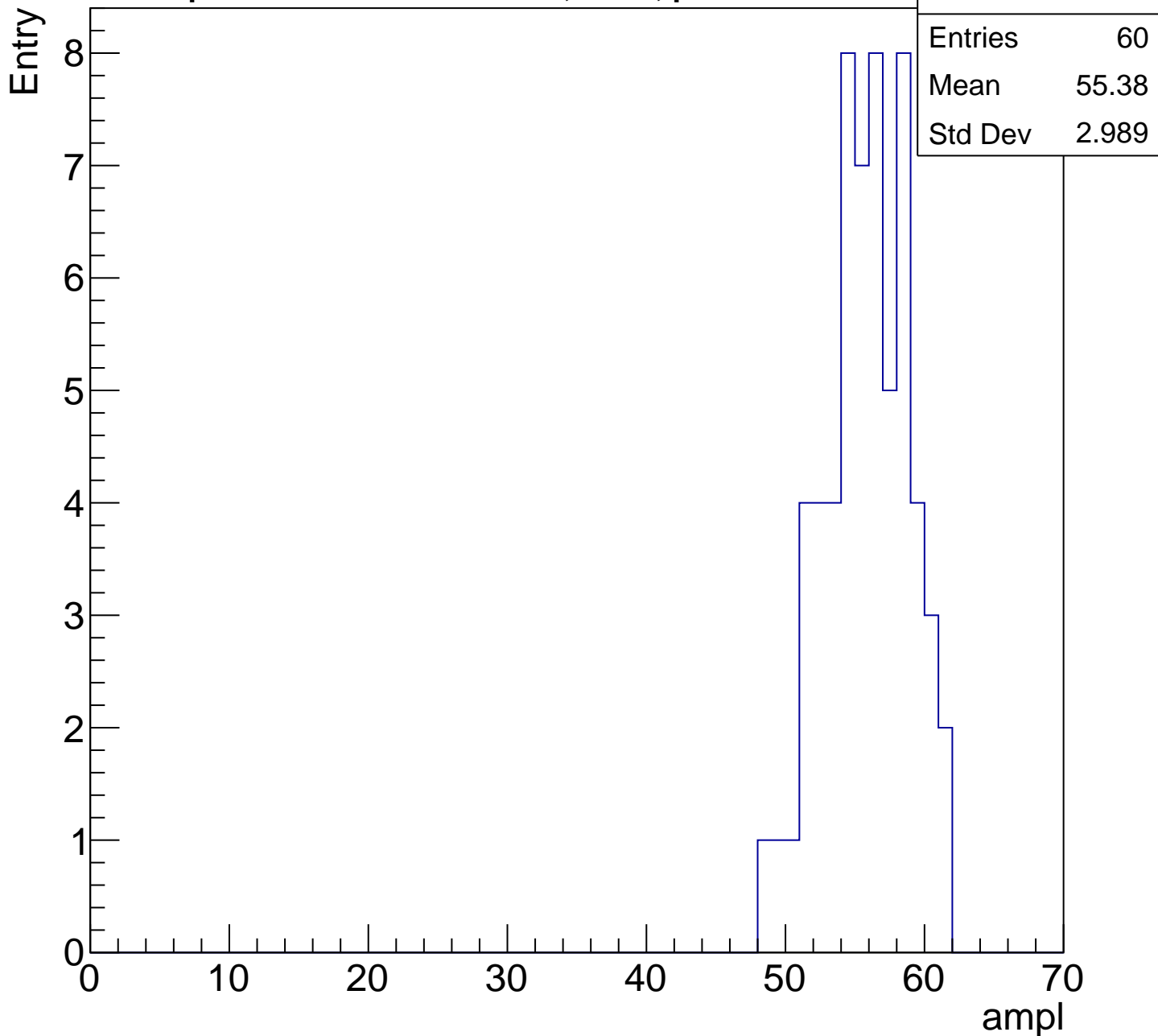
B1L101S, U6-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

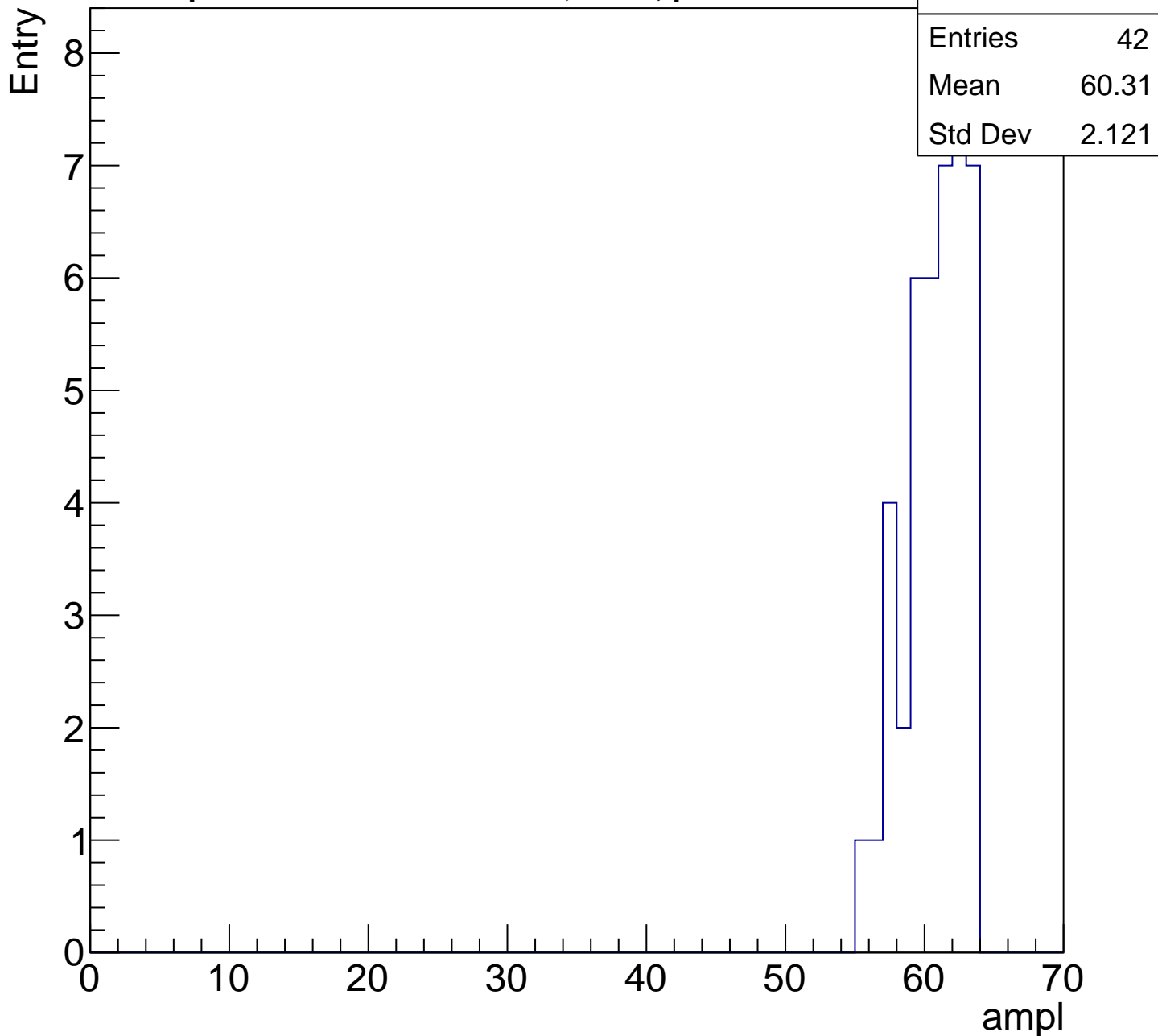
Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.31
Std Dev	2.121

ampl

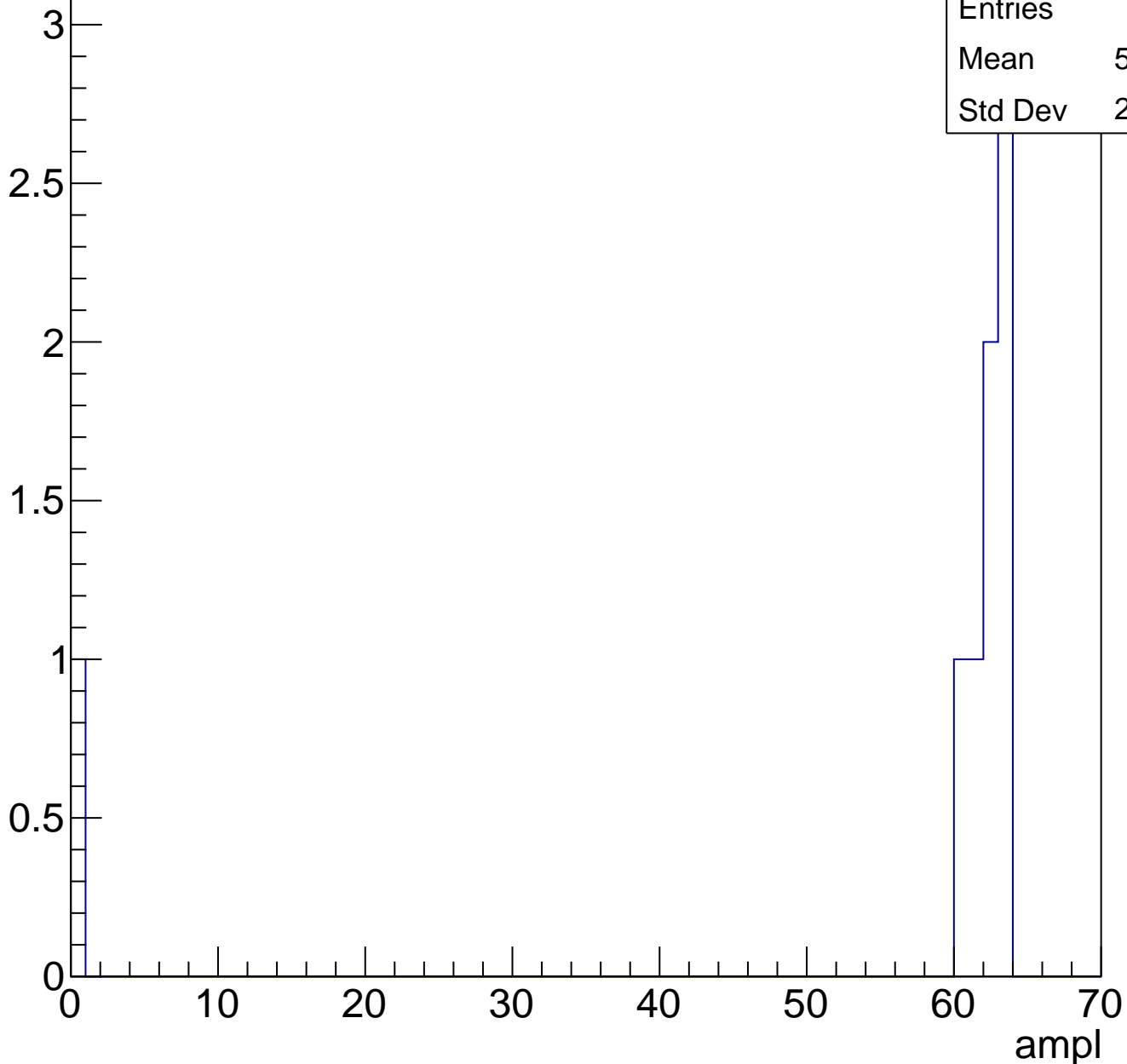
0 10 20 30 40 50 60 70



B1L101S, U6-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	54.25
Std Dev	20.53

B1L101S, U6-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch55, adc0

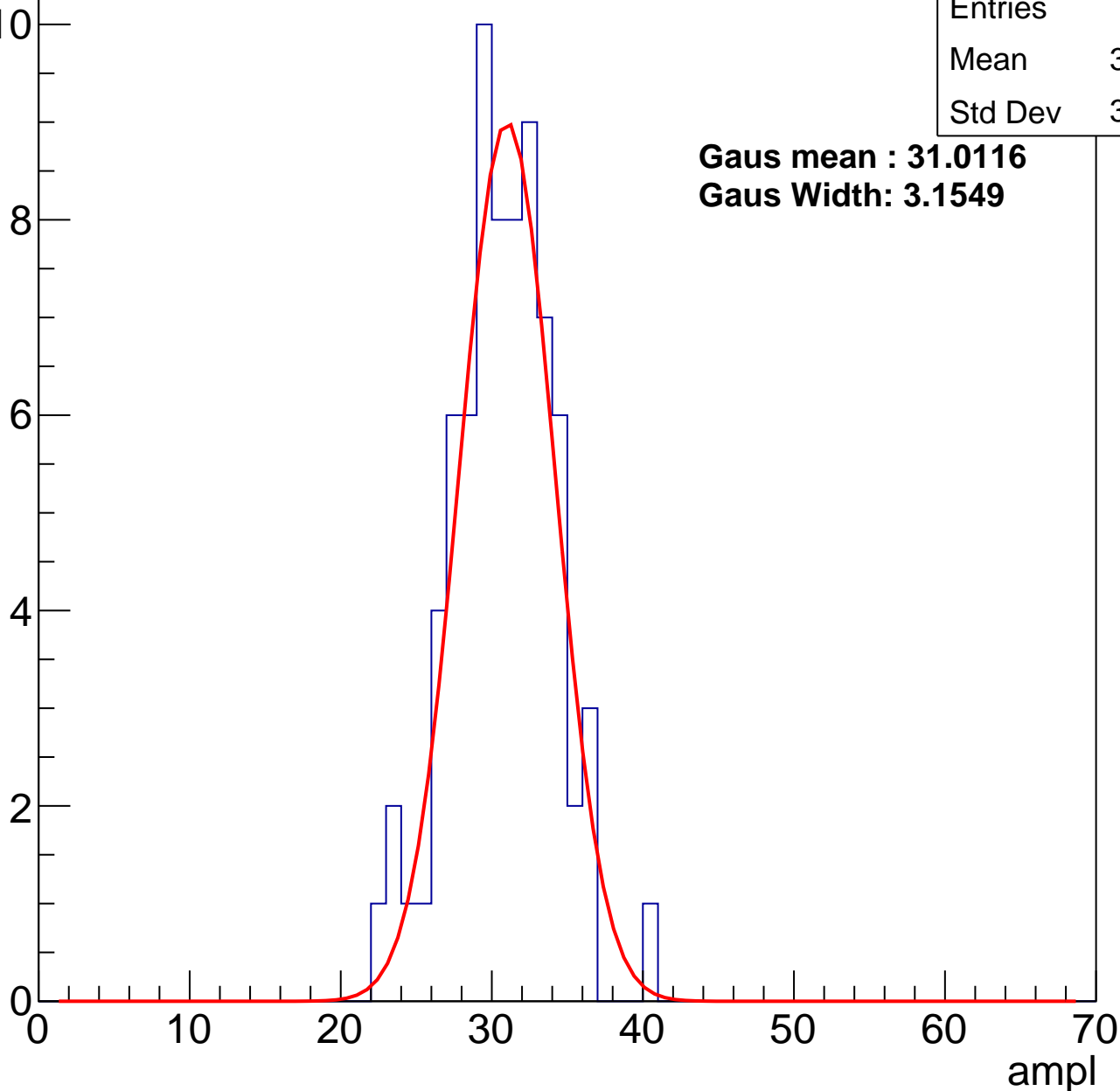
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.27
Std Dev	3.328

Gaus mean : 31.0116

Gaus Width: 3.1549



B1L101S, U6-ch55, adc1

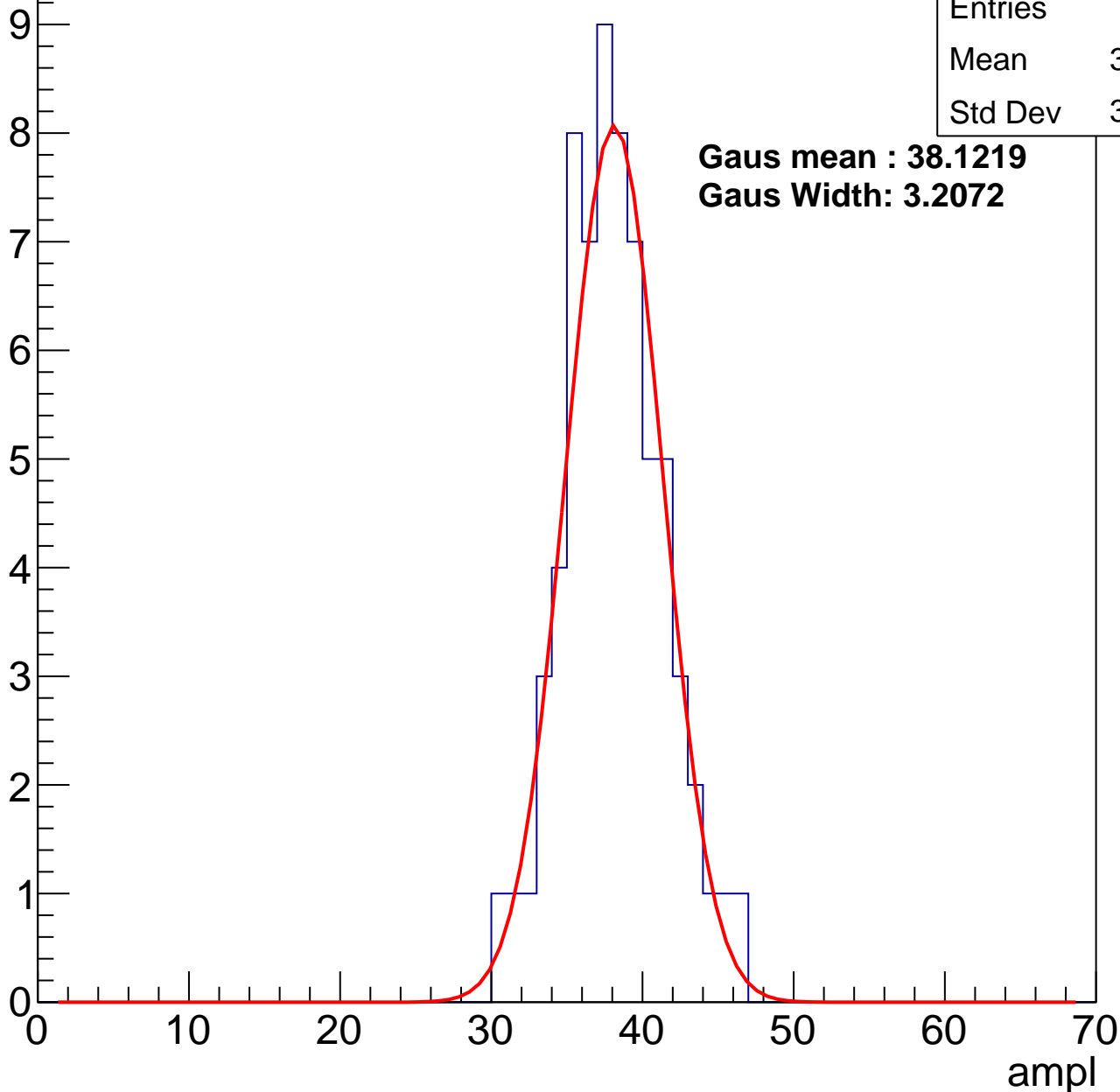
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.64
Std Dev	3.249

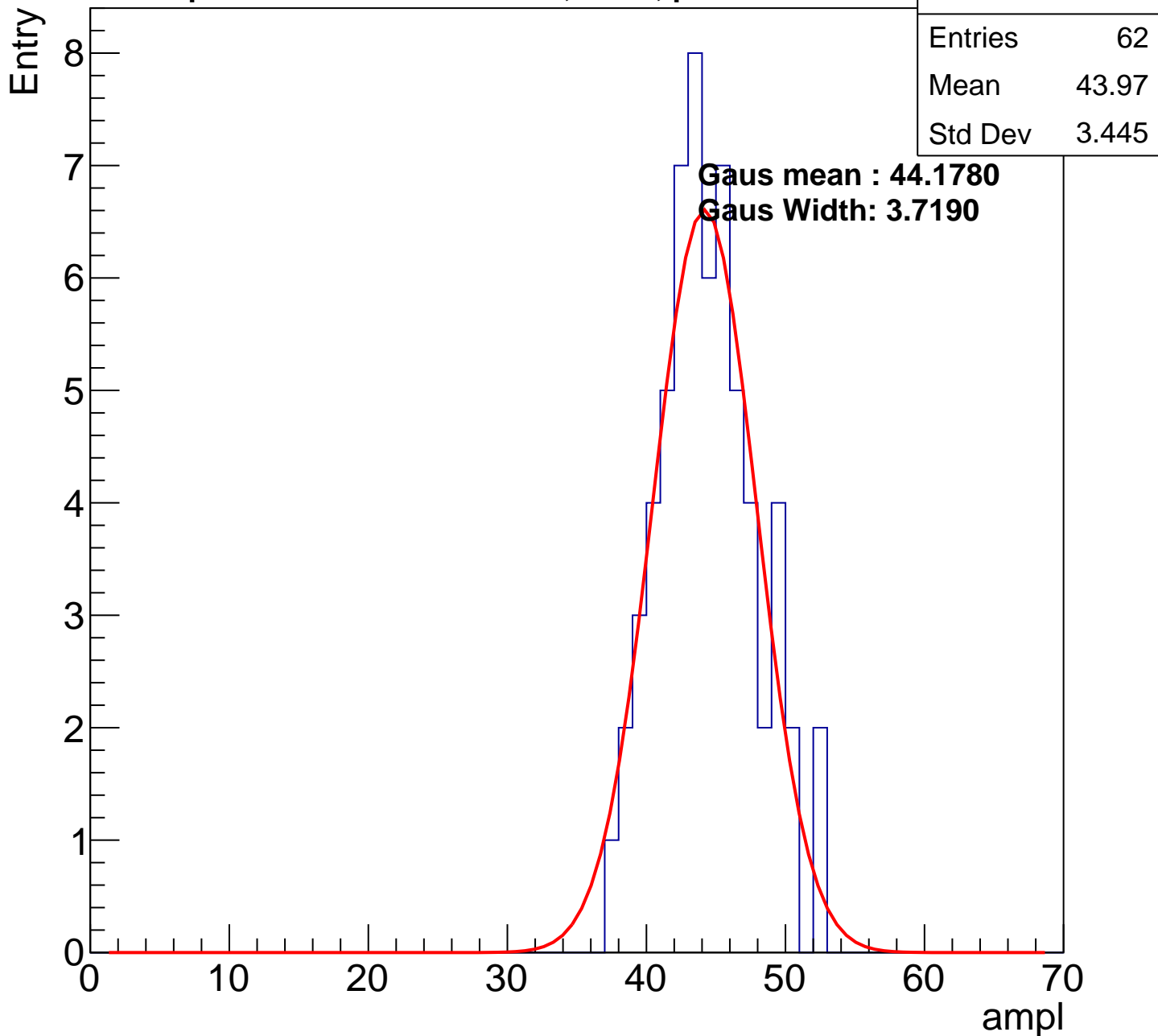
Gaus mean : 38.1219

Gaus Width: 3.2072



B1L101S, U6-ch55, adc2

calib_packv5_042523_0143.root, FC#0, port D2

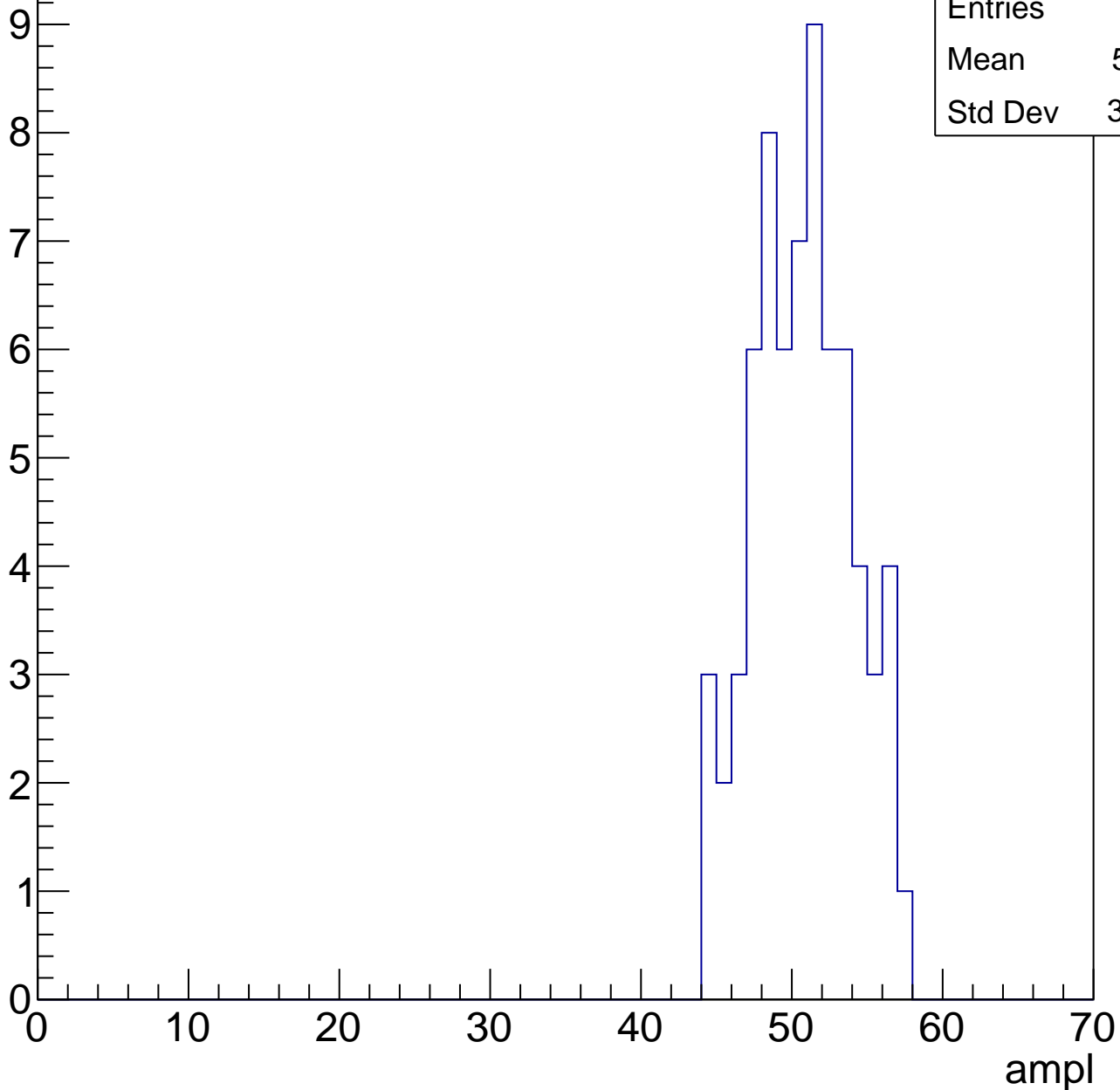


B1L101S, U6-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

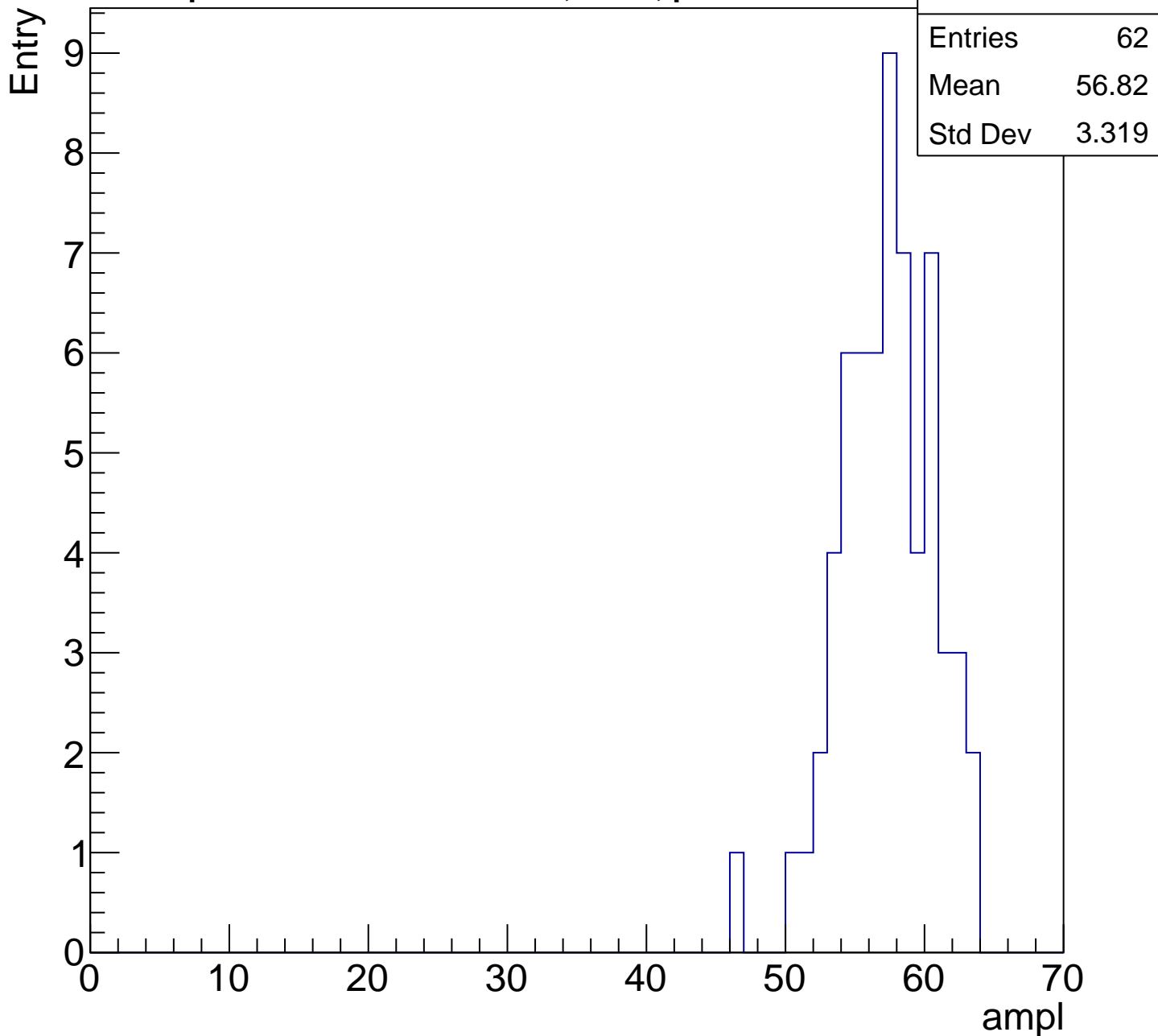
Entry

Entries	68
Mean	50.31
Std Dev	3.232



B1L101S, U6-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

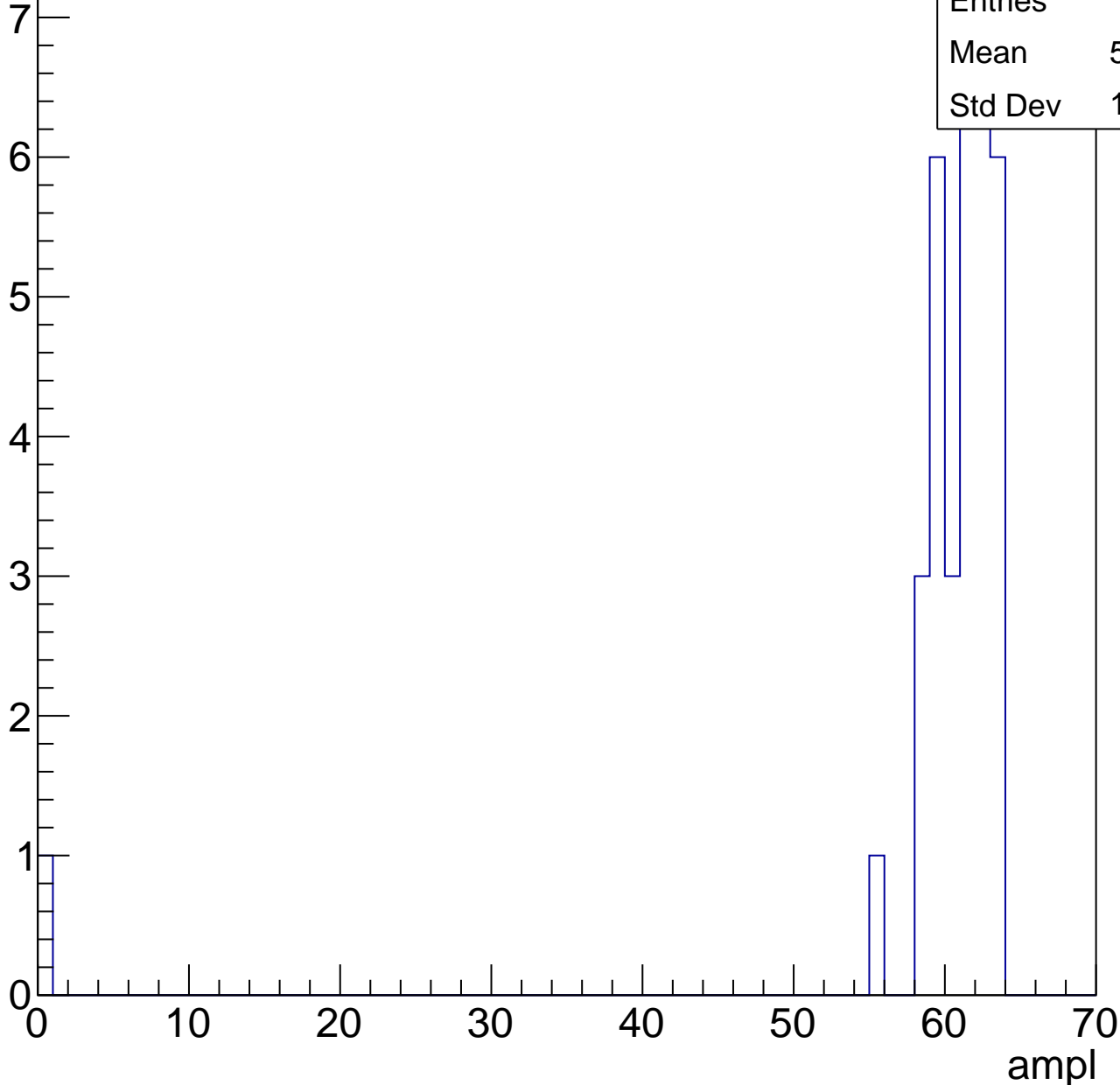


B1L101S, U6-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.88
Std Dev	10.42



B1L101S, U6-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch56, adc0

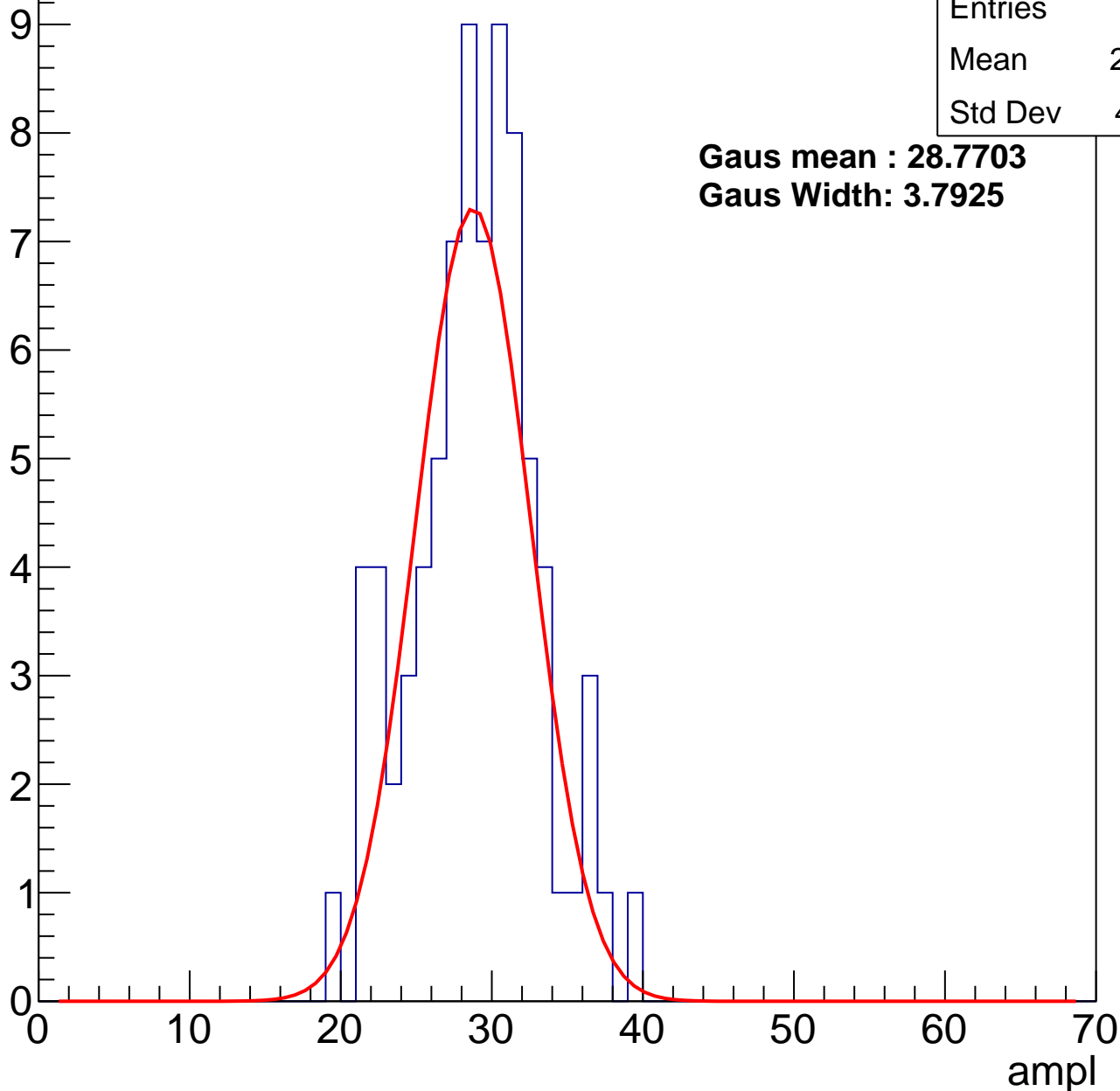
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.43
Std Dev	4.121

Gaus mean : 28.7703

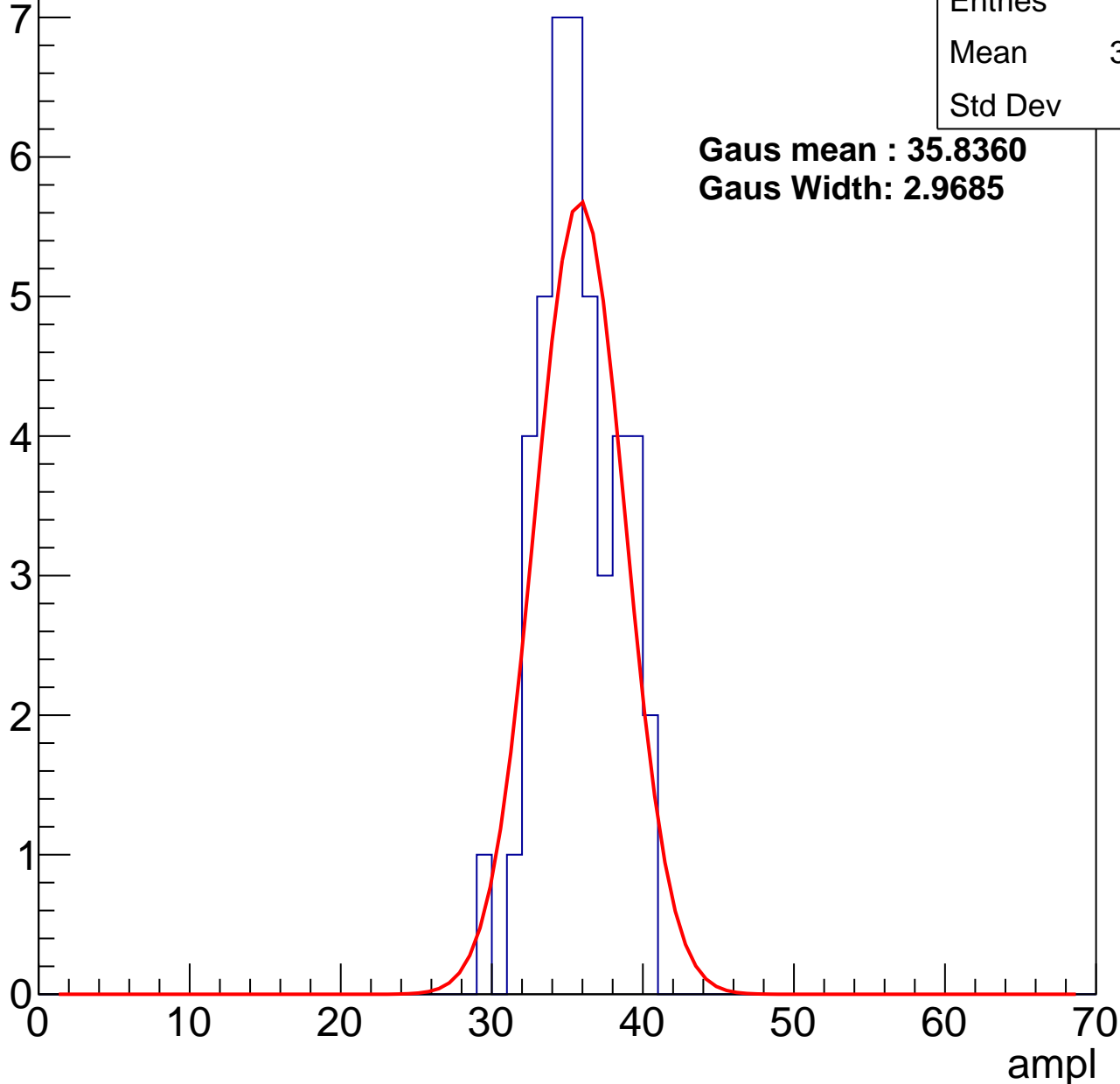
Gaus Width: 3.7925



B1L101S, U6-ch56, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	43
Mean	35.23
Std Dev	2.55

B1L101S, U6-ch56, adc2

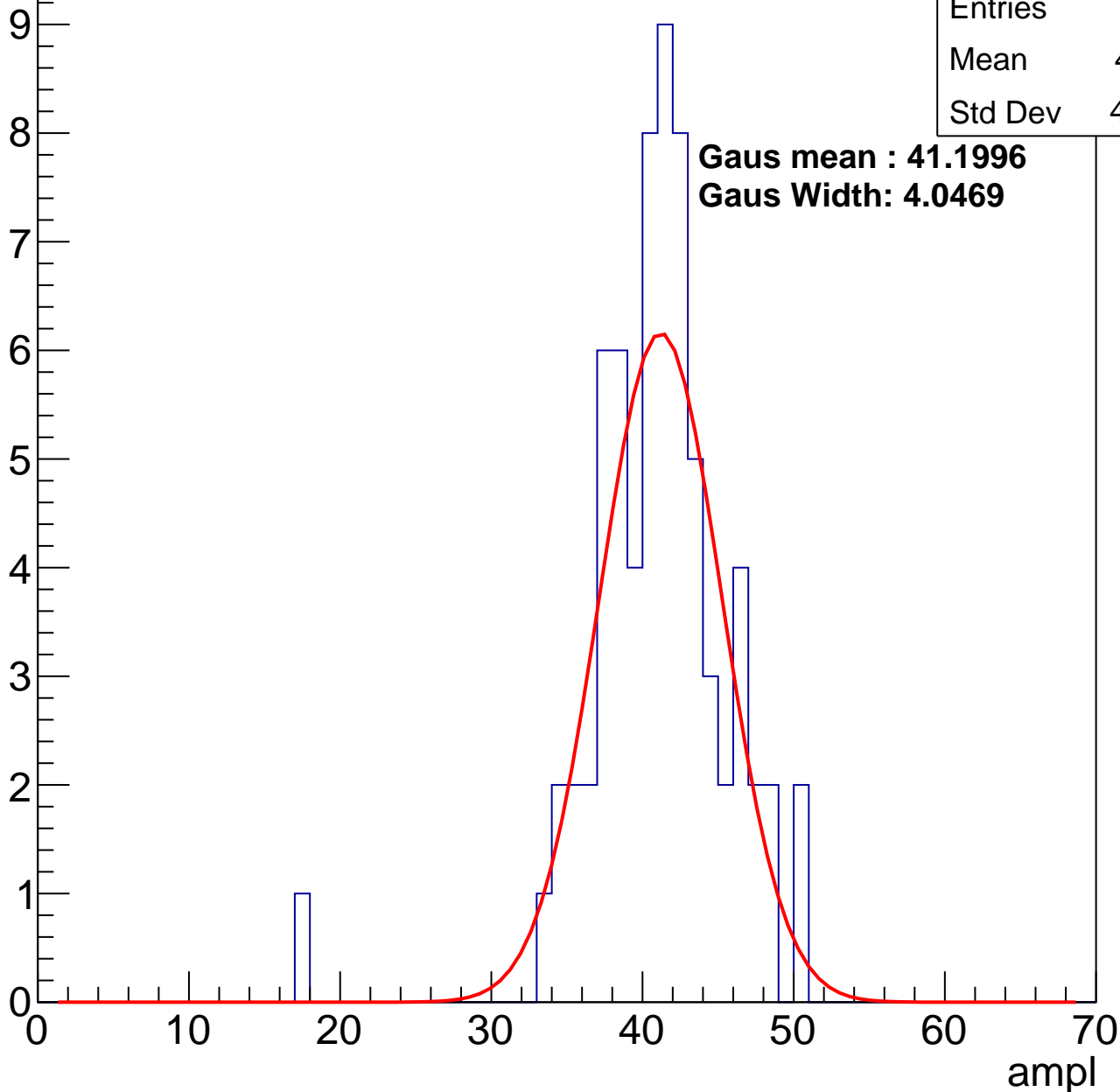
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	40.61
Std Dev	4.728

Gaus mean : 41.1996

Gaus Width: 4.0469

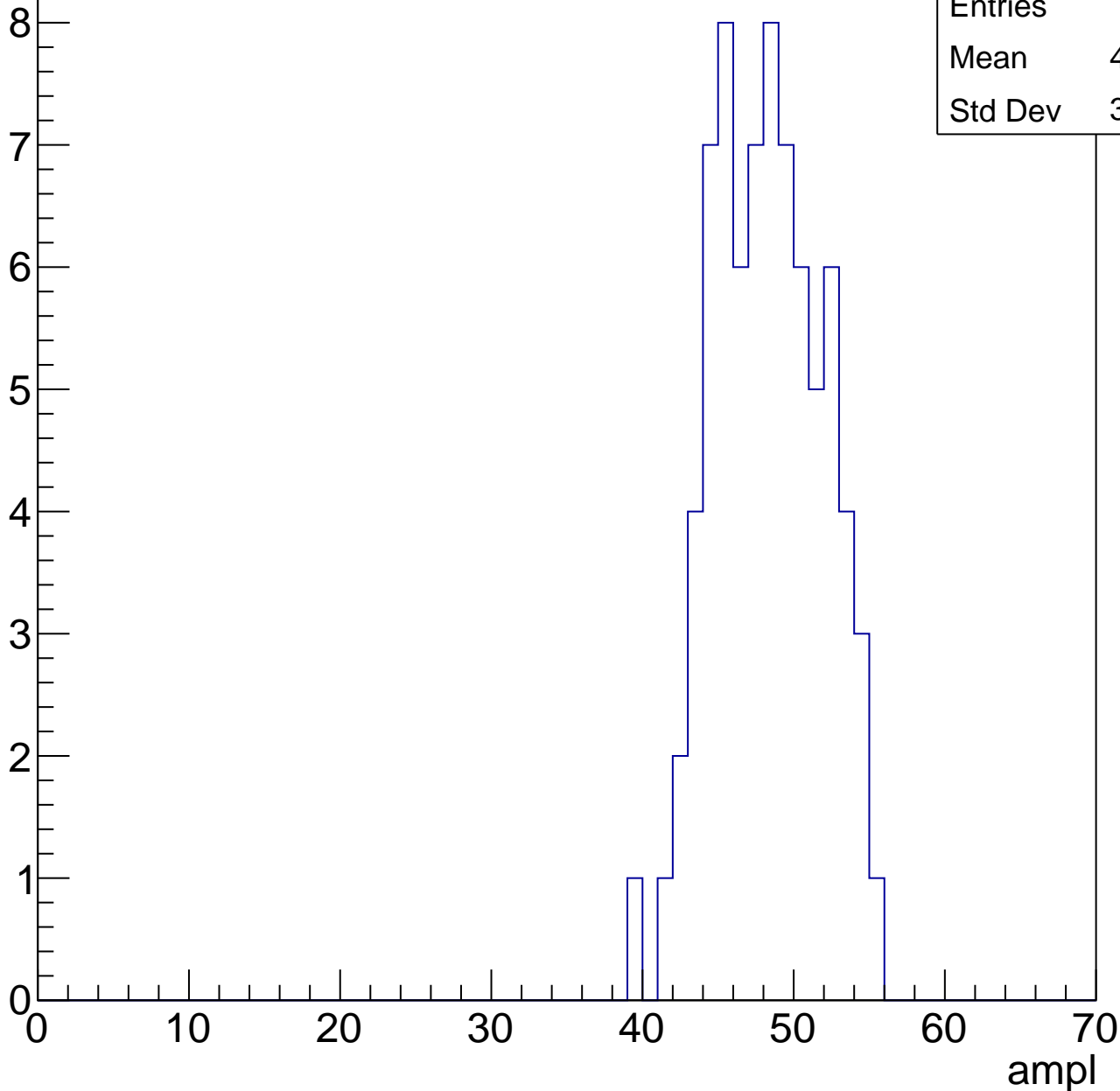


B1L101S, U6-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

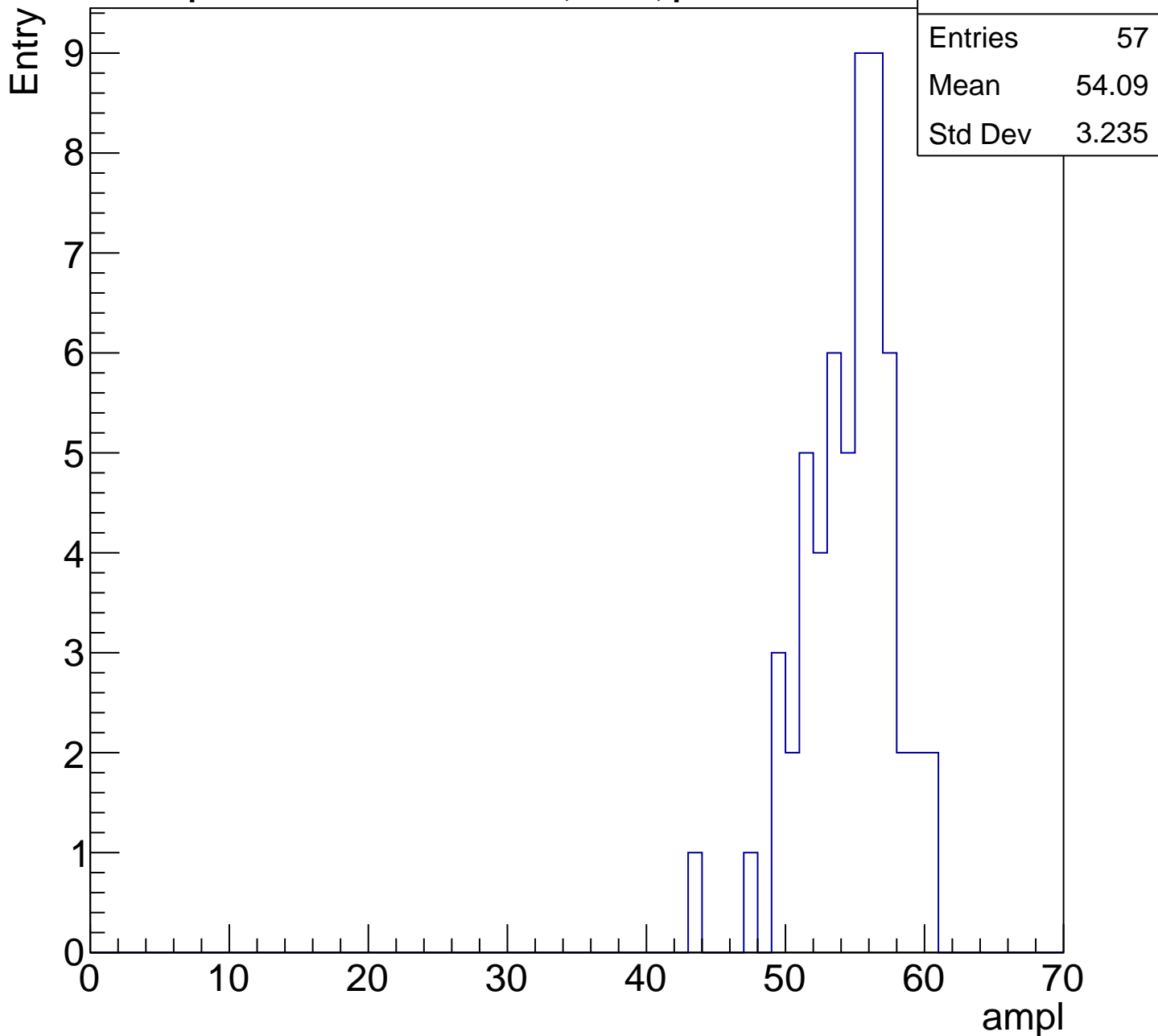
Entry

Entries	76
Mean	47.79
Std Dev	3.522



B1L101S, U6-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

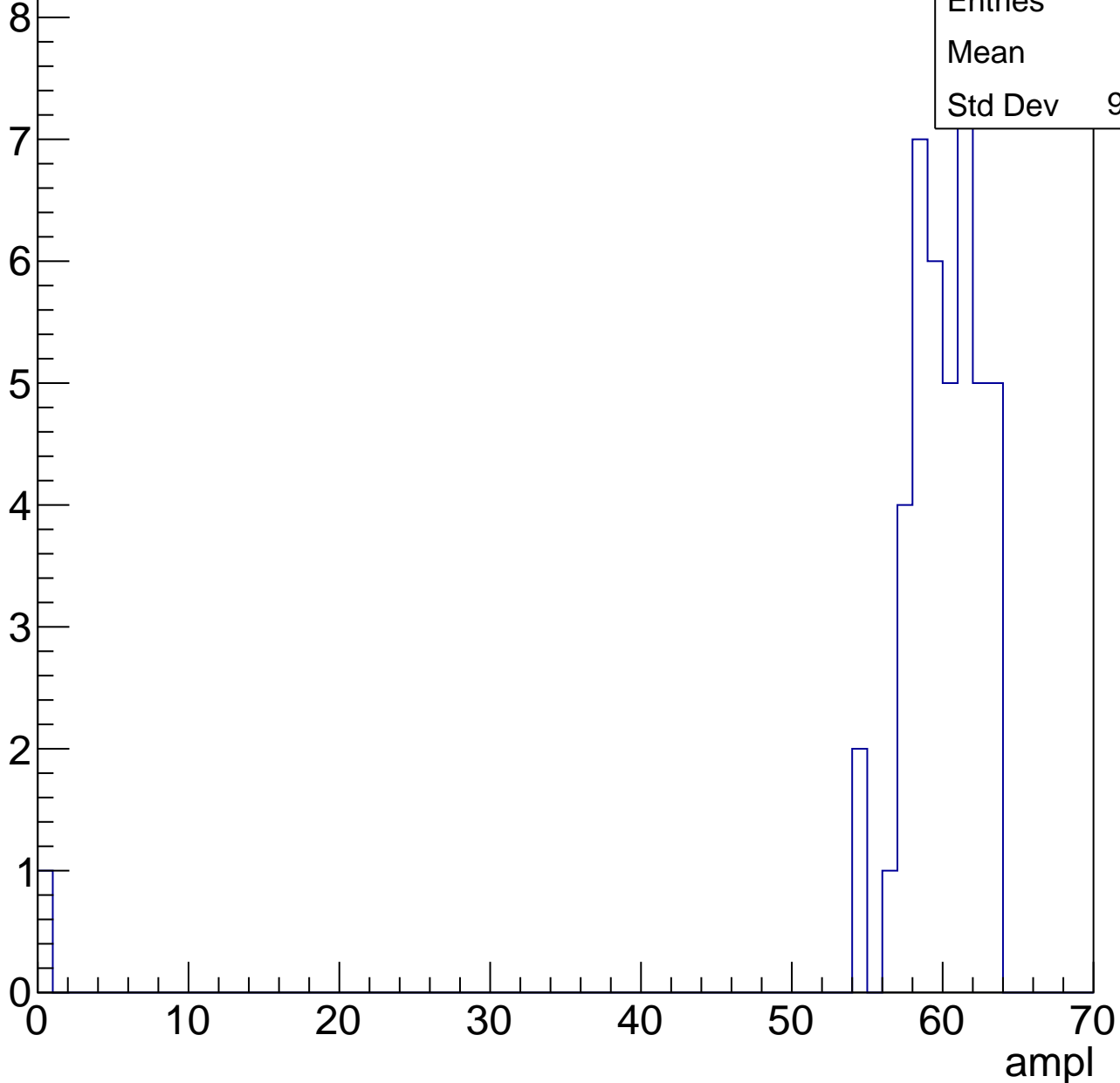


B1L101S, U6-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.3
Std Dev	9.174

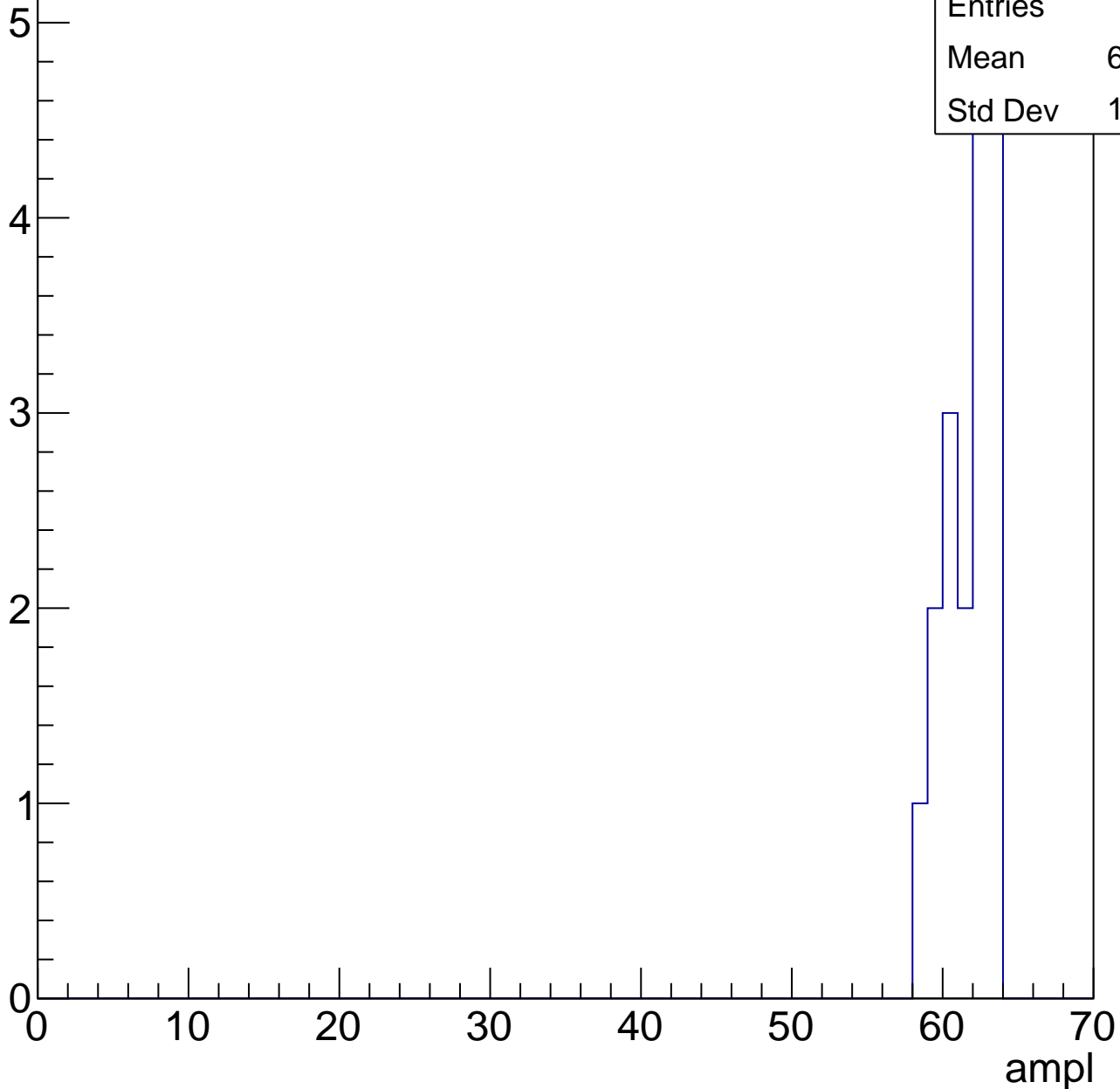


B1L101S, U6-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.28
Std Dev	1.557



B1L101S, U6-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch57, adc0

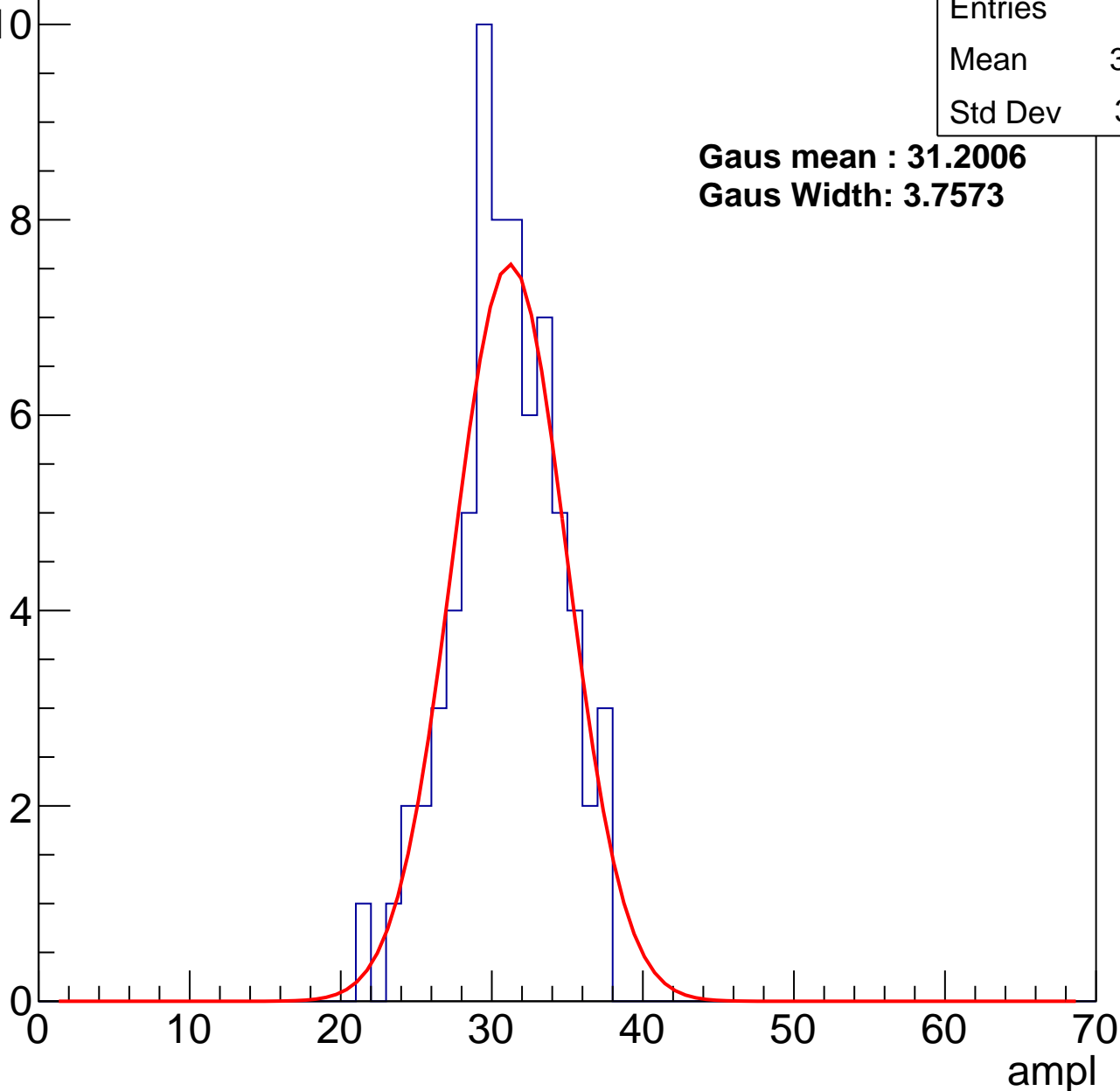
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.45
Std Dev	3.451

Gaus mean : 31.2006

Gaus Width: 3.7573



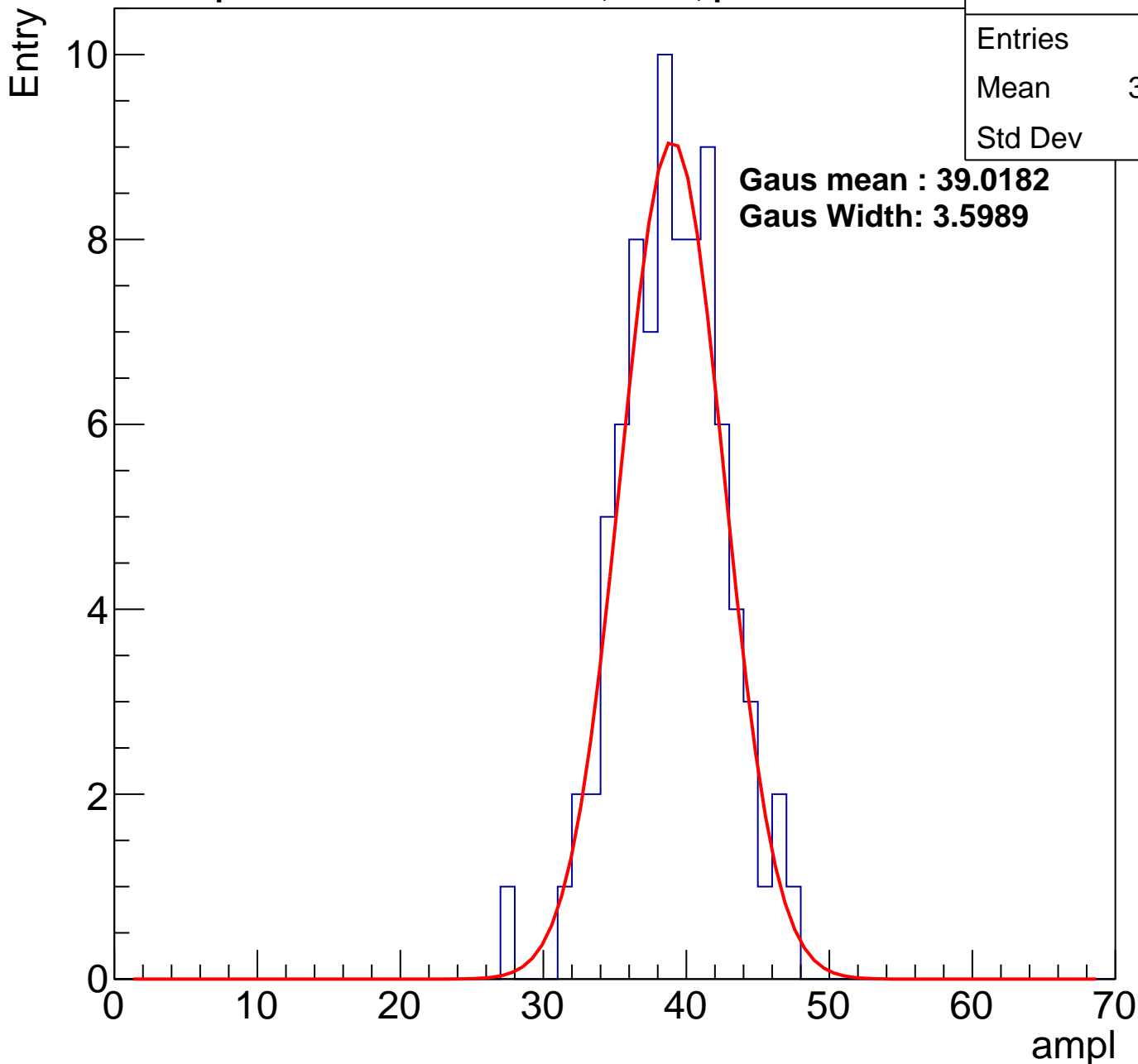
B1L101S, U6-ch57, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	38.52
Std Dev	3.66

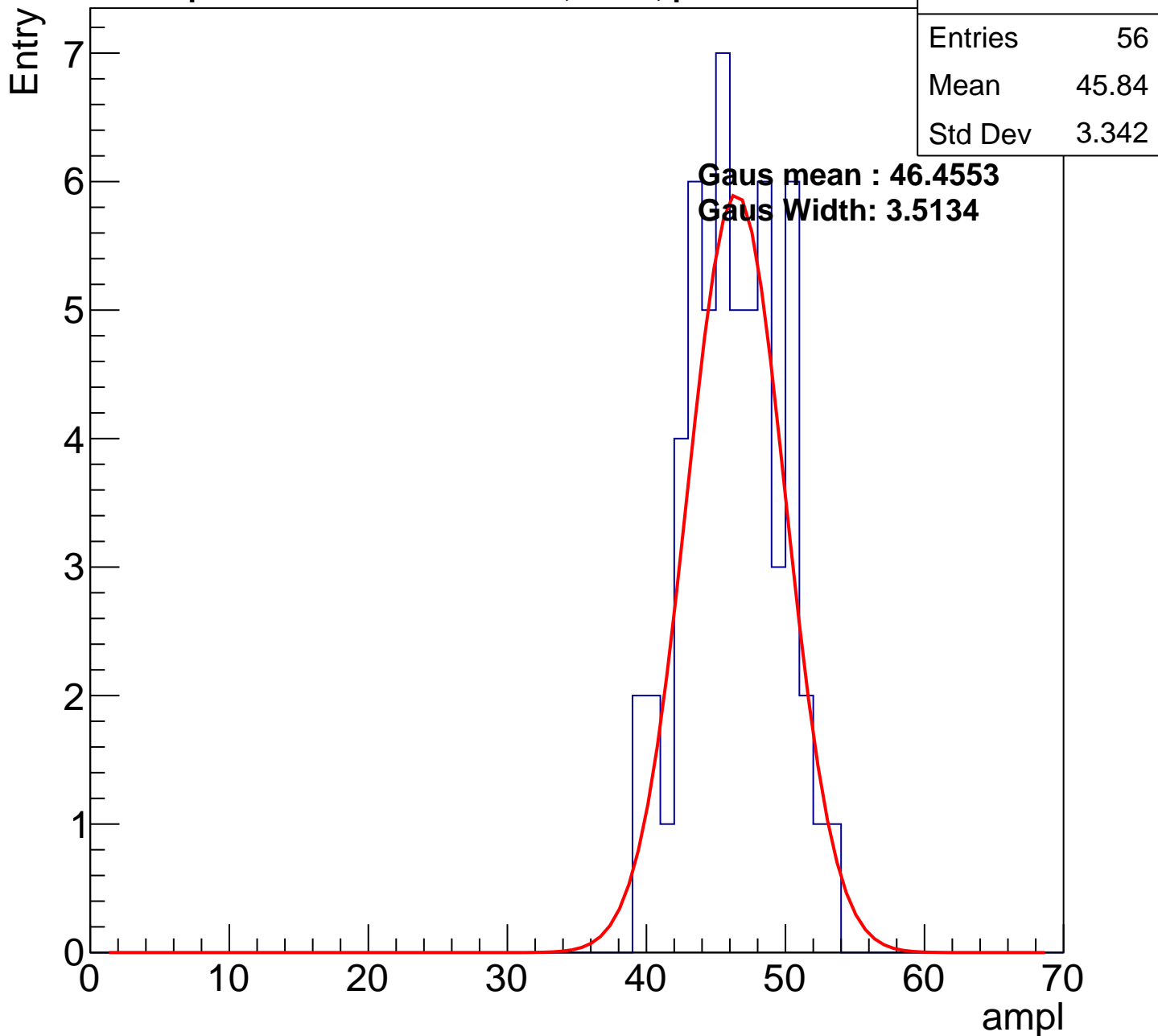
Gaus mean : 39.0182

Gaus Width: 3.5989



B1L101S, U6-ch57, adc2

calib_packv5_042523_0143.root, FC#0, port D2

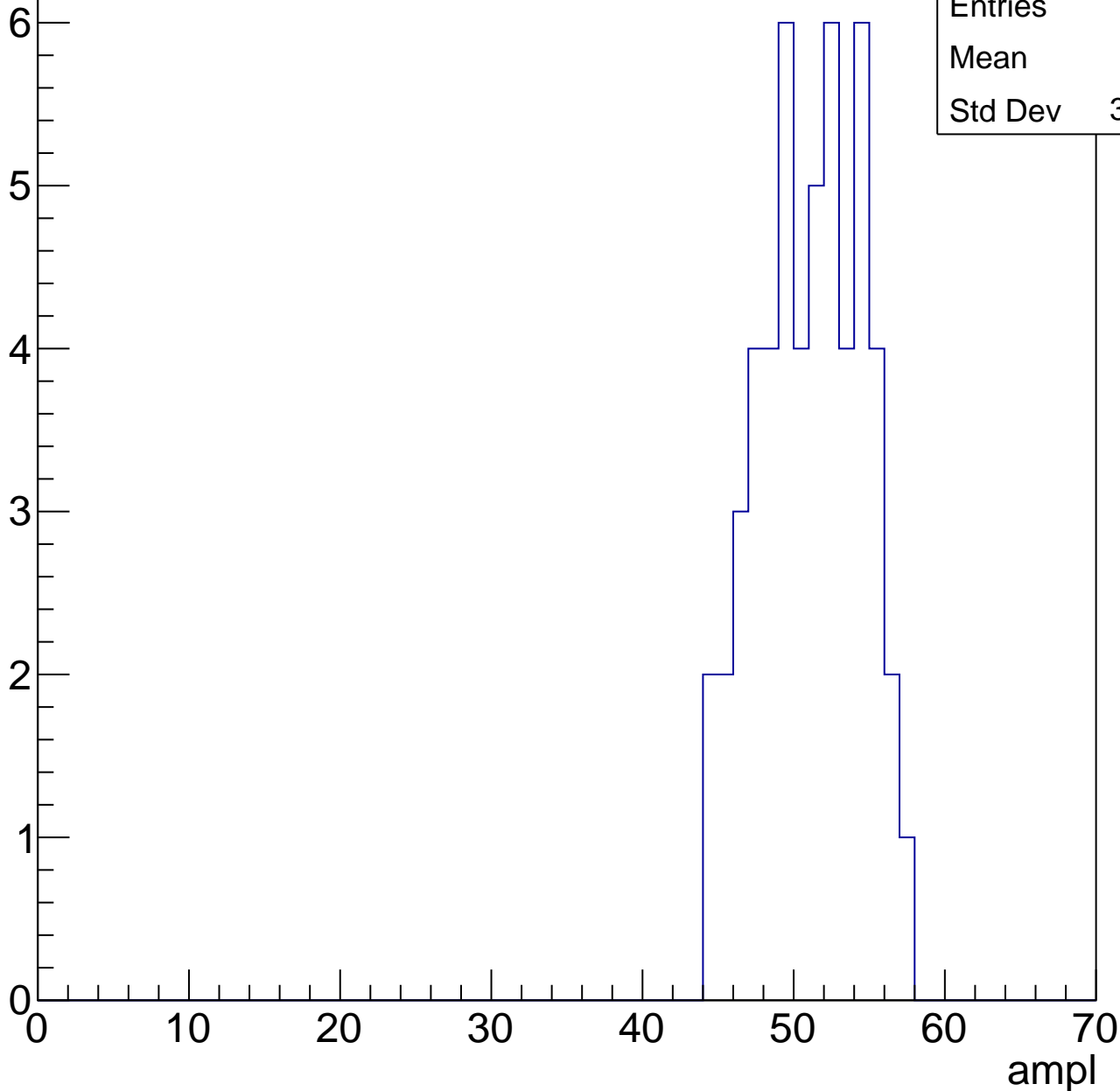


B1L101S, U6-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

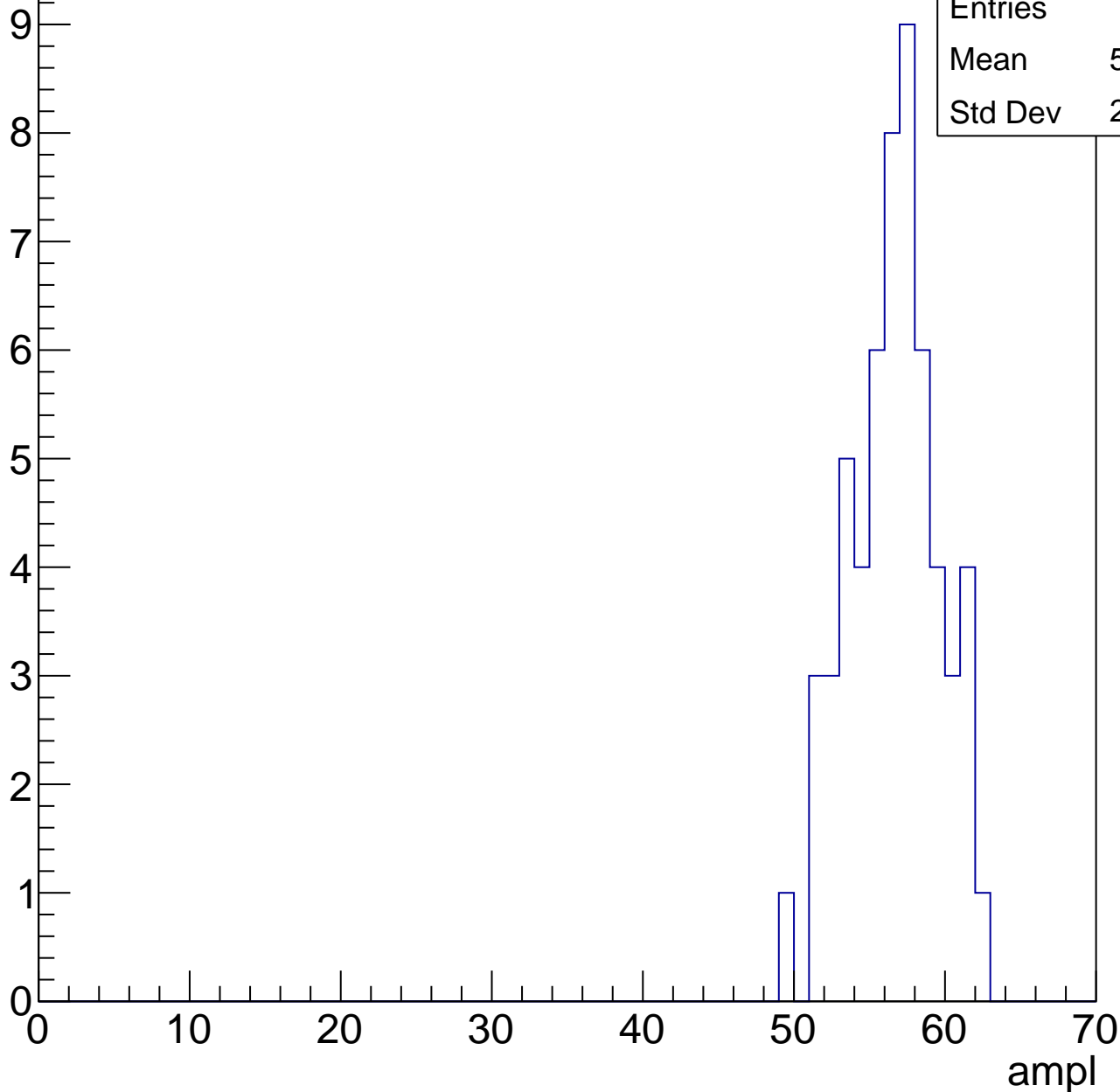
Entries	53
Mean	50.6
Std Dev	3.338



B1L101S, U6-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



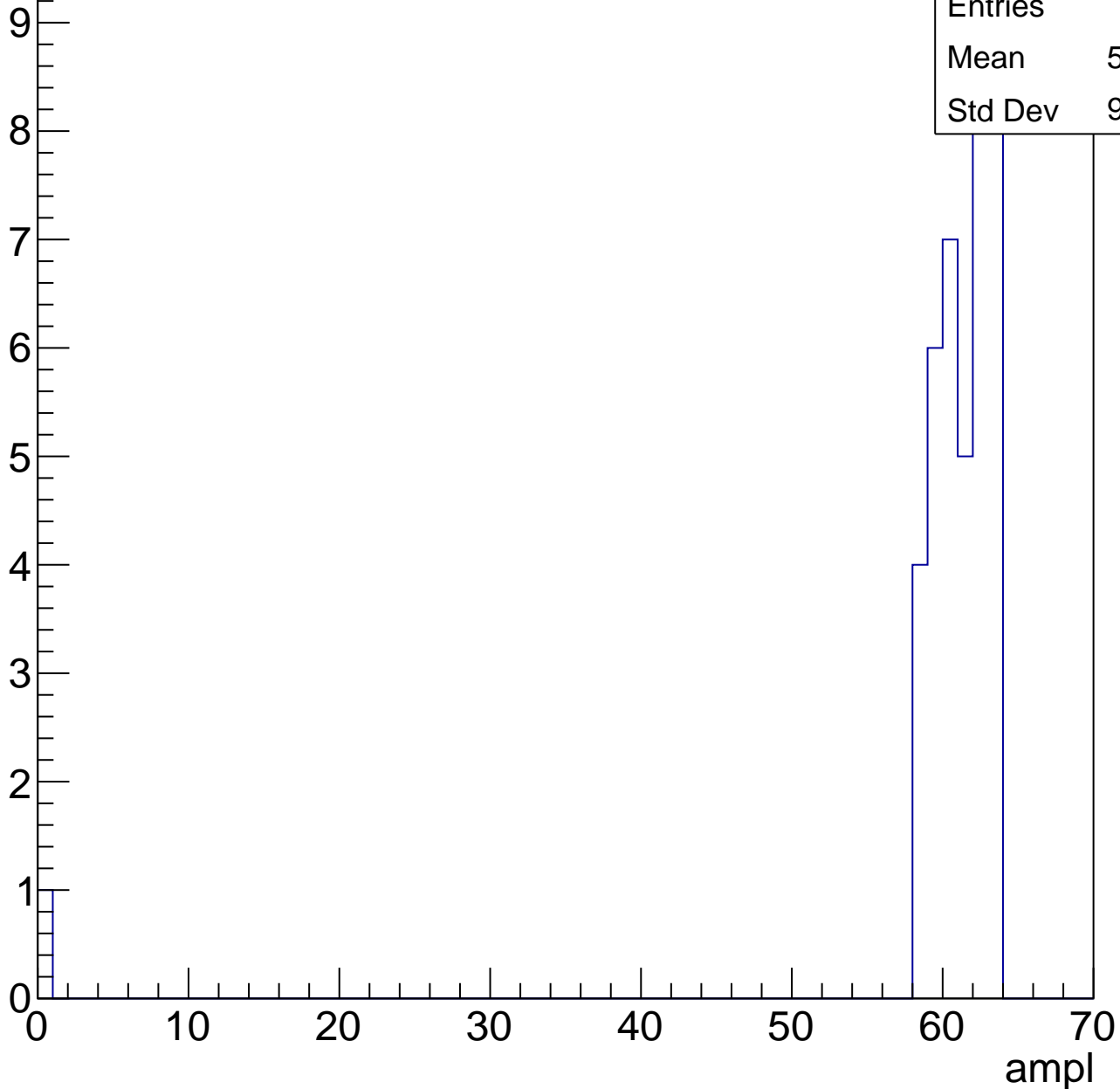
Entries	57
Mean	56.14
Std Dev	2.935

B1L101S, U6-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.33
Std Dev	9.639



B1L101S, U6-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch58, adc0

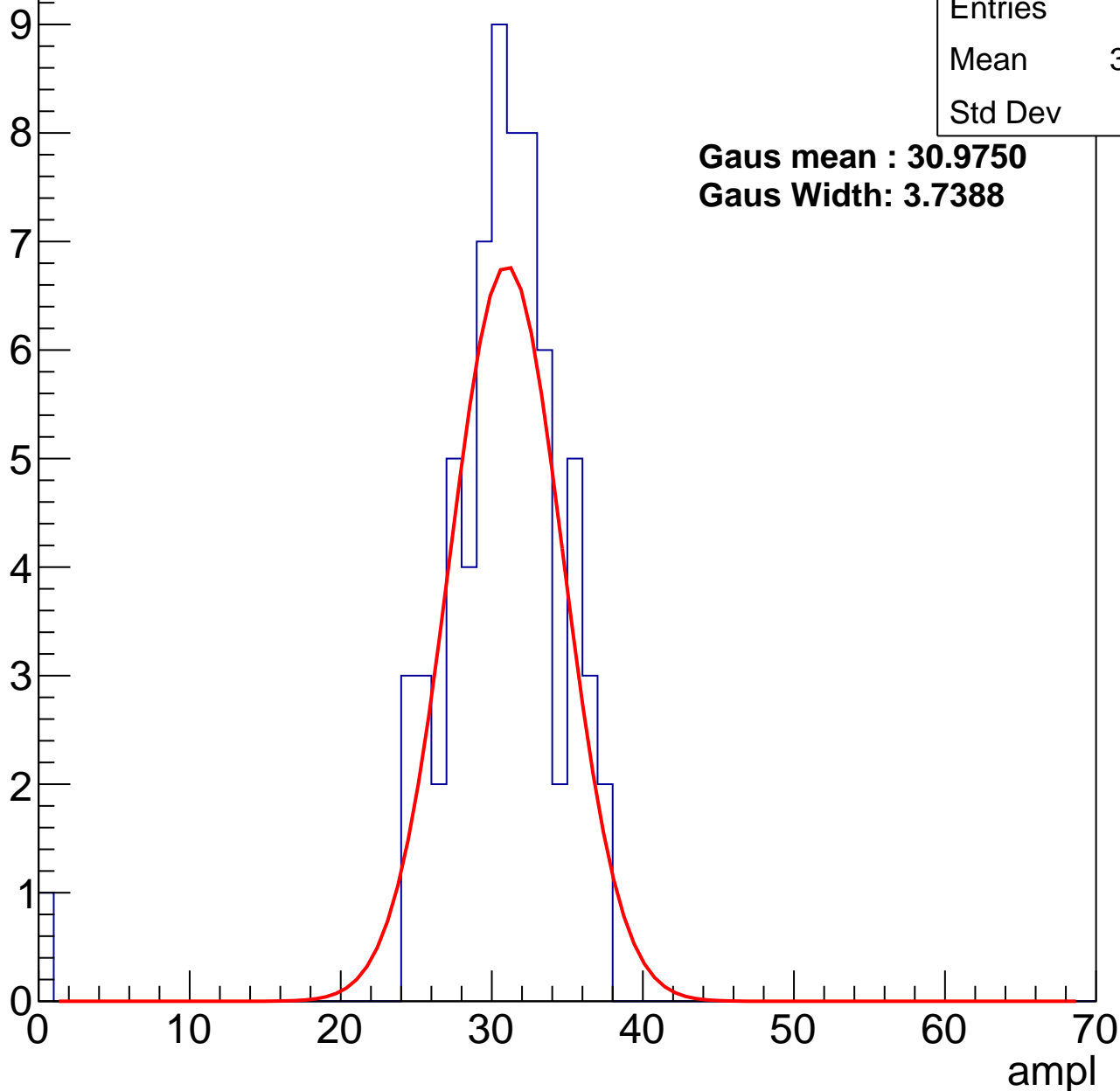
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.09
Std Dev	4.91

Gaus mean : 30.9750

Gaus Width: 3.7388



B1L101S, U6-ch58, adc1

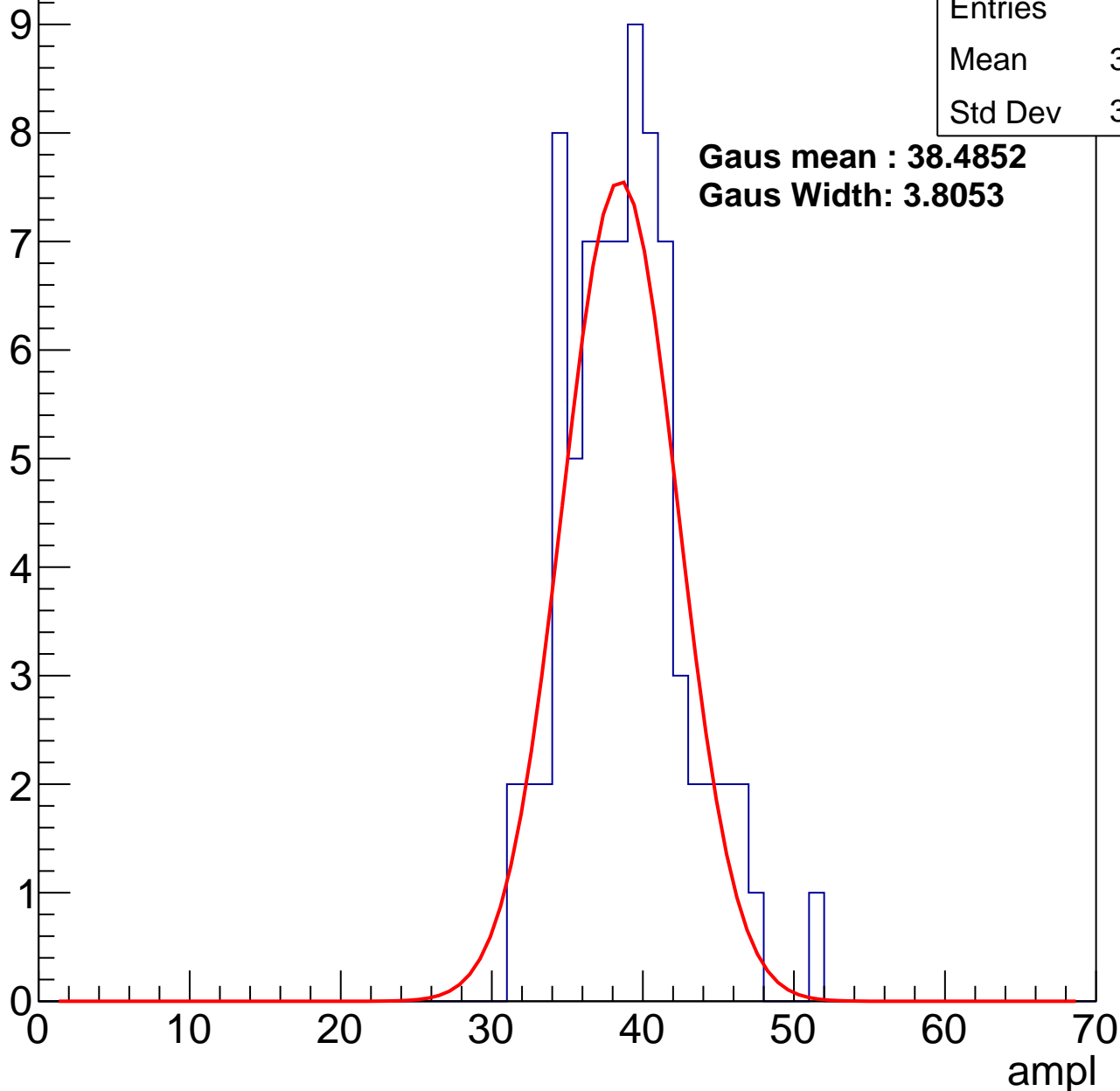
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	38.36
Std Dev	3.894

Gaus mean : 38.4852

Gaus Width: 3.8053



B1L101S, U6-ch58, adc2

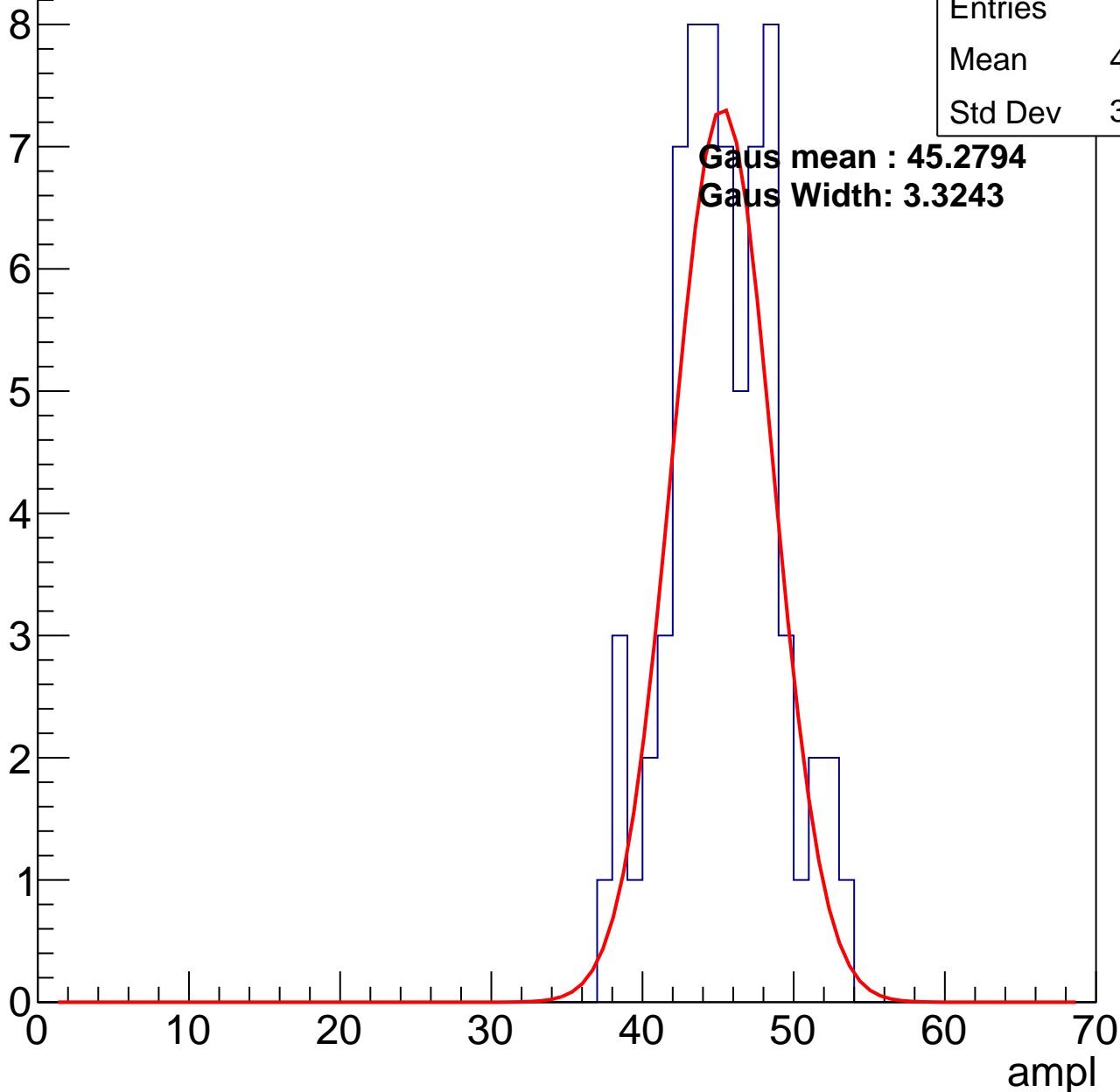
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.88
Std Dev	3.529

Gaus mean : 45.2794

Gaus Width: 3.3243

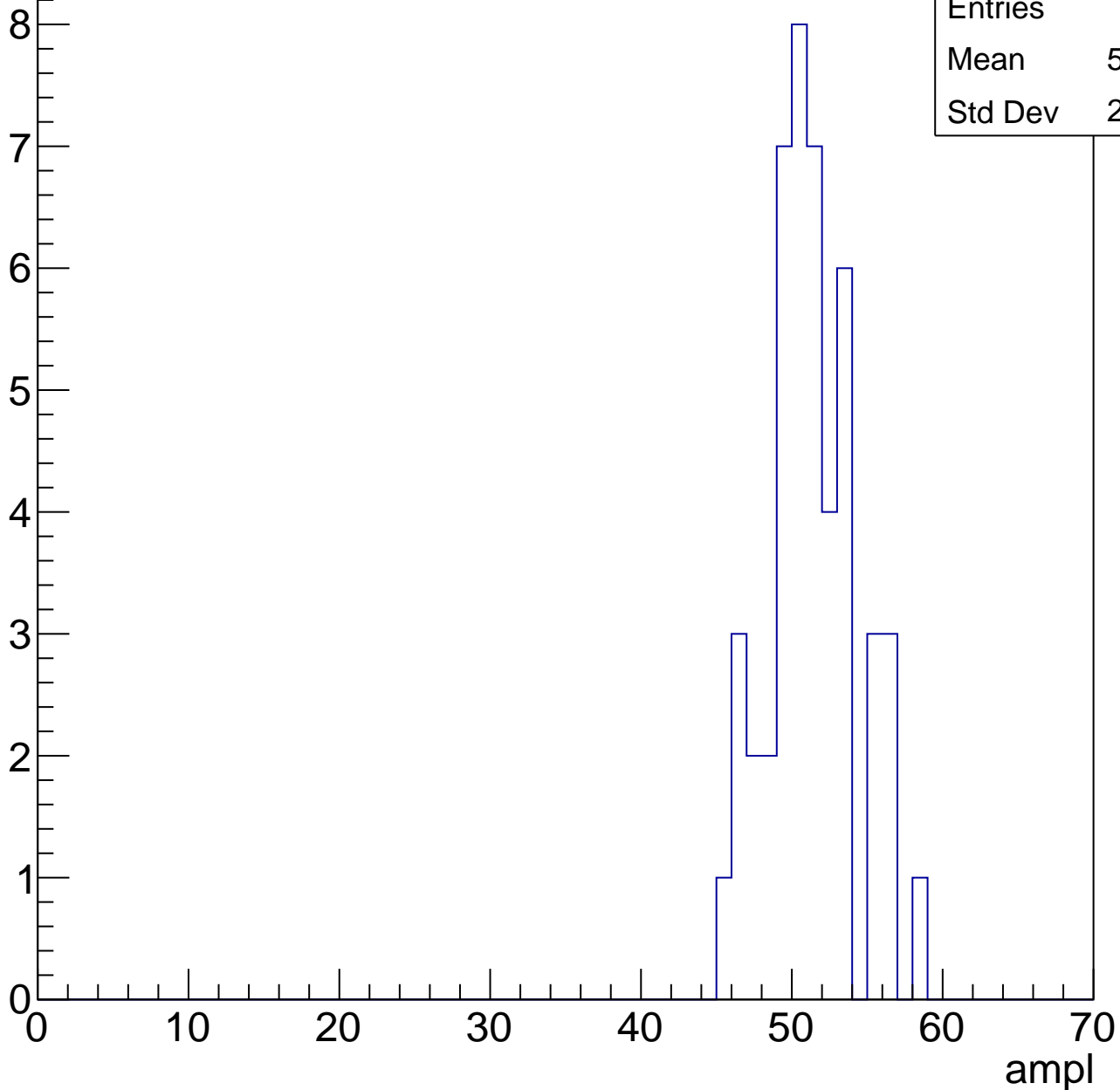


B1L101S, U6-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	50.85
Std Dev	2.903

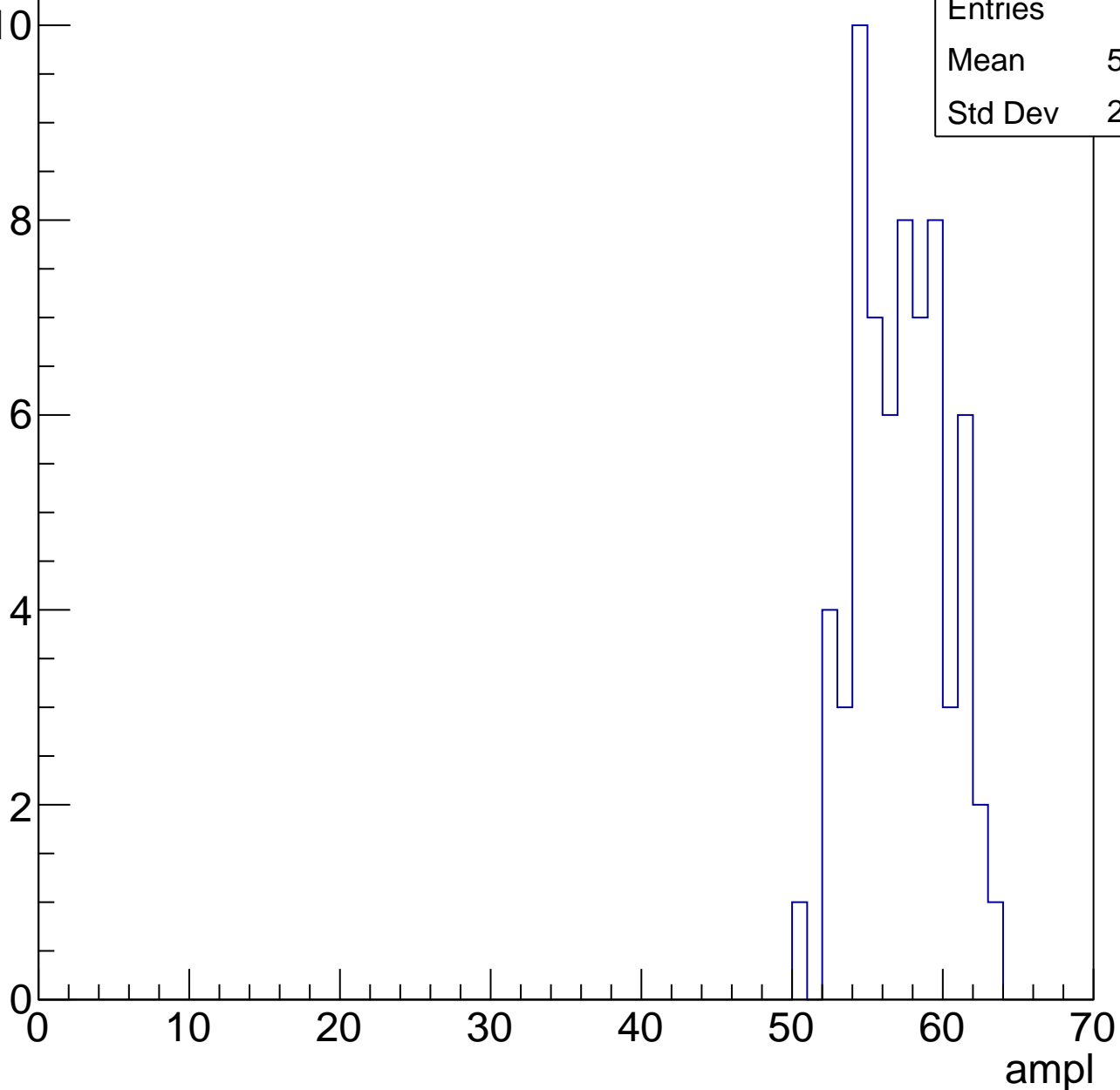


B1L101S, U6-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	56.74
Std Dev	2.925

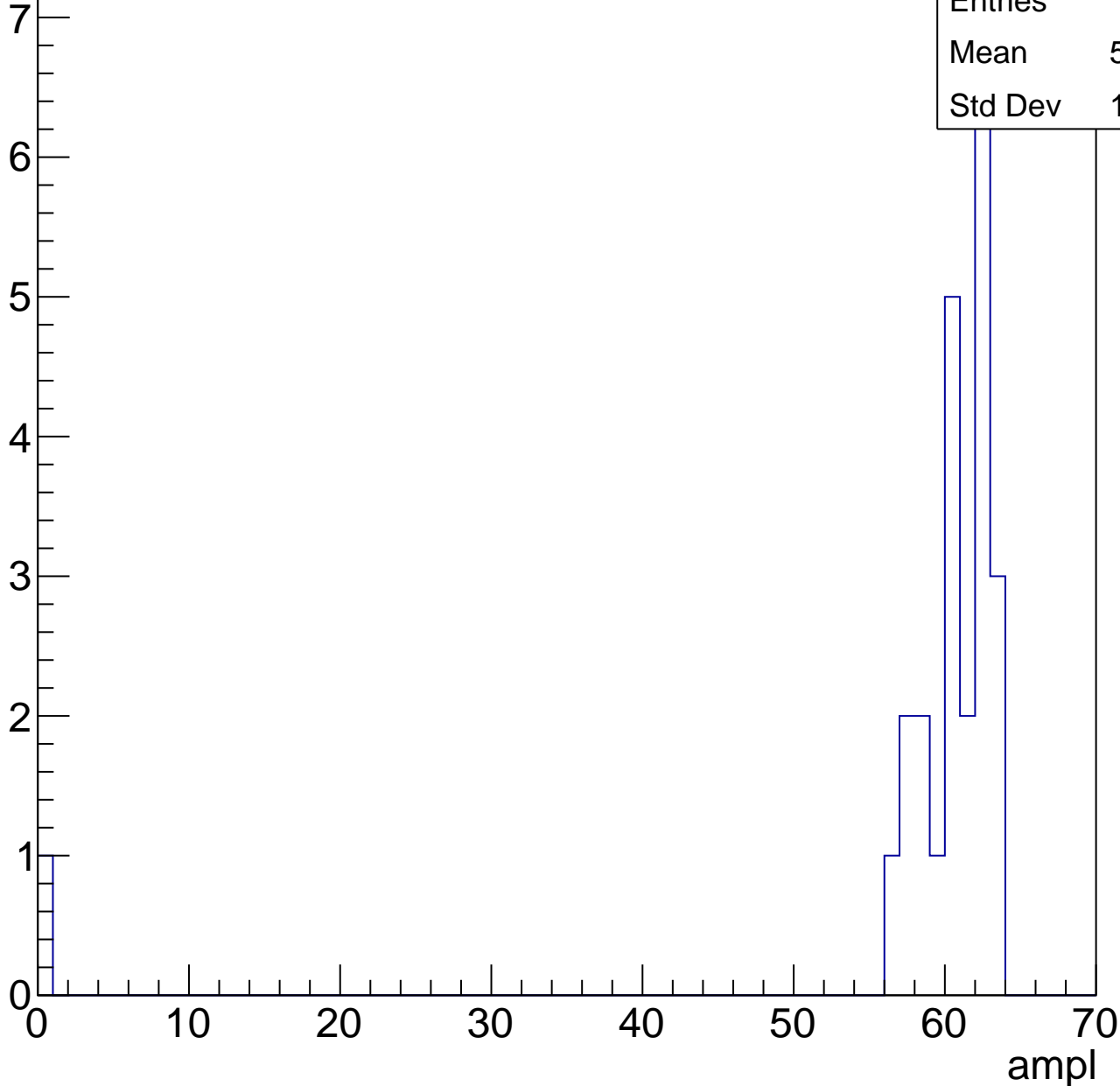


B1L101S, U6-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	57.92
Std Dev	12.24

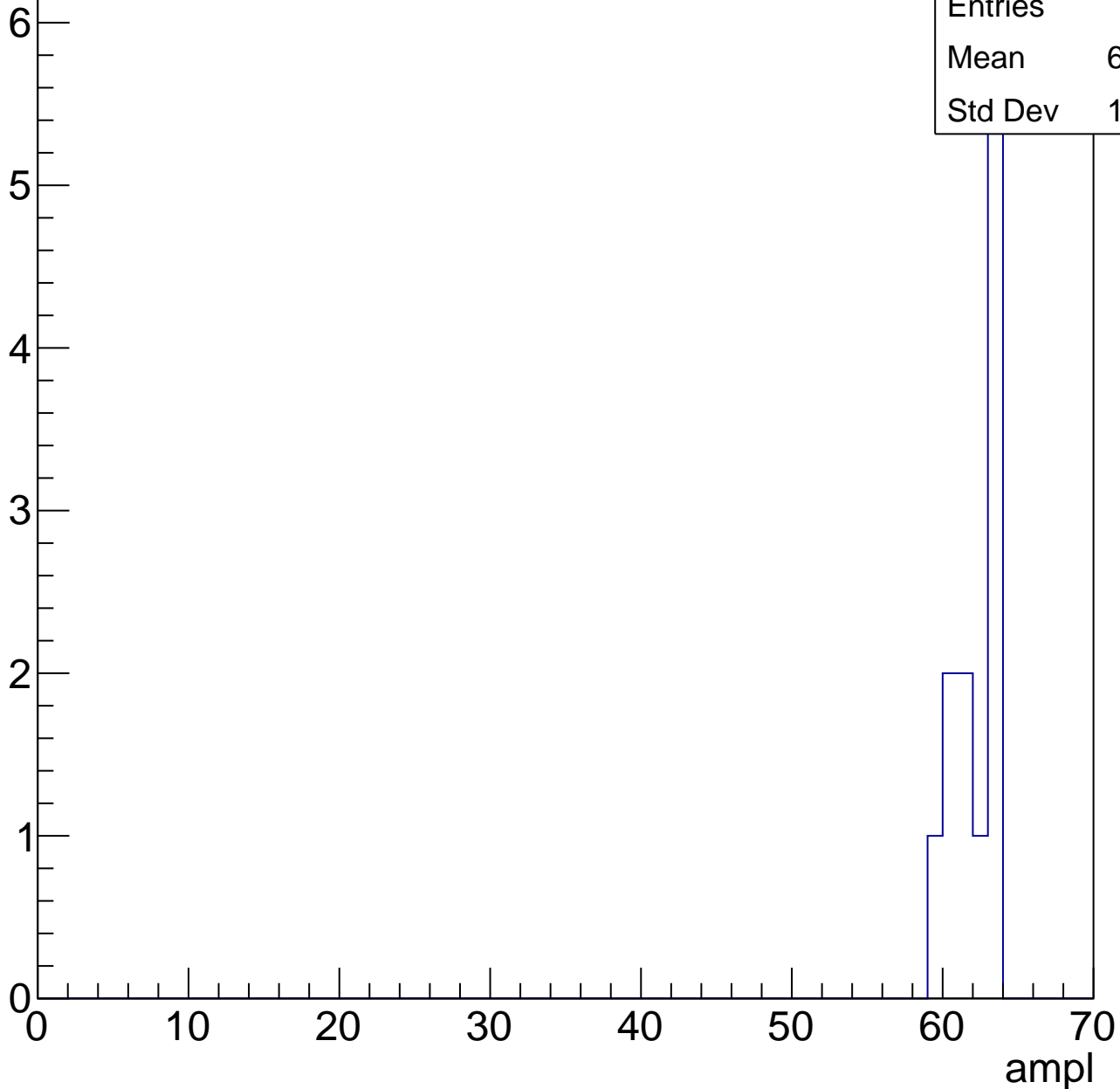


B1L101S, U6-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	61.75
Std Dev	1.422



B1L101S, U6-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch59, adc0

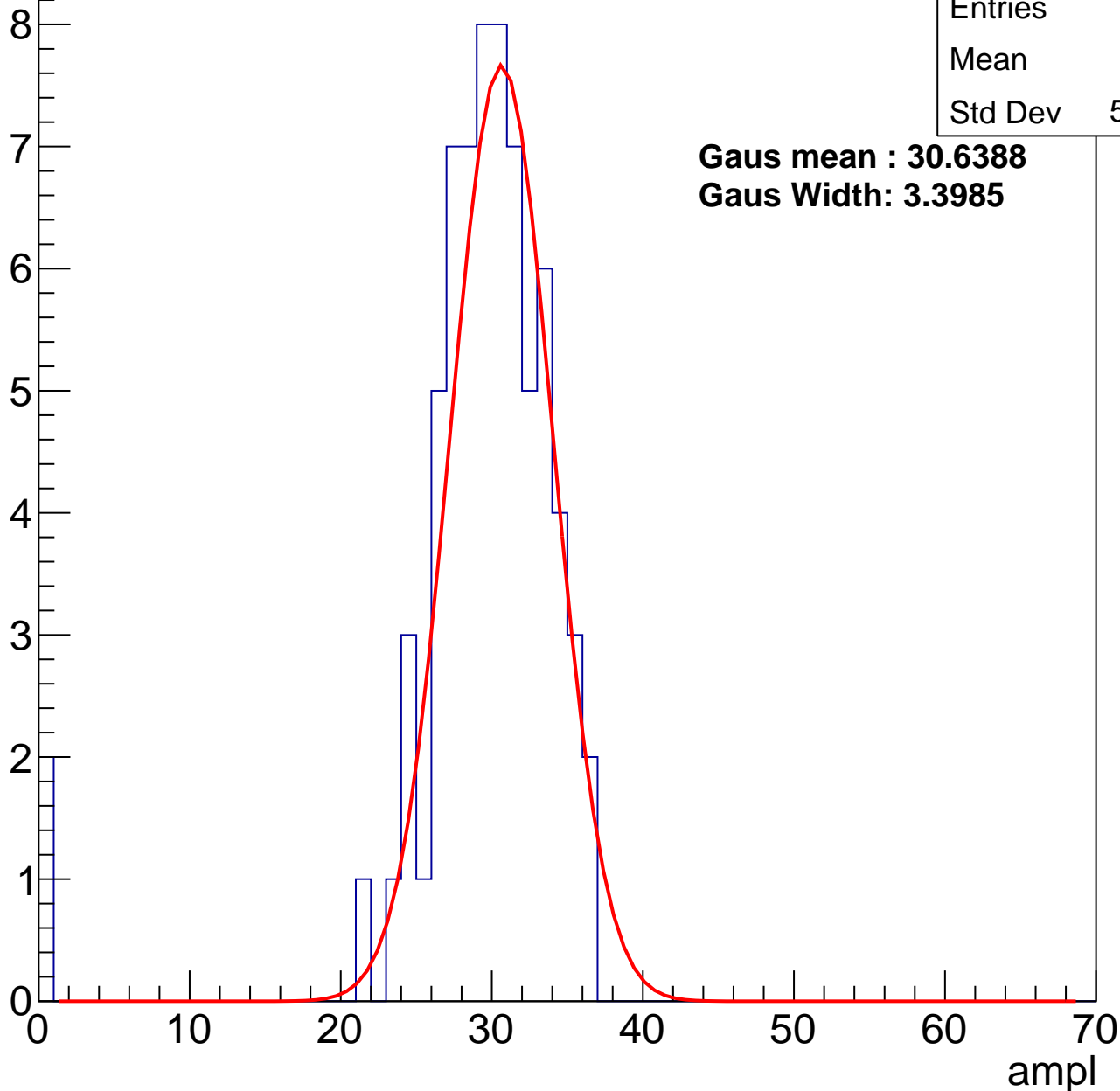
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.8
Std Dev	5.896

Gaus mean : 30.6388

Gaus Width: 3.3985



B1L101S, U6-ch59, adc1

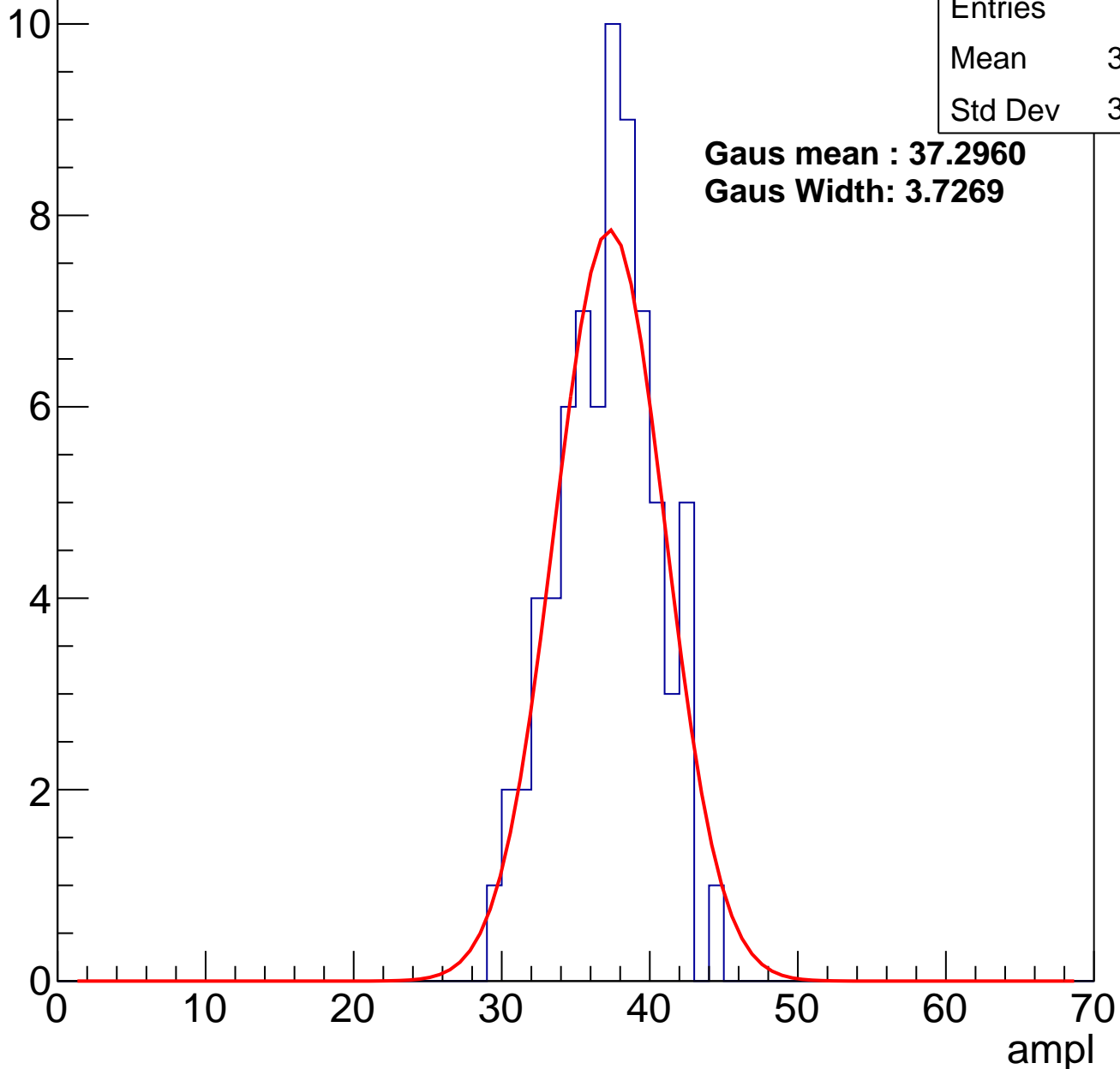
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	36.64
Std Dev	3.293

Gaus mean : 37.2960

Gaus Width: 3.7269

Entry



B1L101S, U6-ch59, adc2

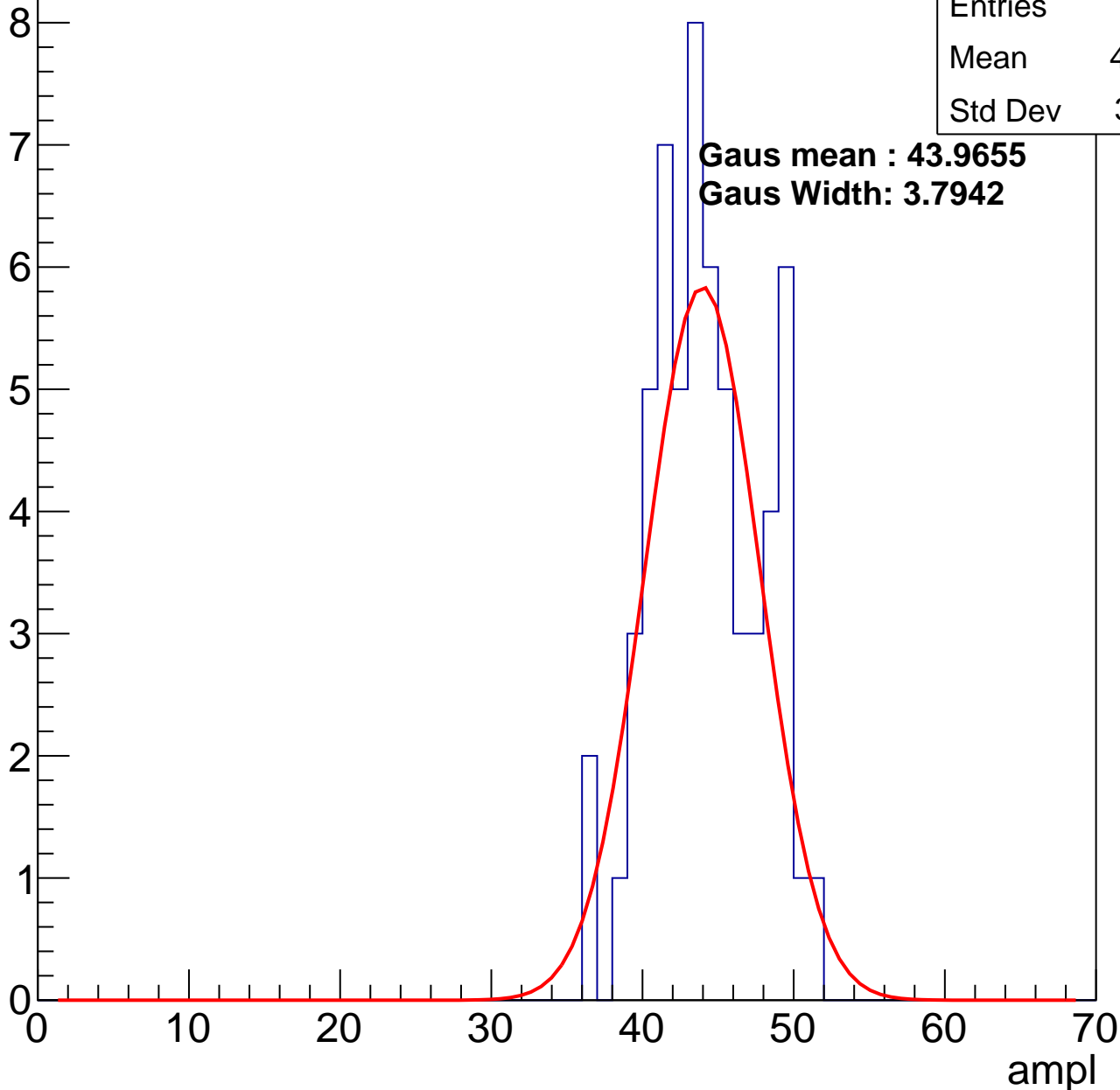
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.72
Std Dev	3.531

Gaus mean : 43.9655

Gaus Width: 3.7942

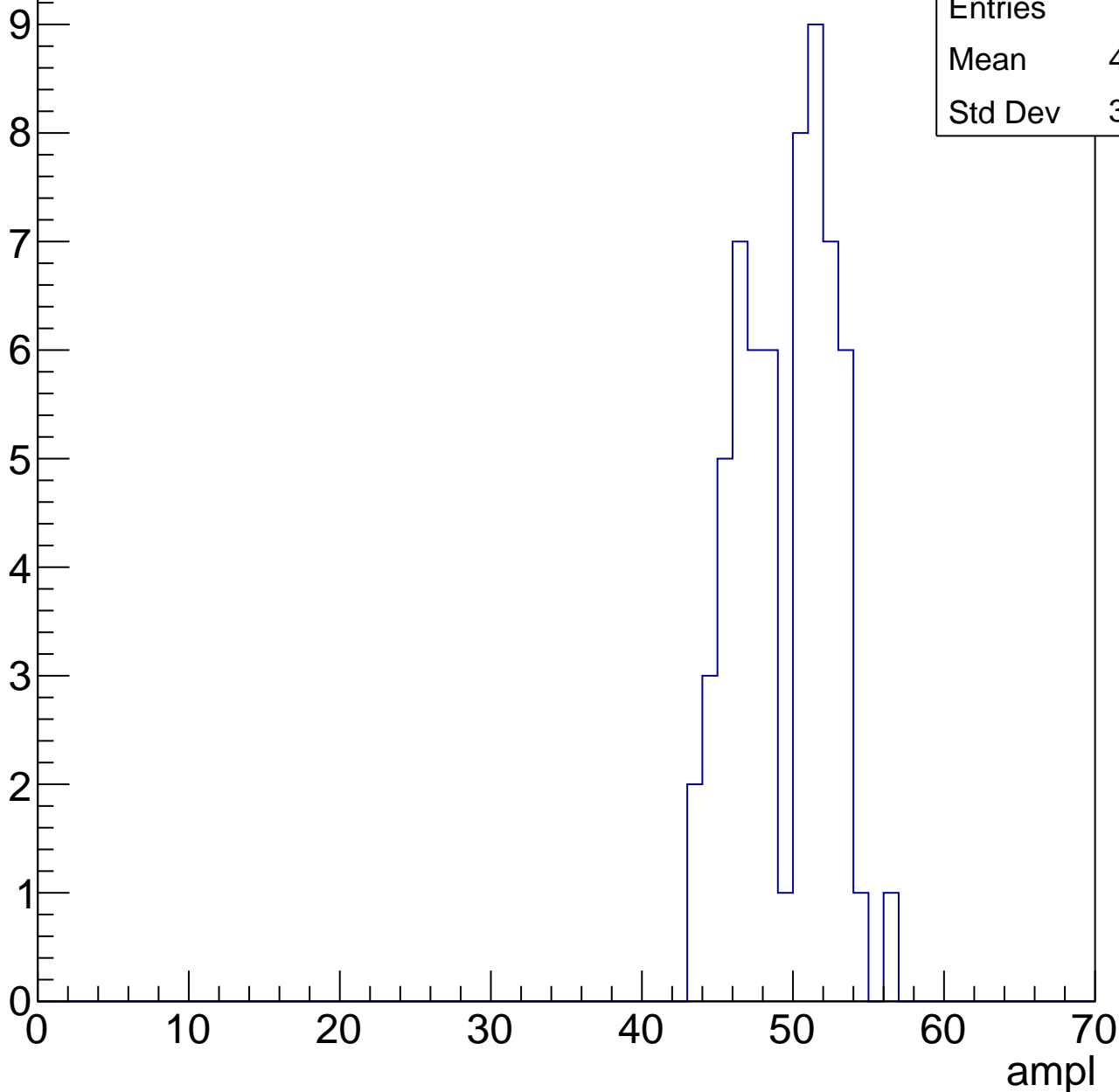


B1L101S, U6-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

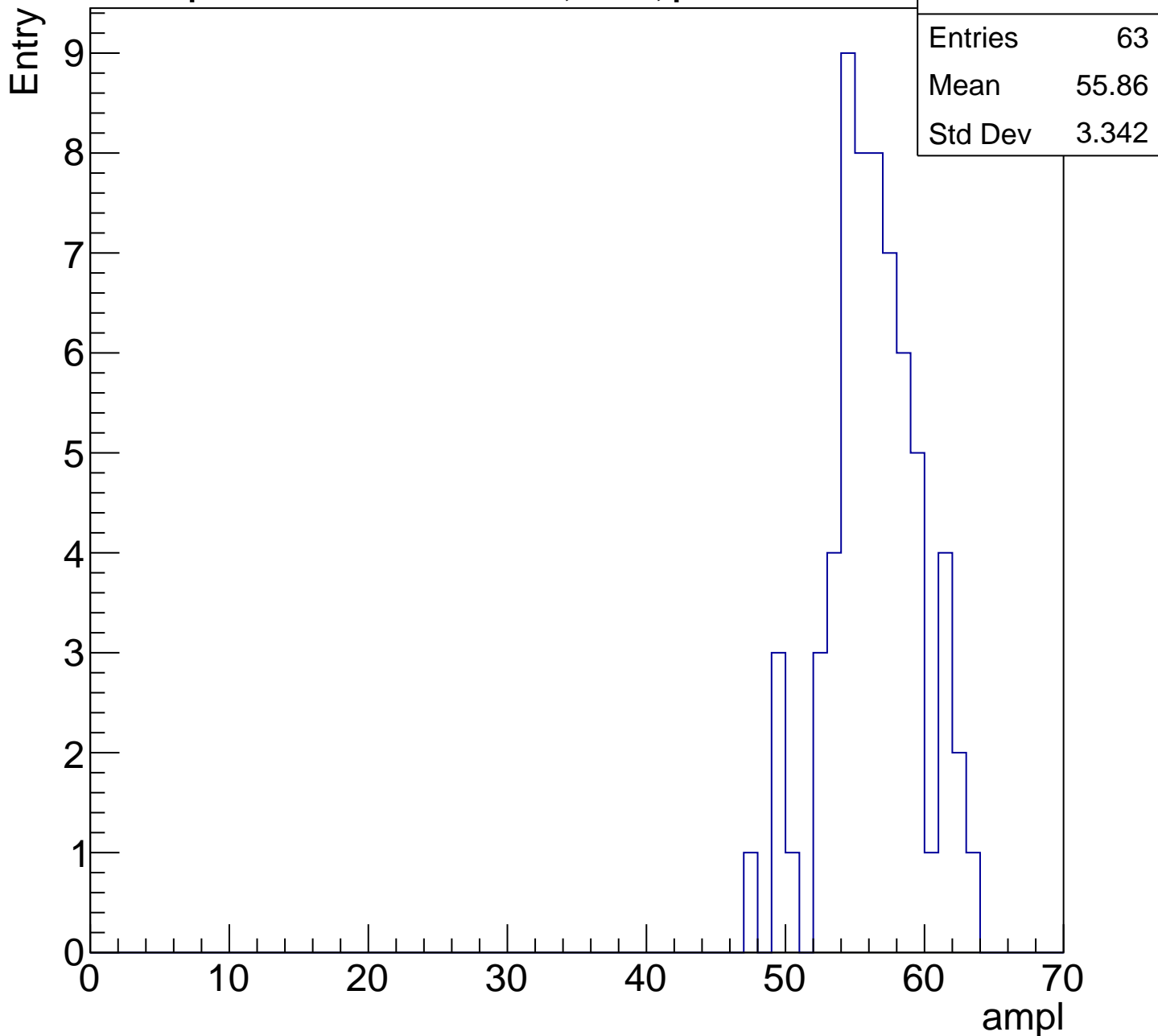
Entry

Entries	62
Mean	48.95
Std Dev	3.103



B1L101S, U6-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch59, adc5

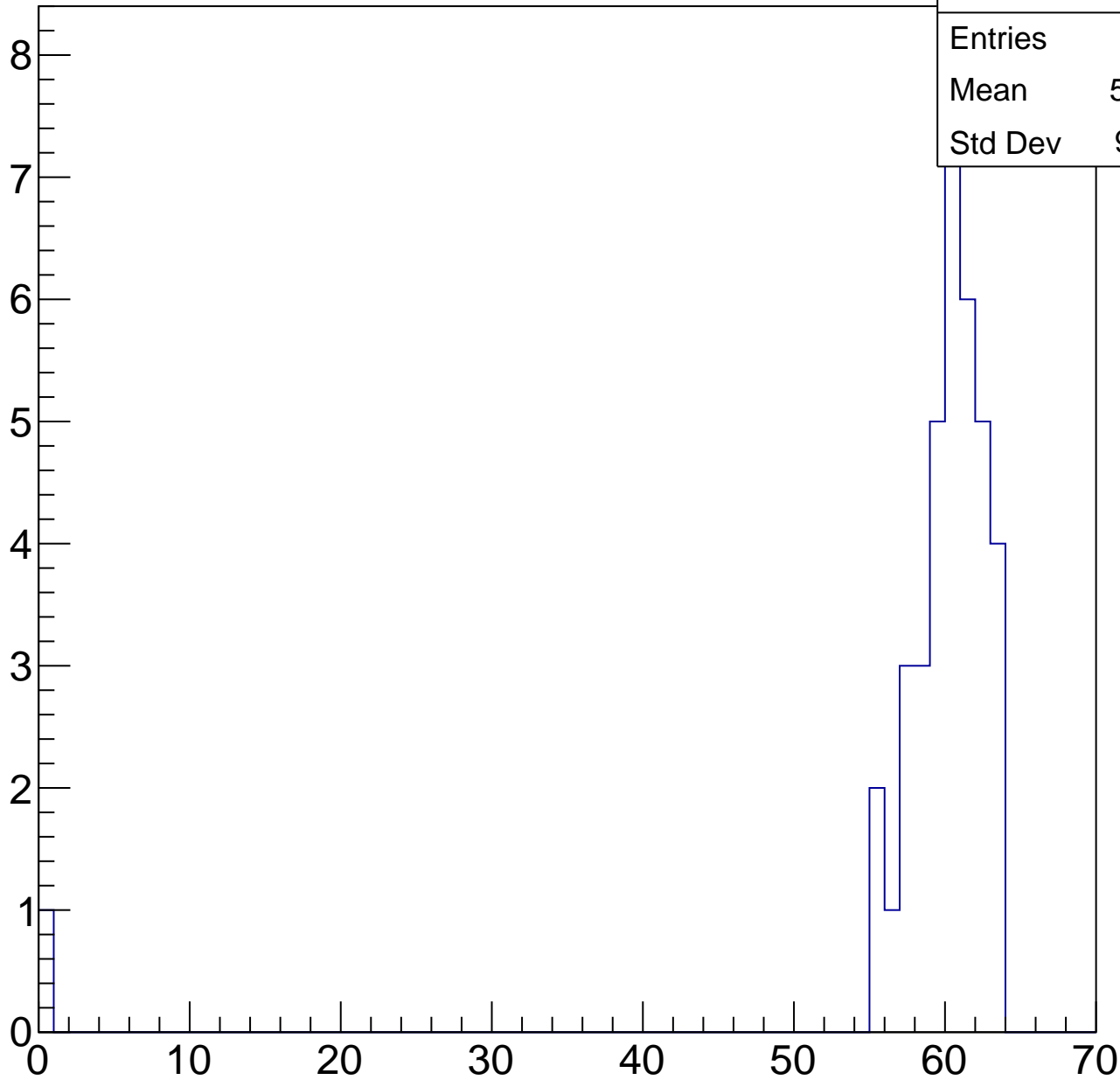
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	58.26
Std Dev	9.811

ampl

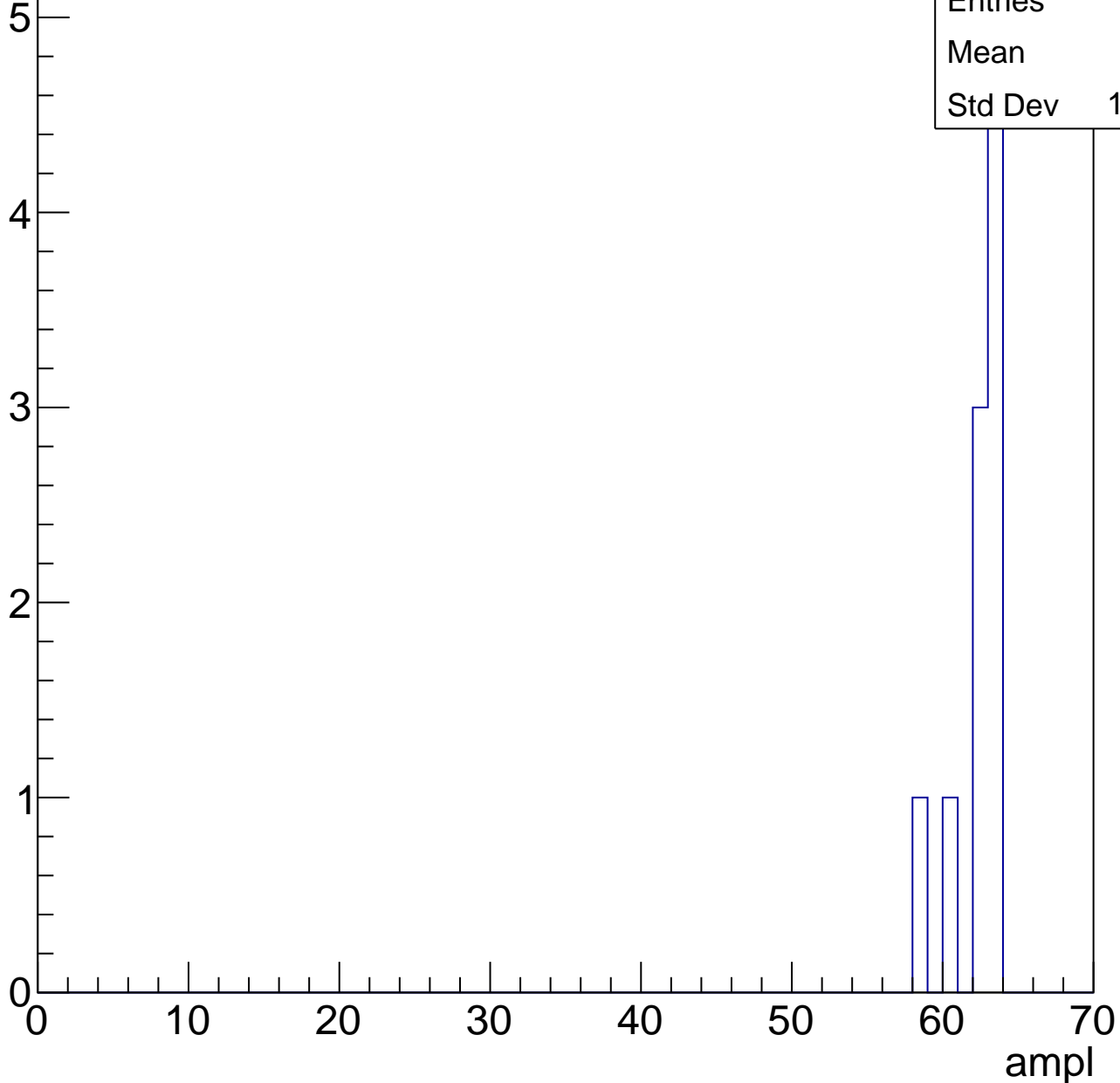


B1L101S, U6-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	61.9
Std Dev	1.578



B1L101S, U6-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch60, adc0

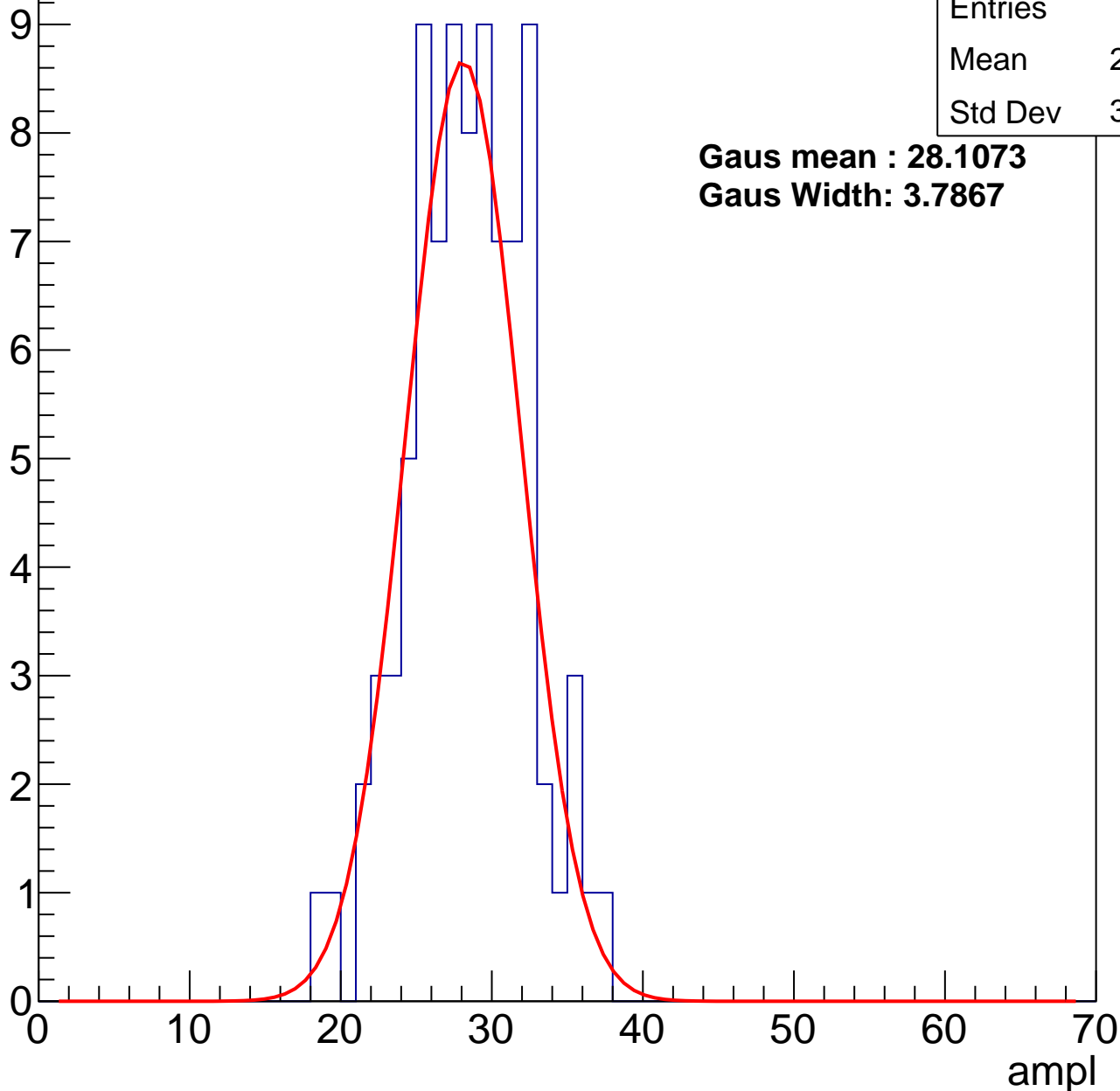
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	27.98
Std Dev	3.817

Gaus mean : 28.1073

Gaus Width: 3.7867



B1L101S, U6-ch60, adc1

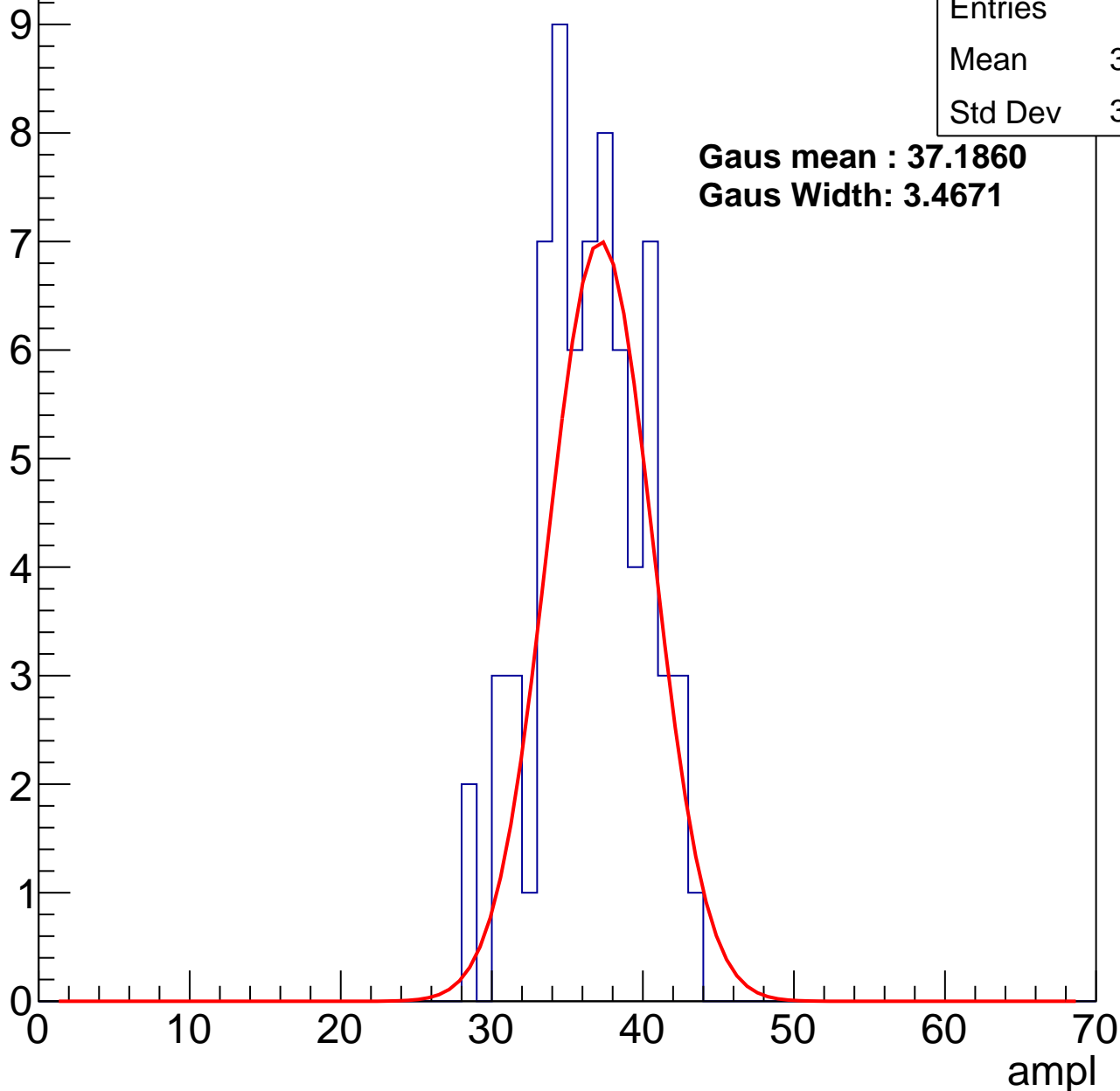
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	36.03
Std Dev	3.472

Gaus mean : 37.1860

Gaus Width: 3.4671



B1L101S, U6-ch60, adc2

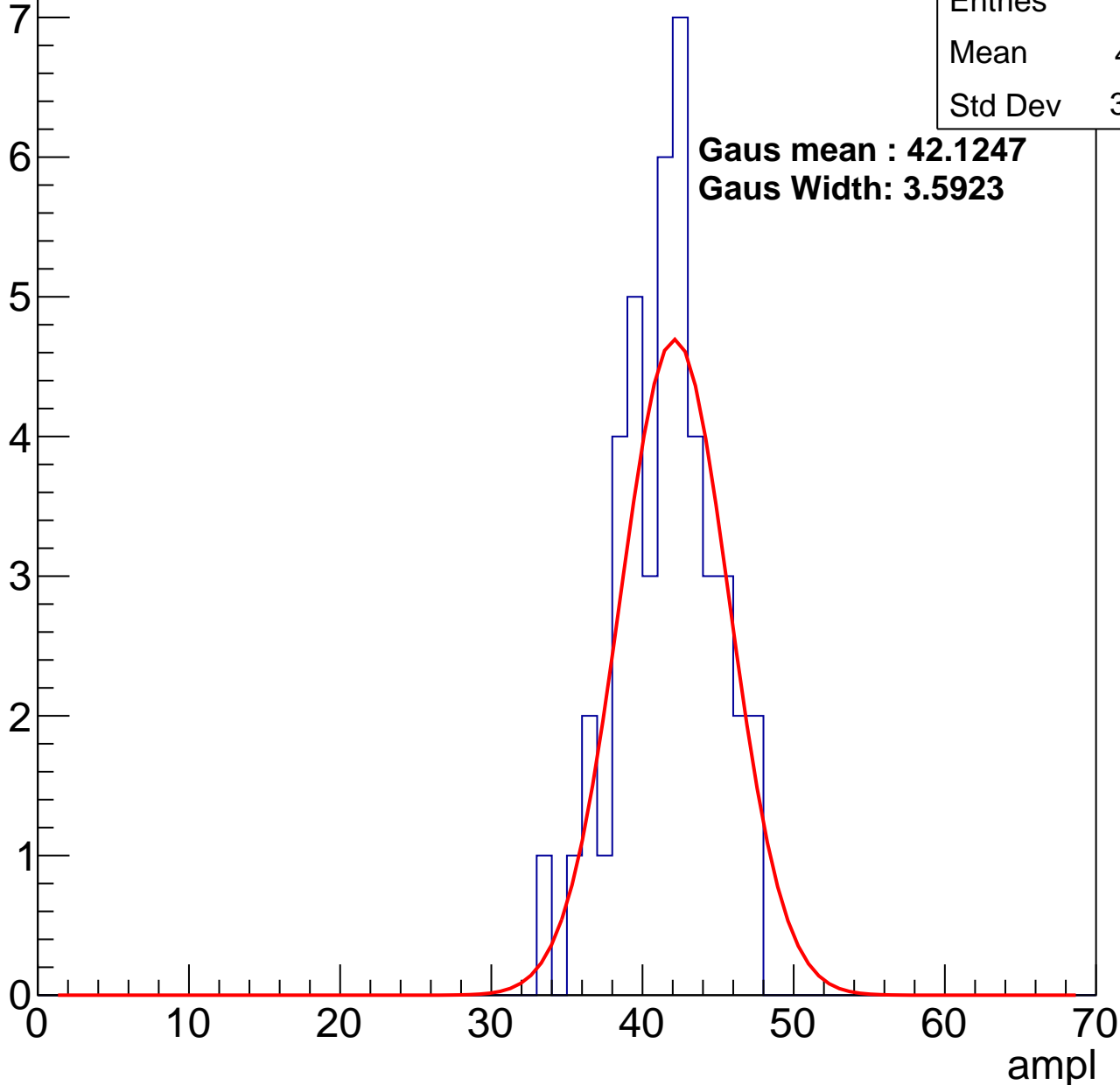
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	41.11
Std Dev	3.178

Gaus mean : 42.1247

Gaus Width: 3.5923

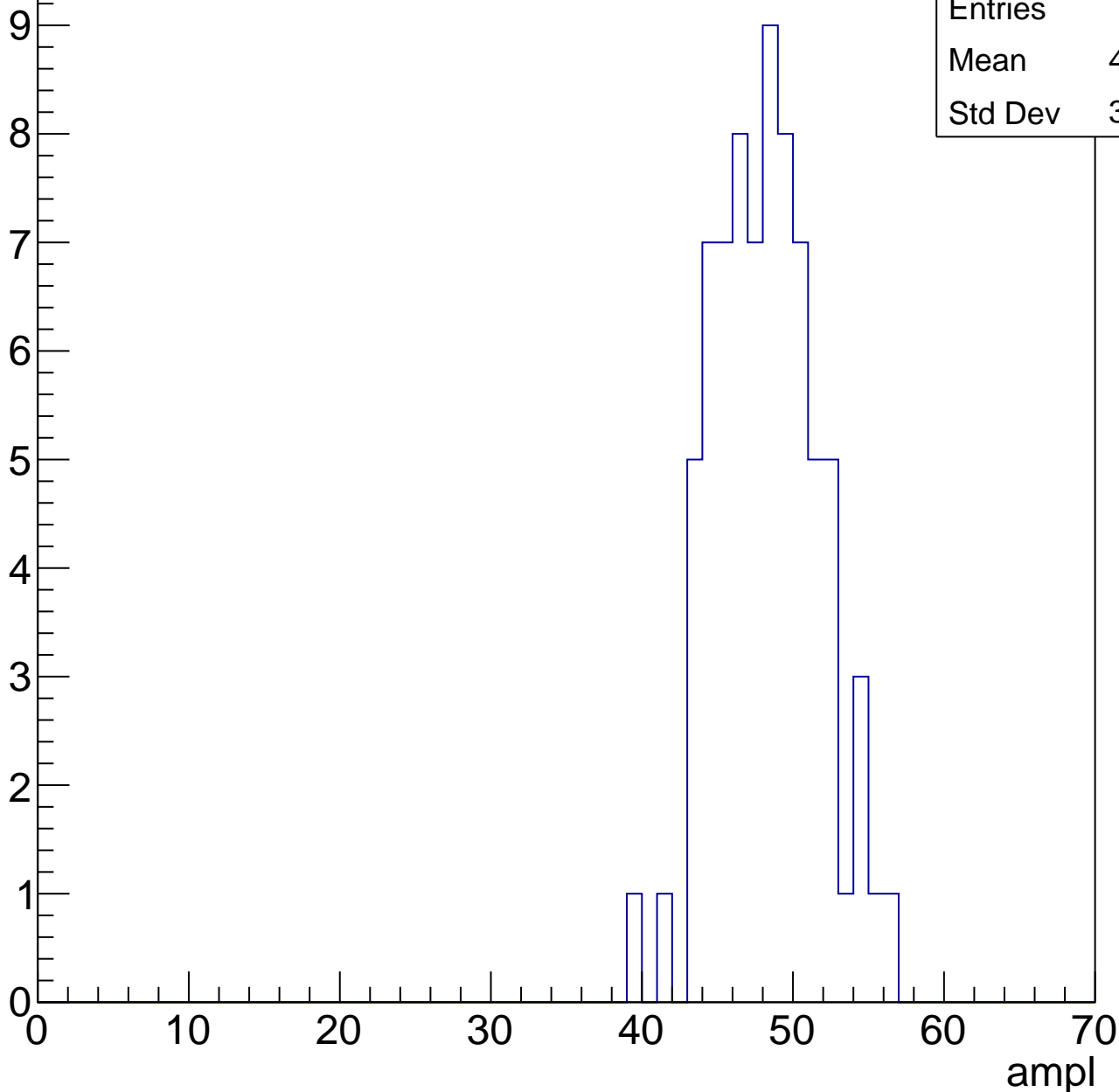


B1L101S, U6-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

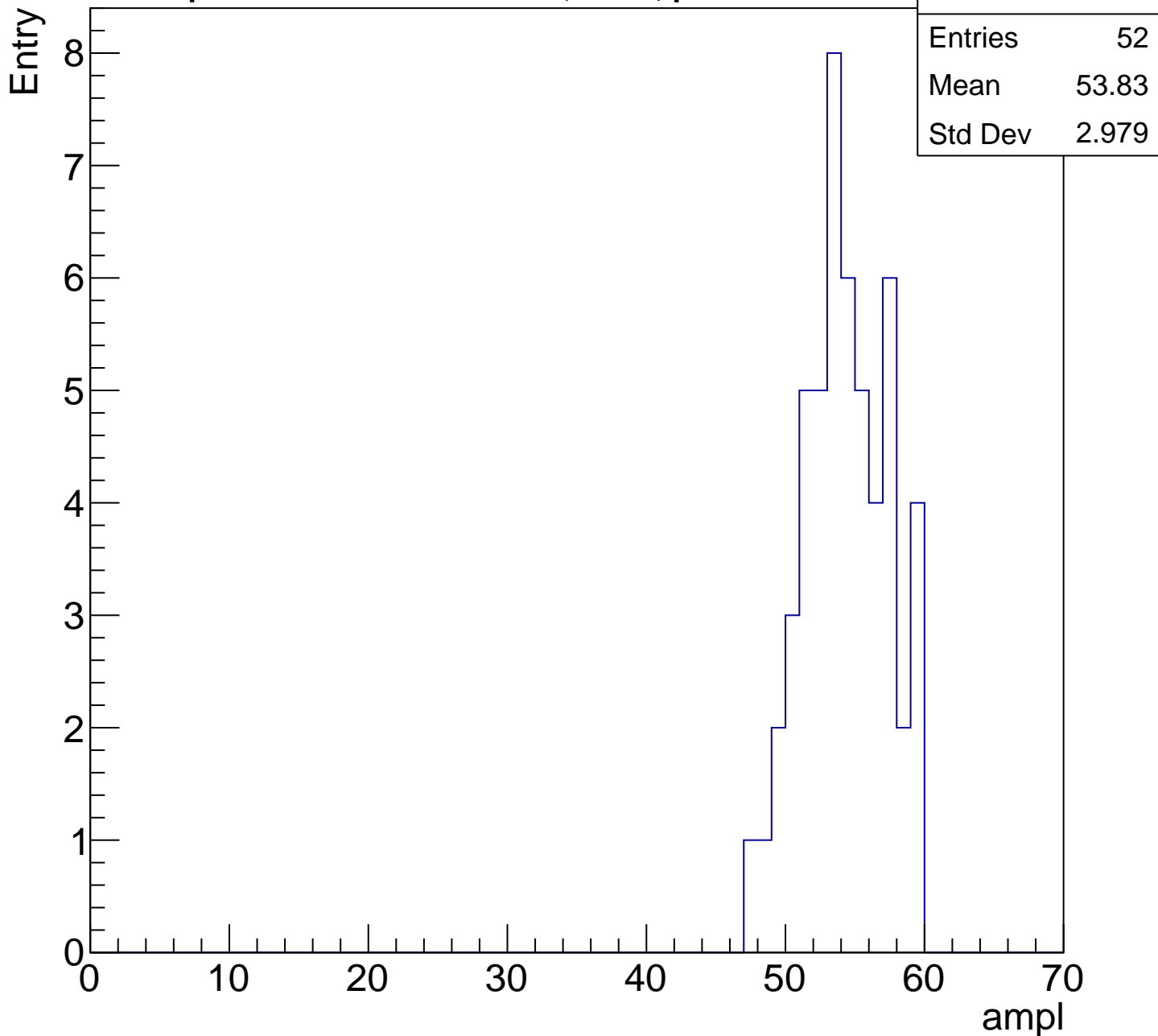
Entry

Entries	76
Mean	47.76
Std Dev	3.387



B1L101S, U6-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch60, adc5

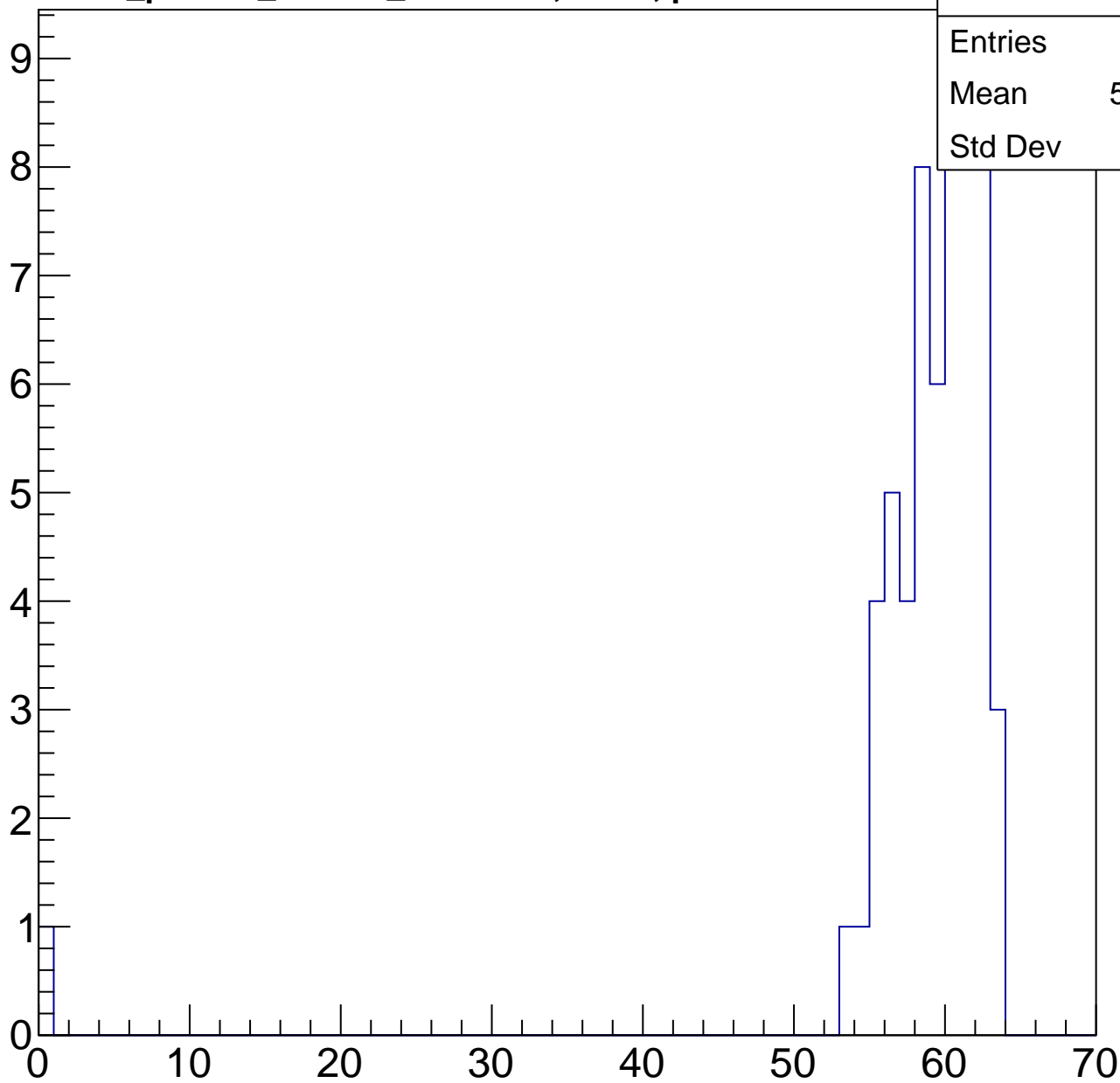
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.05
Std Dev	8.08

ampl

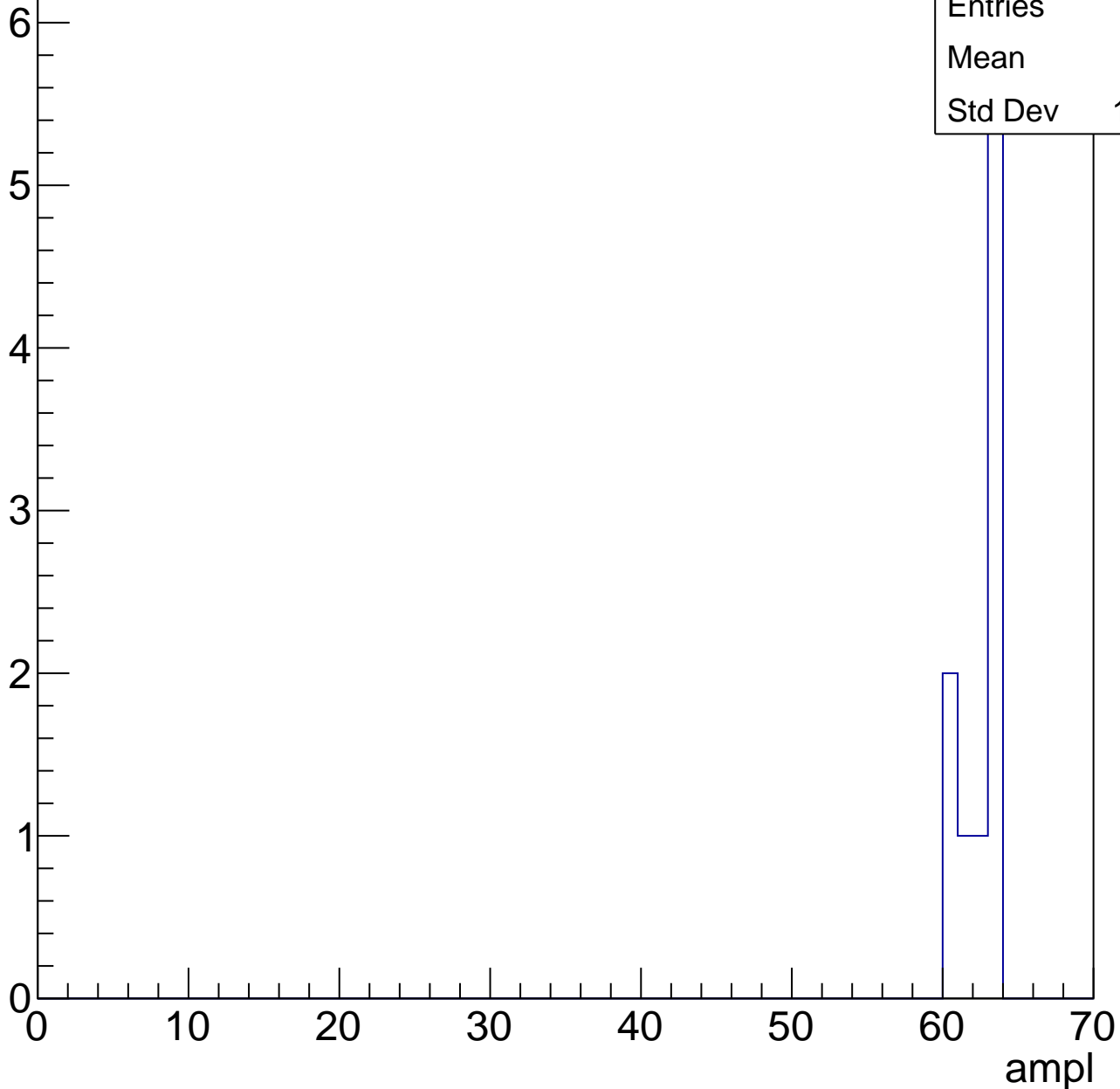


B1L101S, U6-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

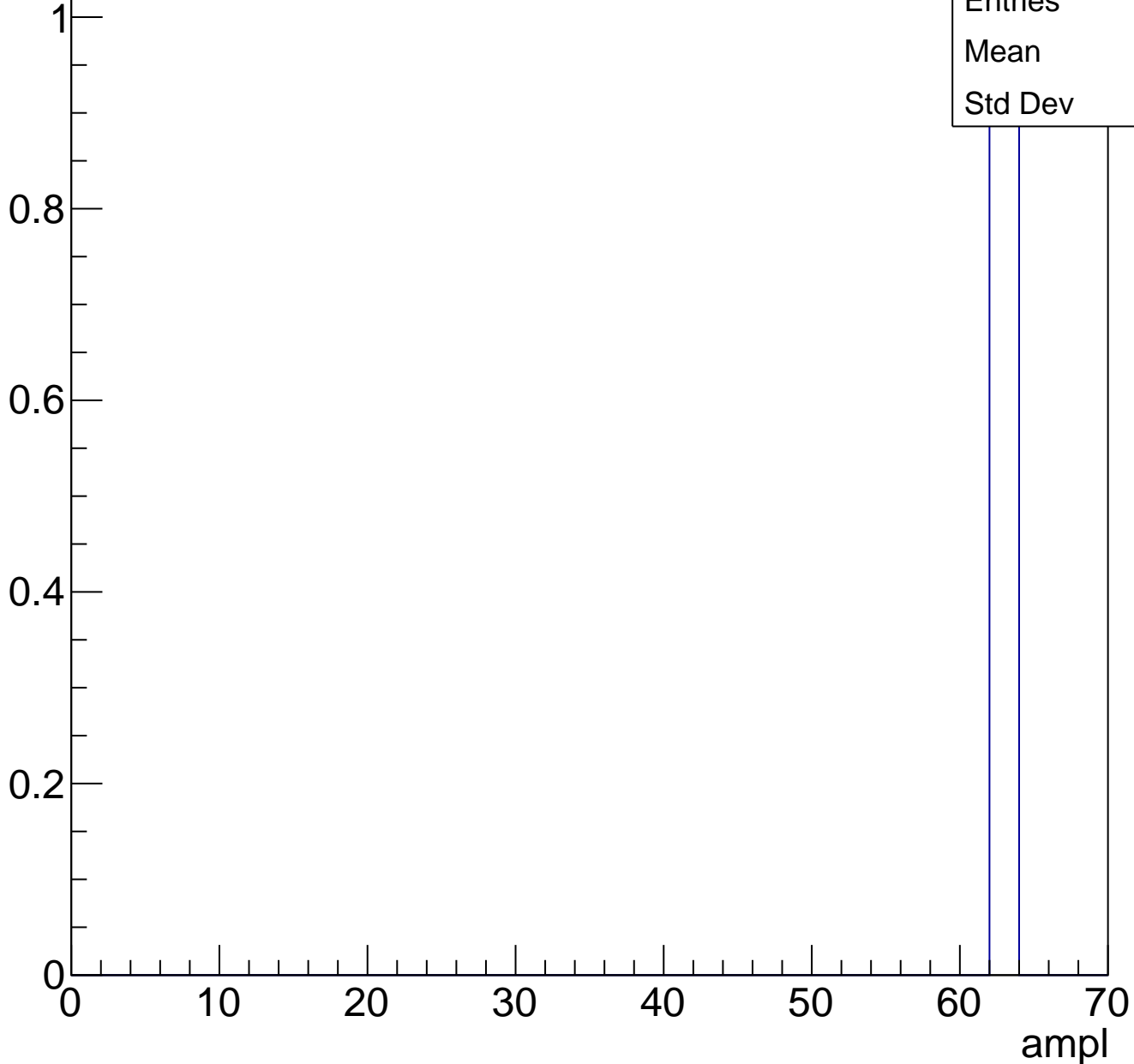
Entries	10
Mean	62.1
Std Dev	1.221



B1L101S, U6-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch61, adc0

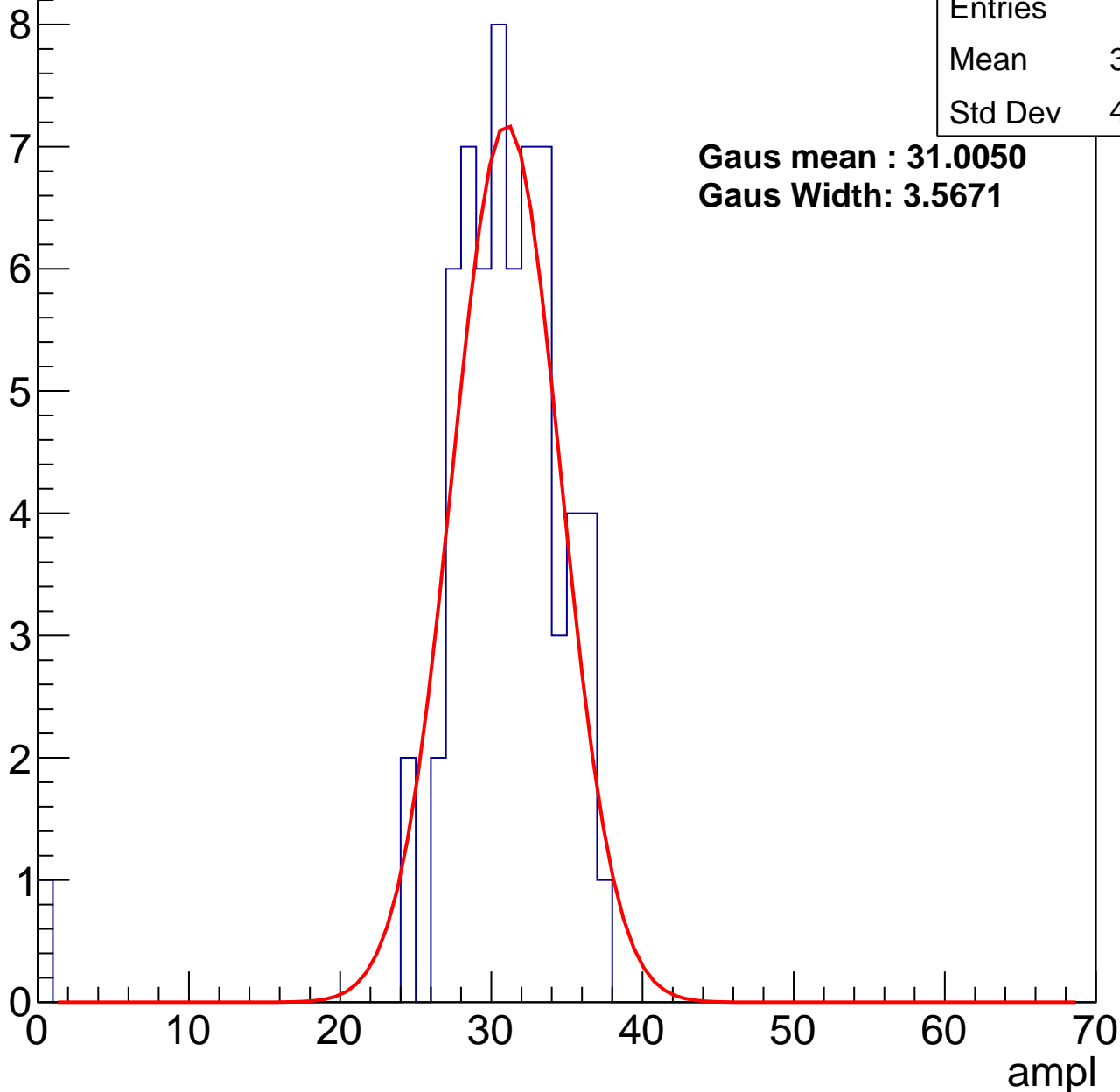
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.25
Std Dev	4.883

Gaus mean : 31.0050

Gaus Width: 3.5671



B1L101S, U6-ch61, adc1

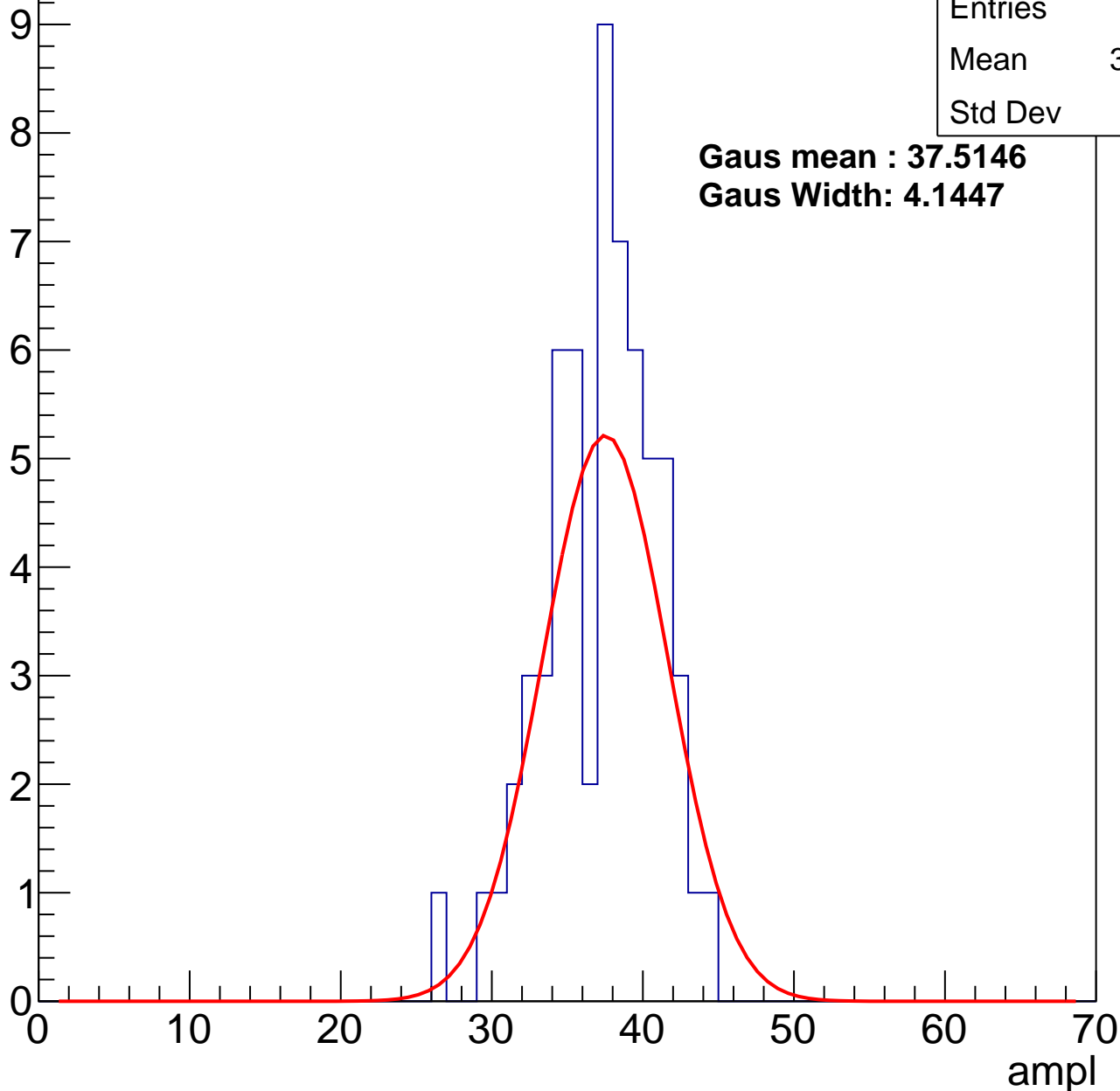
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.76
Std Dev	3.64

Gaus mean : 37.5146

Gaus Width: 4.1447

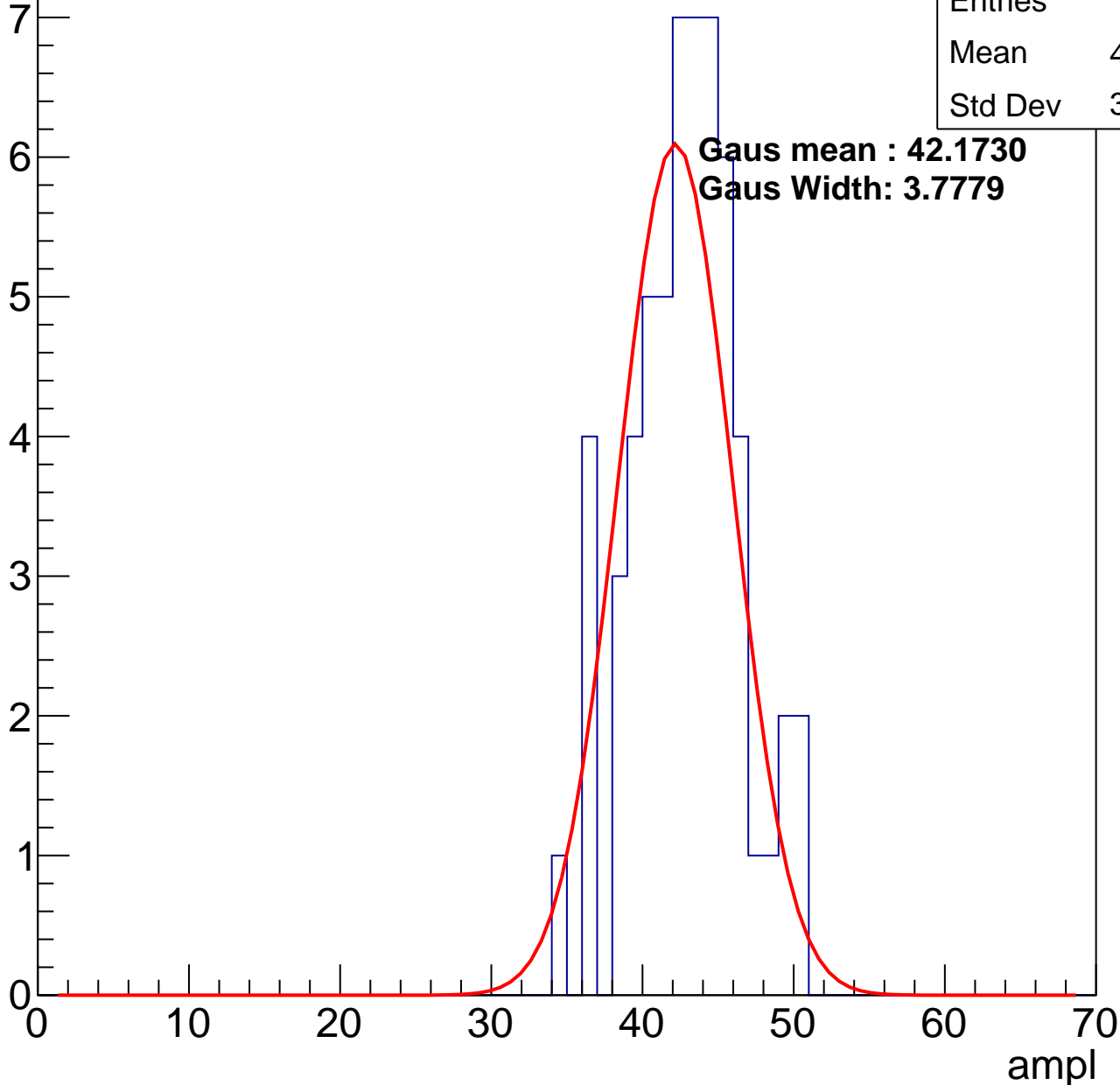


B1L101S, U6-ch61, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.42
Std Dev	3.557

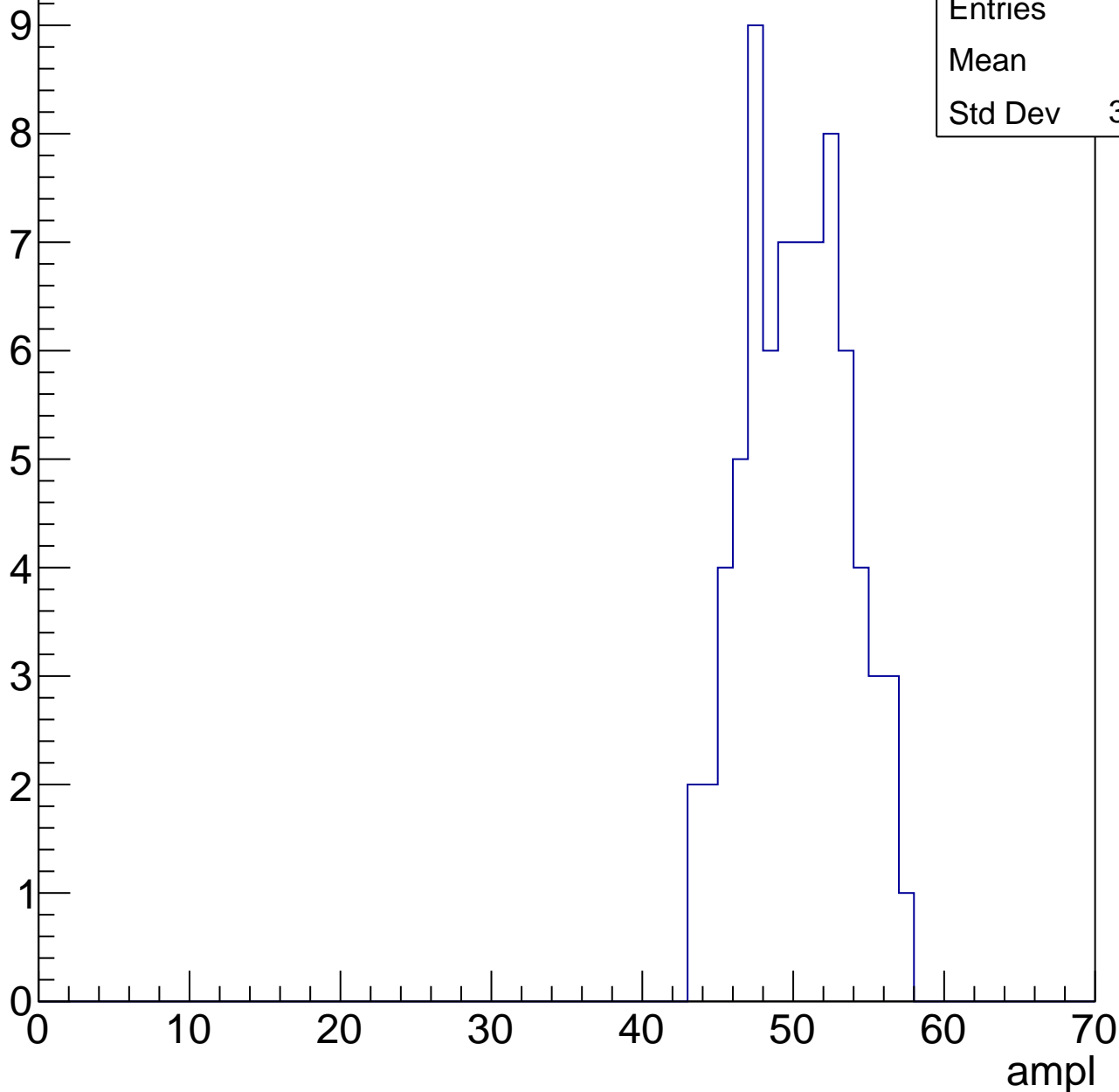


B1L101S, U6-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

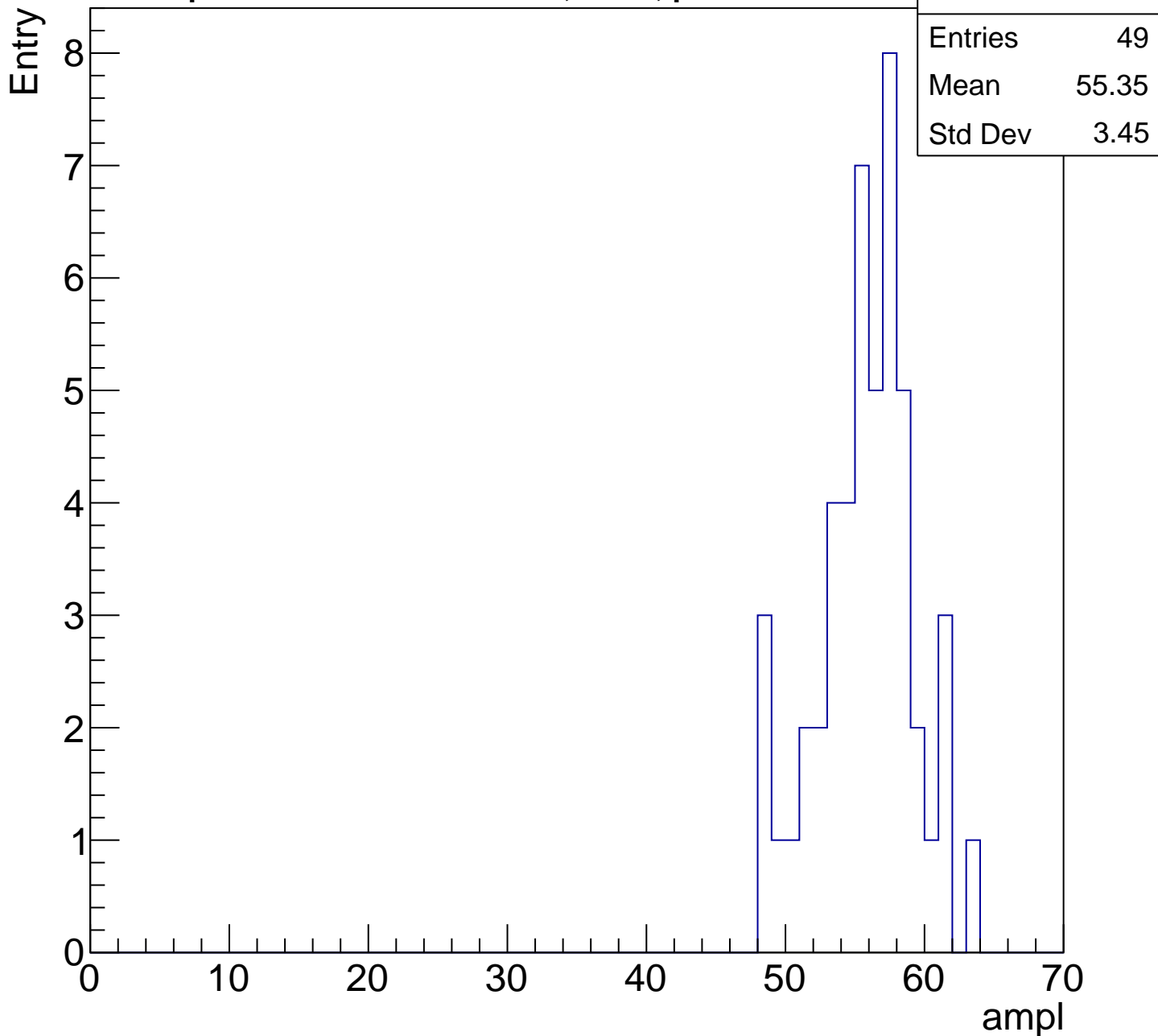
Entry

Entries	74
Mean	49.8
Std Dev	3.385



B1L101S, U6-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

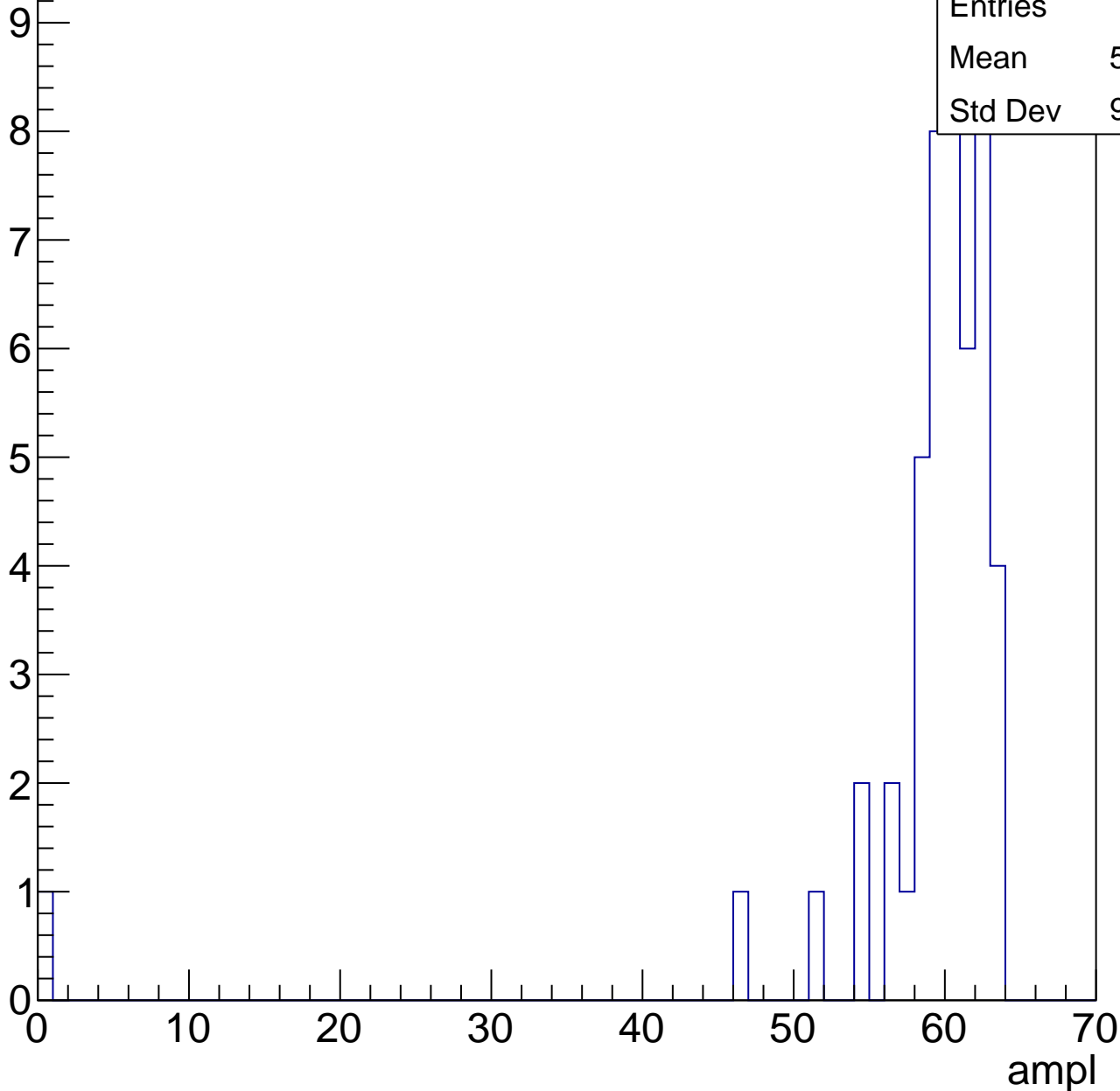


B1L101S, U6-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.17
Std Dev	9.054

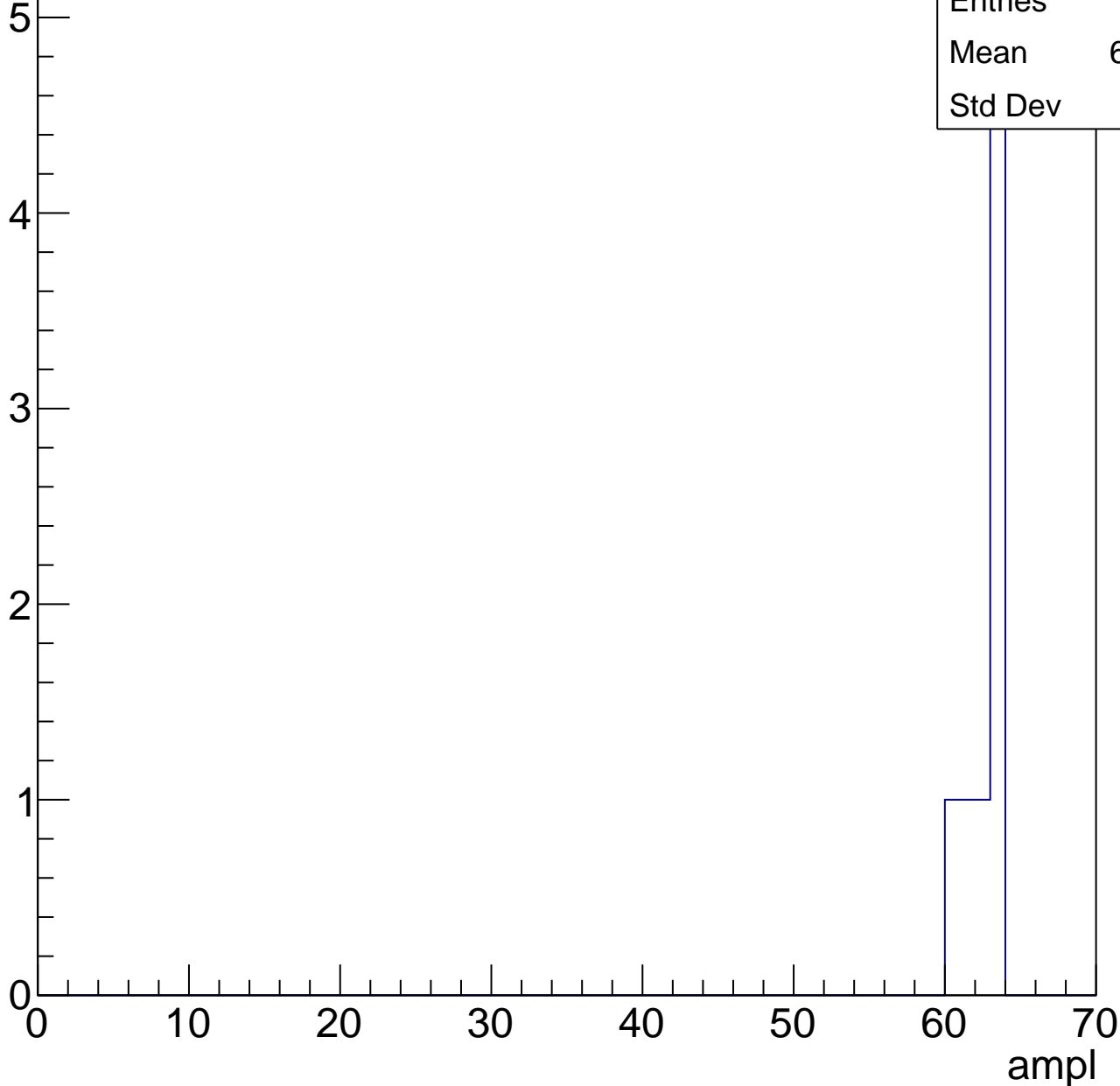


B1L101S, U6-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.25
Std Dev	1.09



B1L101S, U6-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U6-ch62, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	27.54
Std Dev	3.435

Gaus mean : 28.3948

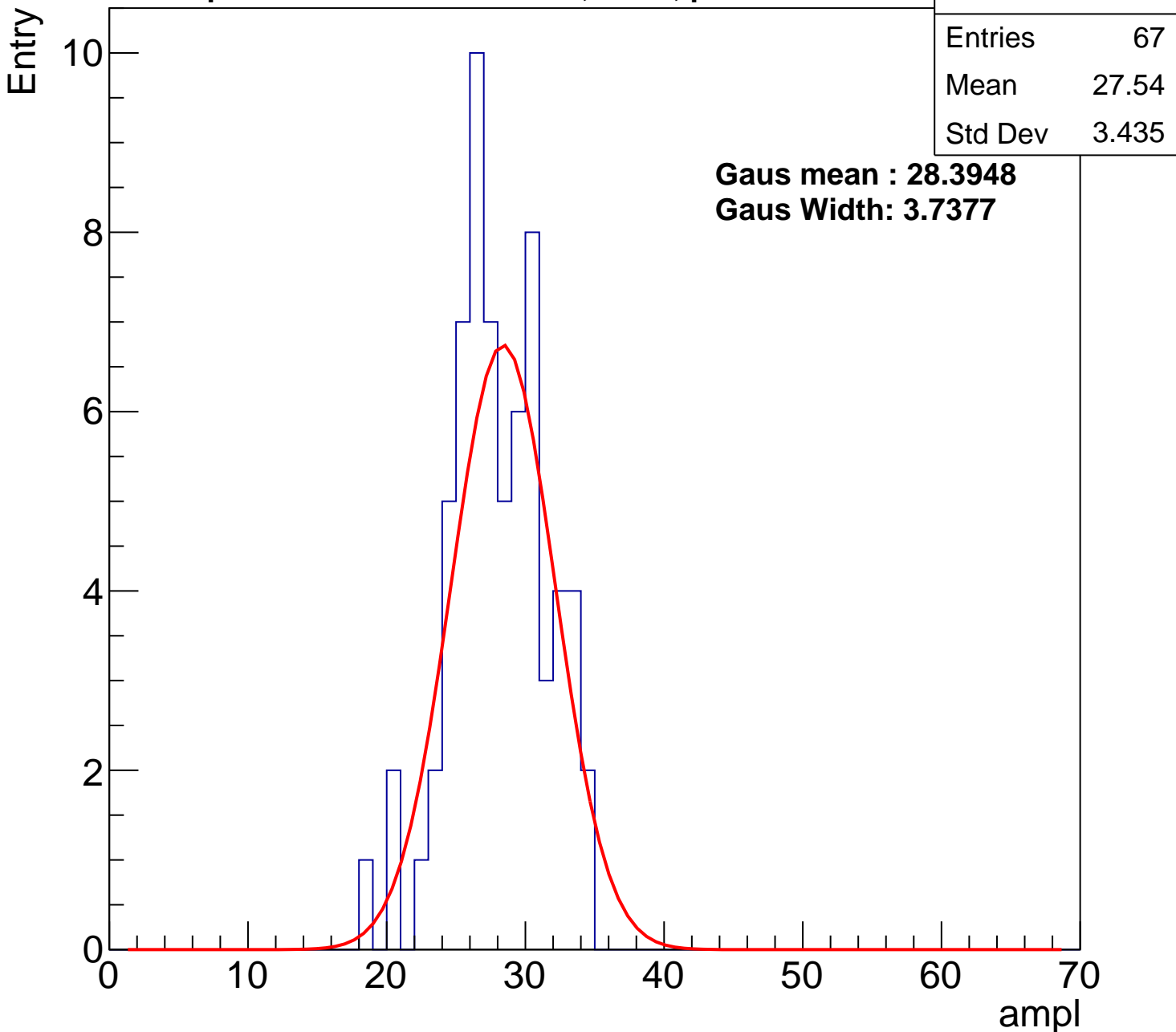
Gaus Width: 3.7377

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch62, adc1

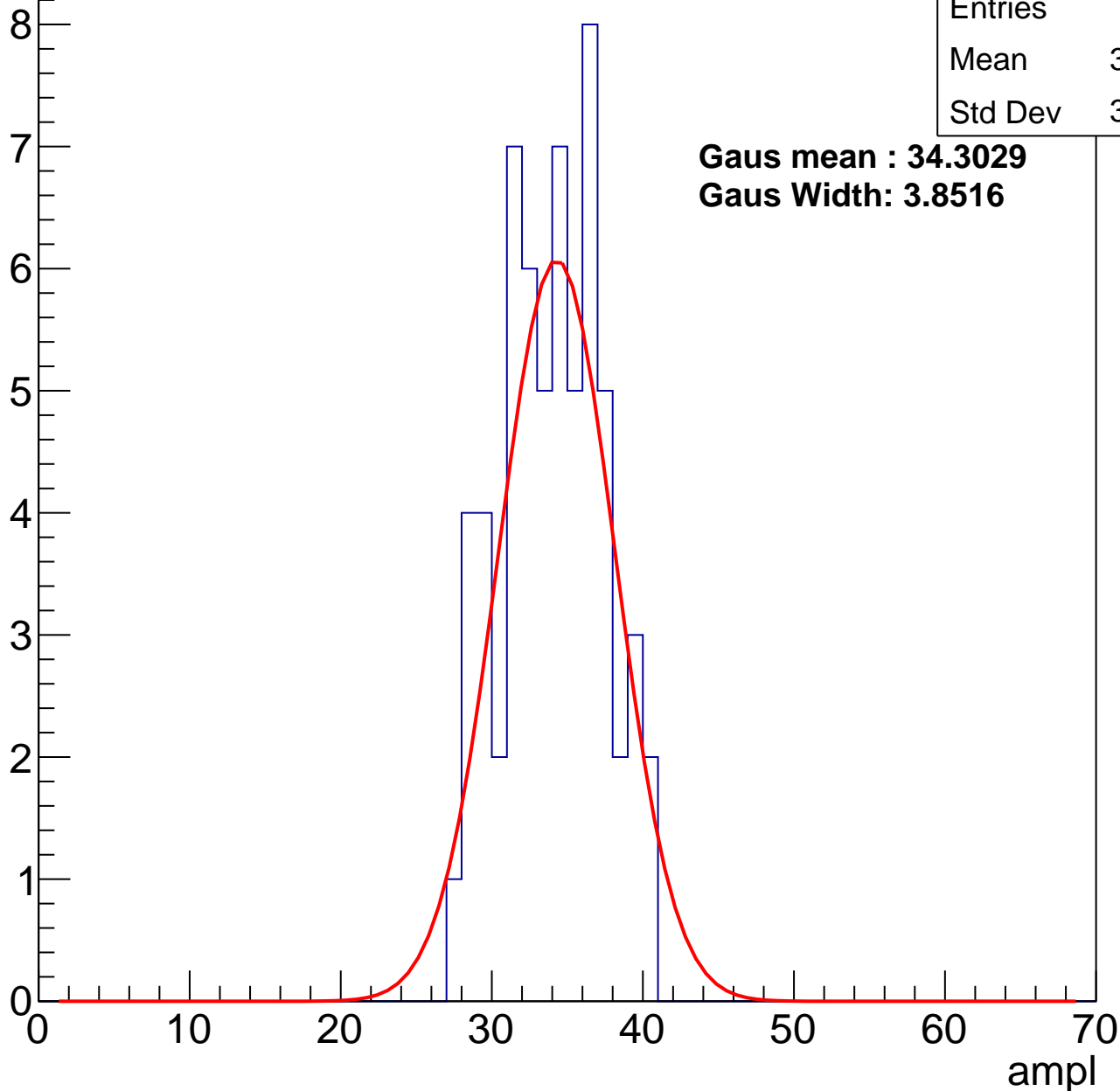
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	33.57
Std Dev	3.307

Gaus mean : 34.3029

Gaus Width: 3.8516



B1L101S, U6-ch62, adc2

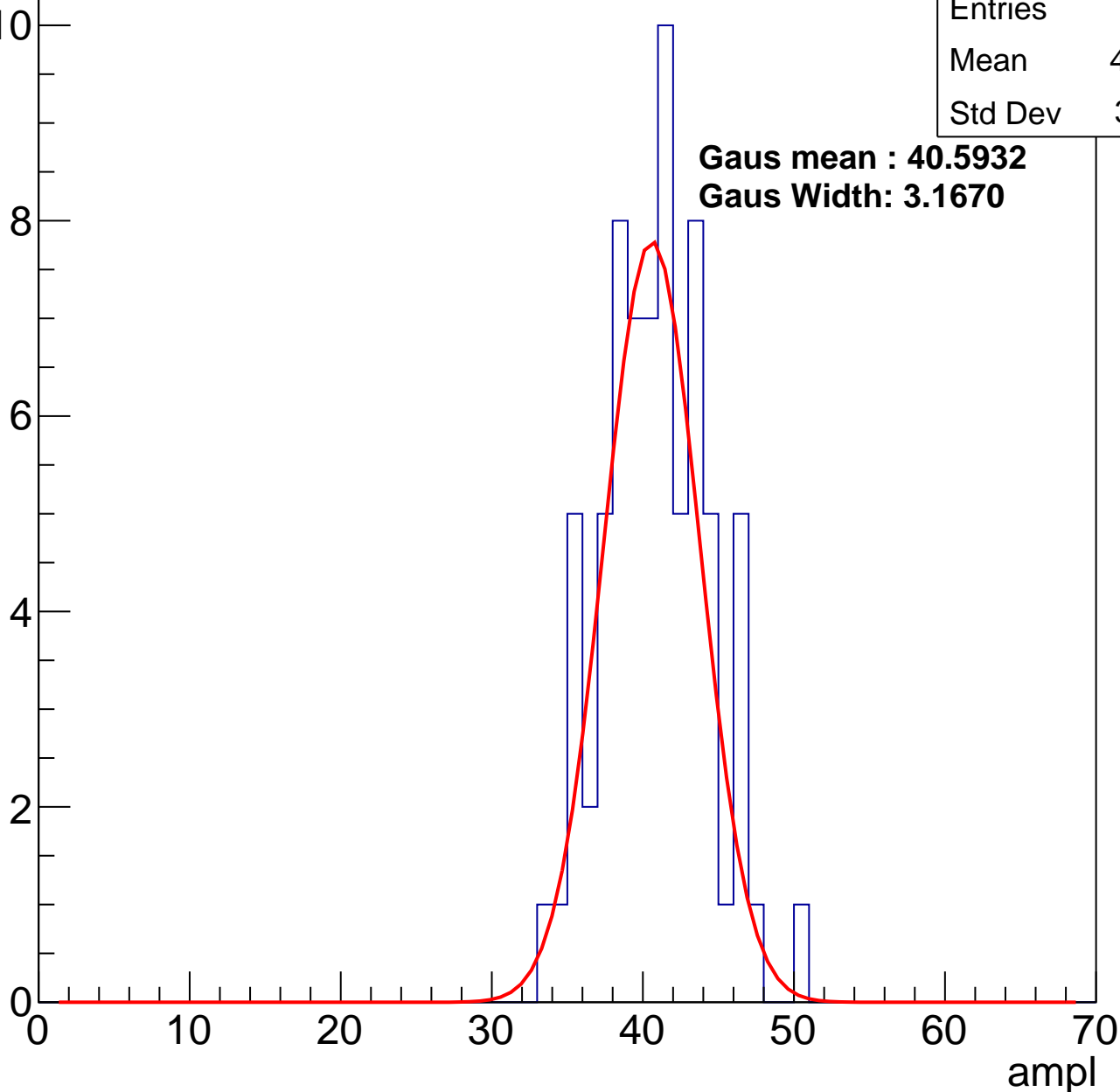
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	40.44
Std Dev	3.431

Gaus mean : 40.5932

Gaus Width: 3.1670

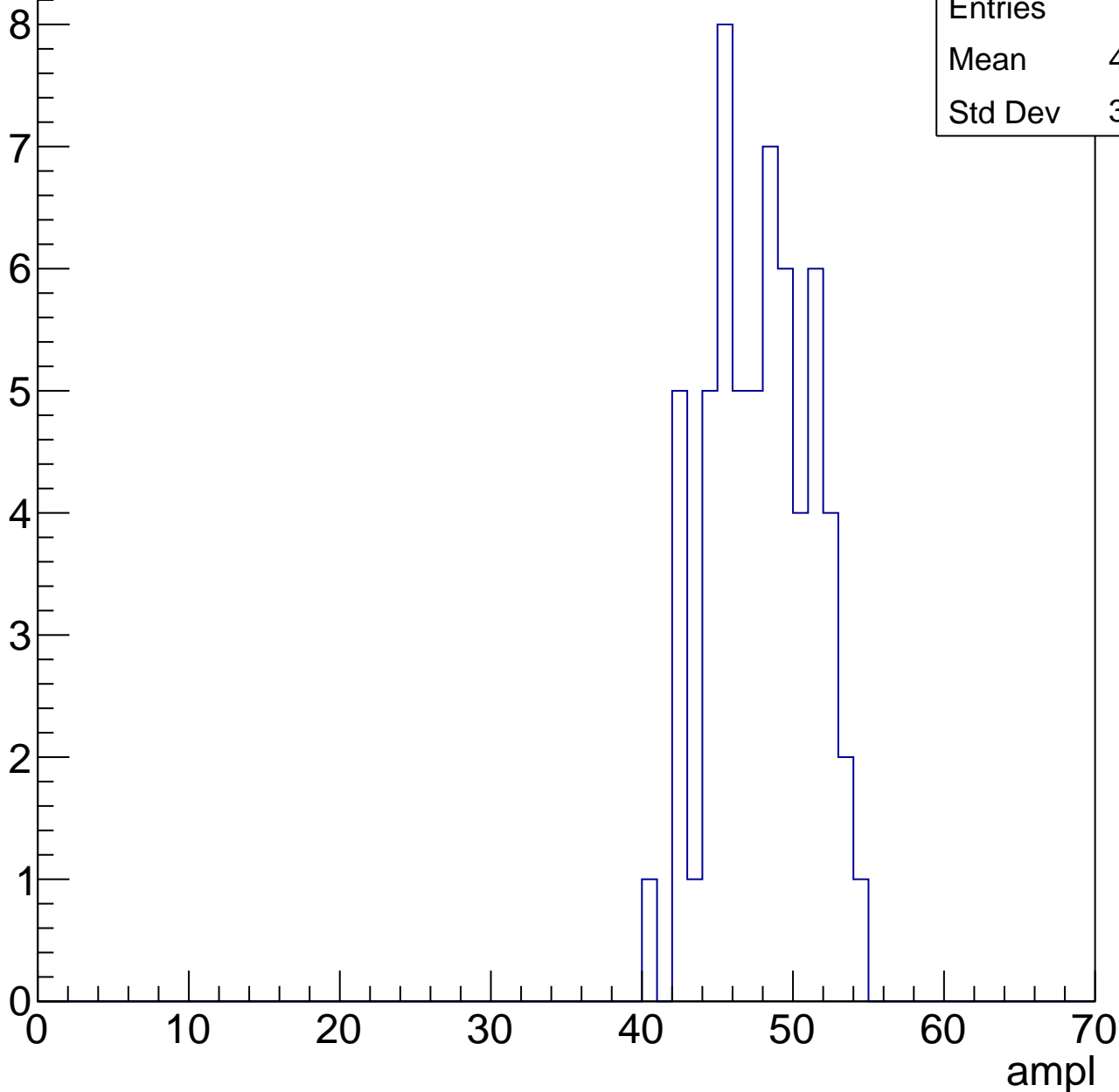


B1L101S, U6-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	47.37
Std Dev	3.286

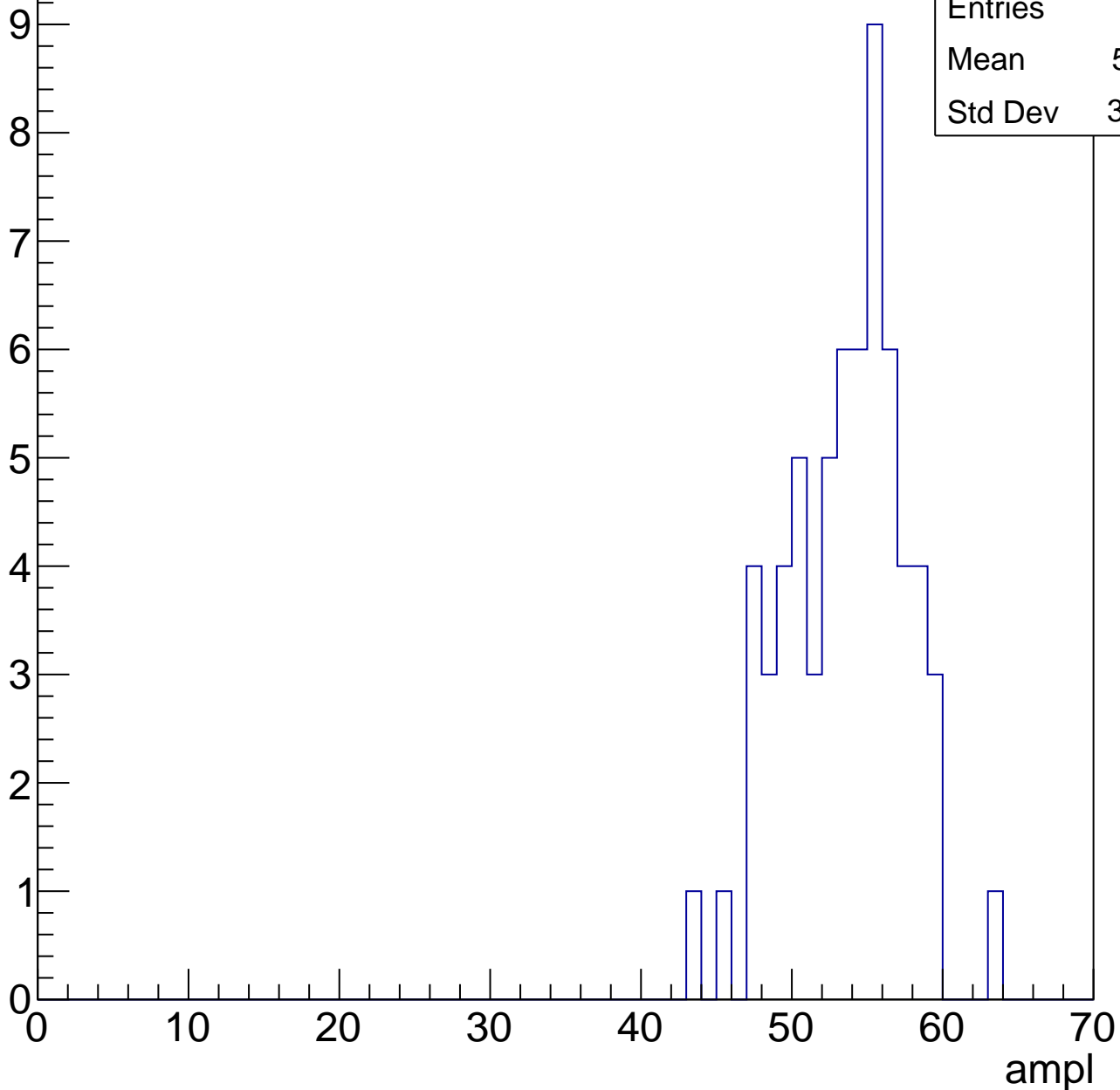


B1L101S, U6-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	53.11
Std Dev	3.875



B1L101S, U6-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	57
Mean	58.77
Std Dev	3.032

ampl

0

10

20

30

40

50

60

70

B1L101S, U6-ch62, adc6

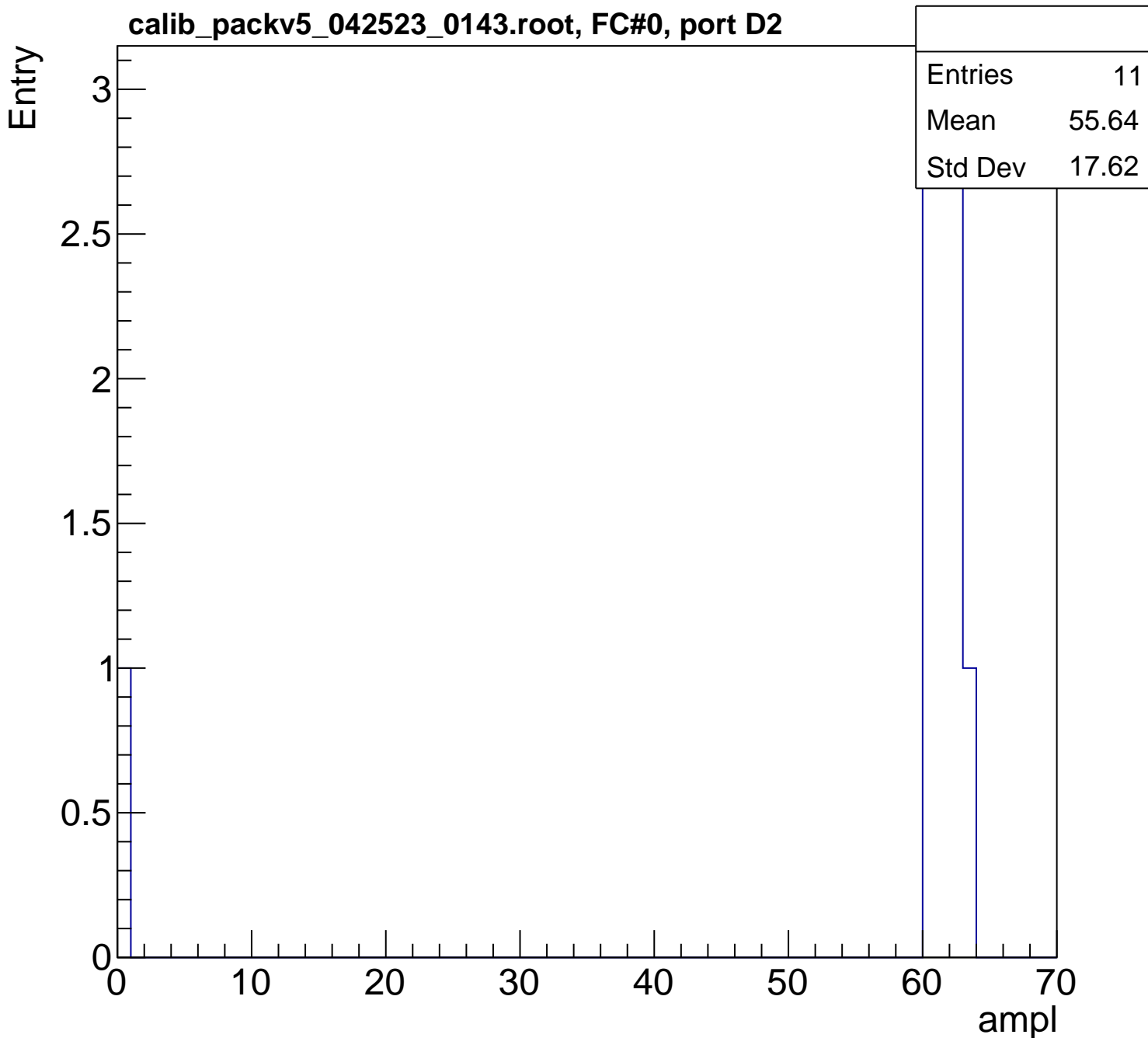
calib_packv5_042523_0143.root, FC#0, port D2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	55.64
Std Dev	17.62

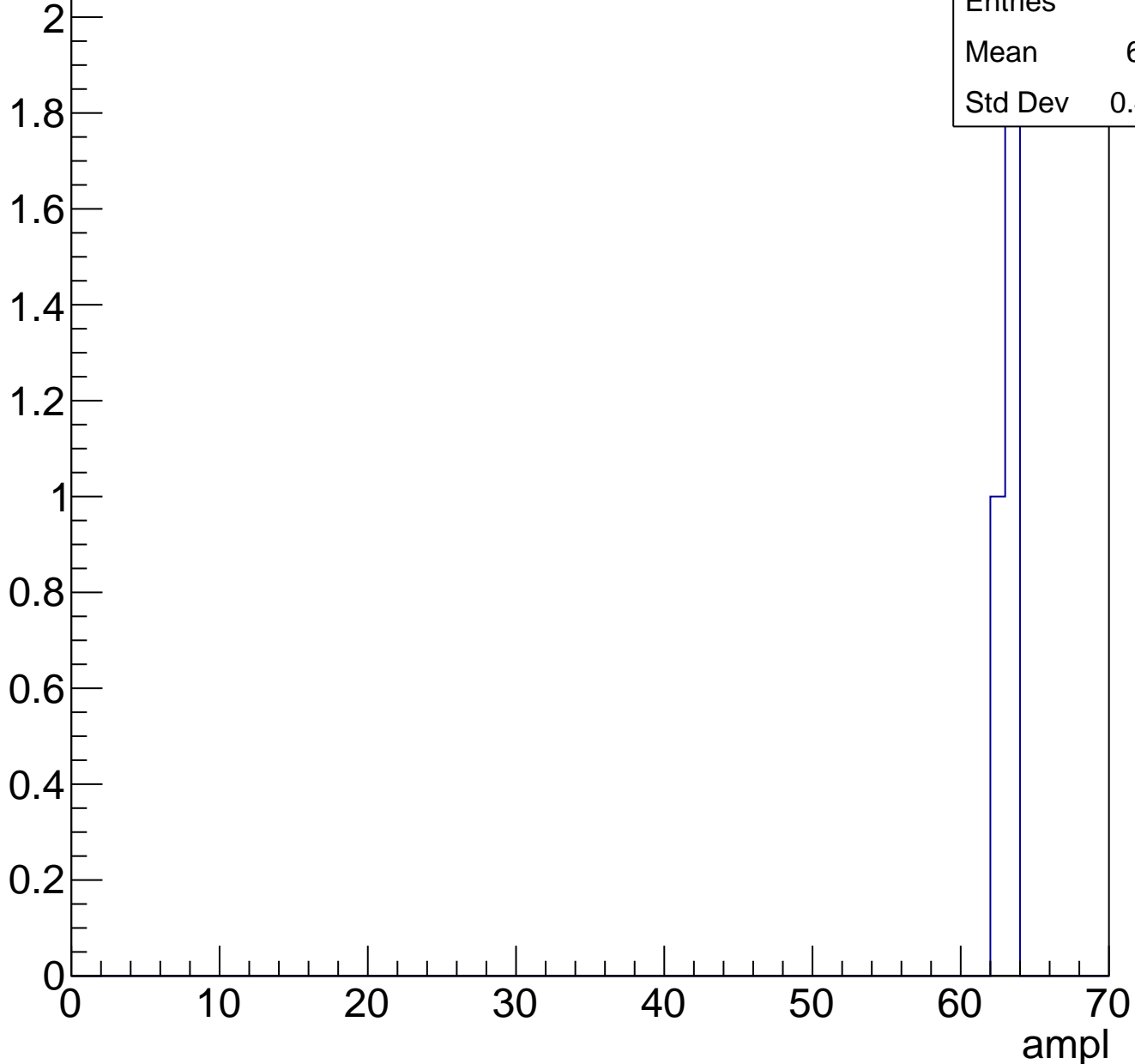
ampl



B1L101S, U6-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch63, adc0

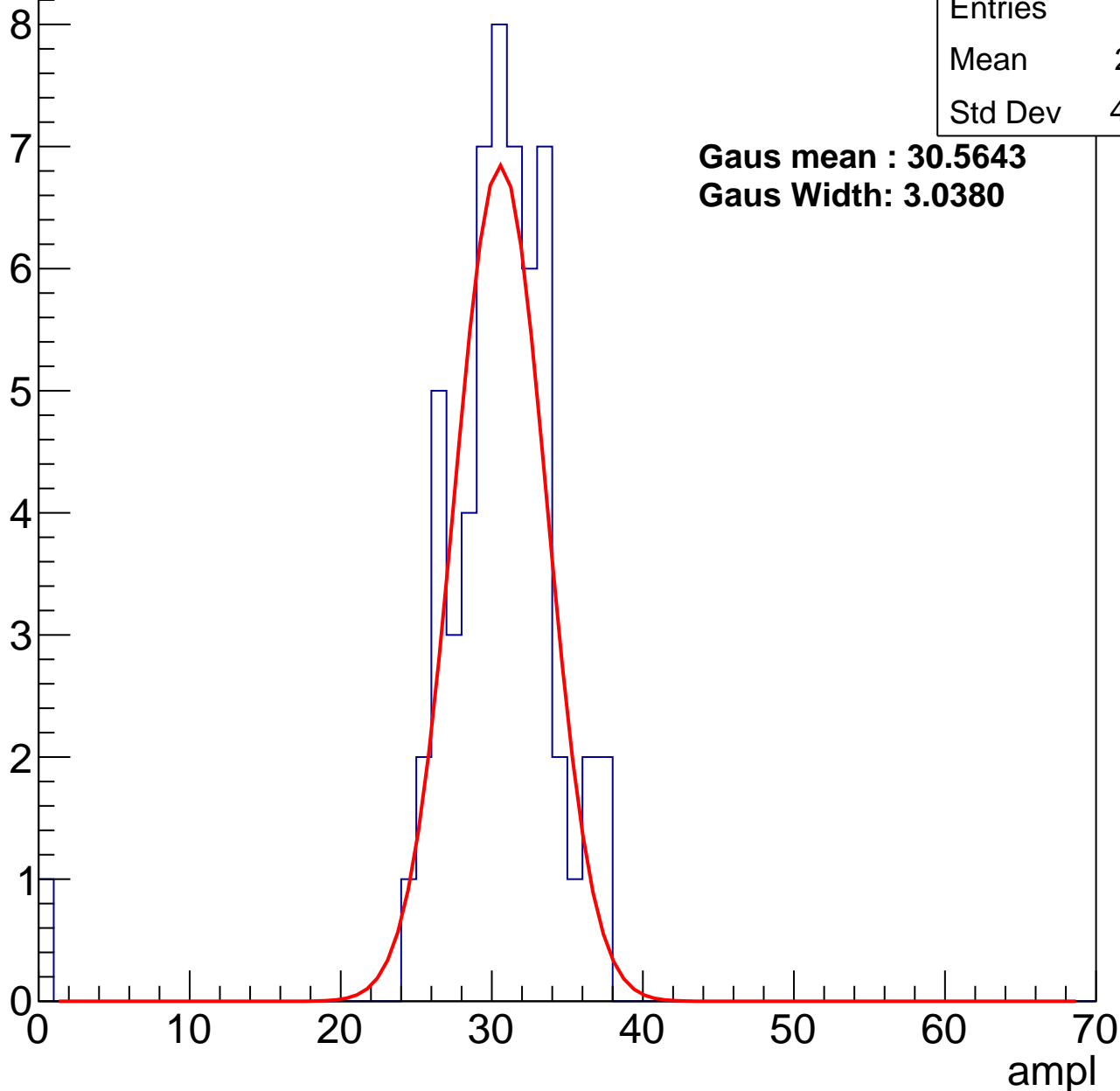
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.81
Std Dev	4.974

Gaus mean : 30.5643

Gaus Width: 3.0380



B1L101S, U6-ch63, adc1

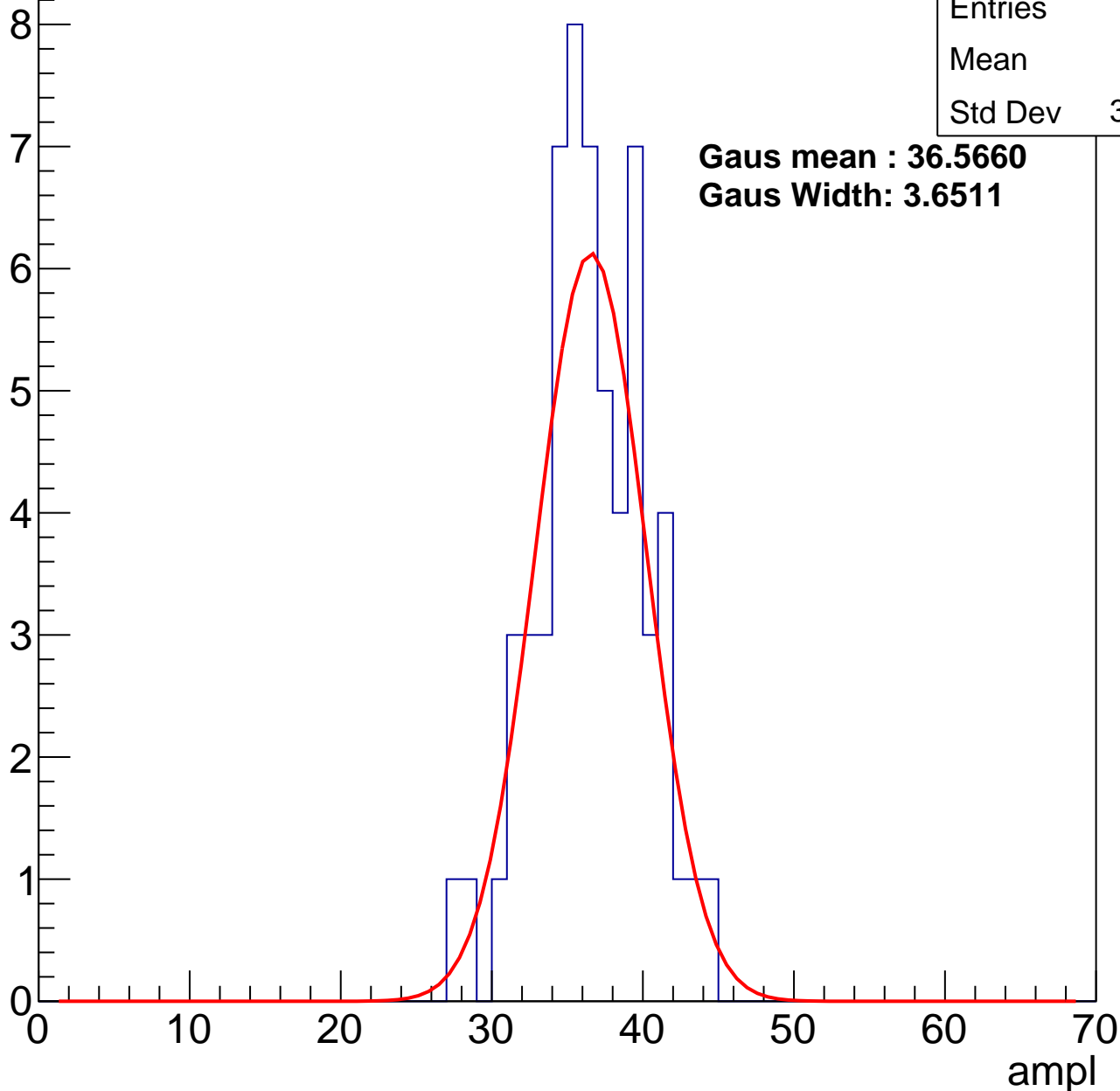
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.1
Std Dev	3.529

Gaus mean : 36.5660

Gaus Width: 3.6511



B1L101S, U6-ch63, adc2

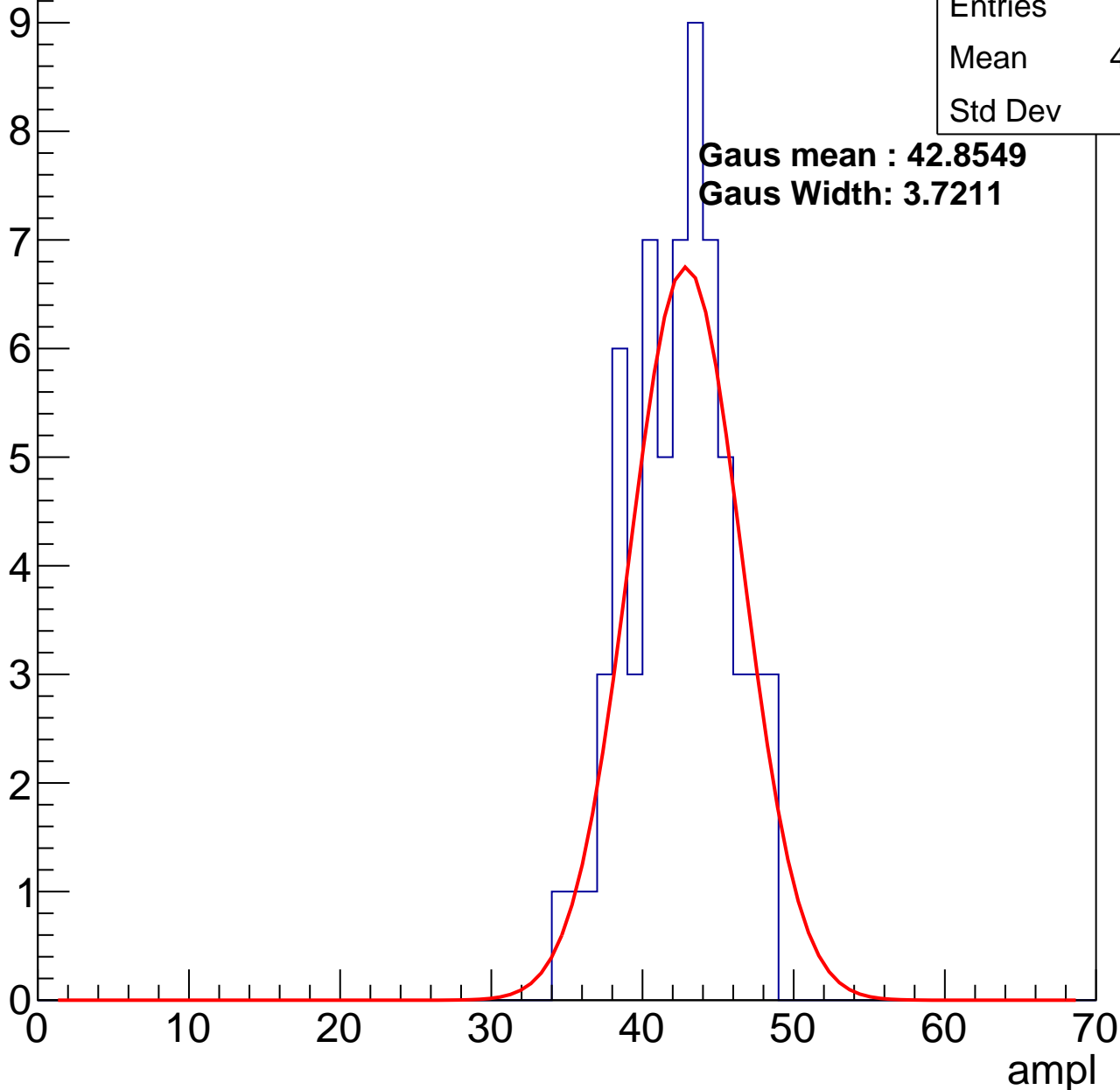
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	41.92
Std Dev	3.29

Gaus mean : 42.8549

Gaus Width: 3.7211



B1L101S, U6-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	49.75
Std Dev	3.66

Entry

10

8

6

4

2

0

0

10

20

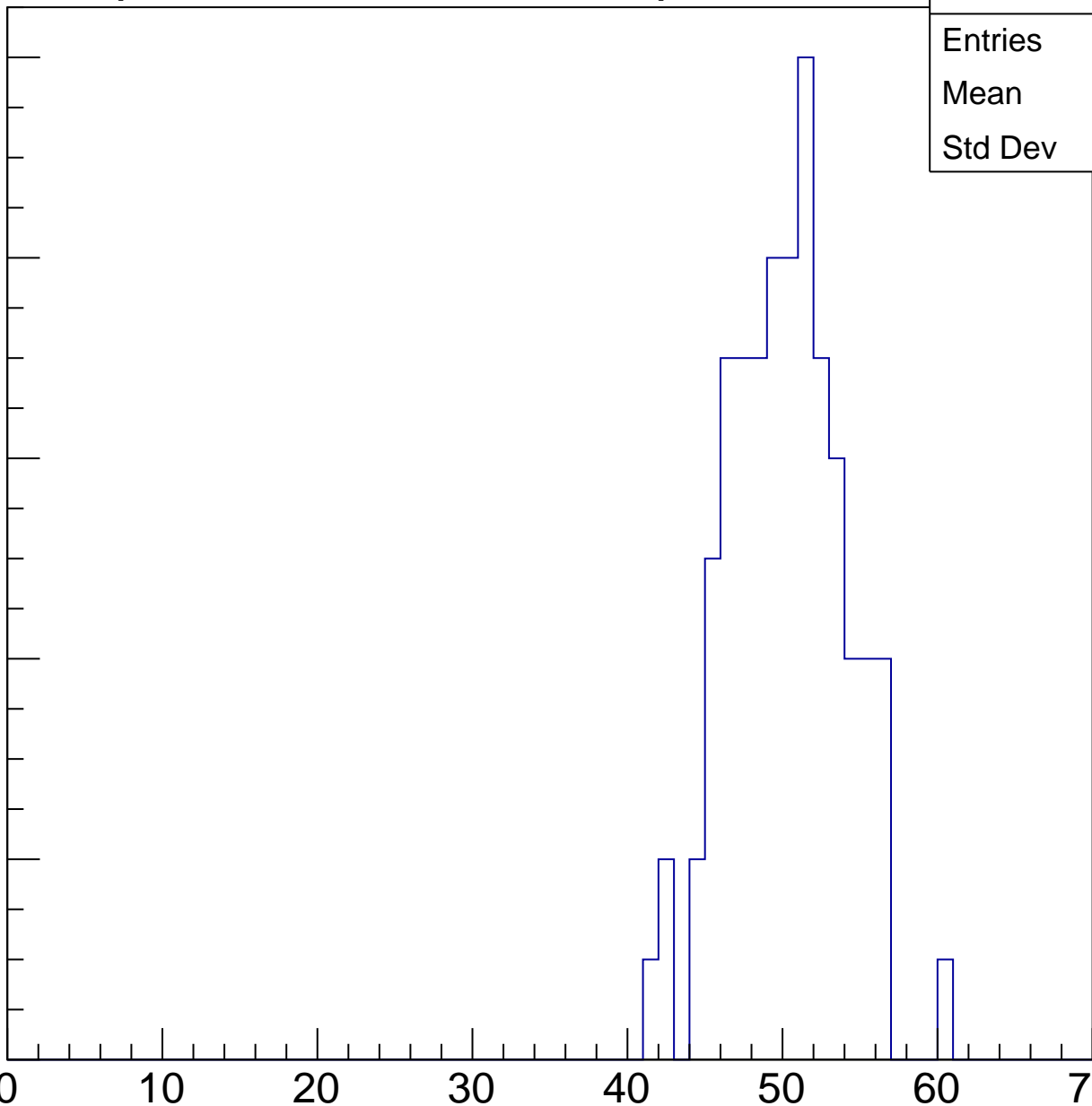
30

40

50

60

ampl

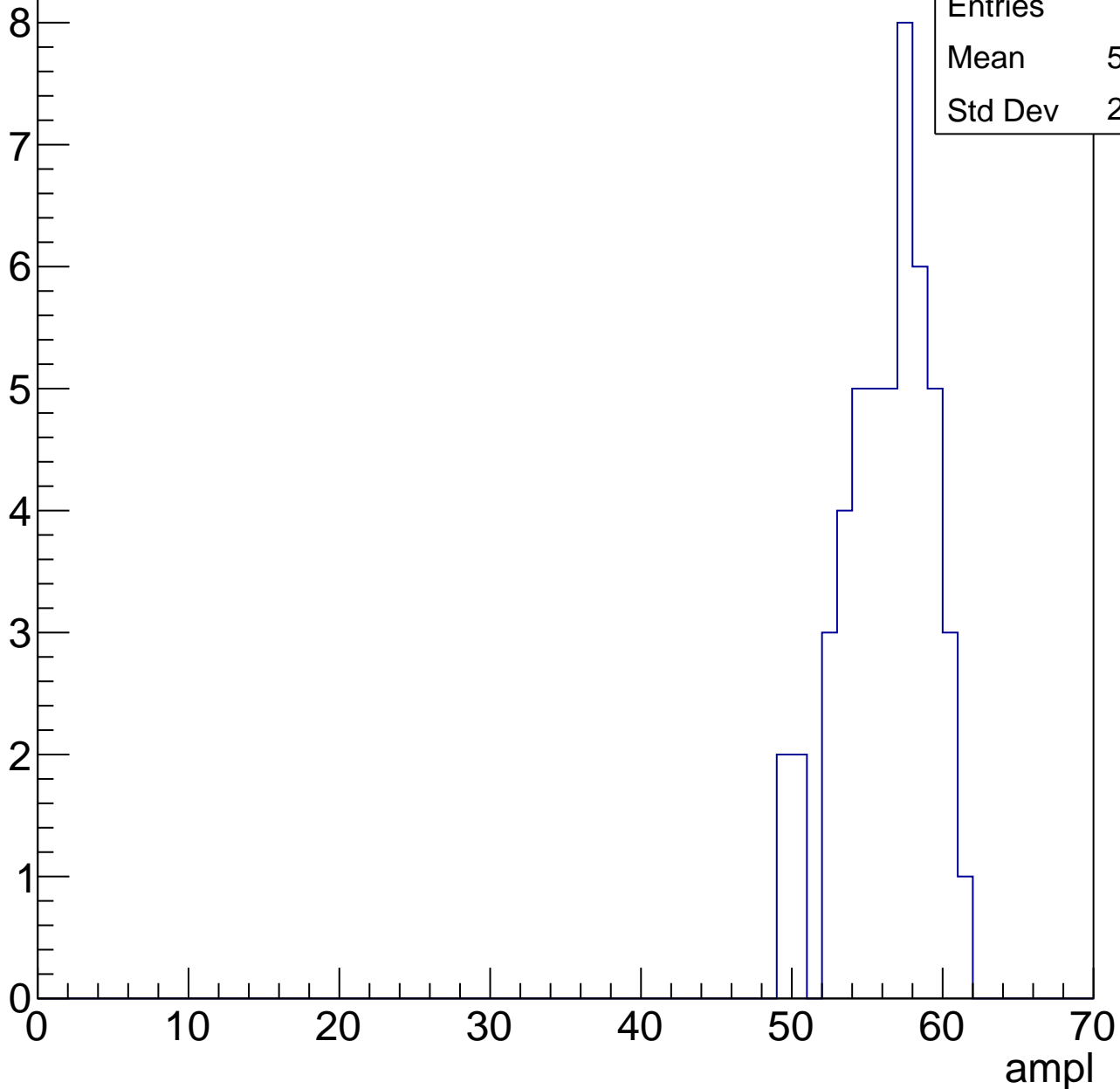


B1L101S, U6-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.73
Std Dev	2.947



B1L101S, U6-ch63, adc5

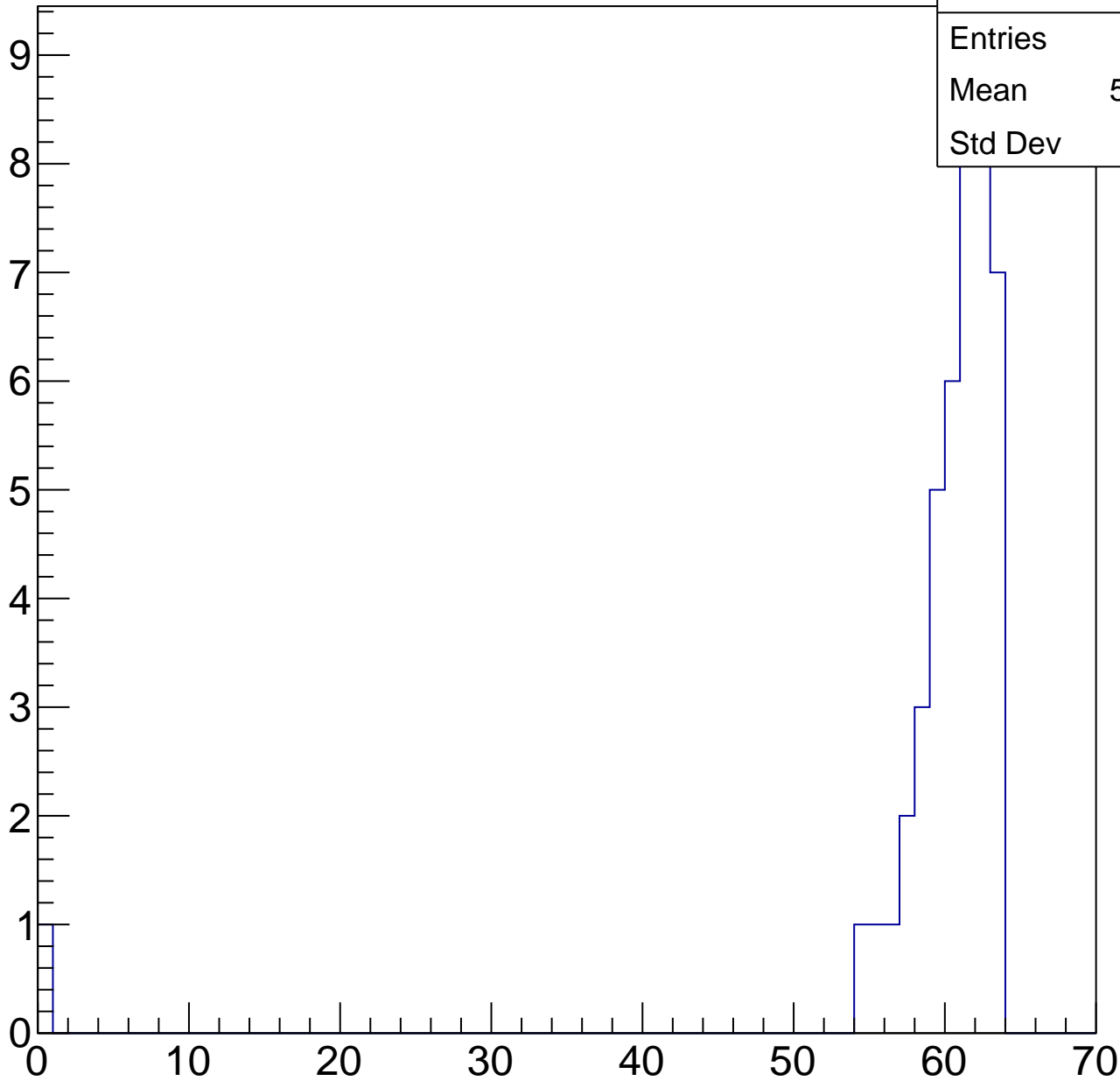
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.98
Std Dev	9.26

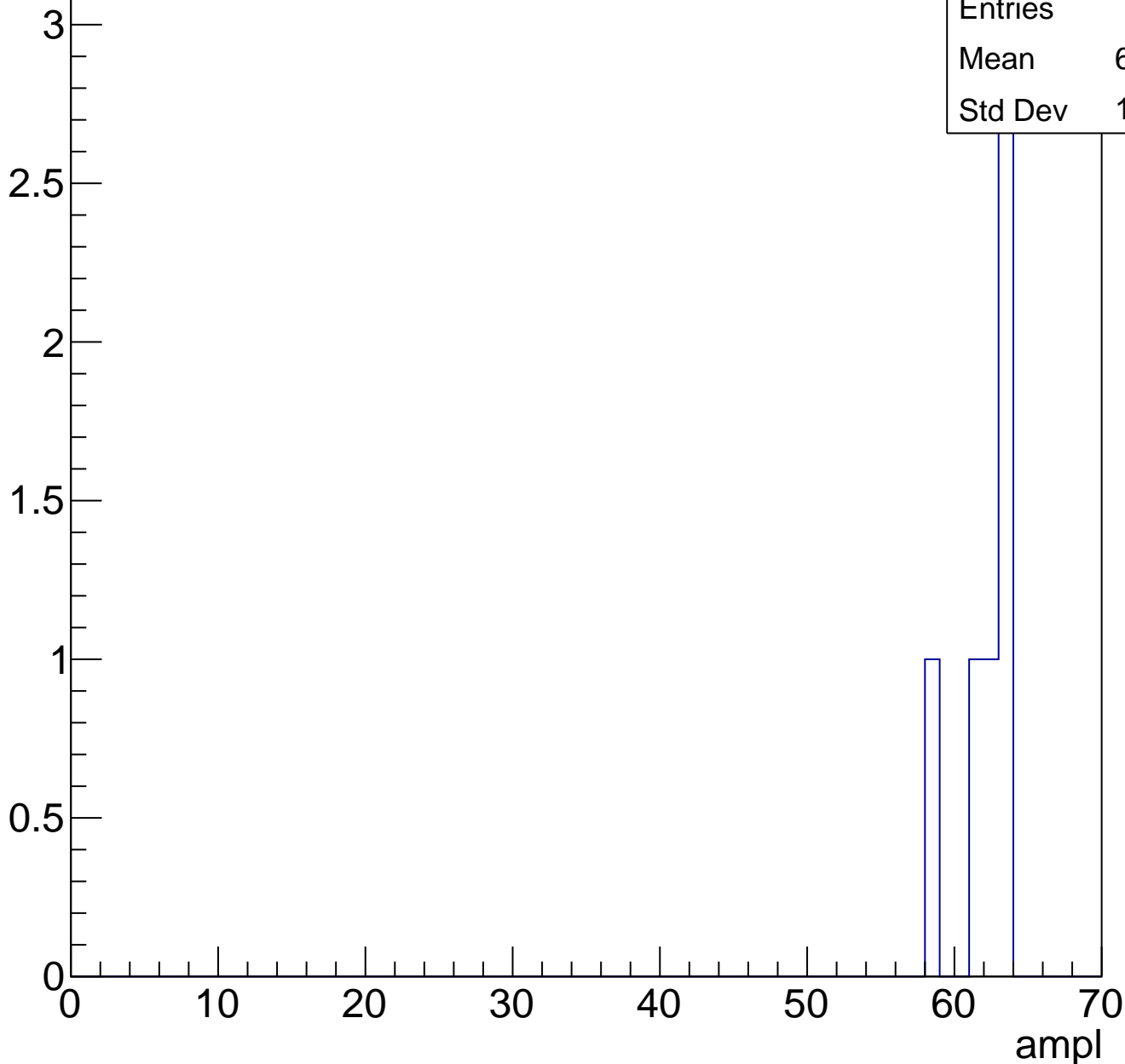
ampl



B1L101S, U6-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

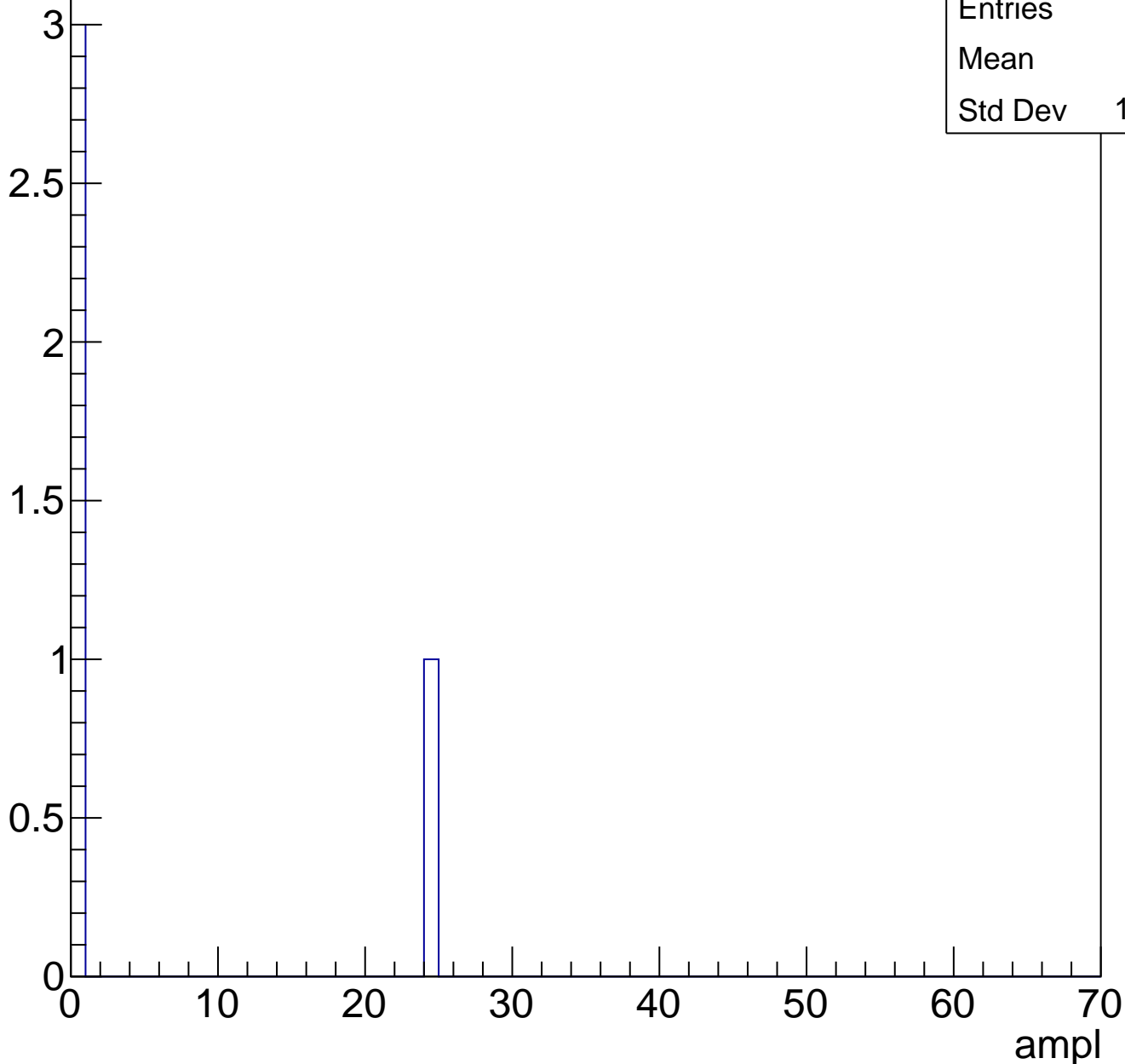
Entry



B1L101S, U6-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch64, adc0

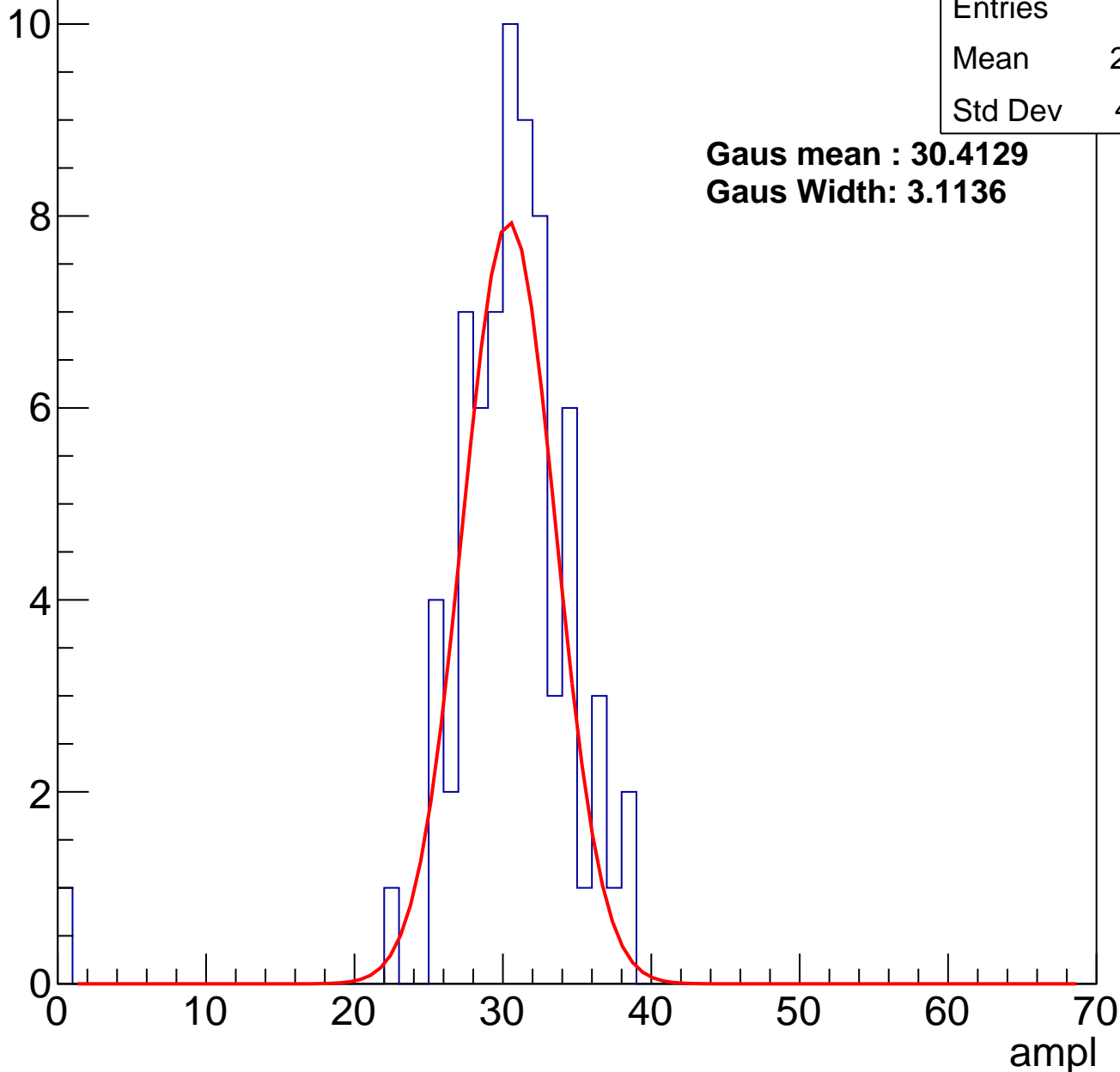
calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	29.97
Std Dev	4.841

Gaus mean : 30.4129

Gaus Width: 3.1136

Entry



B1L101S, U6-ch64, adc1

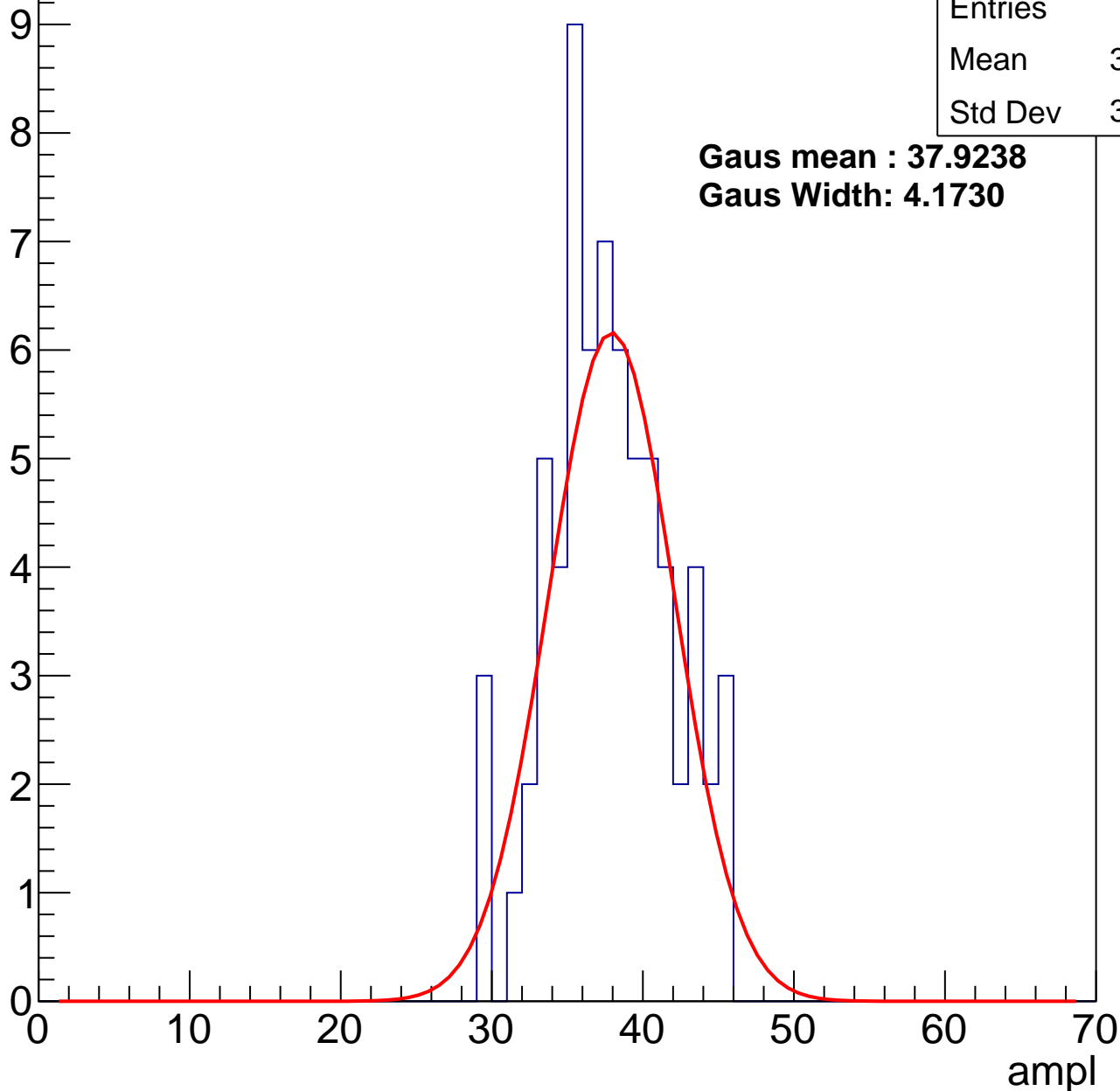
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.34
Std Dev	3.924

Gaus mean : 37.9238

Gaus Width: 4.1730



B1L101S, U6-ch64, adc2

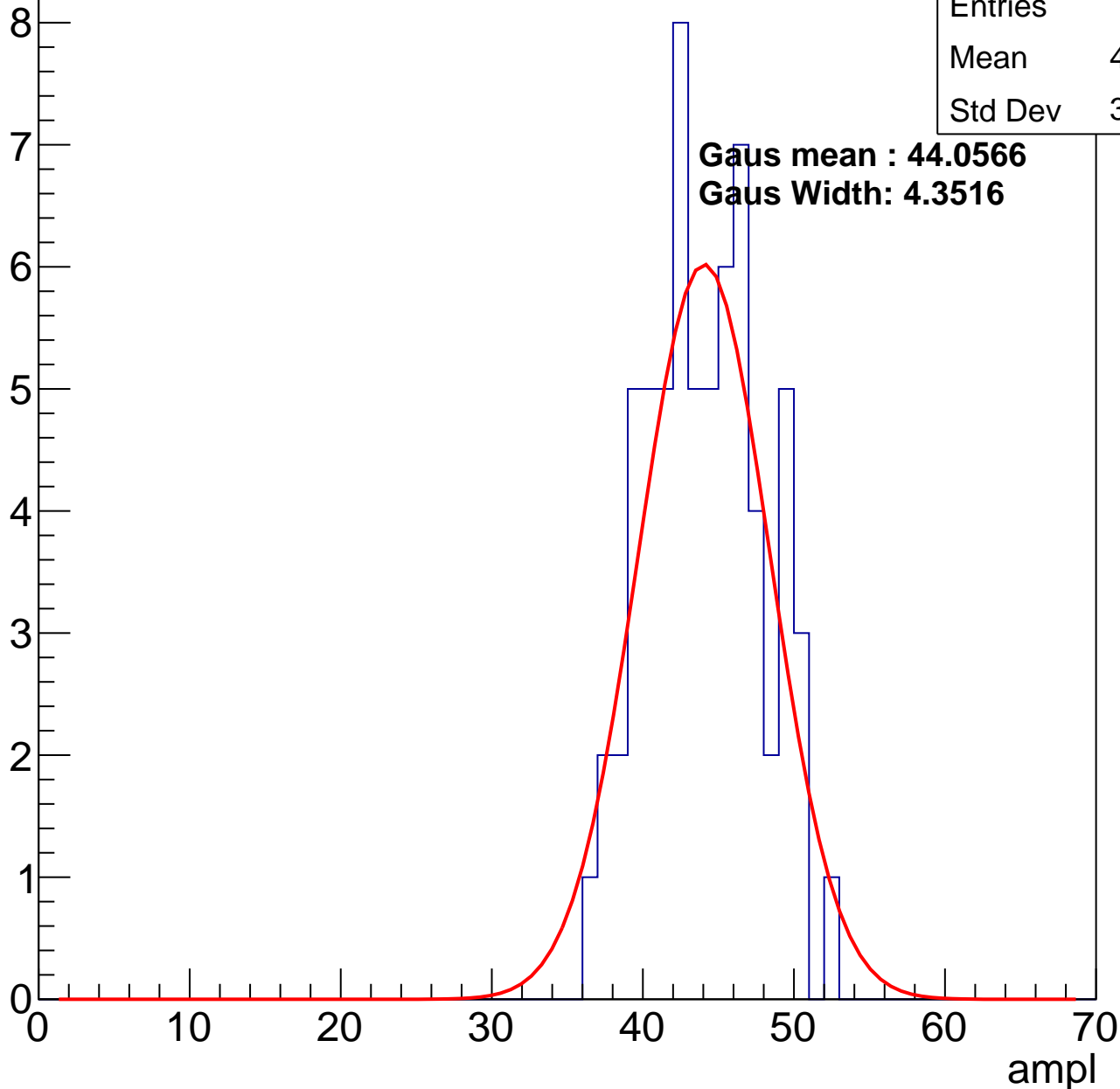
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.64
Std Dev	3.716

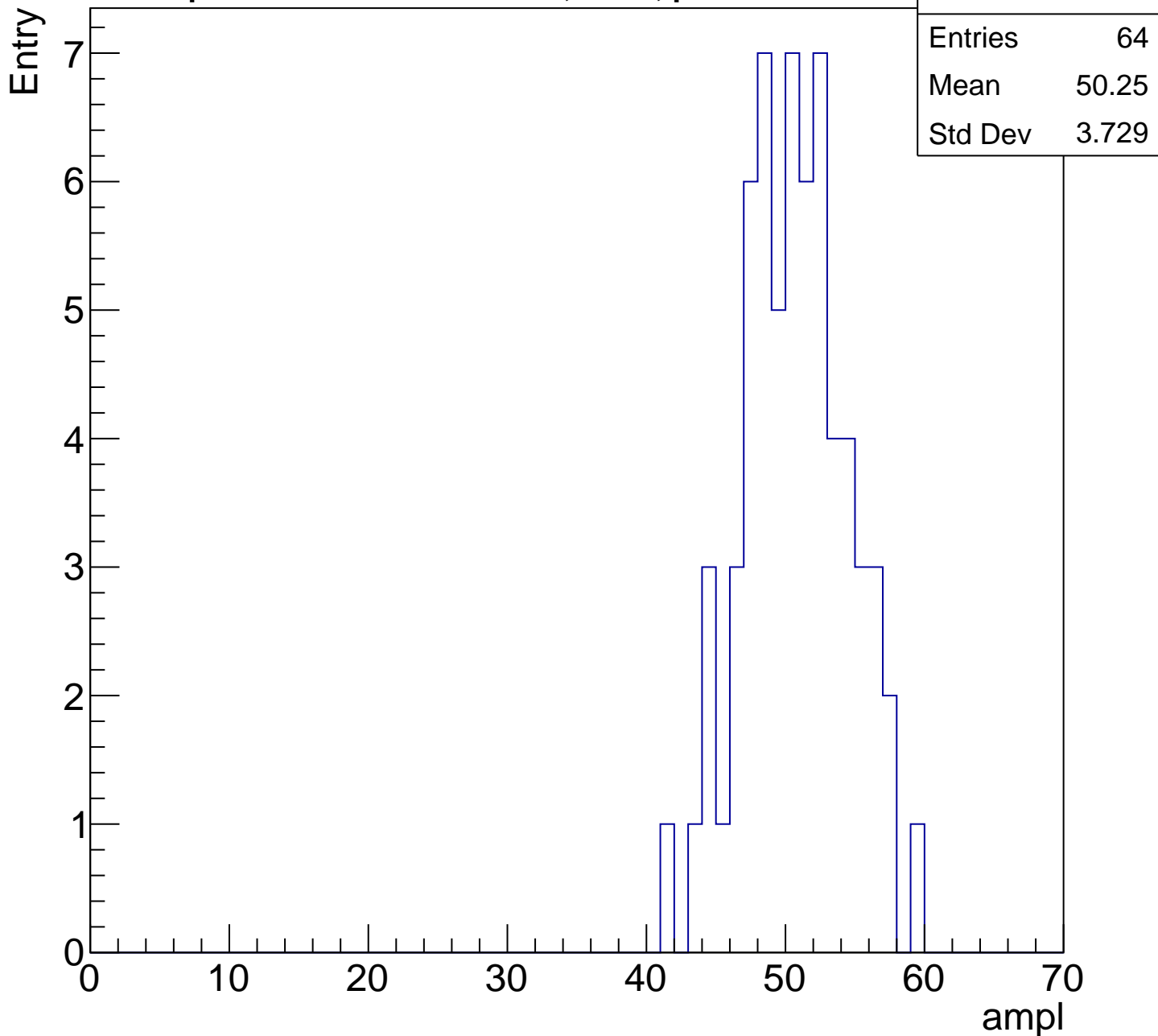
Gaus mean : 44.0566

Gaus Width: 4.3516



B1L101S, U6-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

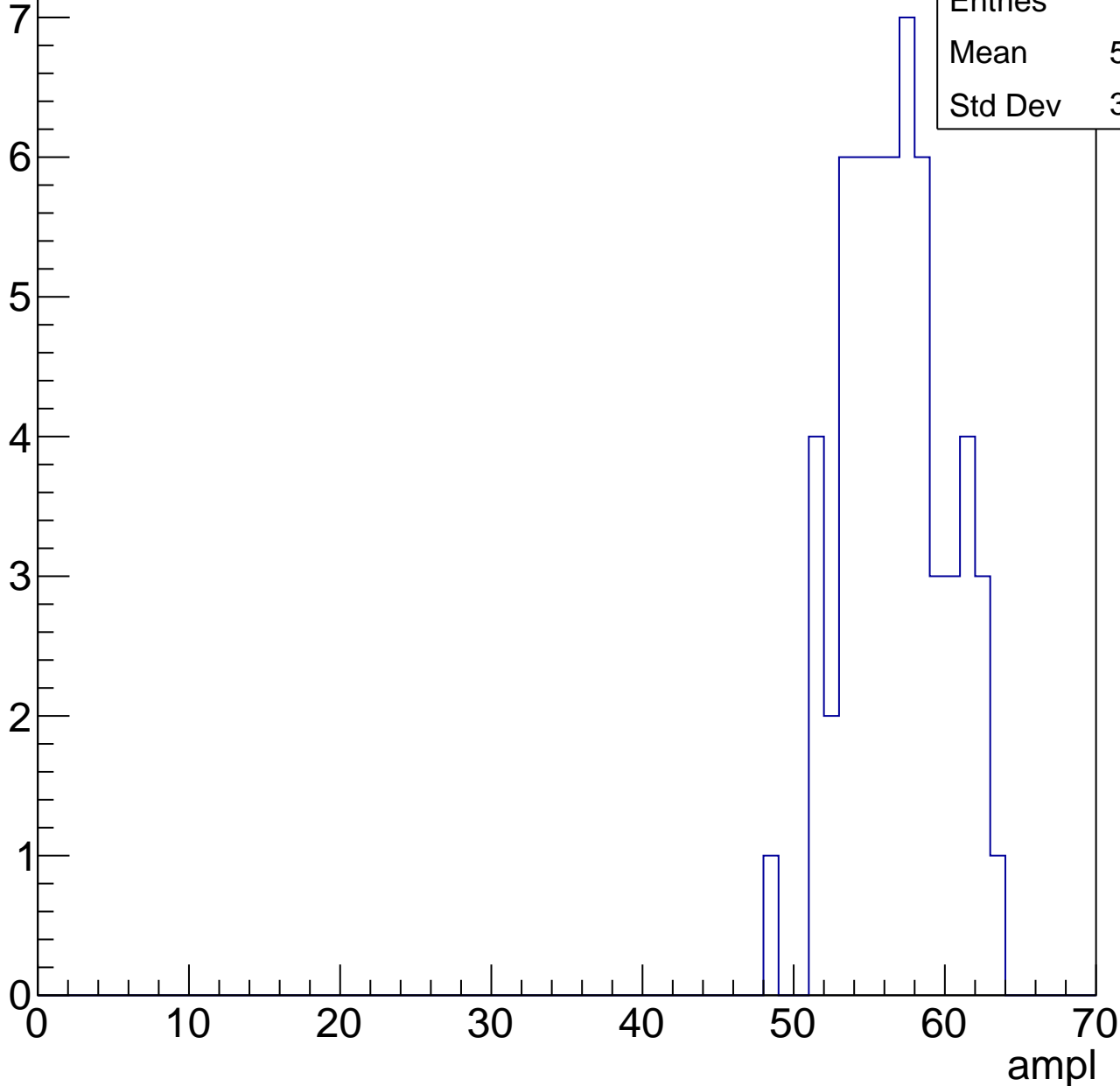


B1L101S, U6-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

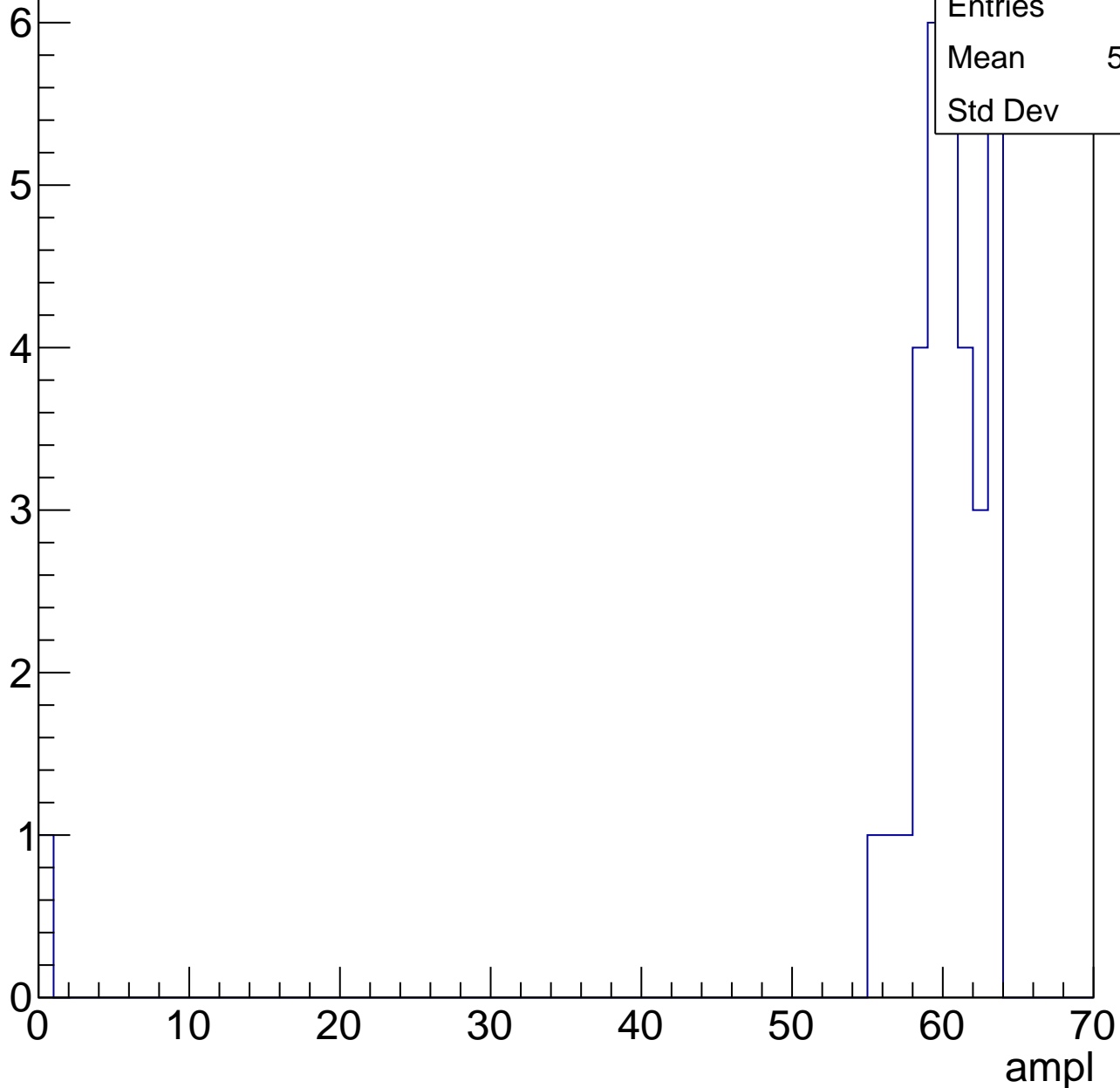
Entries	58
Mean	56.22
Std Dev	3.322



B1L101S, U6-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

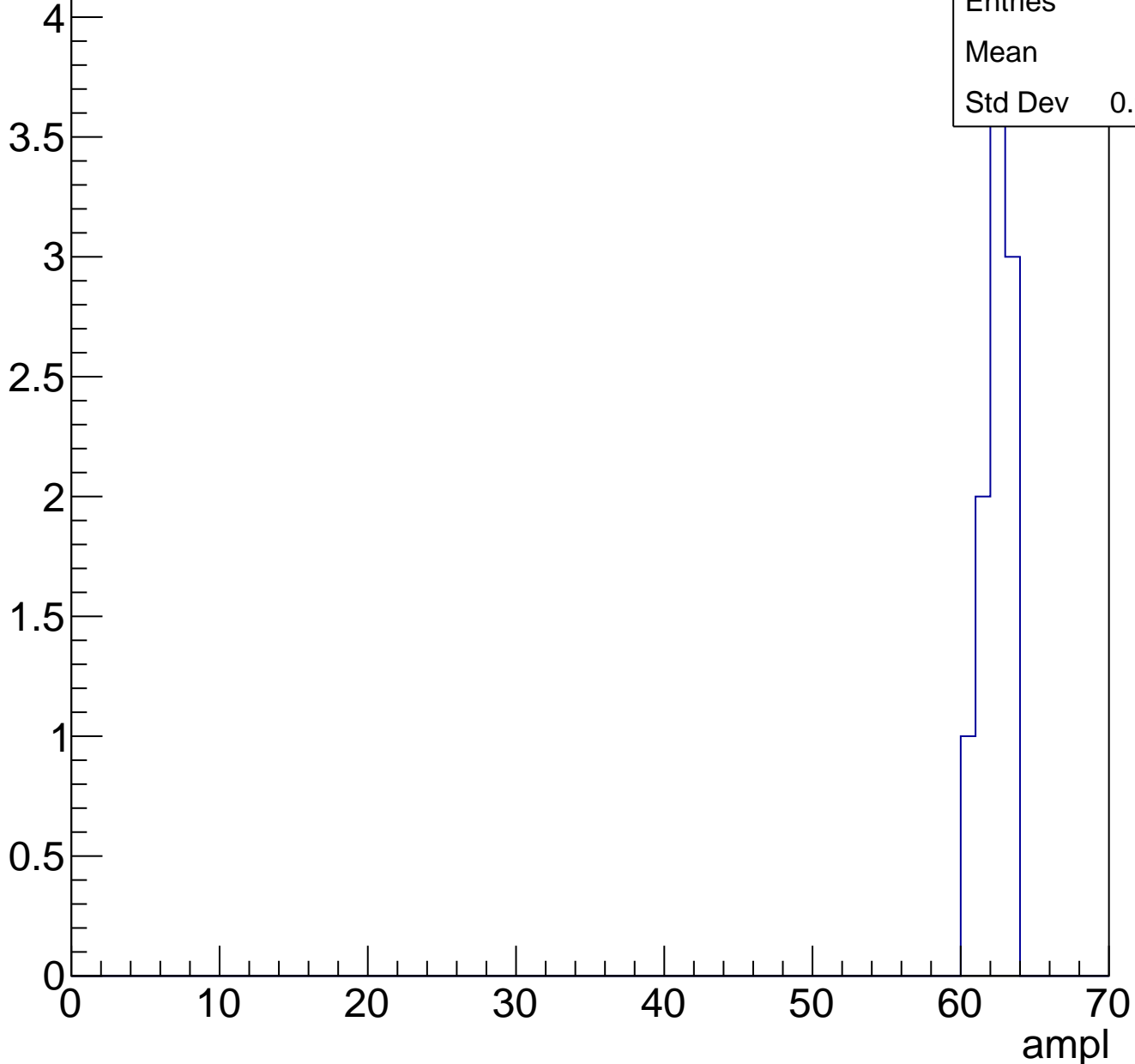
Entry



B1L101S, U6-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	61.9
Std Dev	0.9434

B1L101S, U6-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch65, adc0

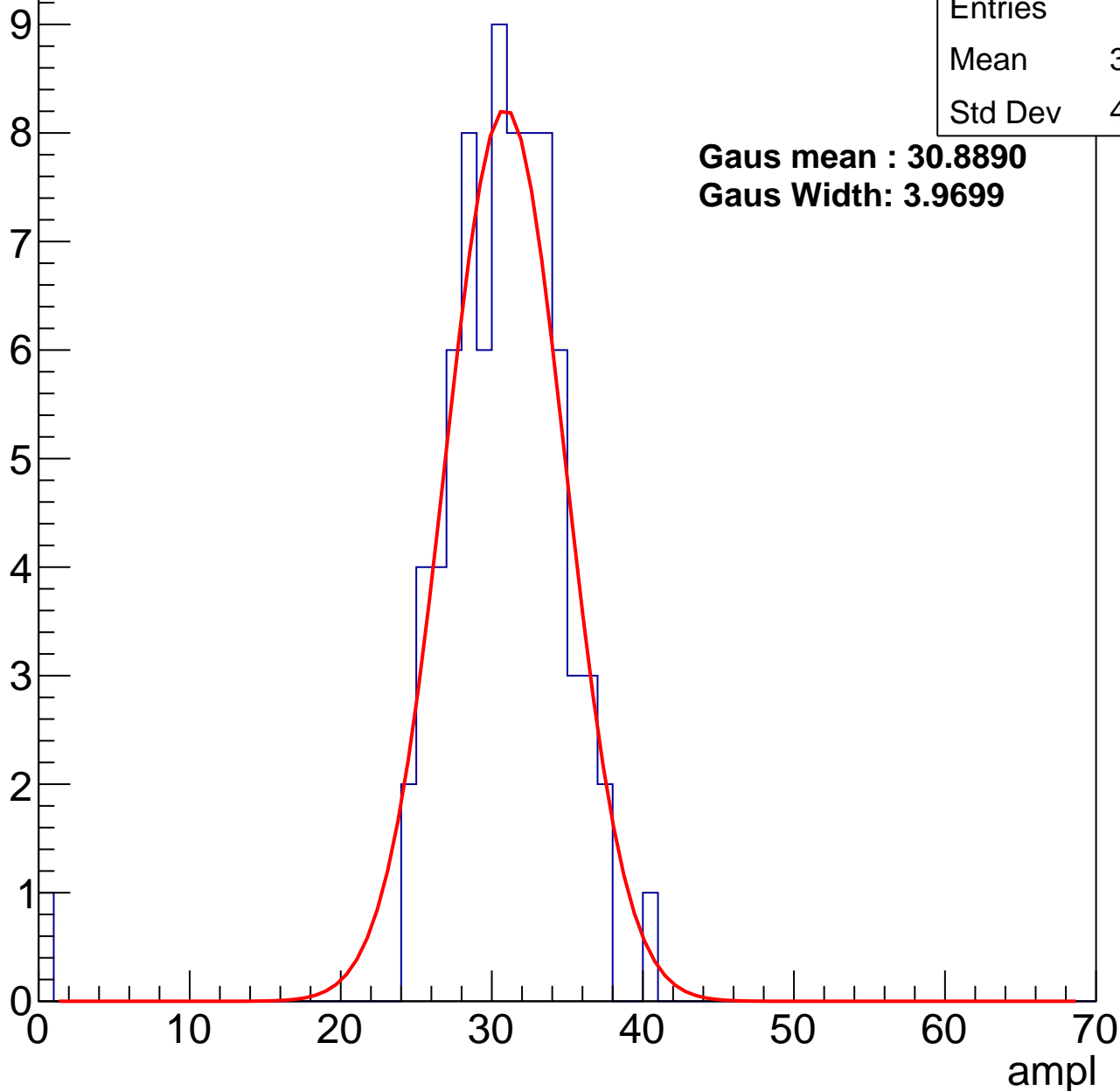
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	30.14
Std Dev	4.794

Gaus mean : 30.8890

Gaus Width: 3.9699



B1L101S, U6-ch65, adc1

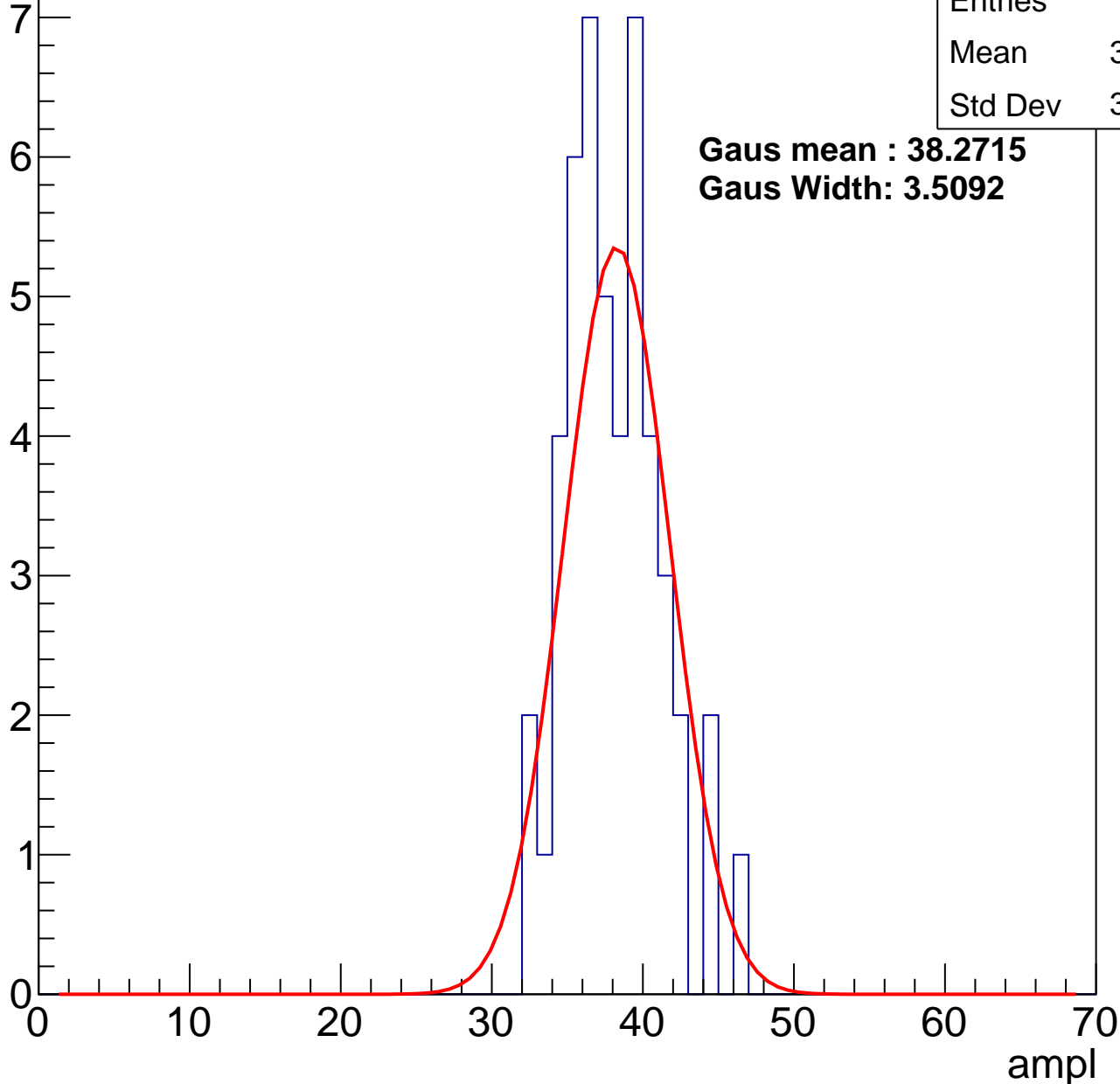
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	37.62
Std Dev	3.093

Gaus mean : 38.2715

Gaus Width: 3.5092



B1L101S, U6-ch65, adc2

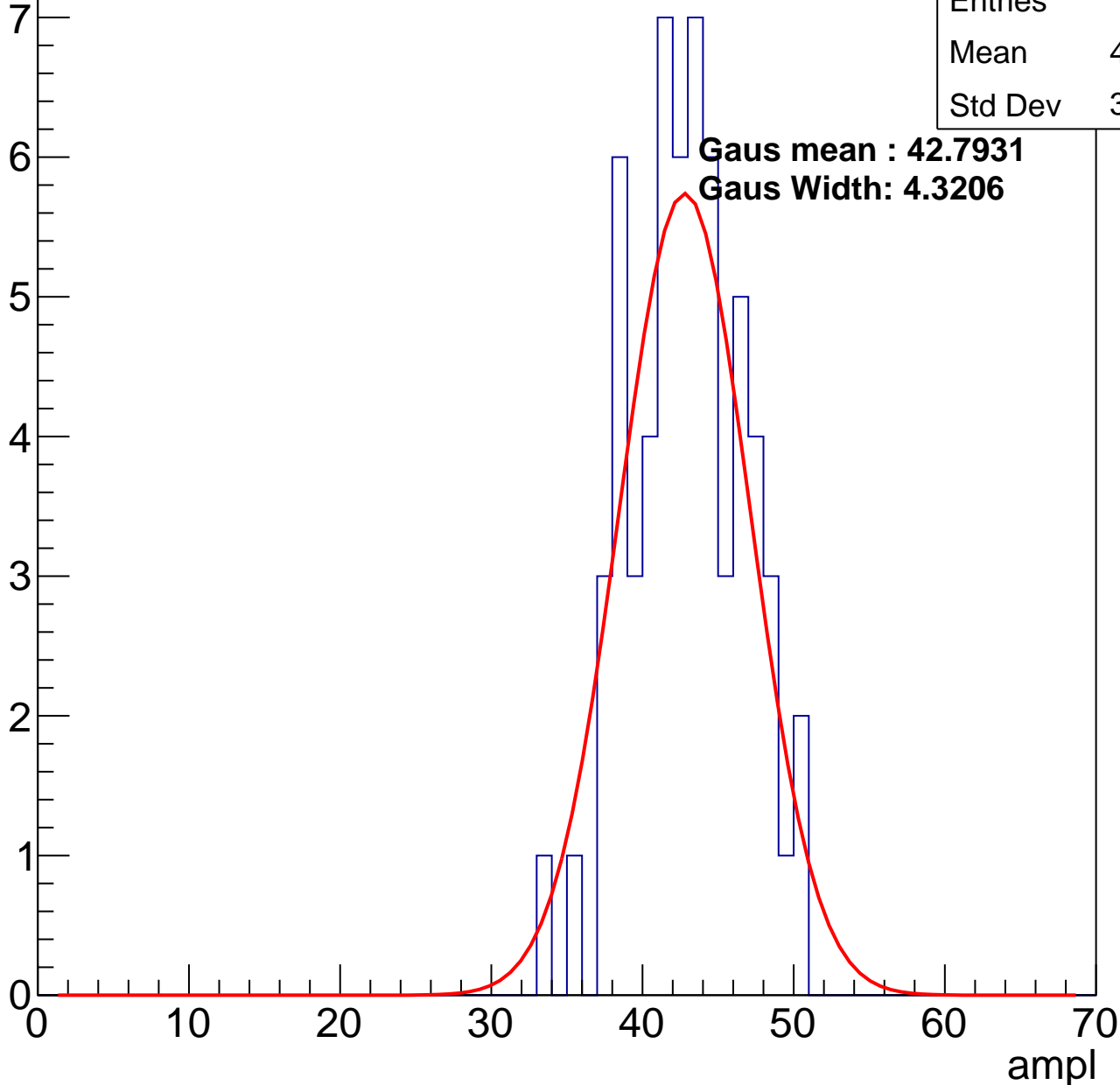
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.48
Std Dev	3.723

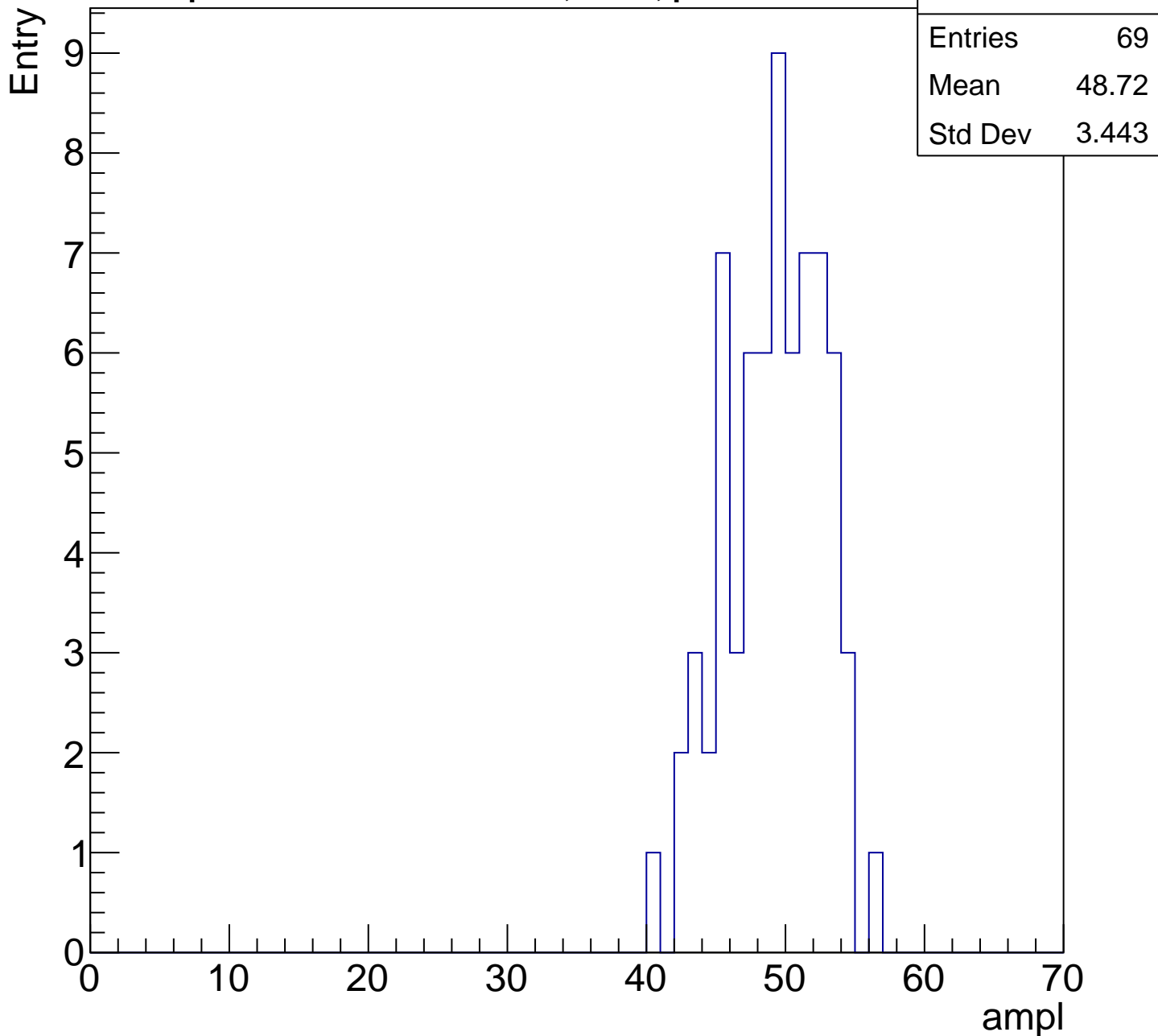
Gaus mean : 42.7931

Gaus Width: 4.3206



B1L101S, U6-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

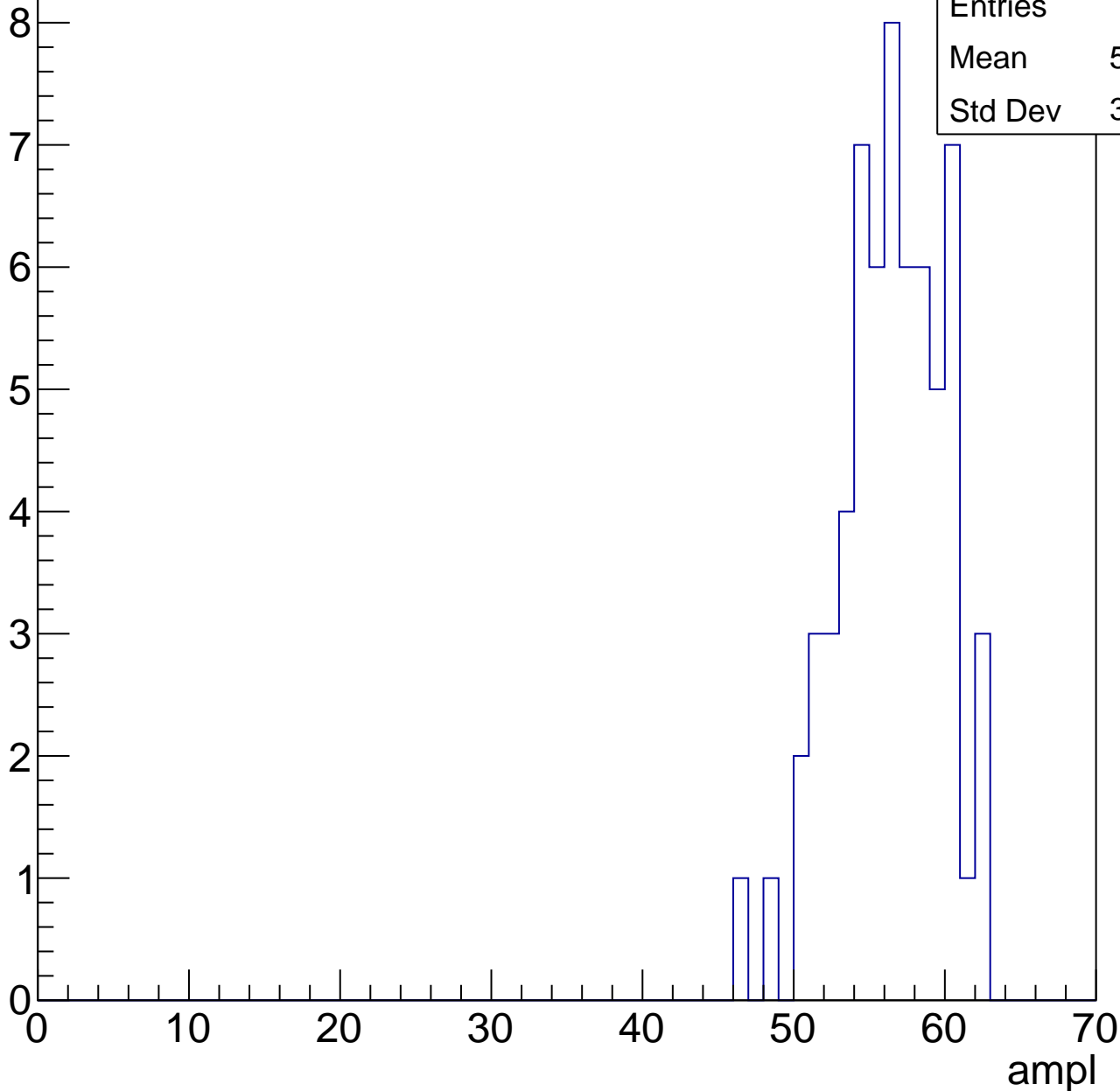


B1L101S, U6-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.92
Std Dev	3.447



B1L101S, U6-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

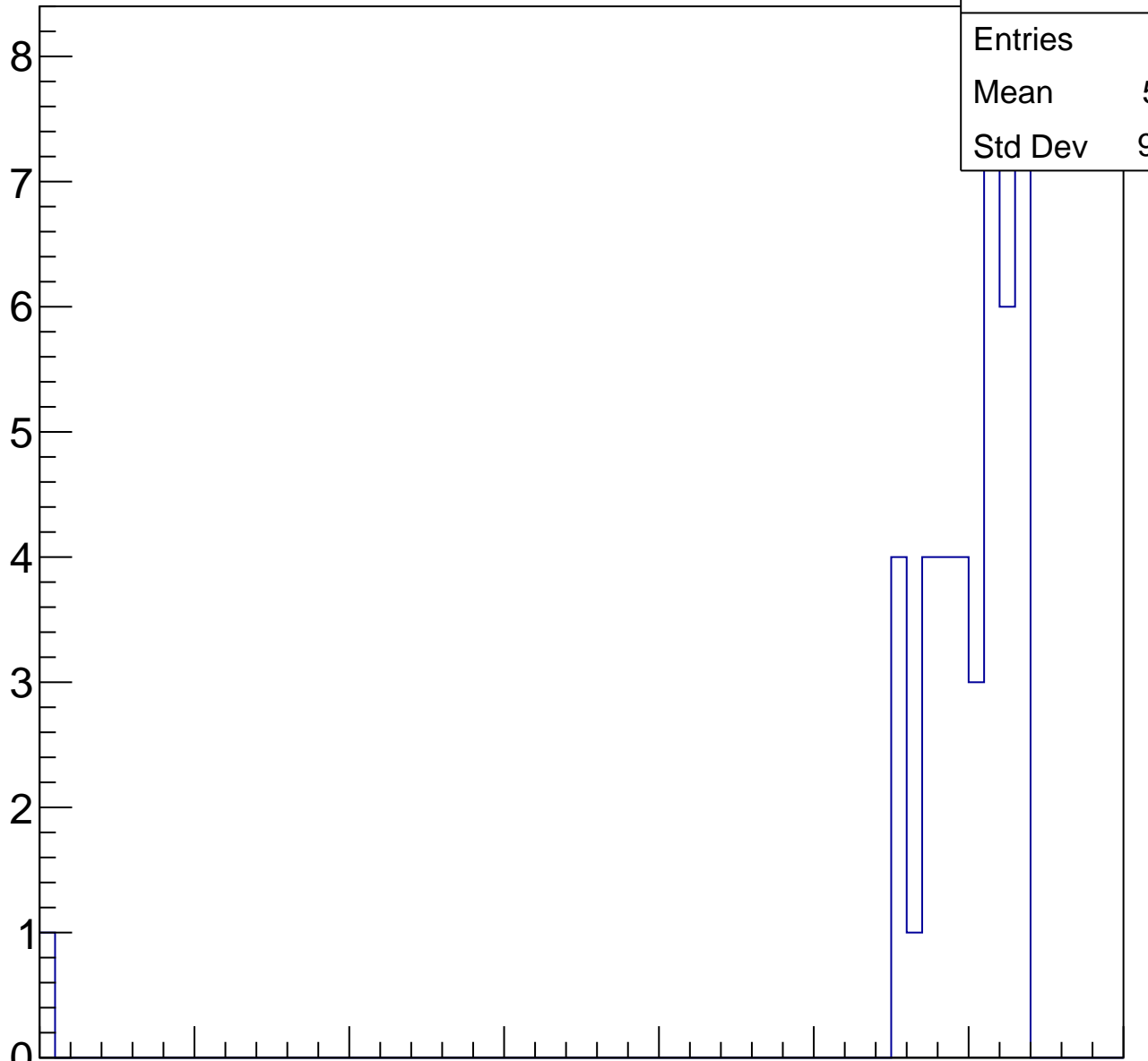
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.51
Std Dev	9.377

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

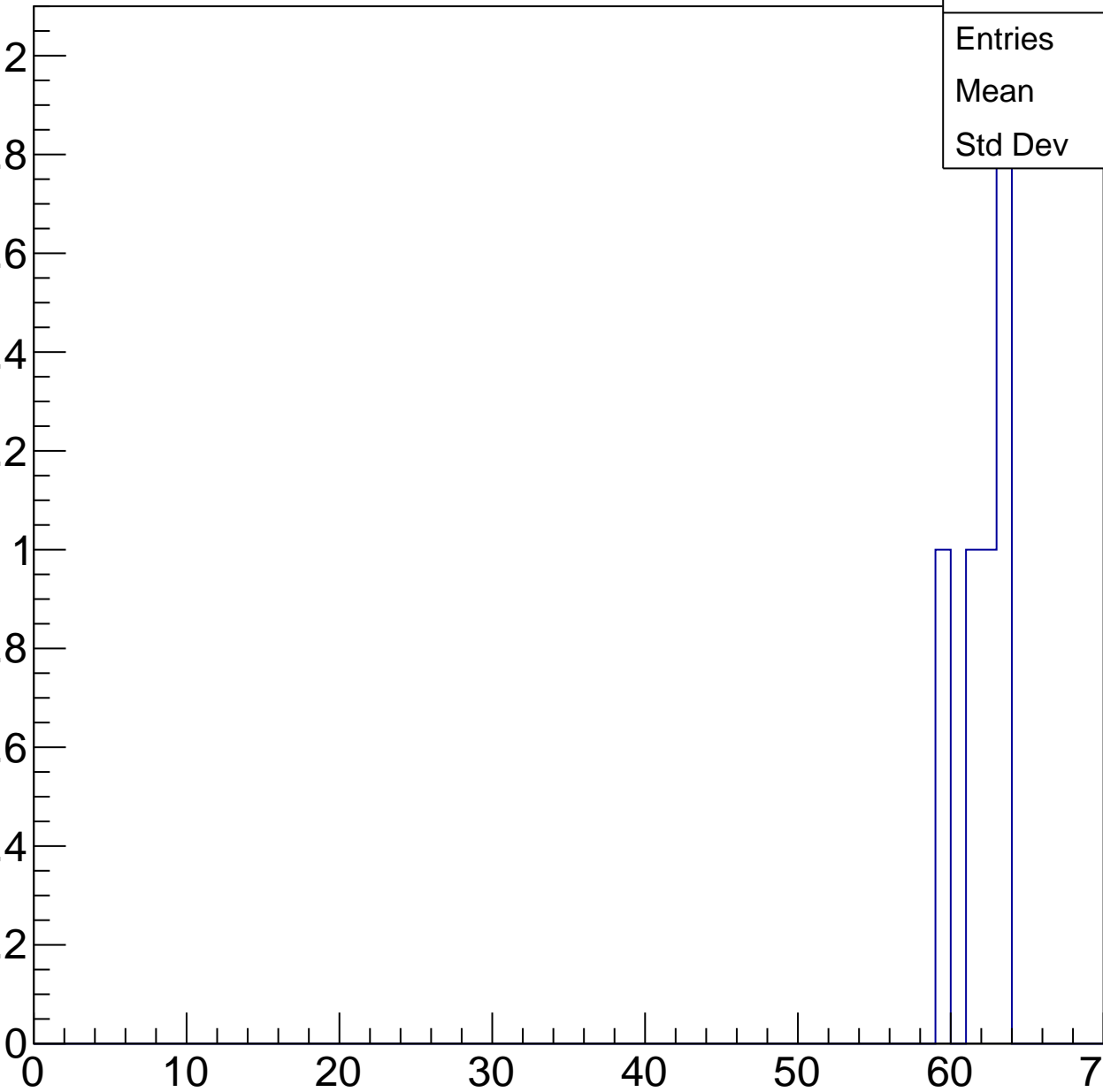
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.497

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U6-ch66, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	29.37
Std Dev	4.894

Gaus mean : 30.6154

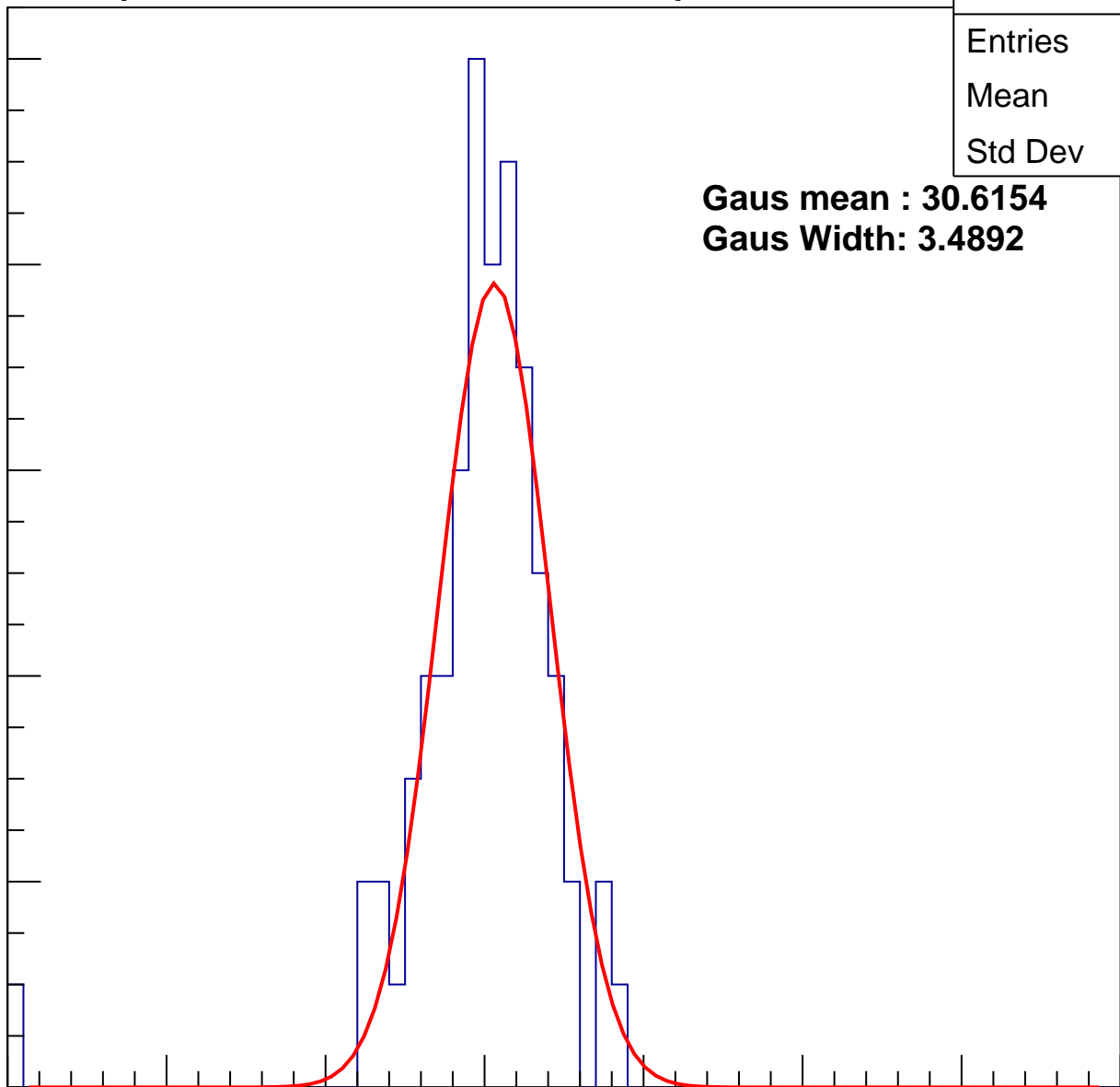
Gaus Width: 3.4892

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch66, adc1

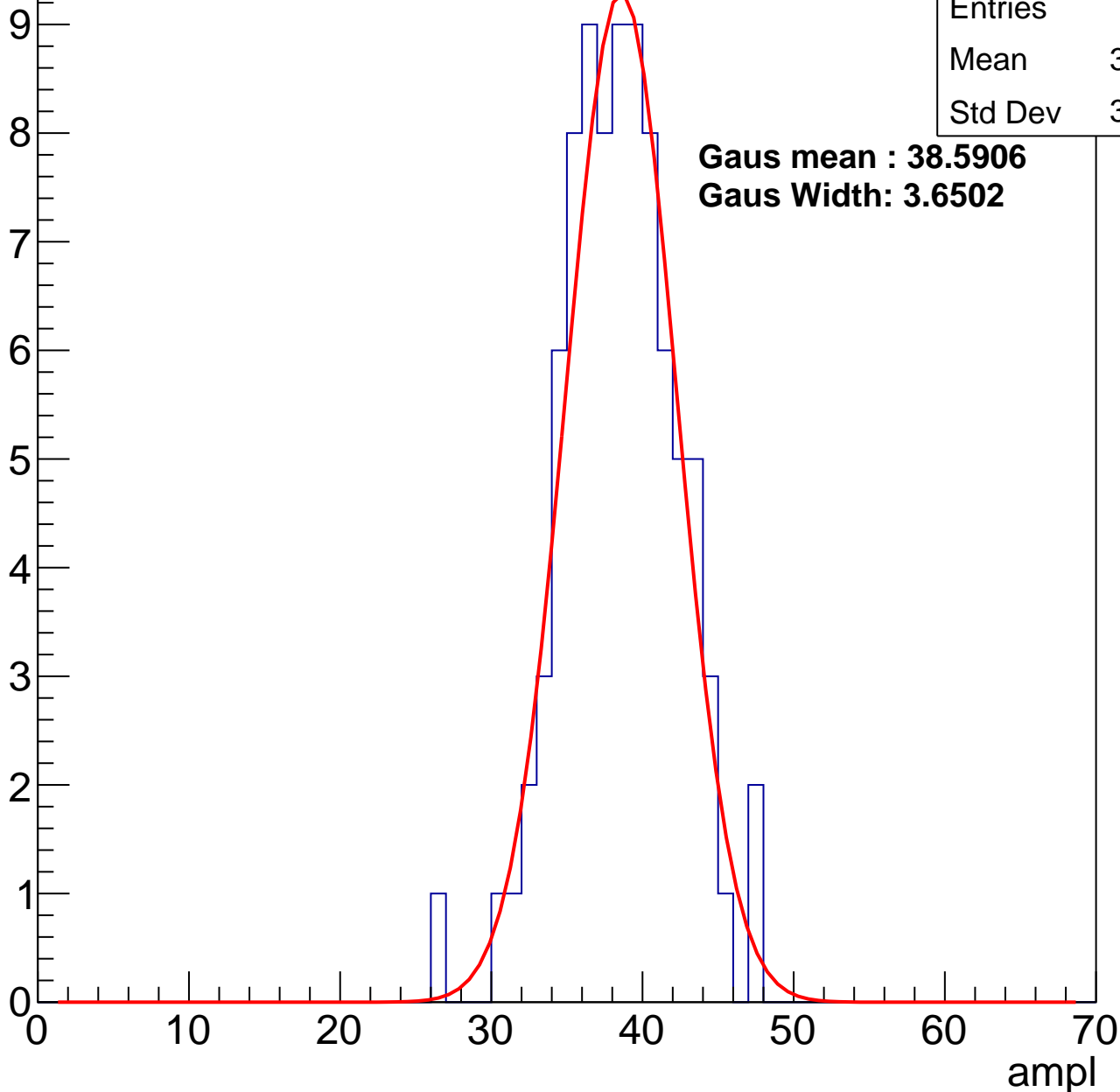
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	38.03
Std Dev	3.768

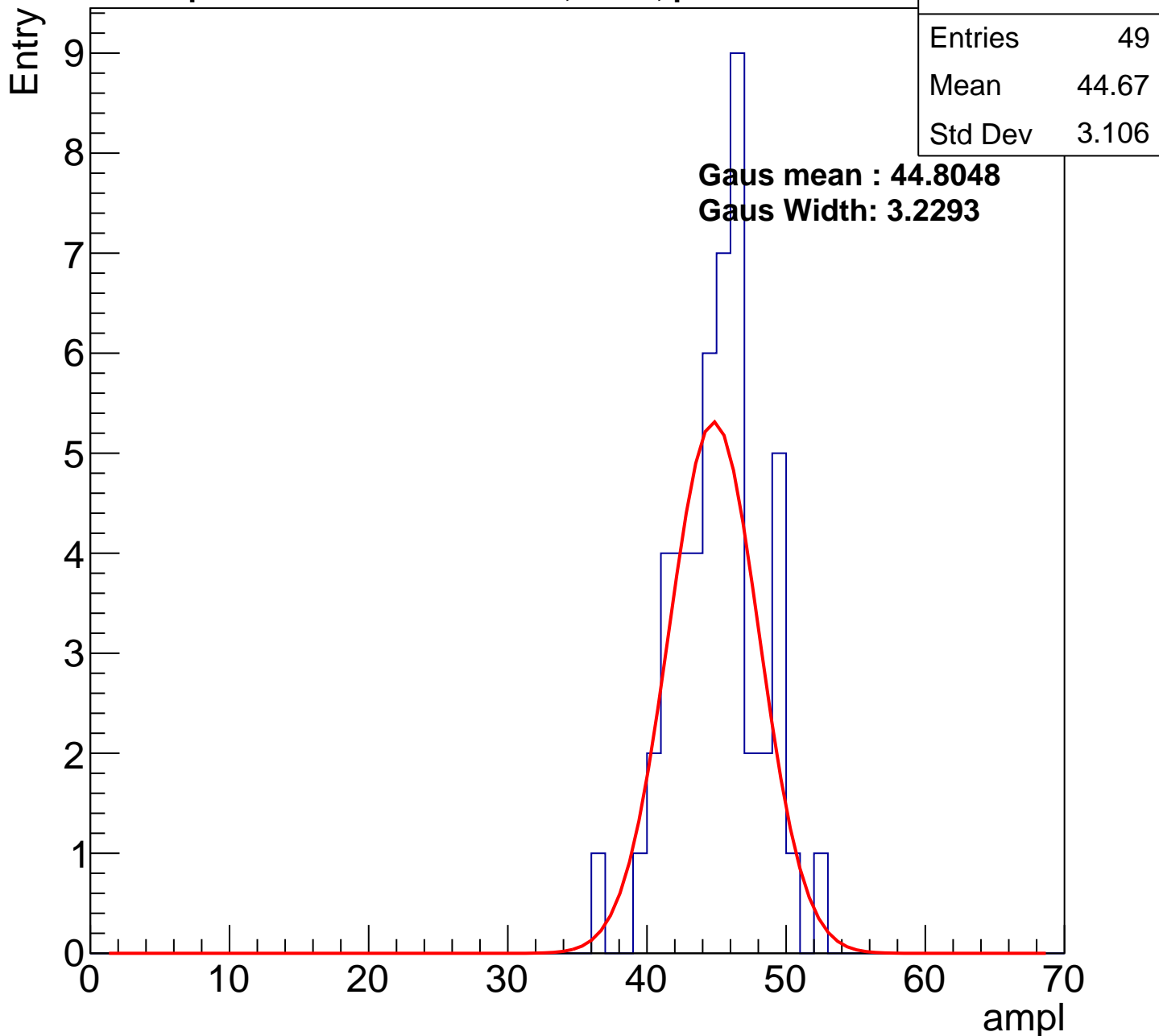
Gaus mean : 38.5906

Gaus Width: 3.6502



B1L101S, U6-ch66, adc2

calib_packv5_042523_0143.root, FC#0, port D2

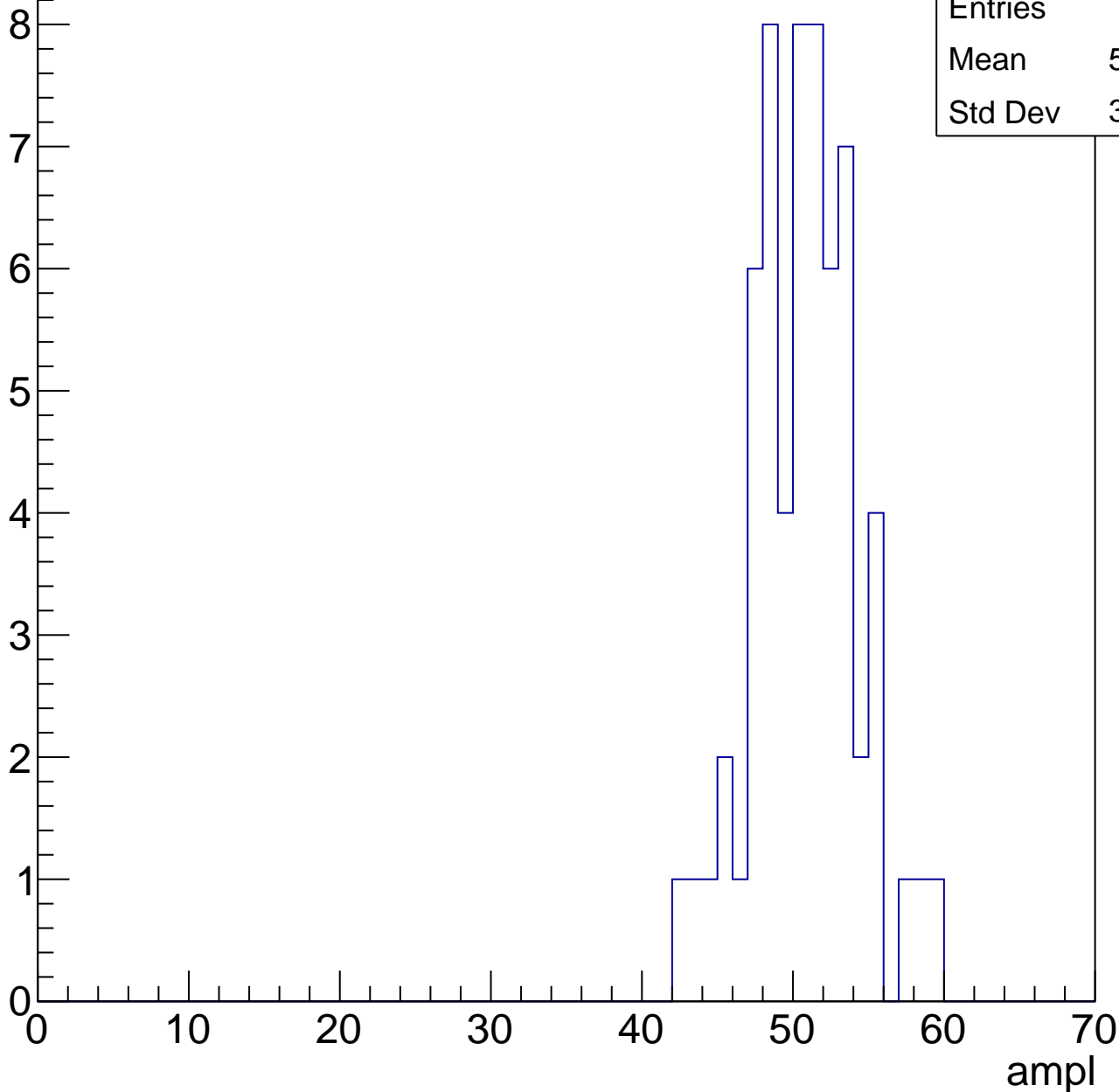


B1L101S, U6-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	50.32
Std Dev	3.407

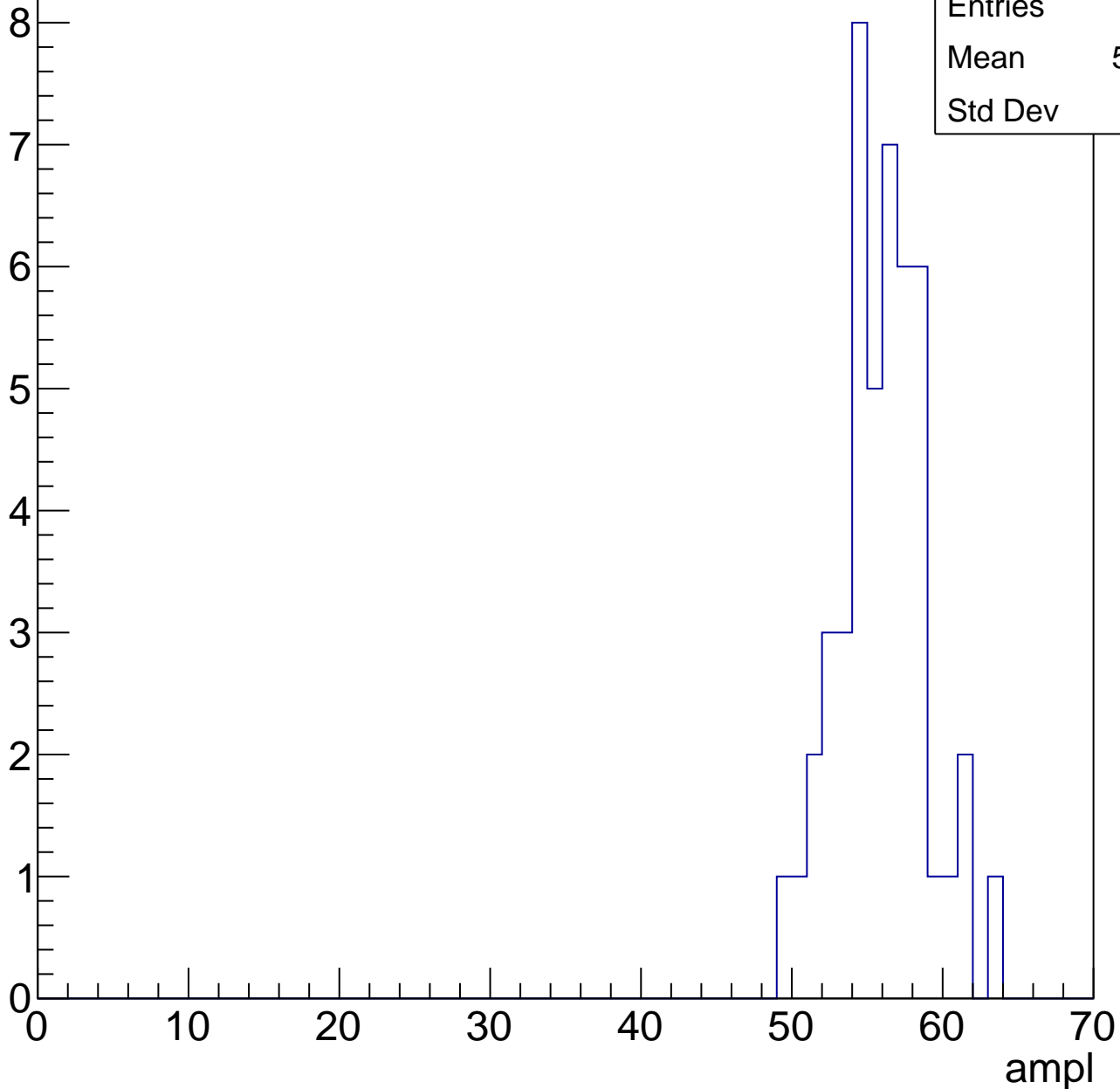


B1L101S, U6-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

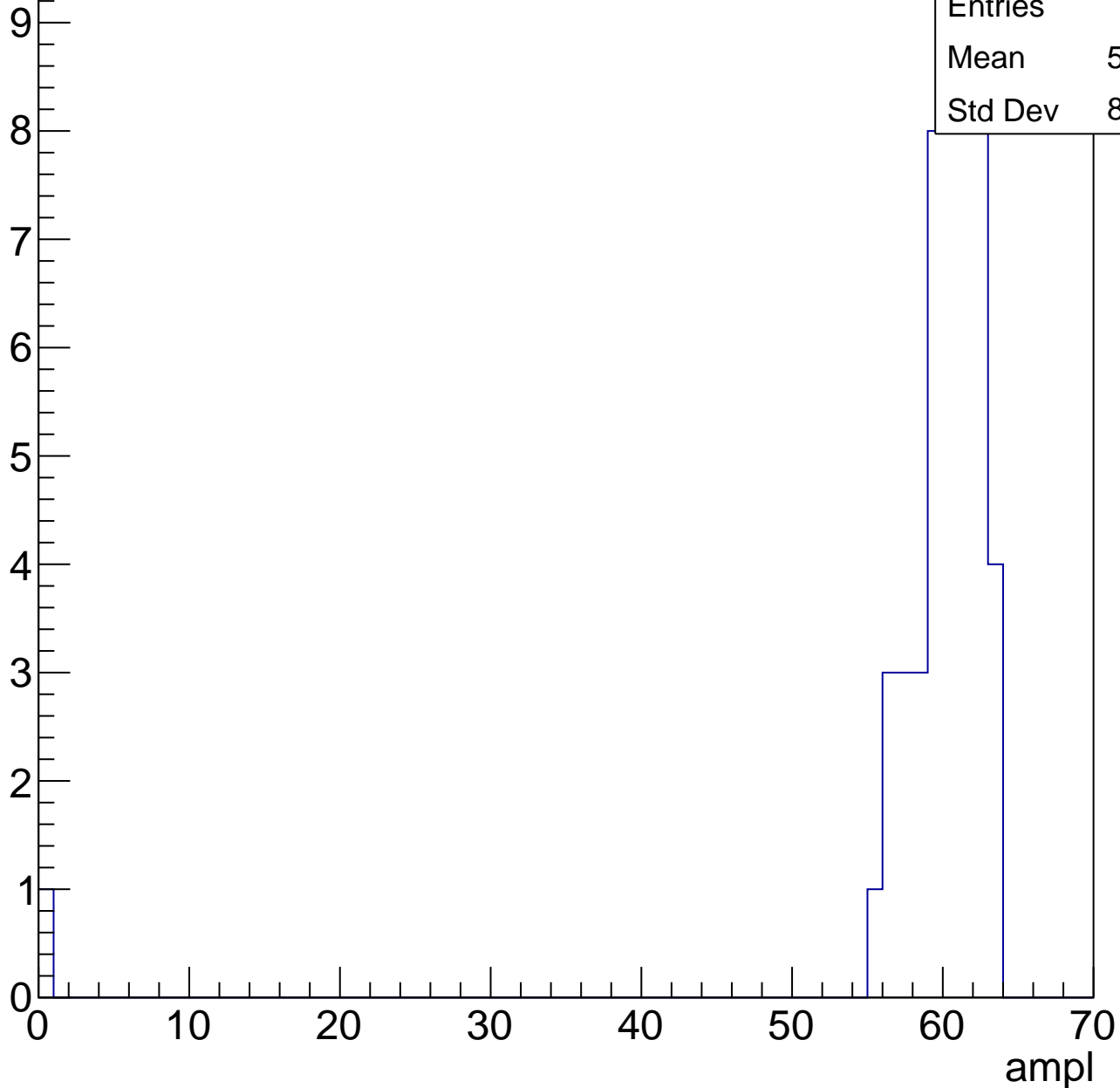
Entries	47
Mean	55.51
Std Dev	2.88



B1L101S, U6-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

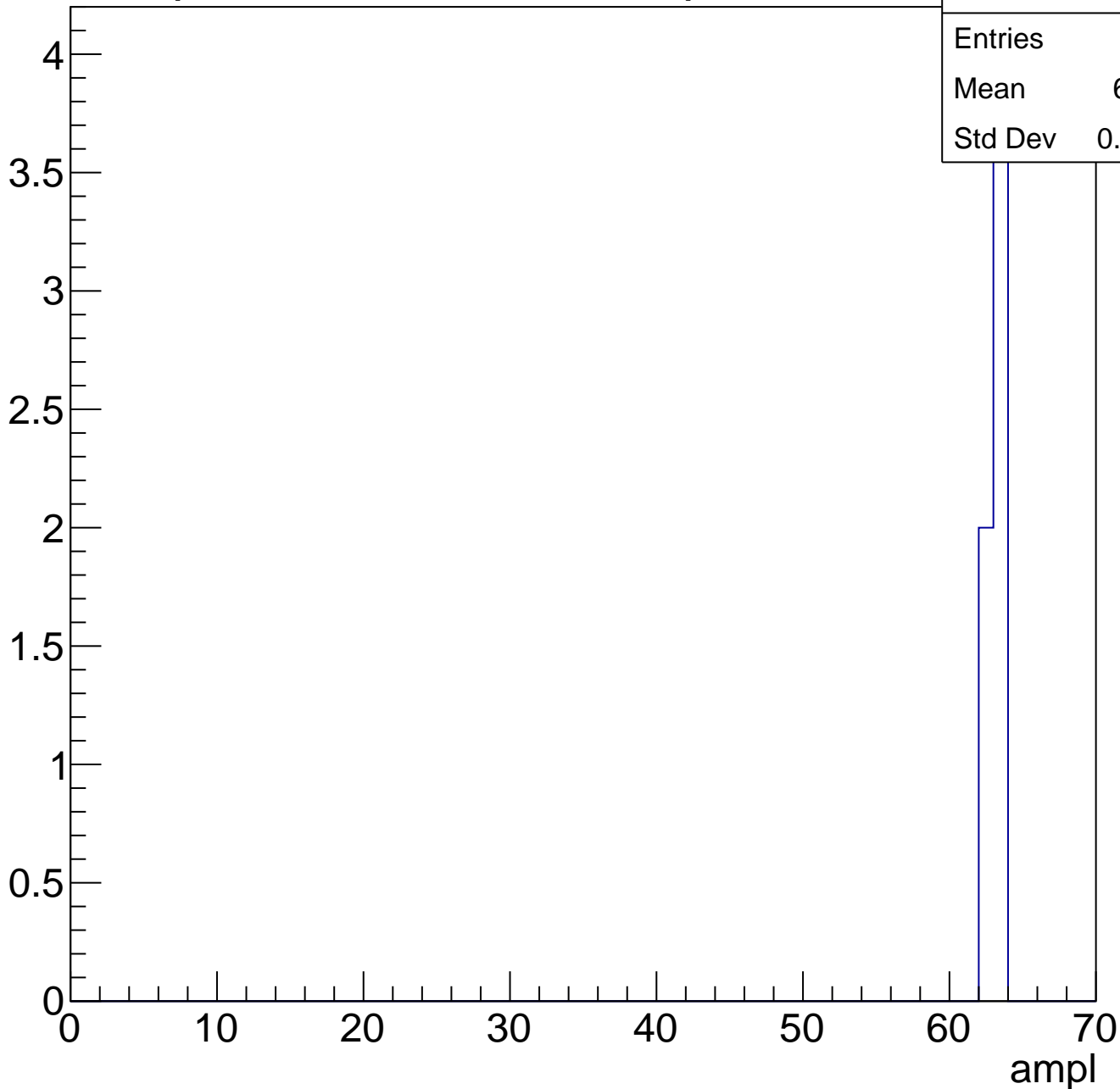
Entry



B1L101S, U6-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U6-ch67, adc0

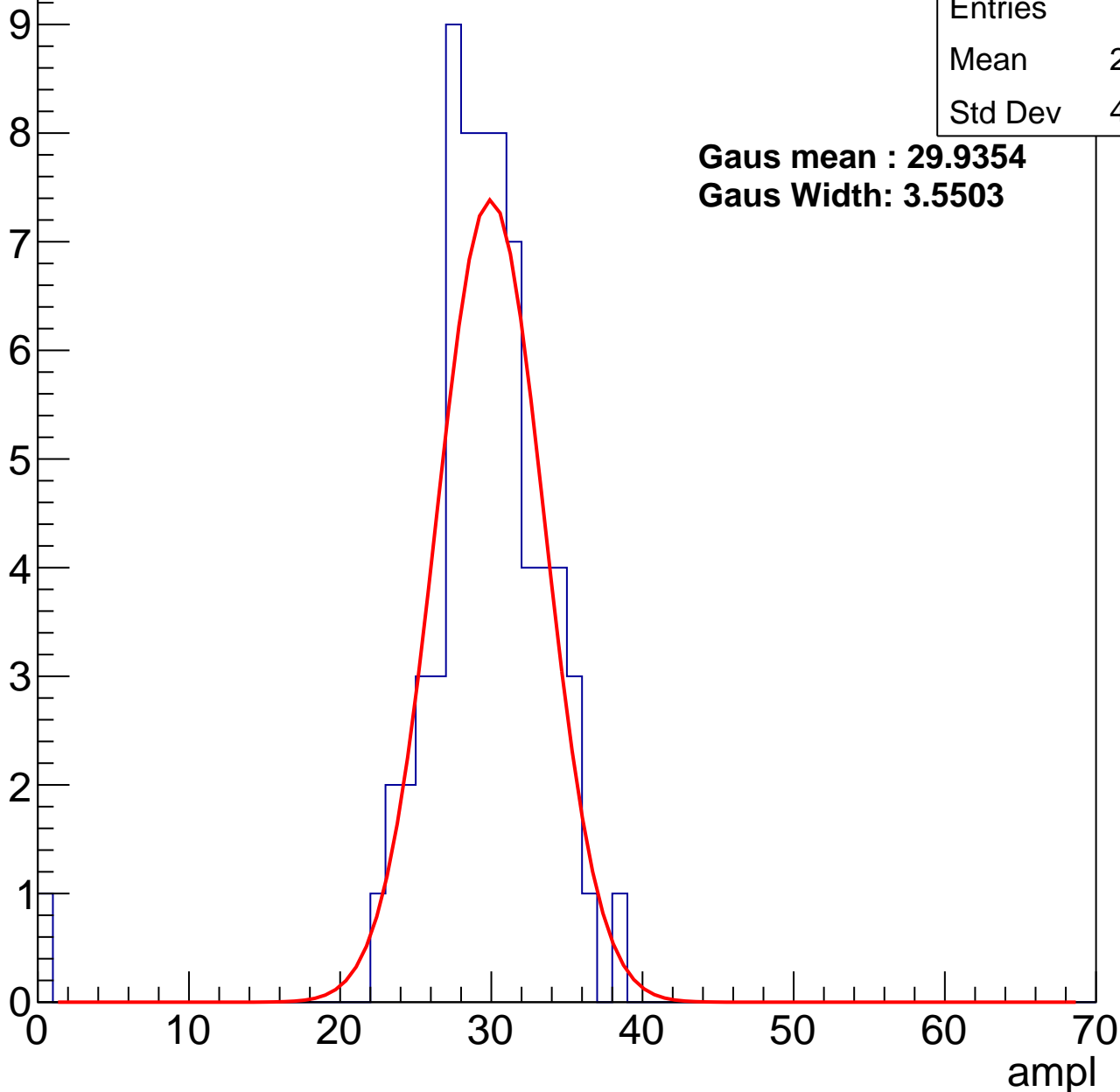
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.99
Std Dev	4.823

Gaus mean : 29.9354

Gaus Width: 3.5503



B1L101S, U6-ch67, adc1

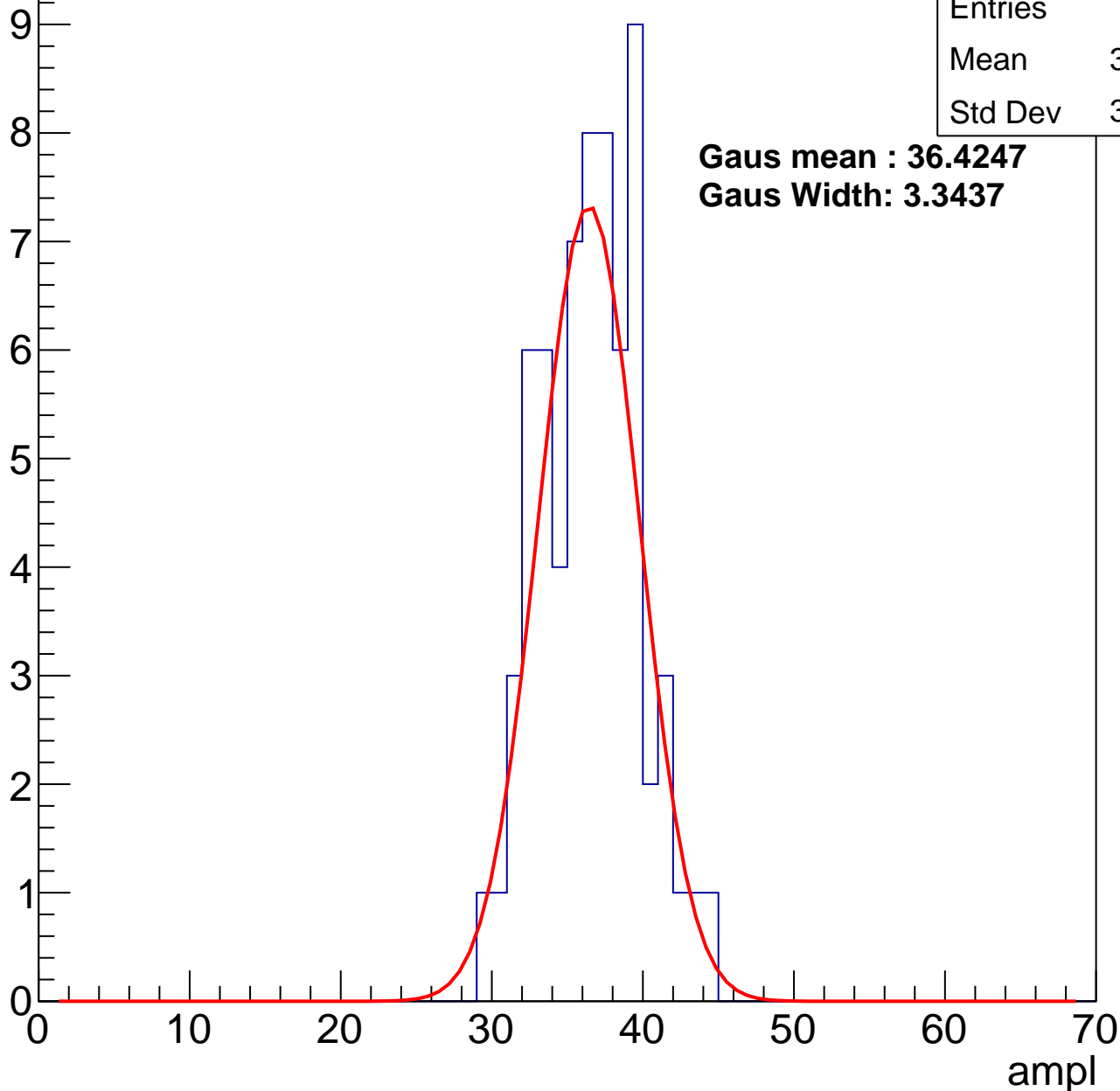
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.09
Std Dev	3.236

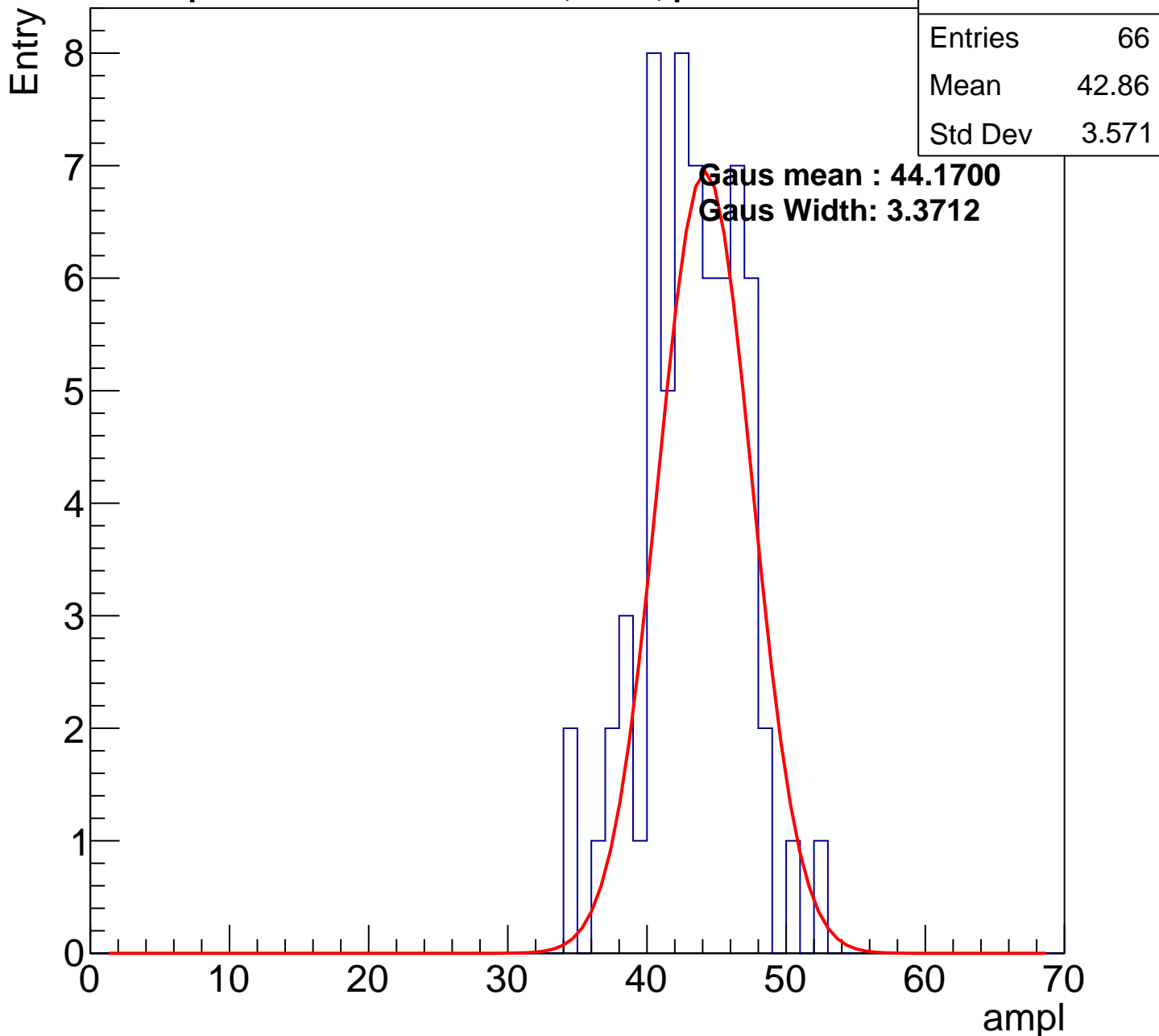
Gaus mean : 36.4247

Gaus Width: 3.3437



B1L101S, U6-ch67, adc2

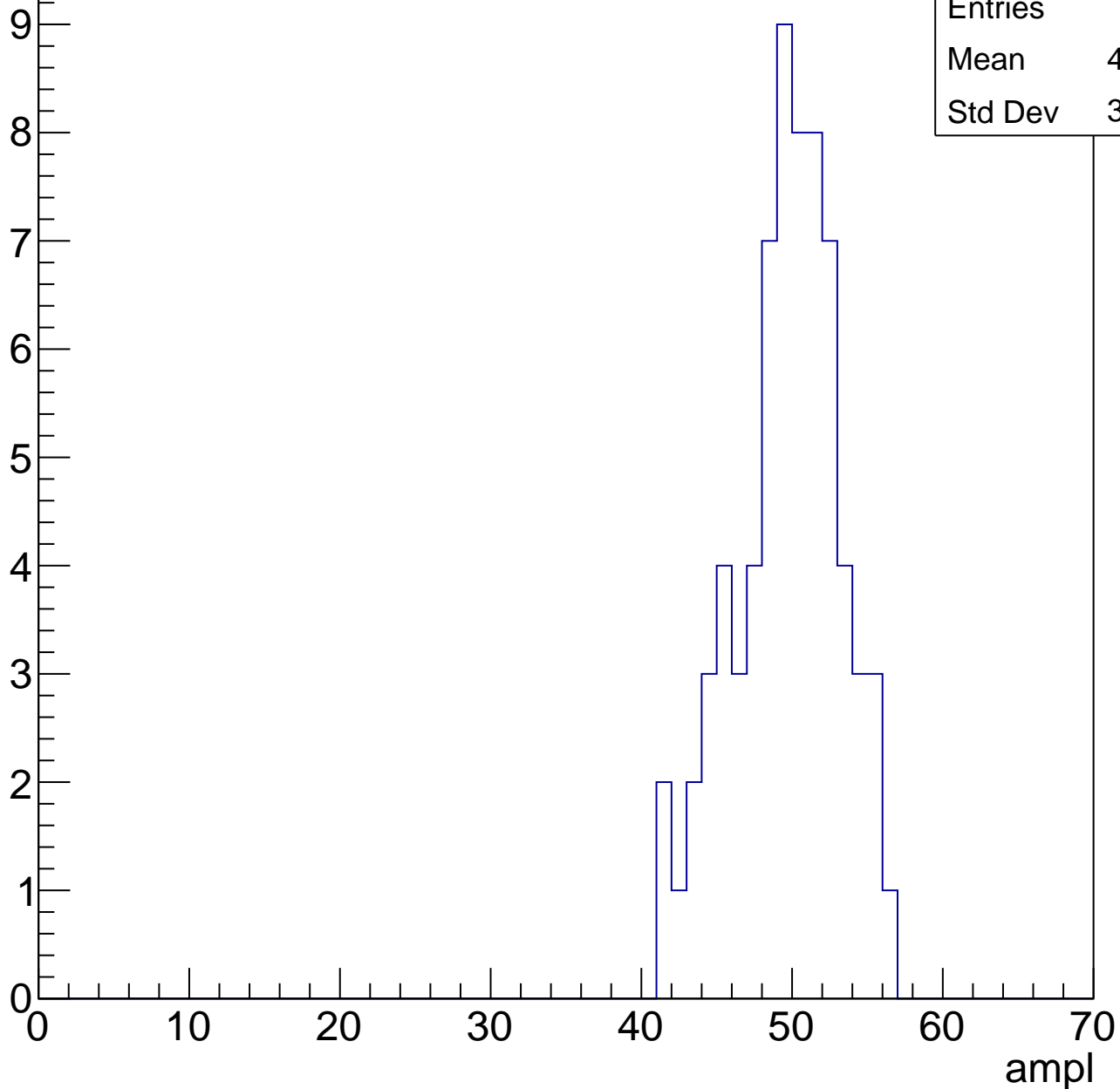
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

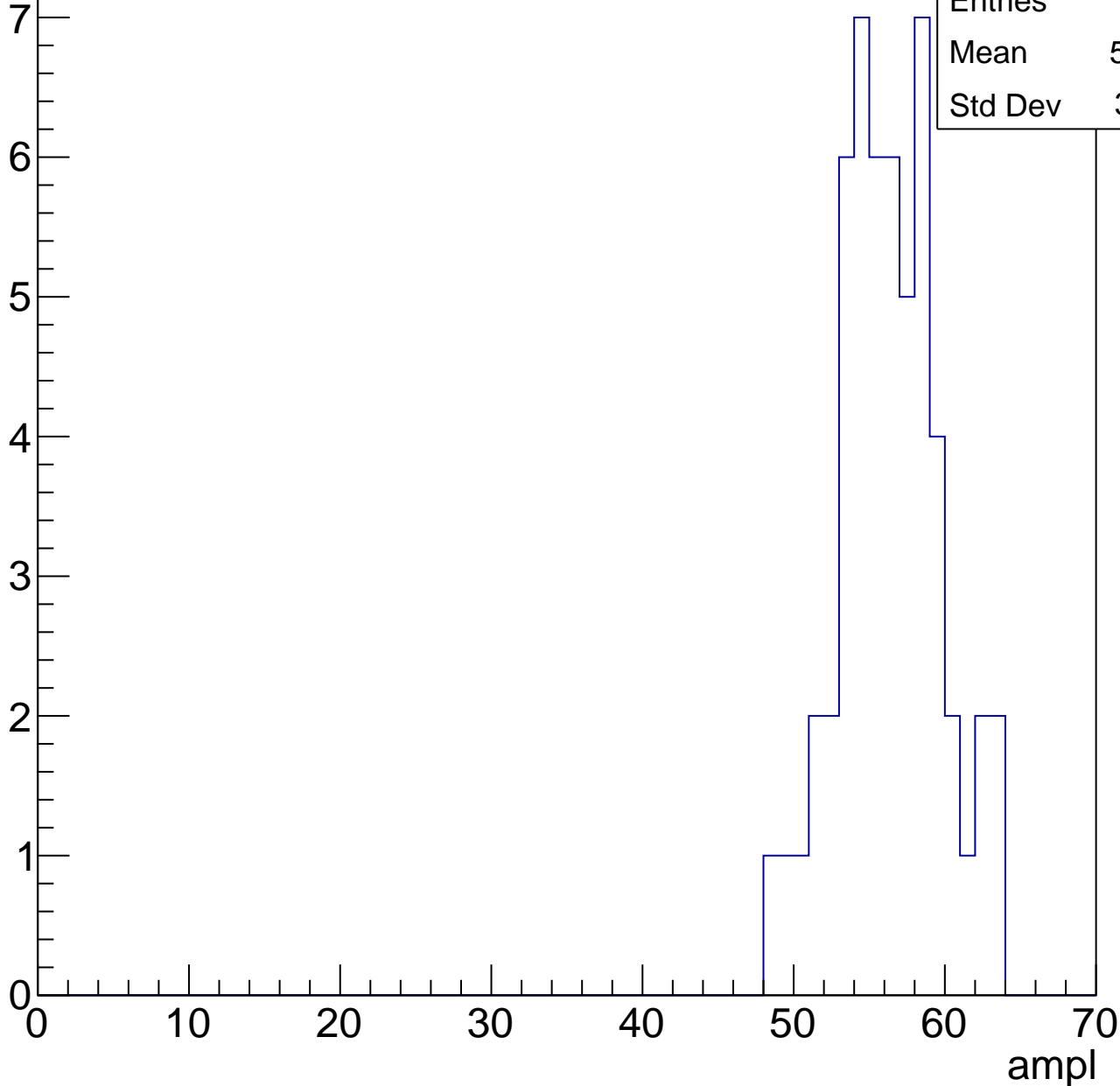


B1L101S, U6-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.87
Std Dev	3.341



B1L101S, U6-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	59.71
Std Dev	2.207

ampl

0

10

20

30

40

50

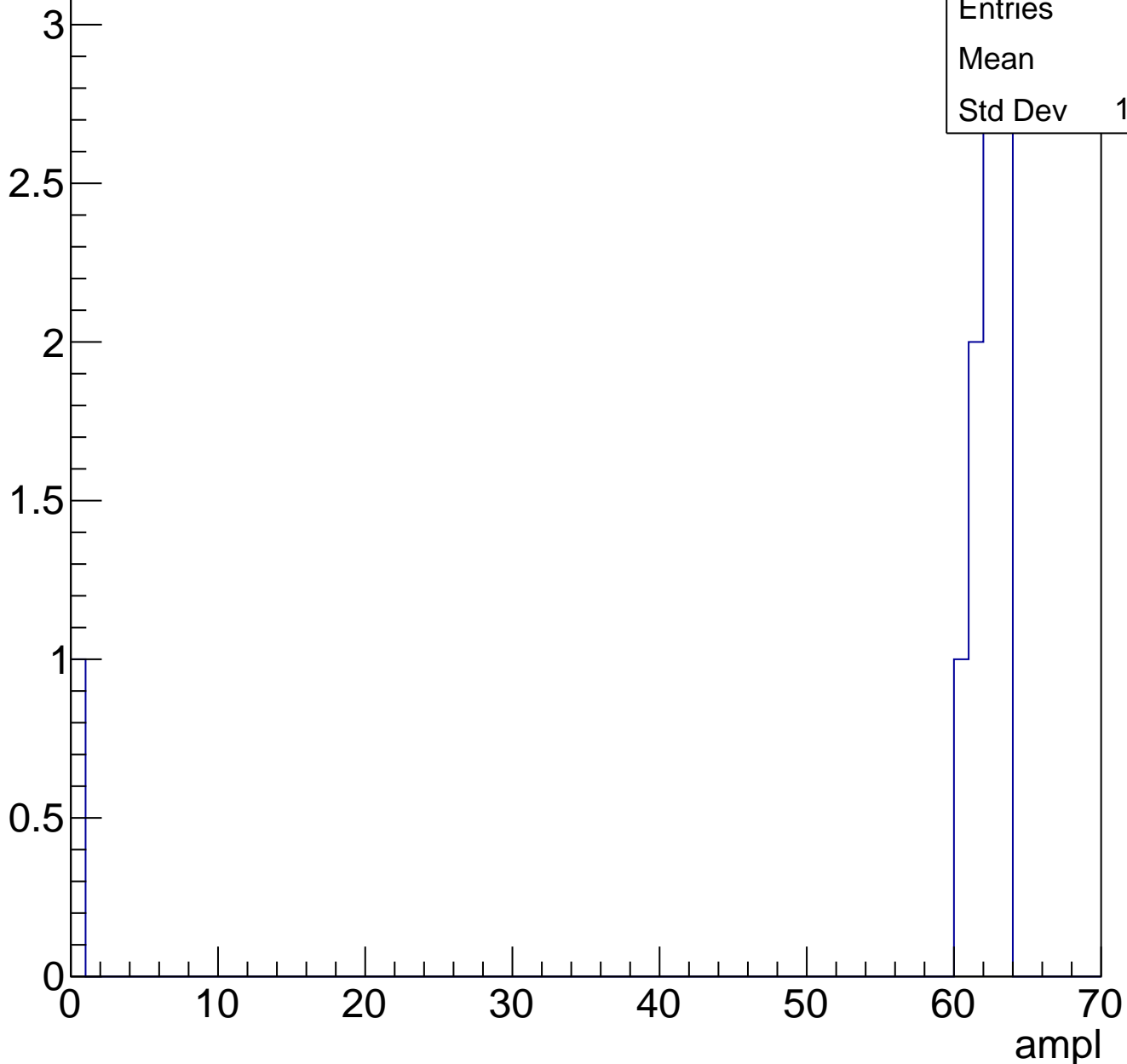
60

70

B1L101S, U6-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U6-ch68, adc0

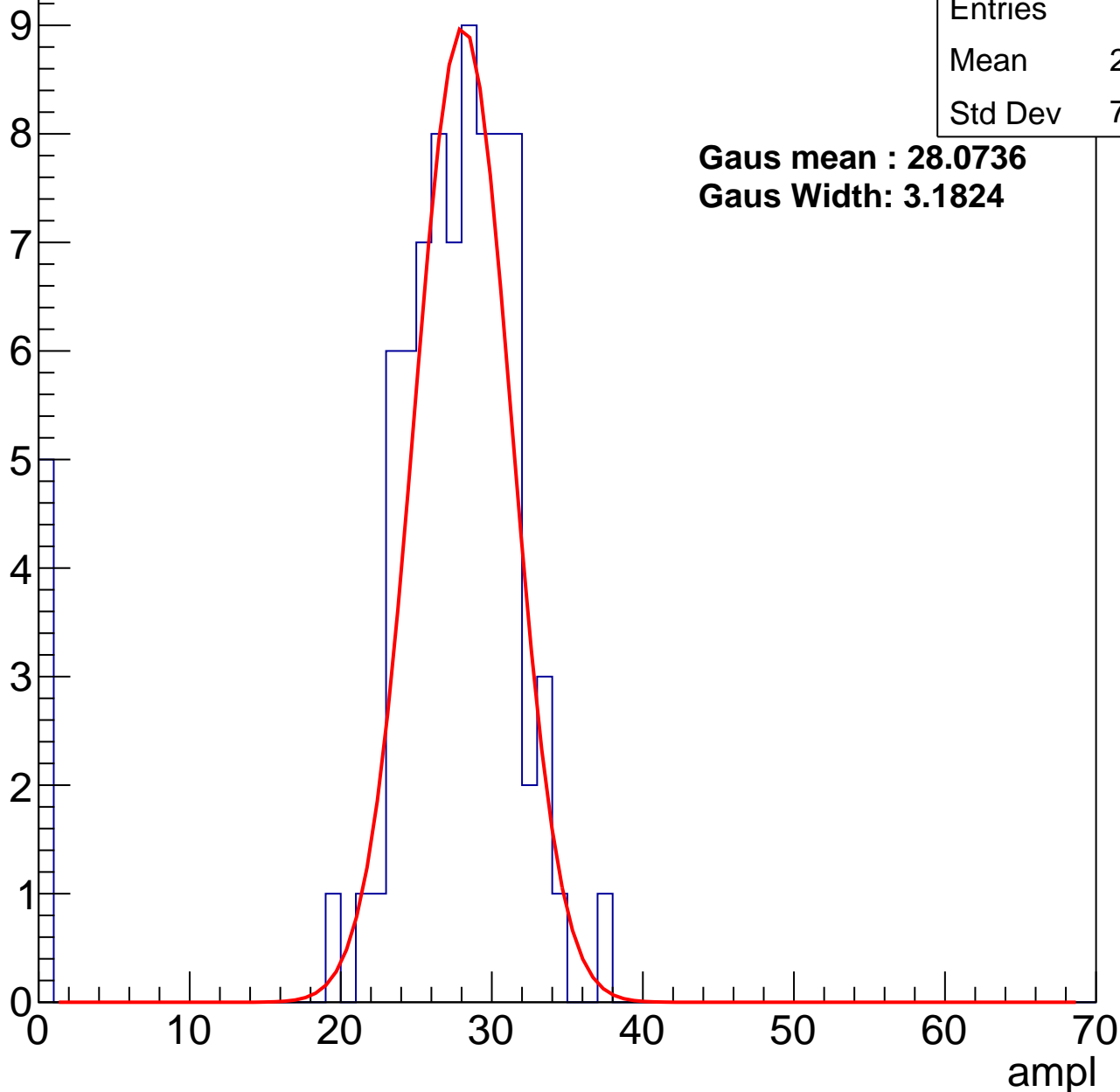
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	25.88
Std Dev	7.326

Gaus mean : 28.0736

Gaus Width: 3.1824



B1L101S, U6-ch68, adc1

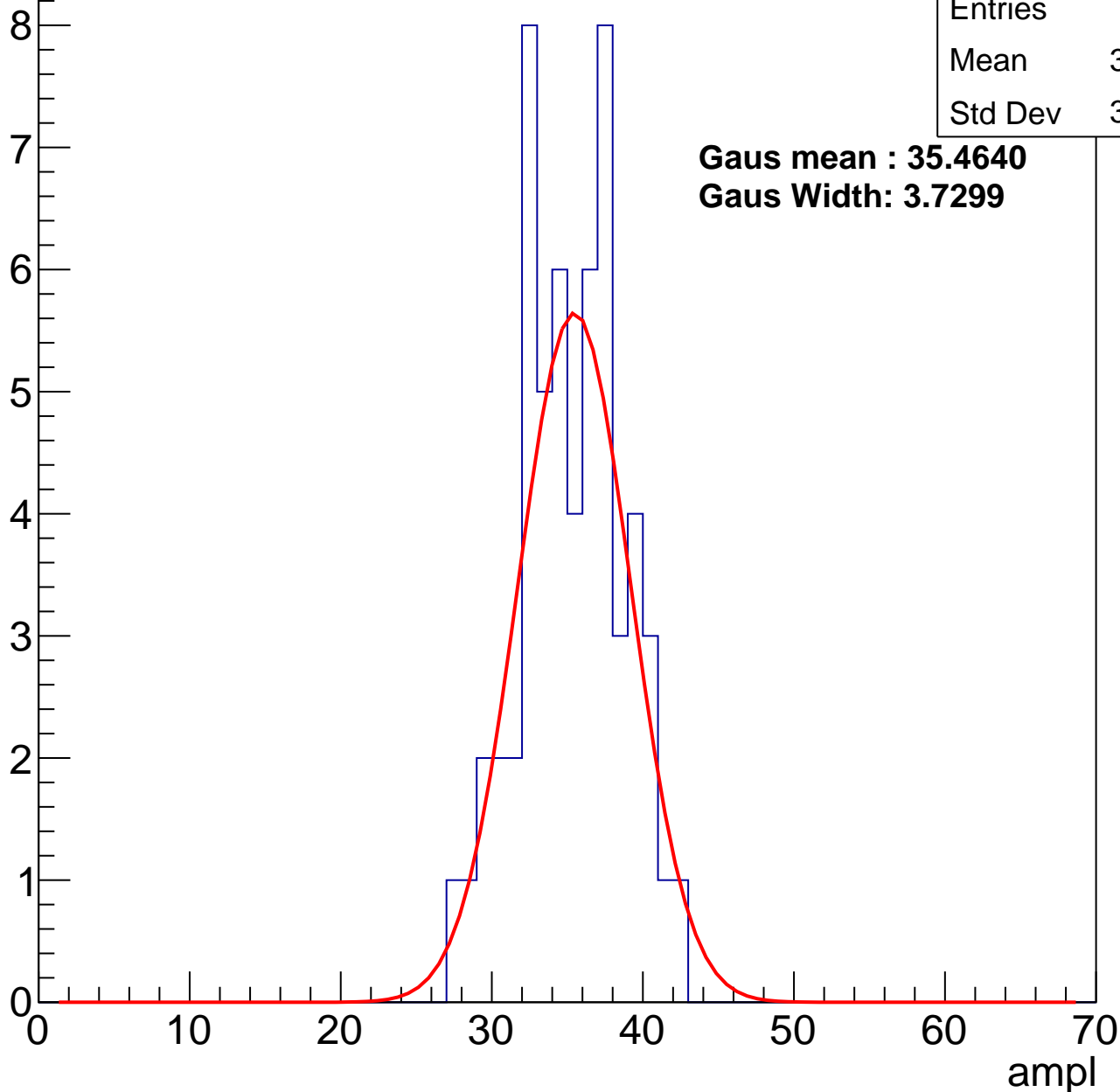
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	34.82
Std Dev	3.383

Gaus mean : 35.4640

Gaus Width: 3.7299



B1L101S, U6-ch68, adc2

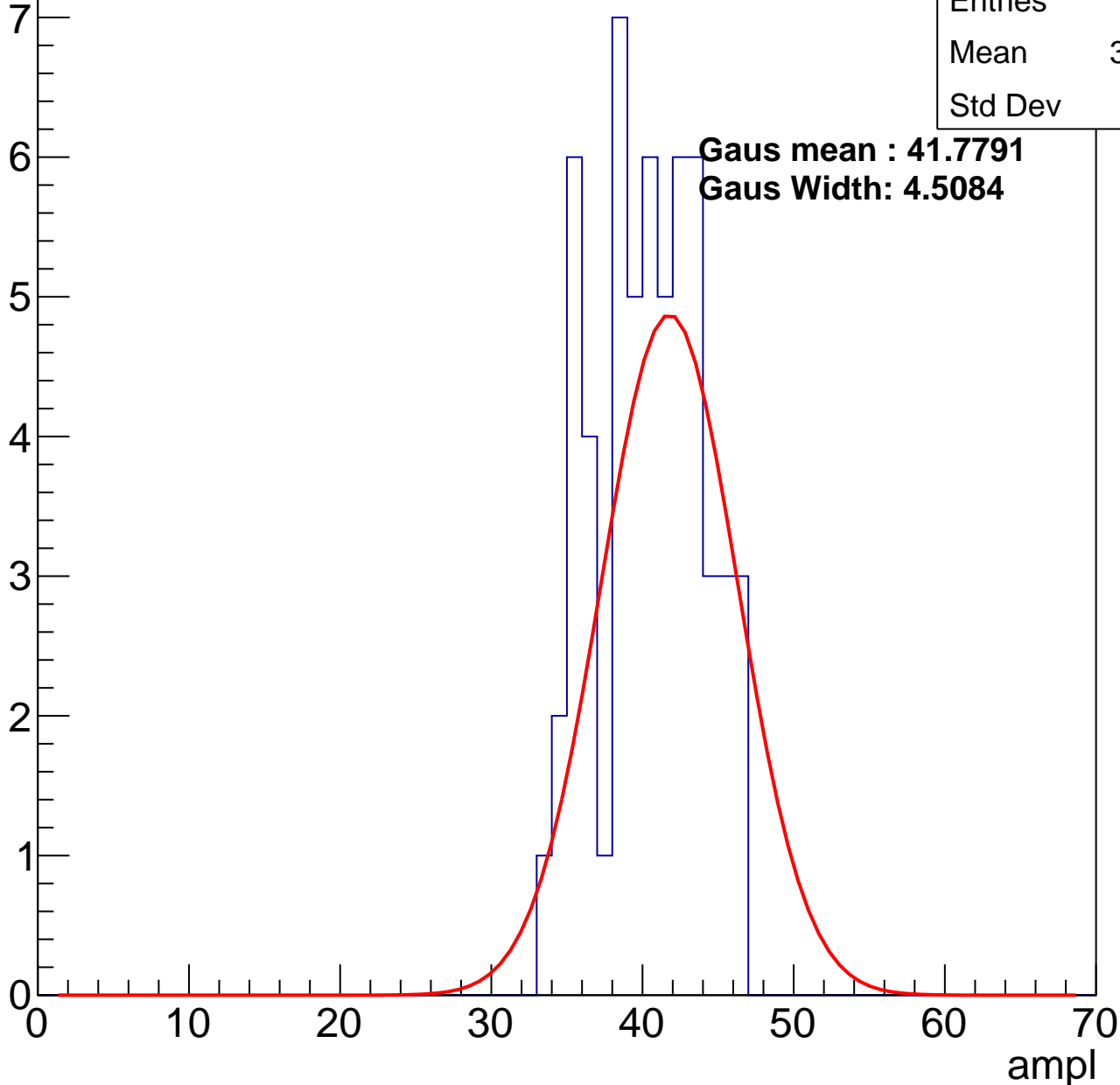
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	39.88
Std Dev	3.45

Gaus mean : 41.7791

Gaus Width: 4.5084

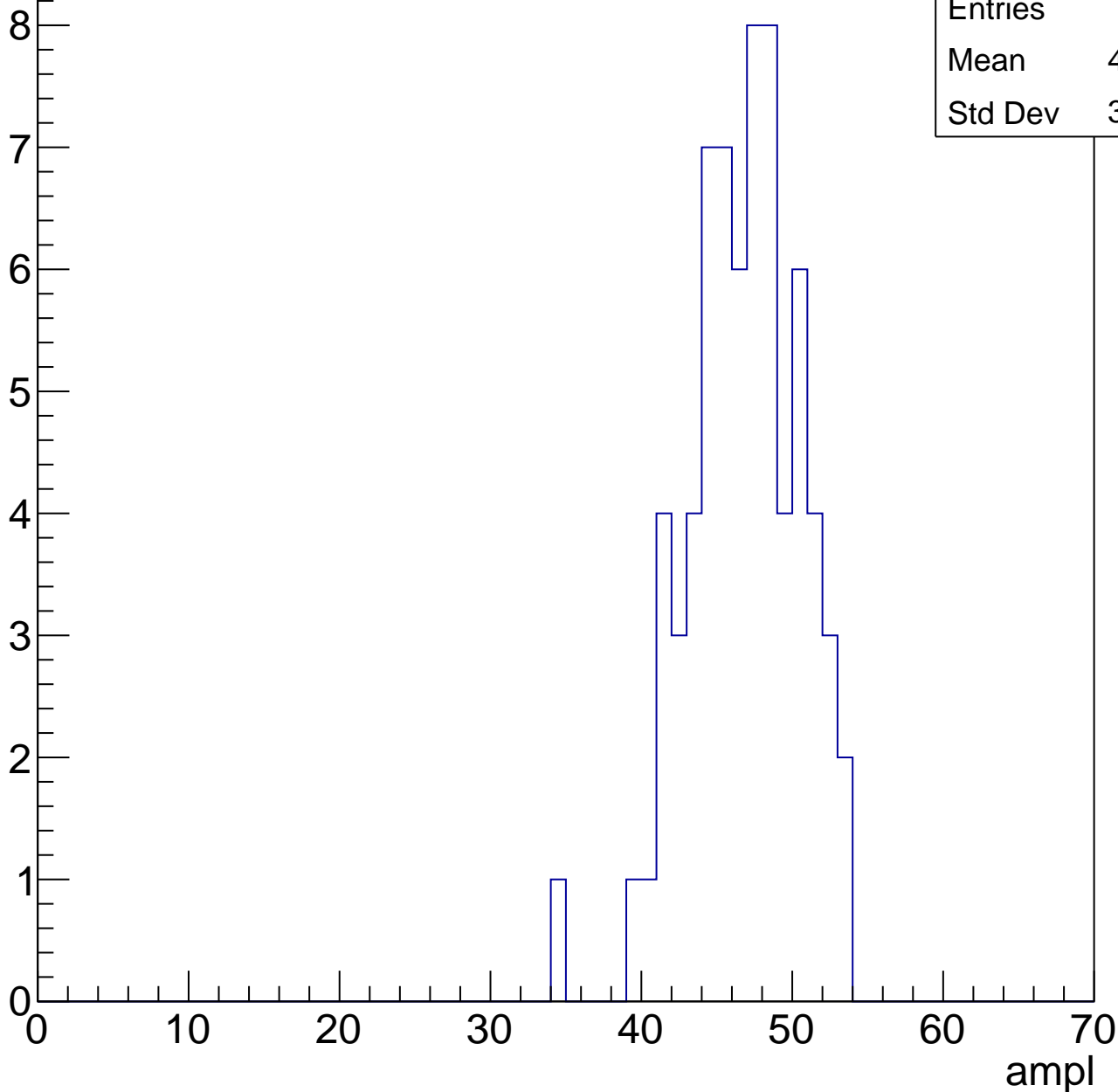


B1L101S, U6-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

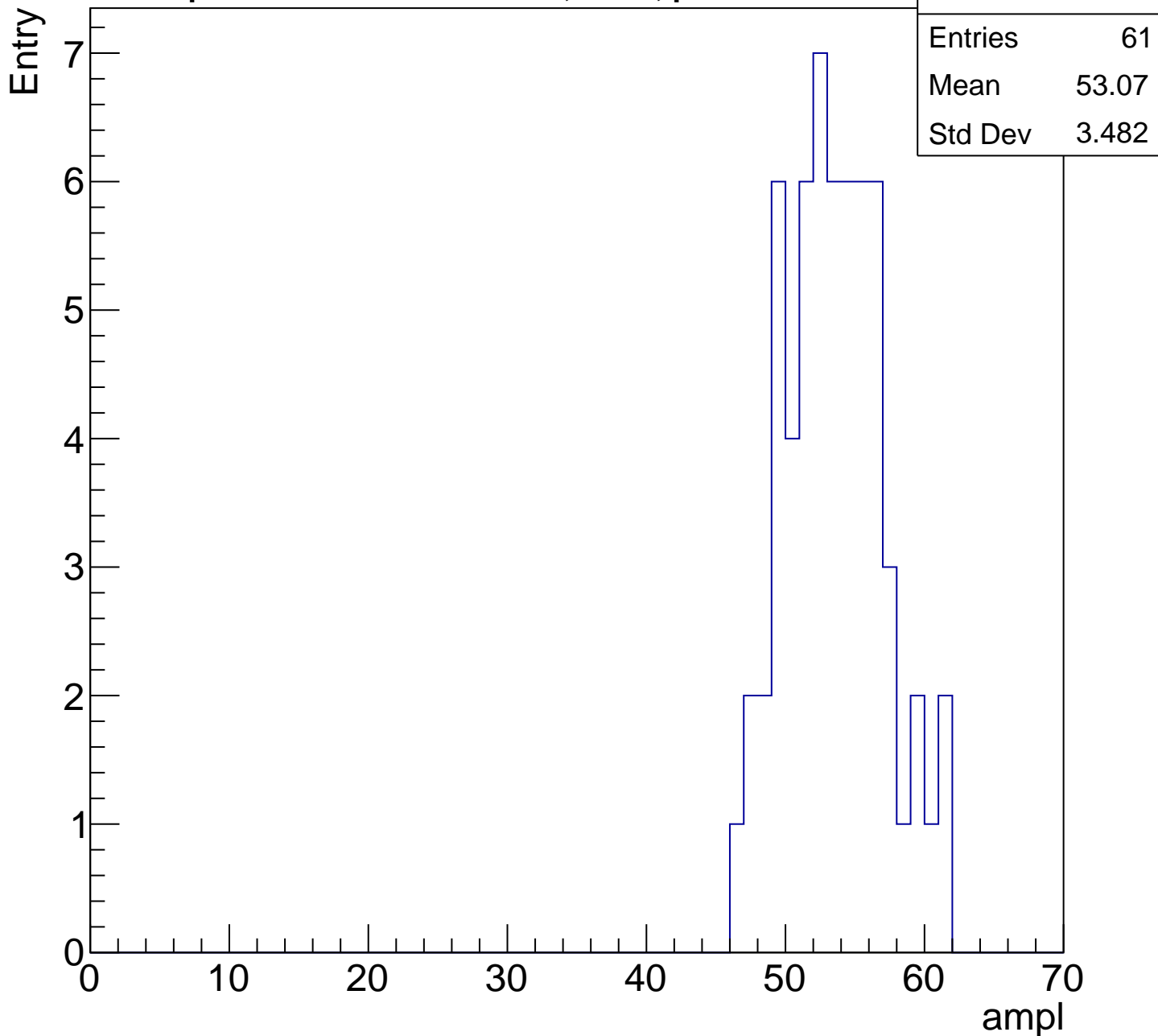
Entry

Entries	69
Mean	46.32
Std Dev	3.649



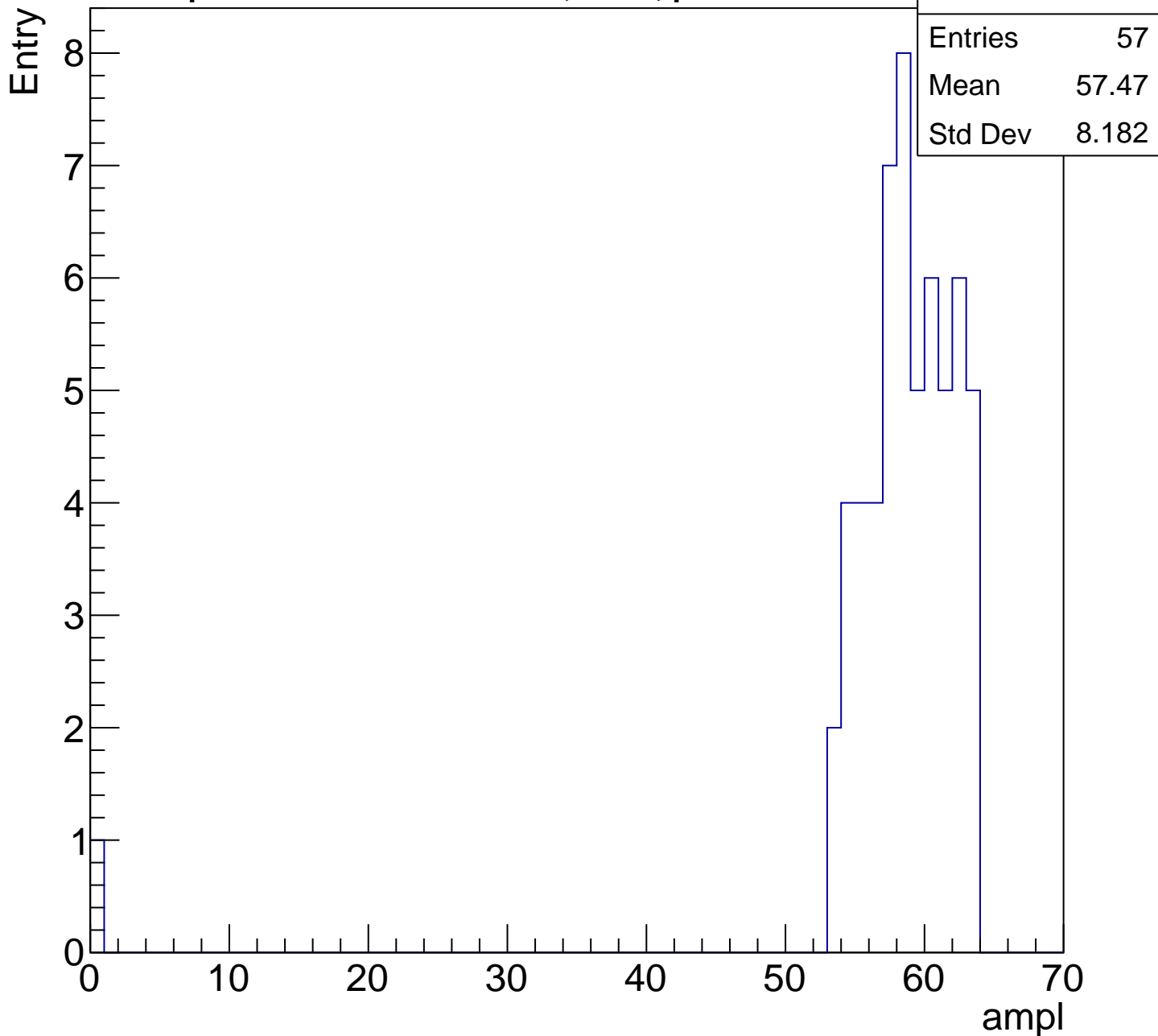
B1L101S, U6-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

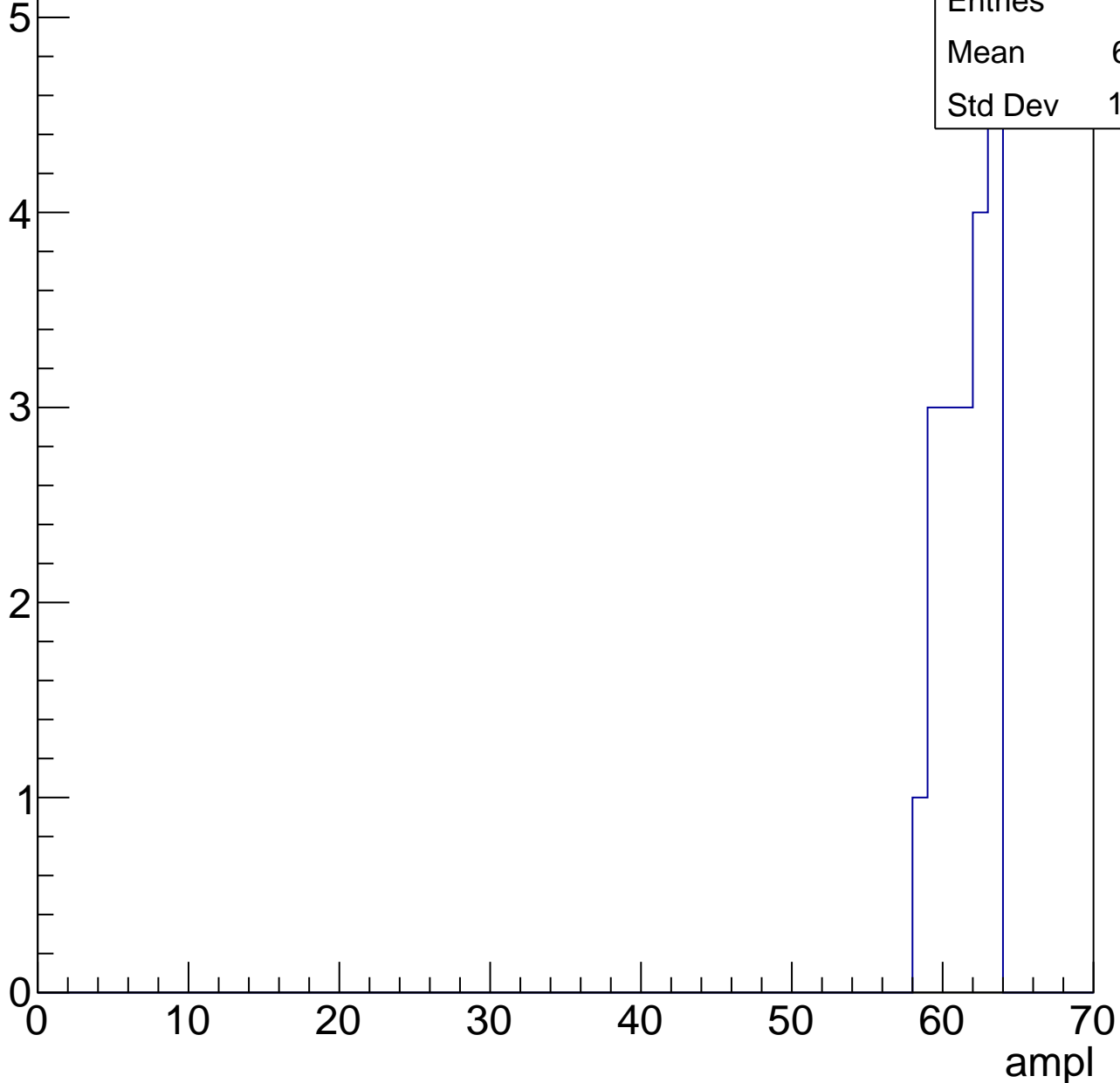


B1L101S, U6-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.11
Std Dev	1.586



B1L101S, U6-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch69, adc0

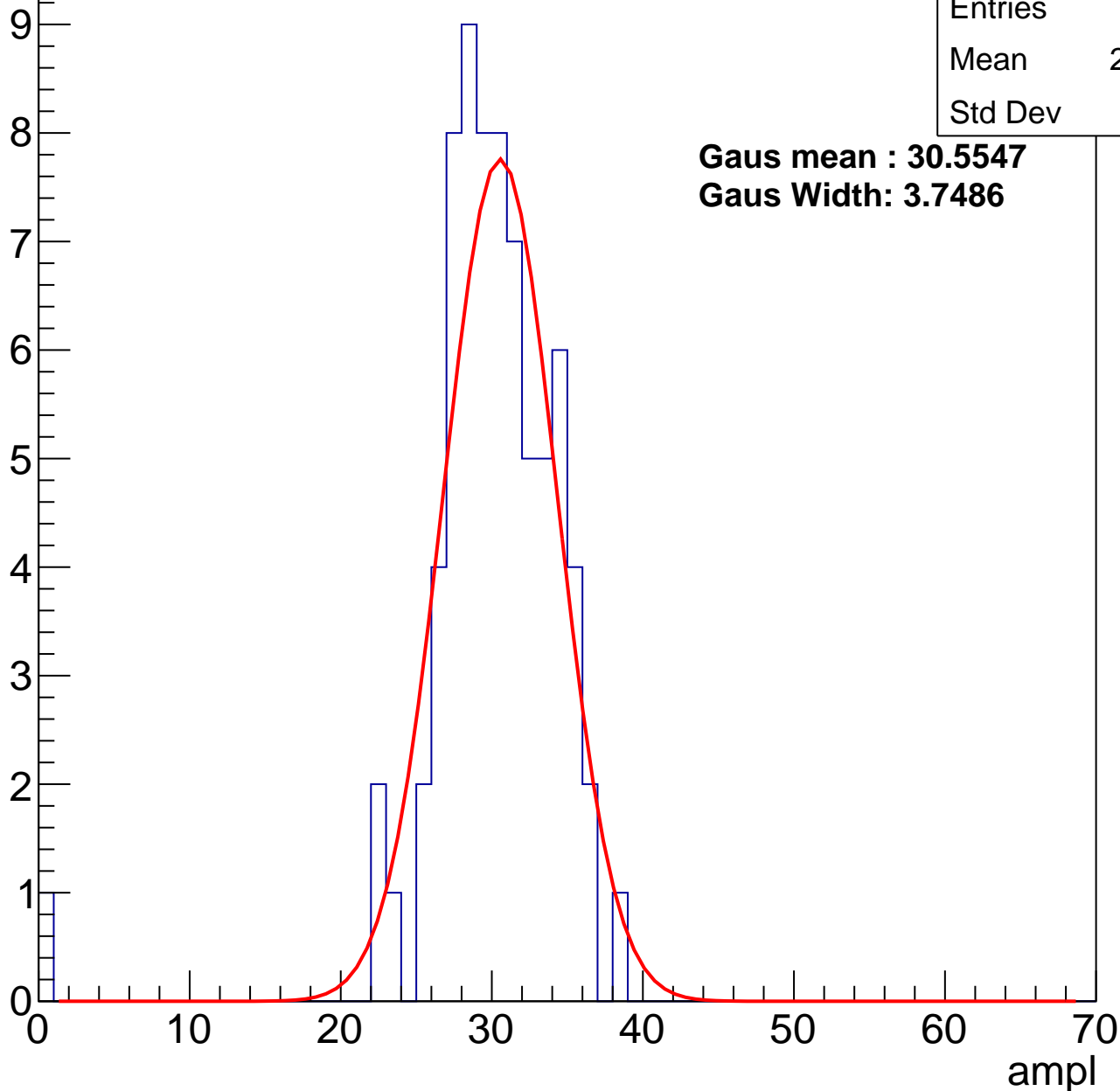
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.55
Std Dev	4.82

Gaus mean : 30.5547

Gaus Width: 3.7486



B1L101S, U6-ch69, adc1

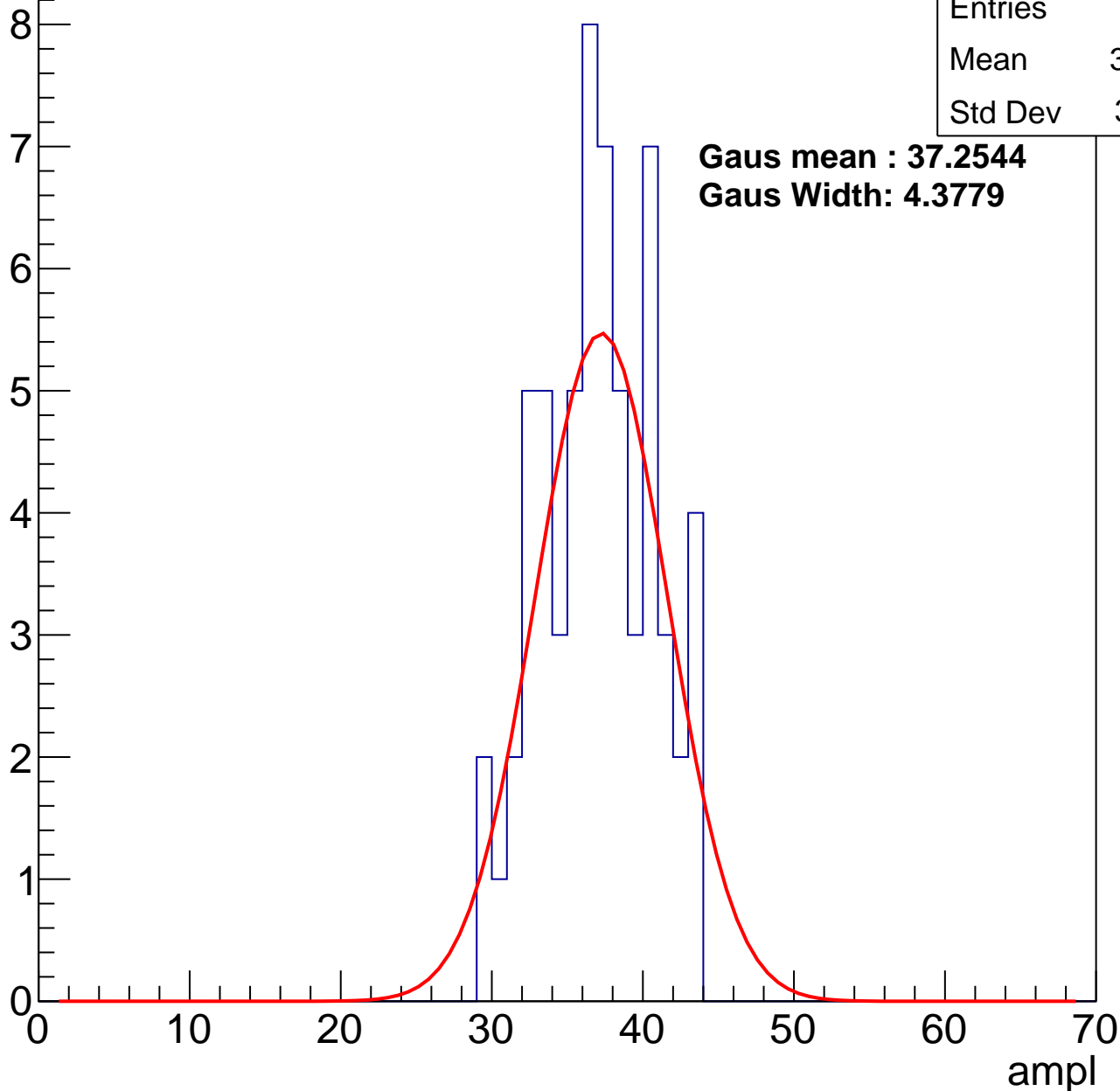
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.53
Std Dev	3.631

Gaus mean : 37.2544

Gaus Width: 4.3779



B1L101S, U6-ch69, adc2

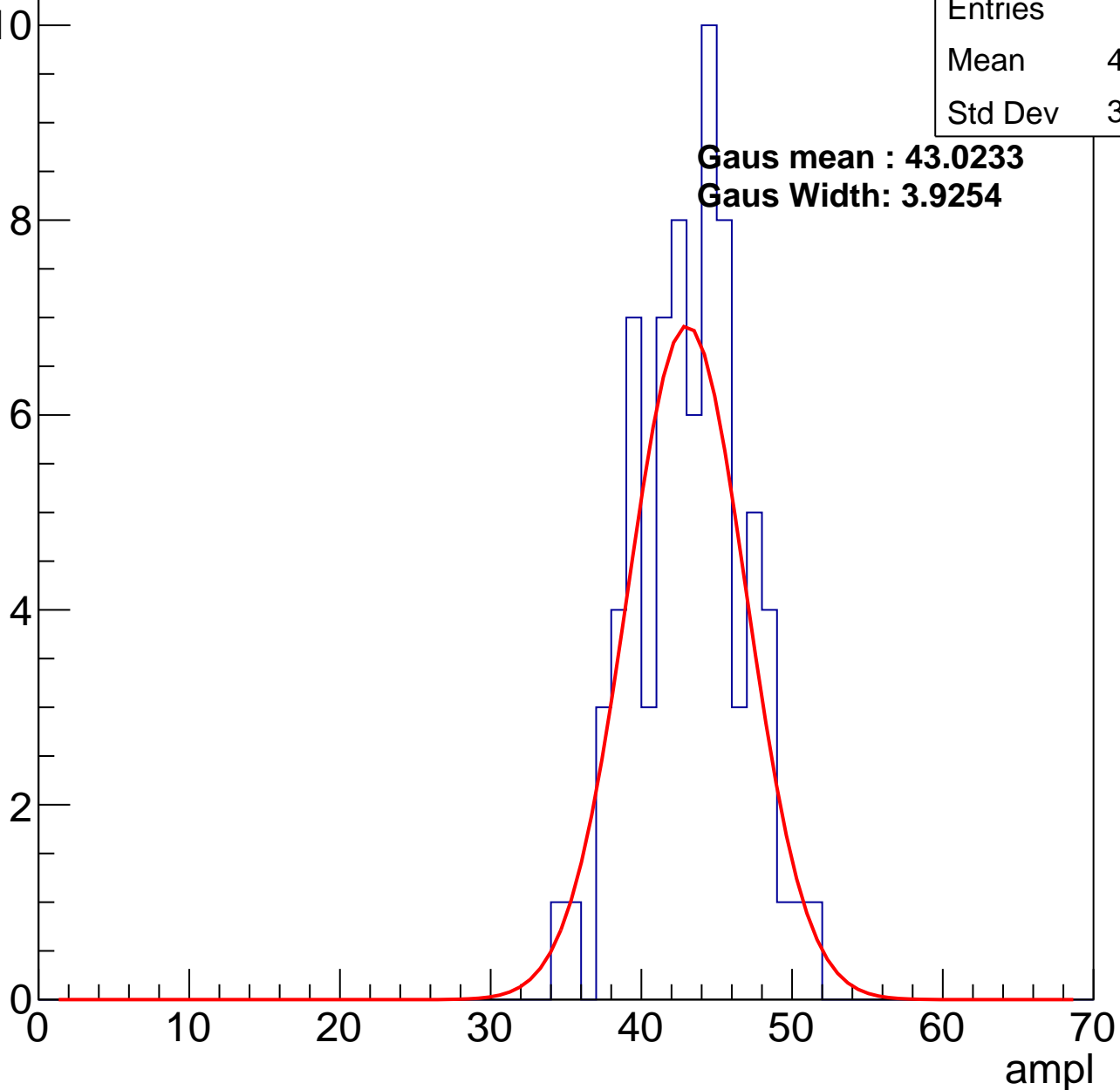
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	42.75
Std Dev	3.553

Gaus mean : 43.0233

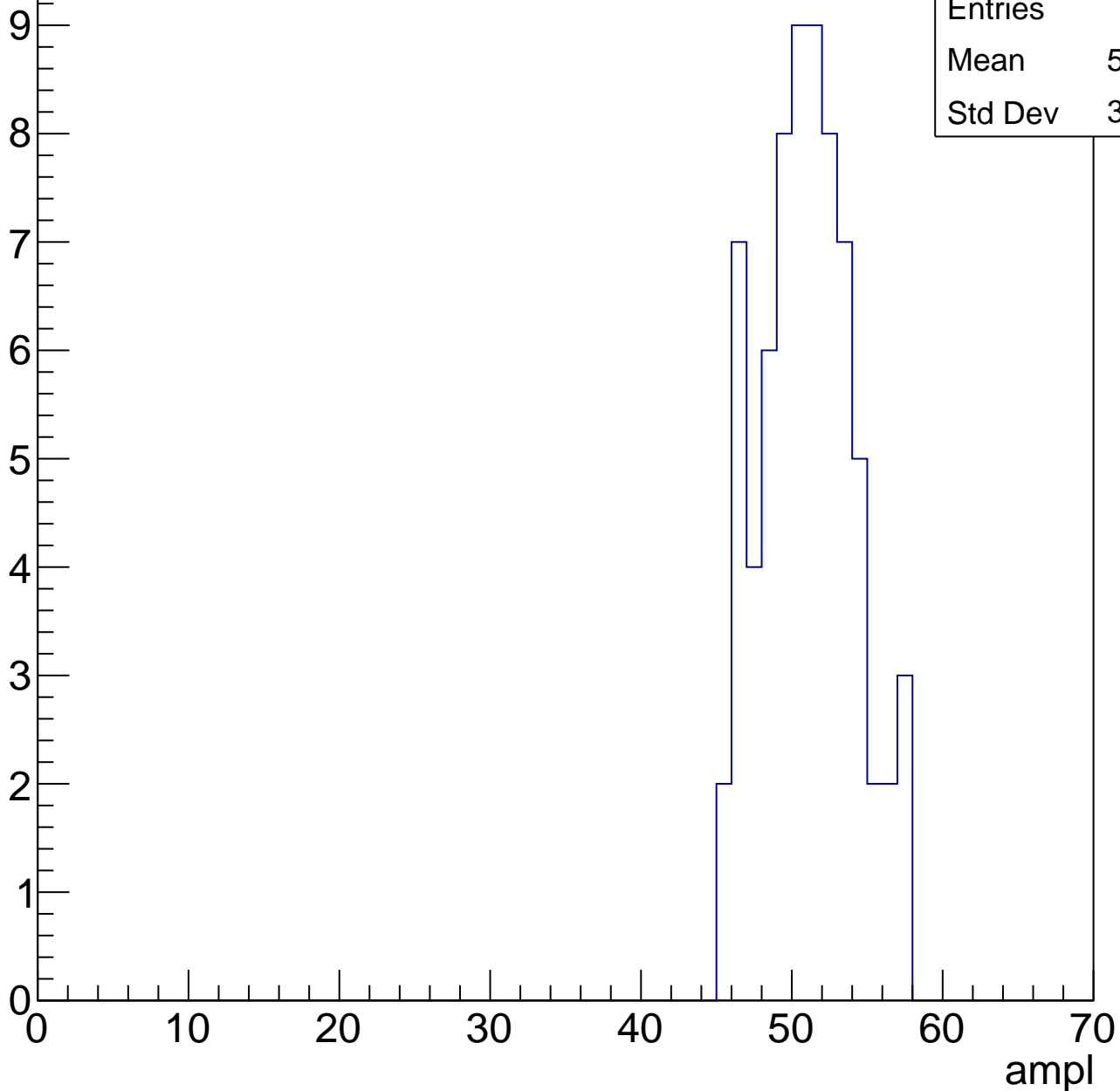
Gaus Width: 3.9254



B1L101S, U6-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

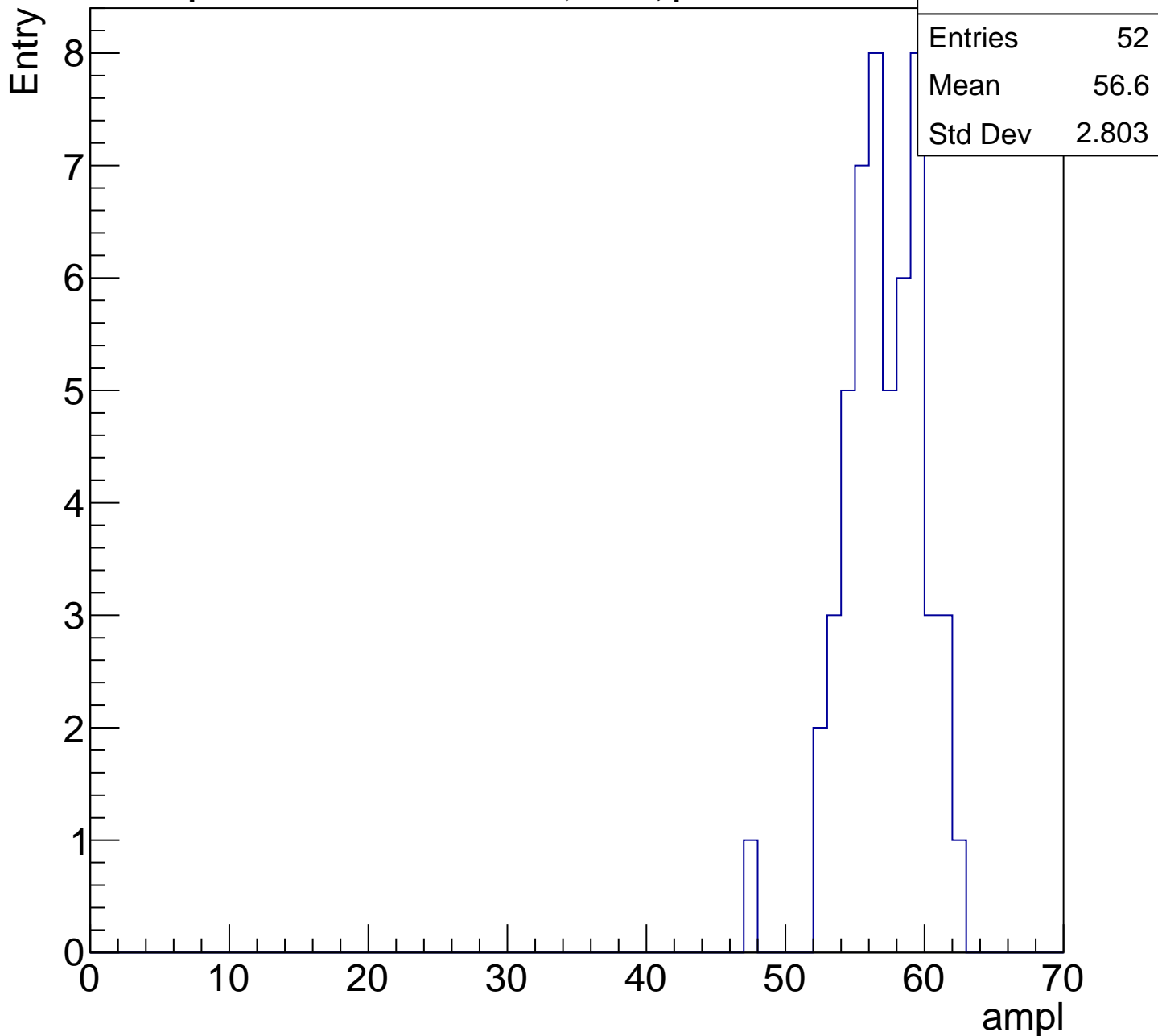
Entry



Entries	72
Mean	50.54
Std Dev	3.032

B1L101S, U6-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch69, adc5

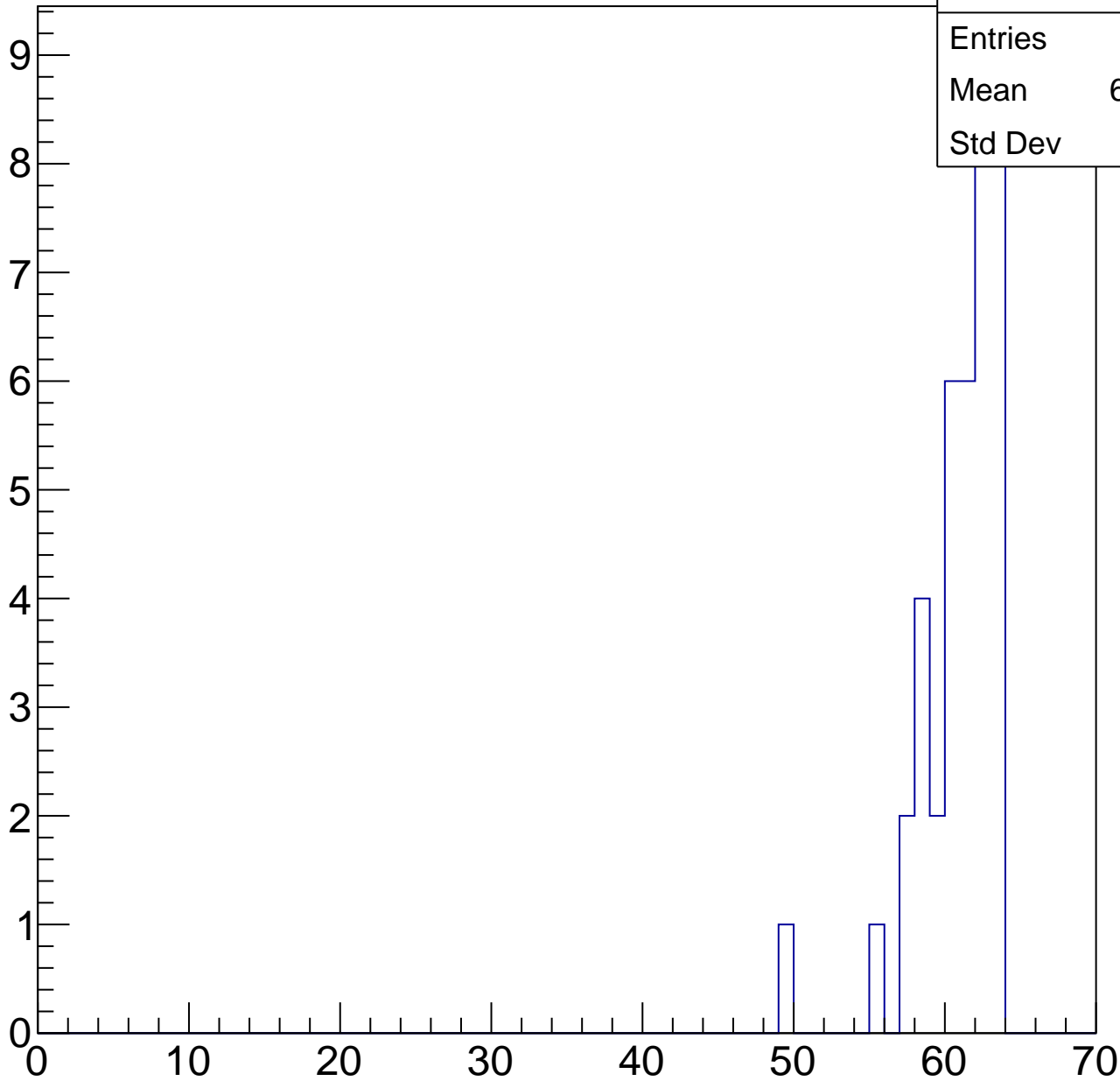
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.48
Std Dev	2.72

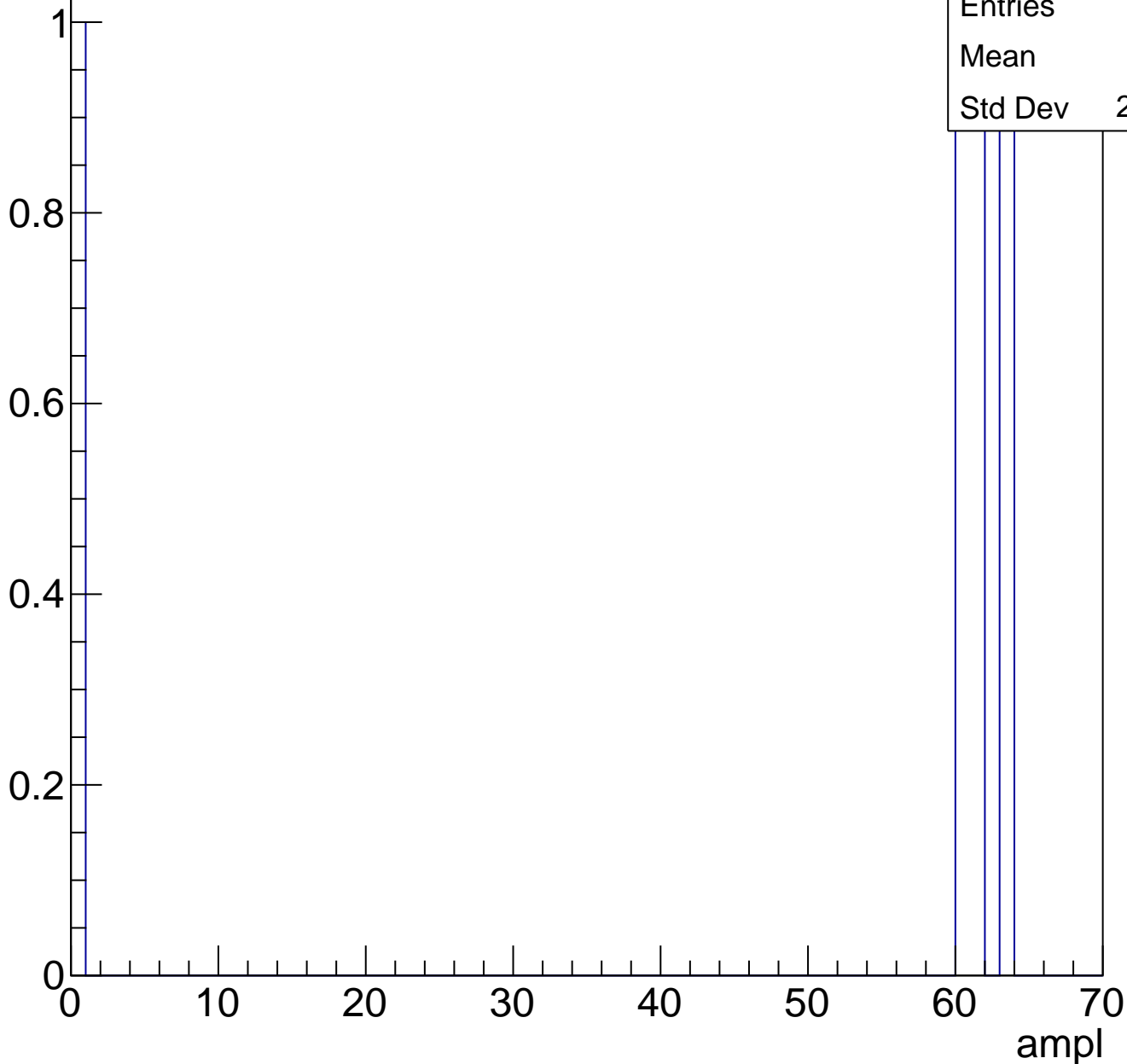
ampl



B1L101S, U6-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch70, adc0

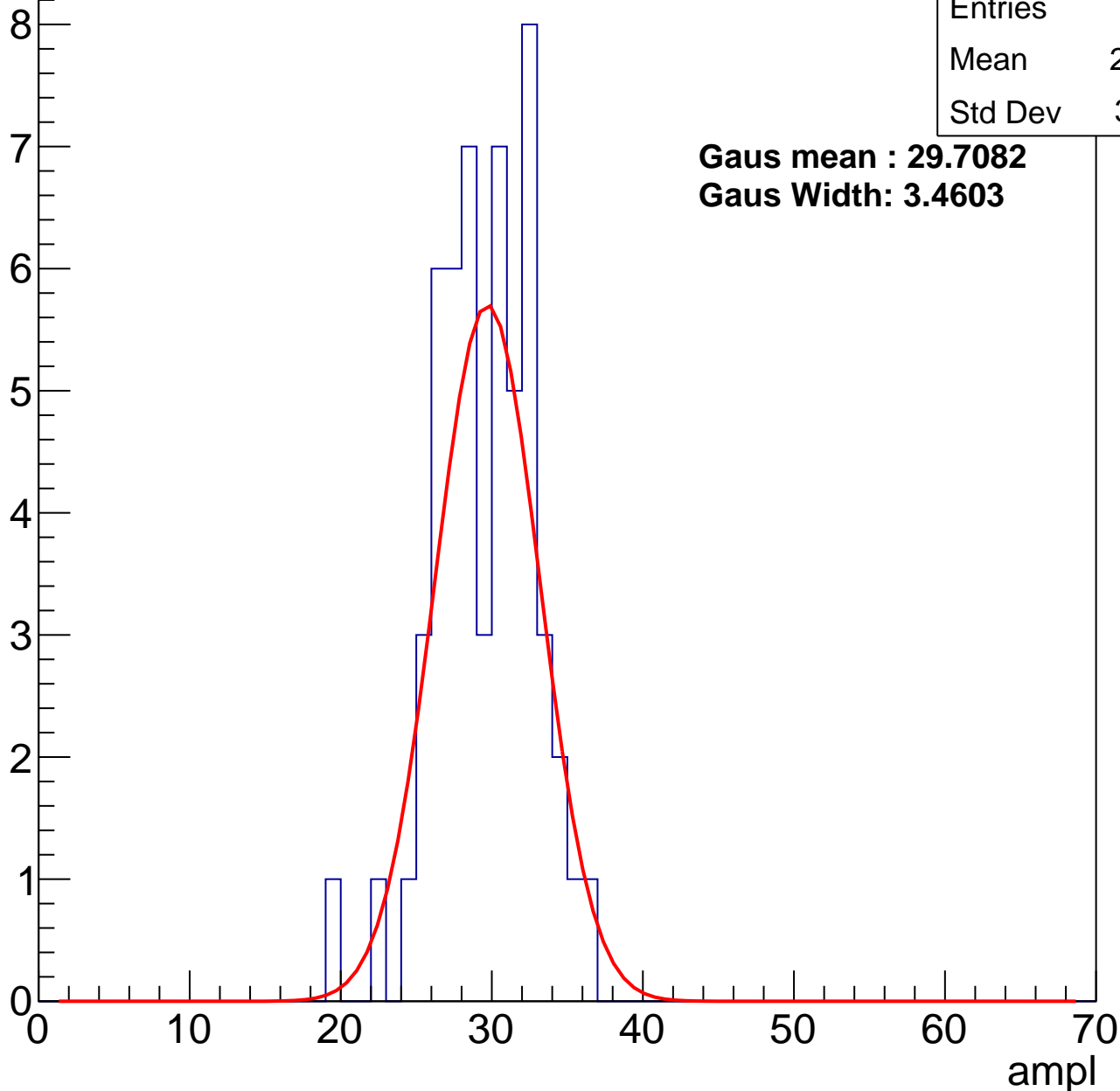
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	29.09
Std Dev	3.271

Gaus mean : 29.7082

Gaus Width: 3.4603



B1L101S, U6-ch70, adc1

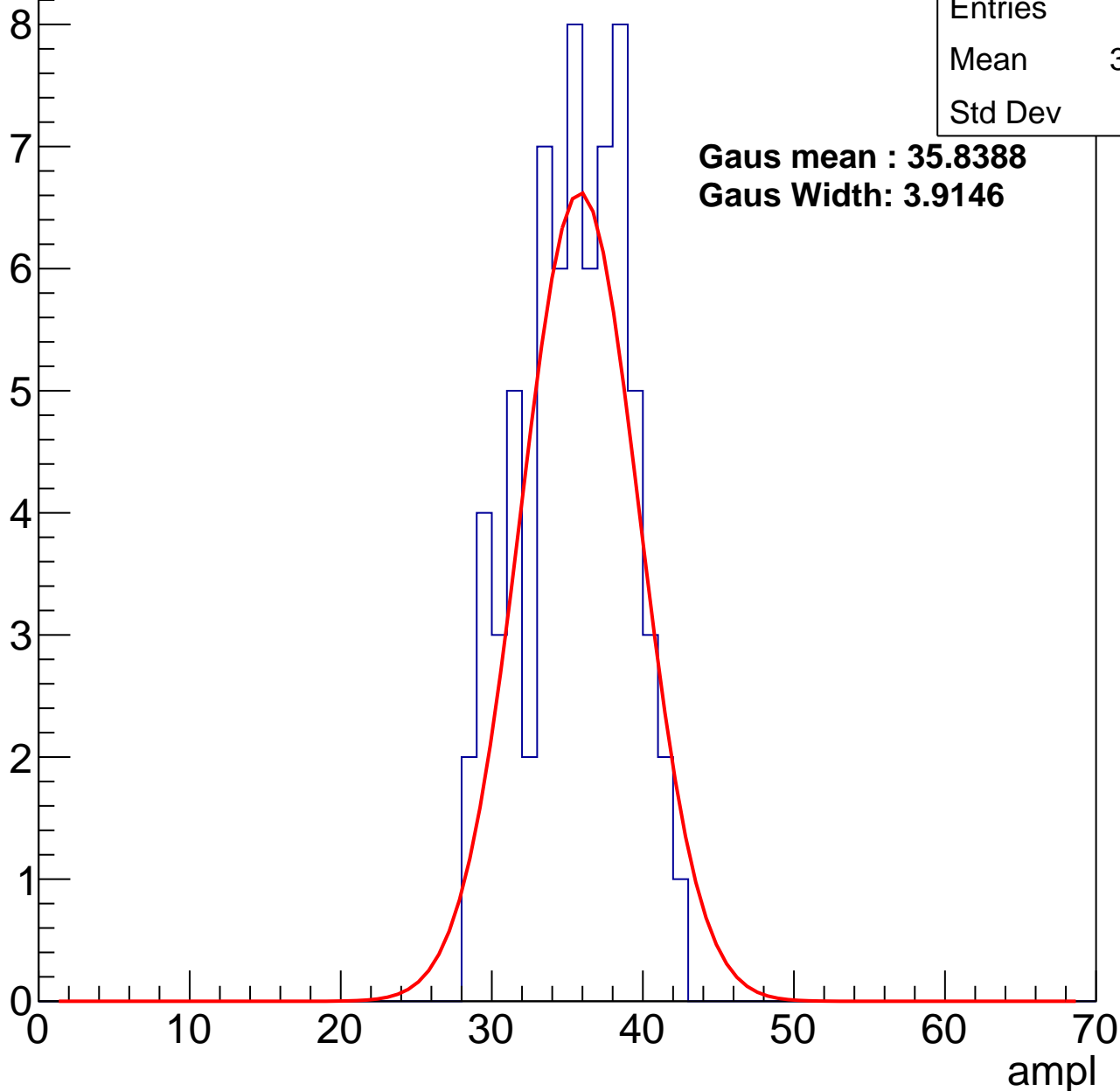
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	34.99
Std Dev	3.47

Gaus mean : 35.8388

Gaus Width: 3.9146



B1L101S, U6-ch70, adc2

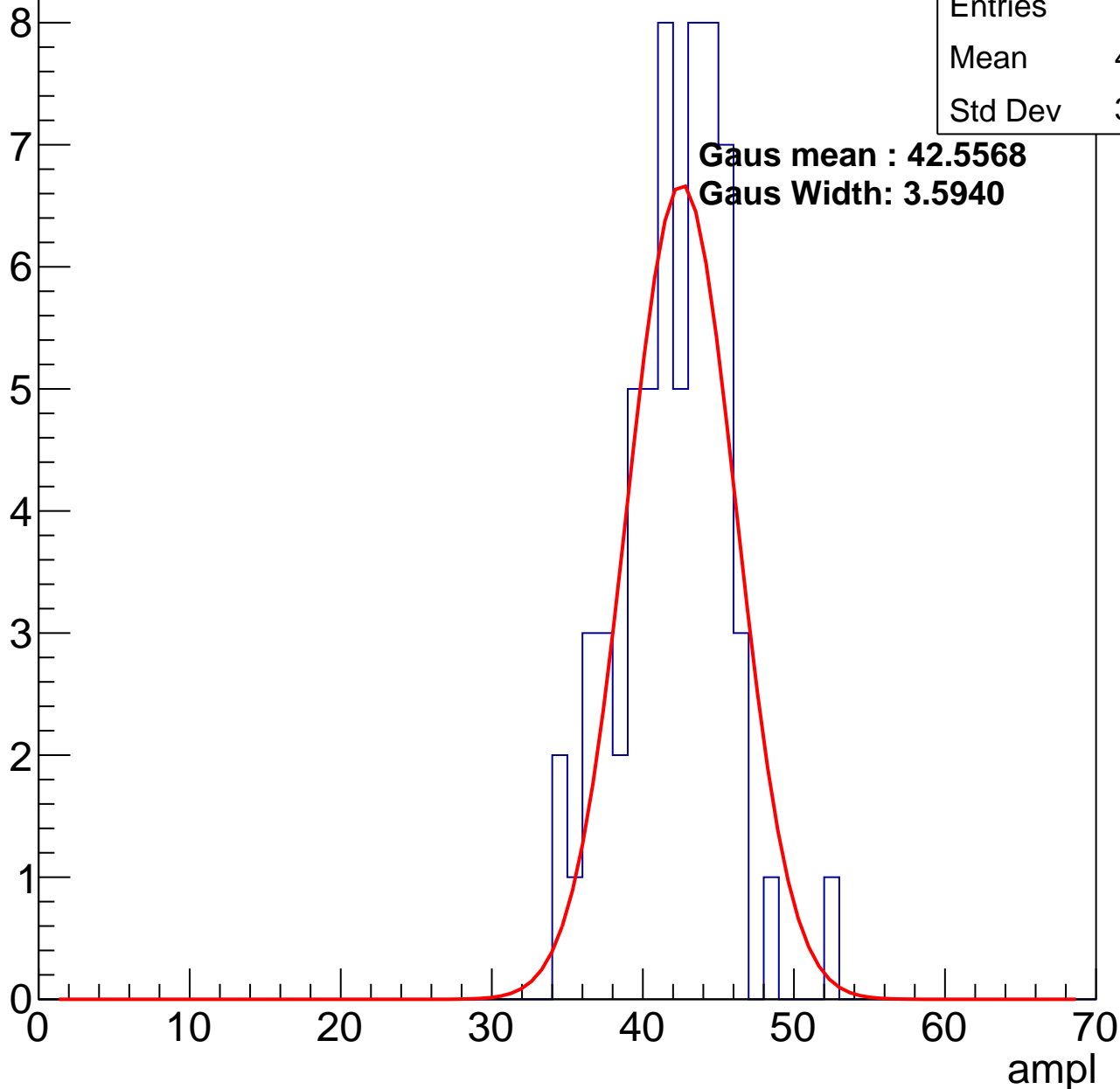
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.61
Std Dev	3.461

Gaus mean : 42.5568

Gaus Width: 3.5940



B1L101S, U6-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	48.54
Std Dev	3.585

Entry

10

8

6

4

2

0

0

10

20

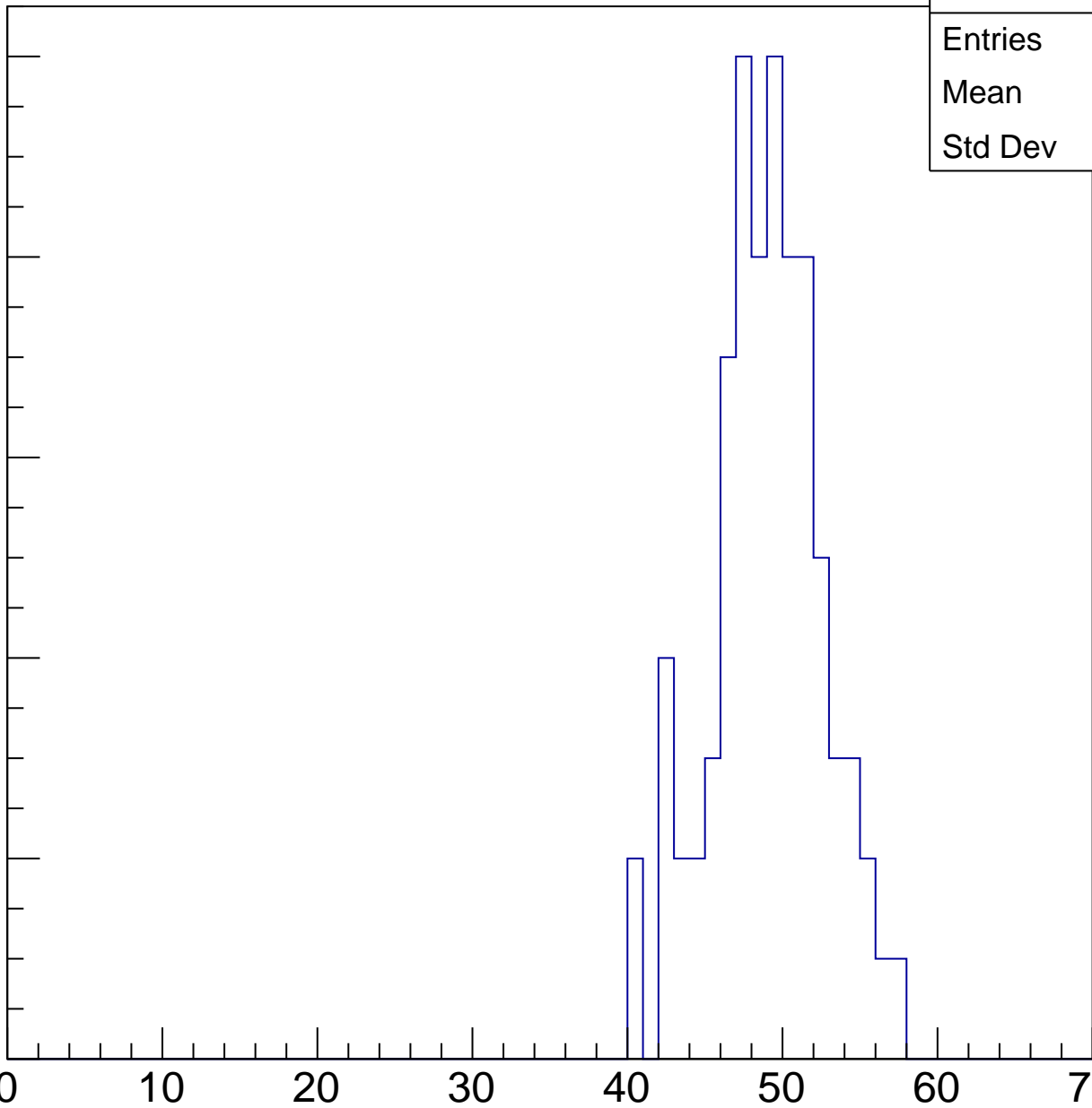
30

40

50

60

ampl

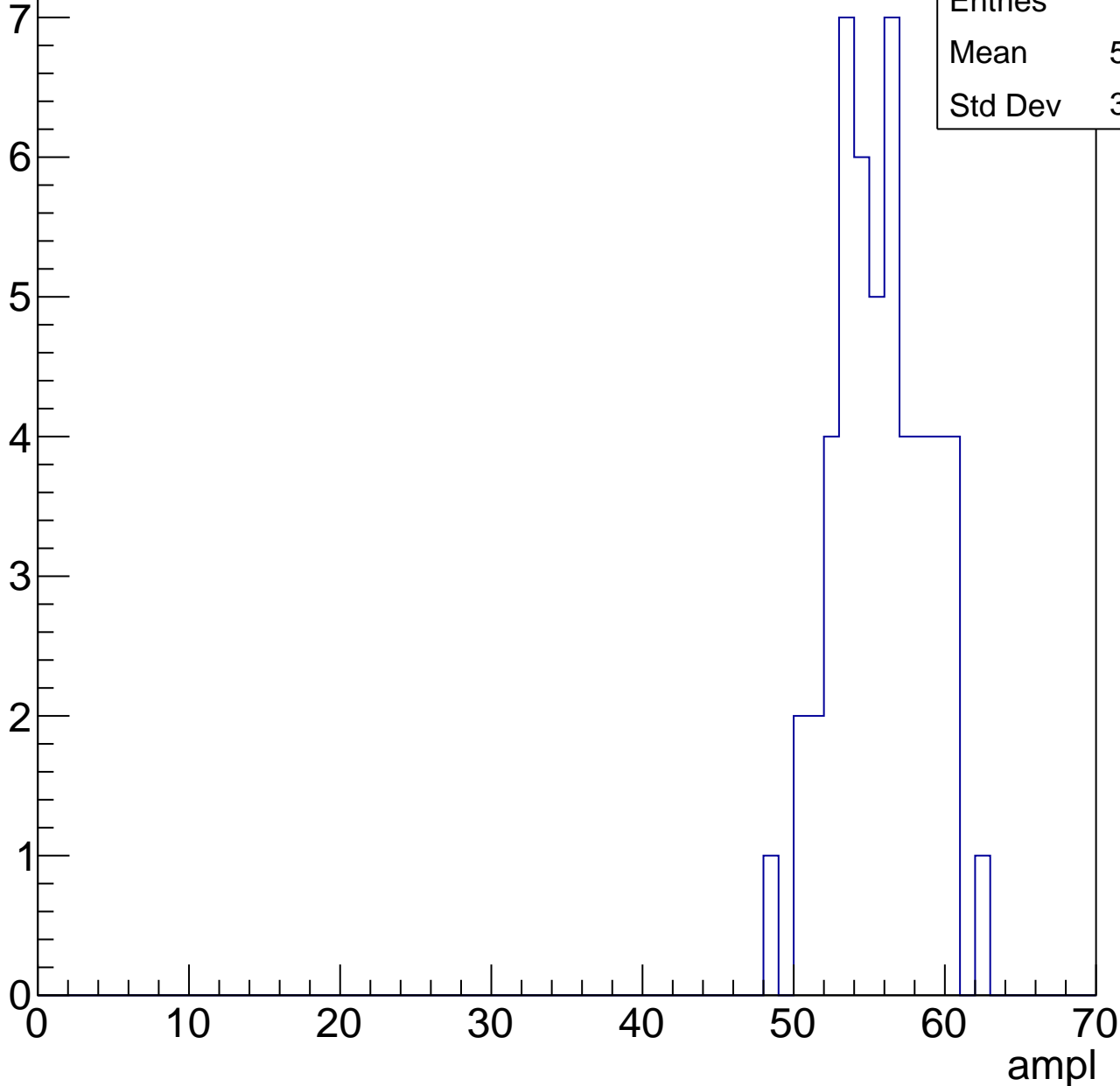


B1L101S, U6-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.25
Std Dev	3.035

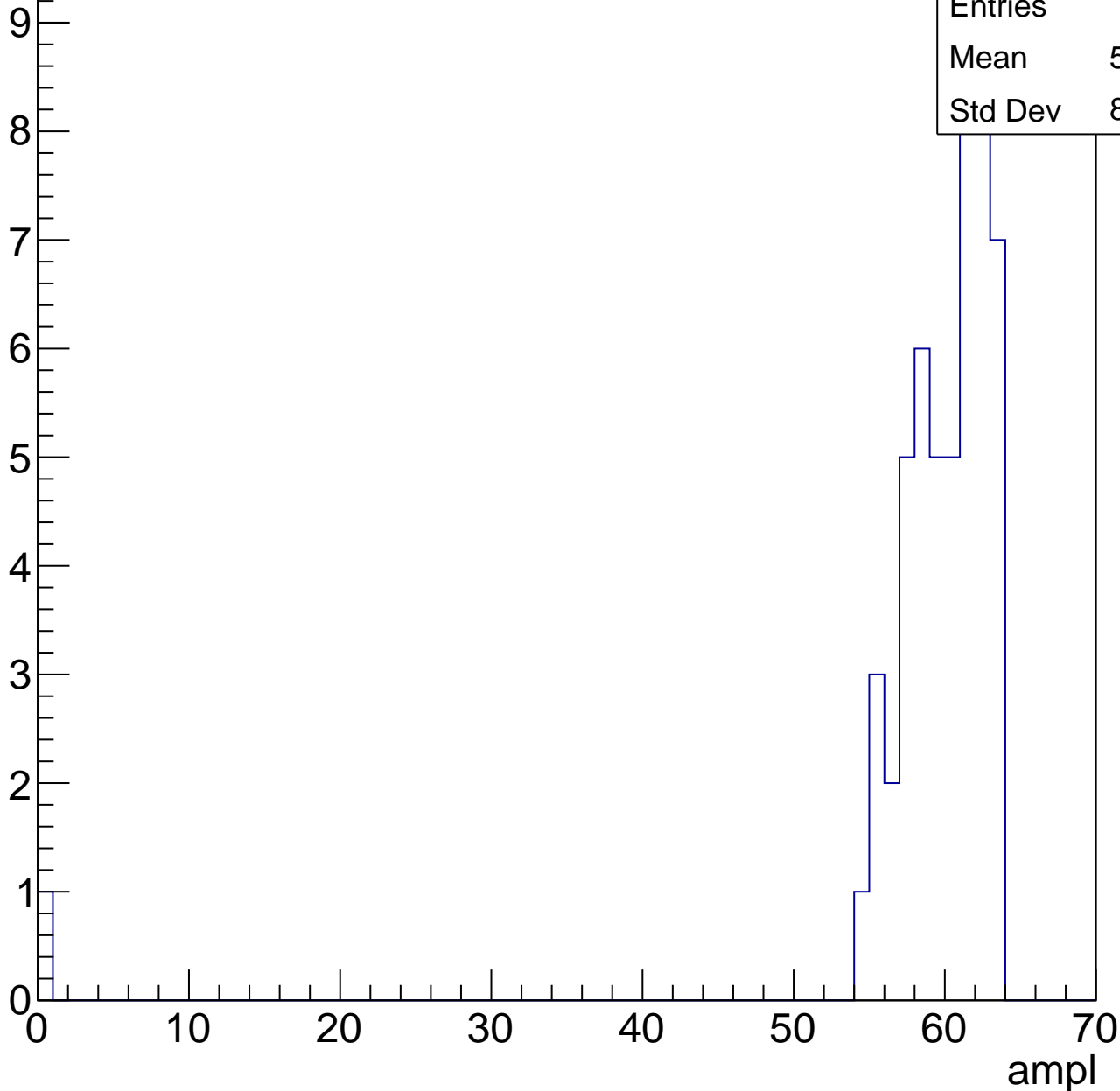


B1L101S, U6-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

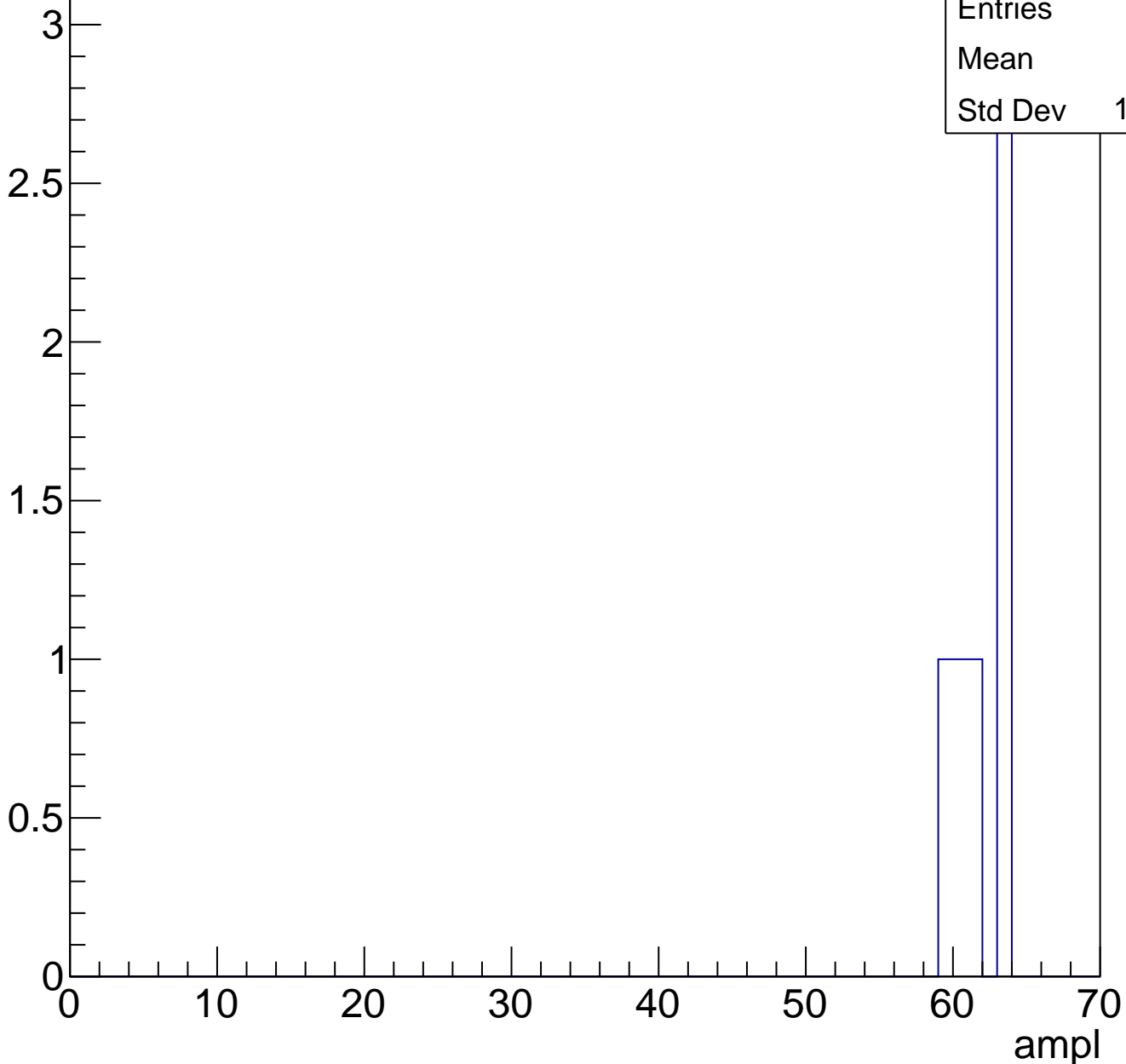
Entries	53
Mean	58.62
Std Dev	8.495



B1L101S, U6-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch71, adc0

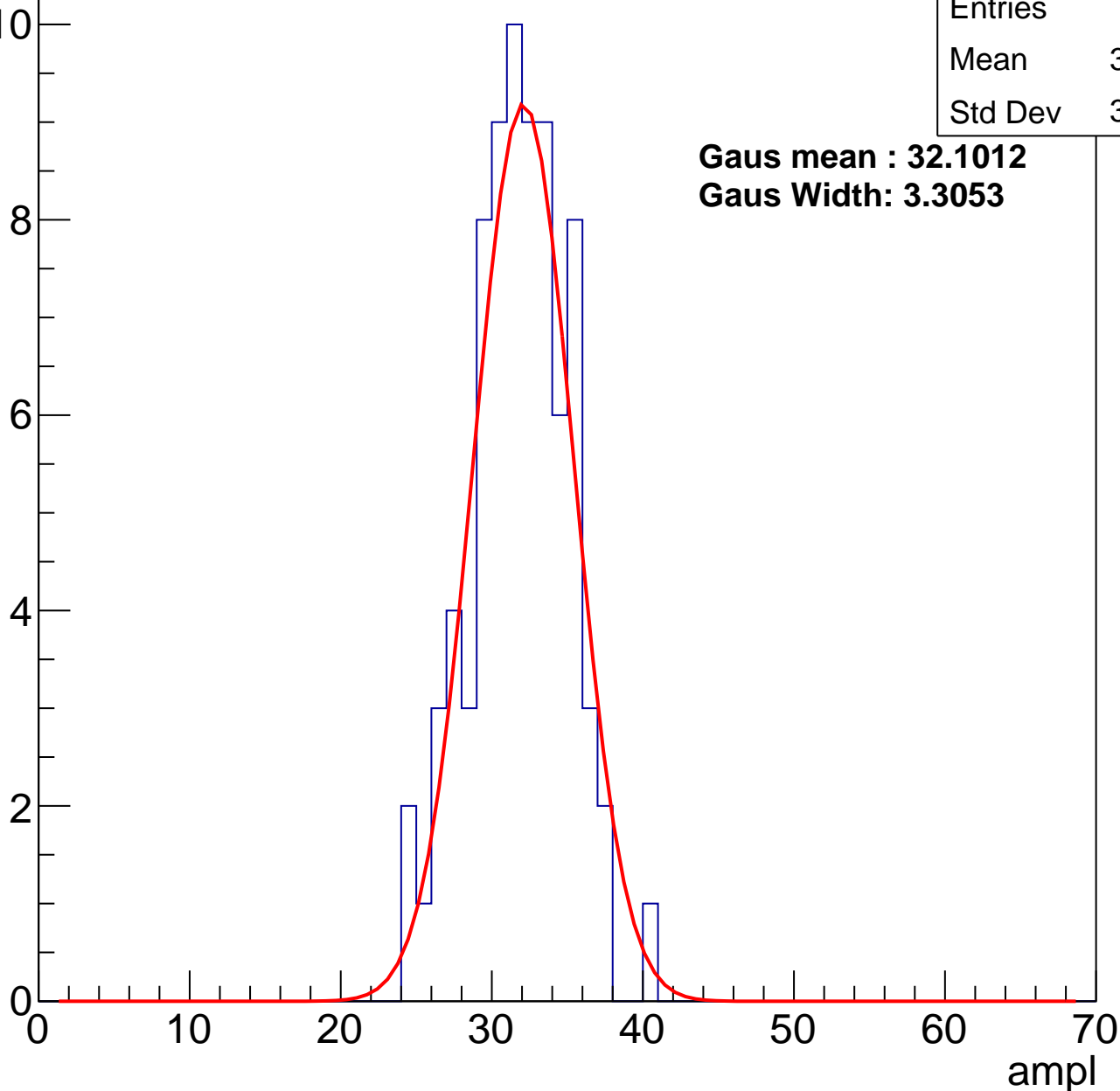
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	31.36
Std Dev	3.174

Gaus mean : 32.1012

Gaus Width: 3.3053



B1L101S, U6-ch71, adc1

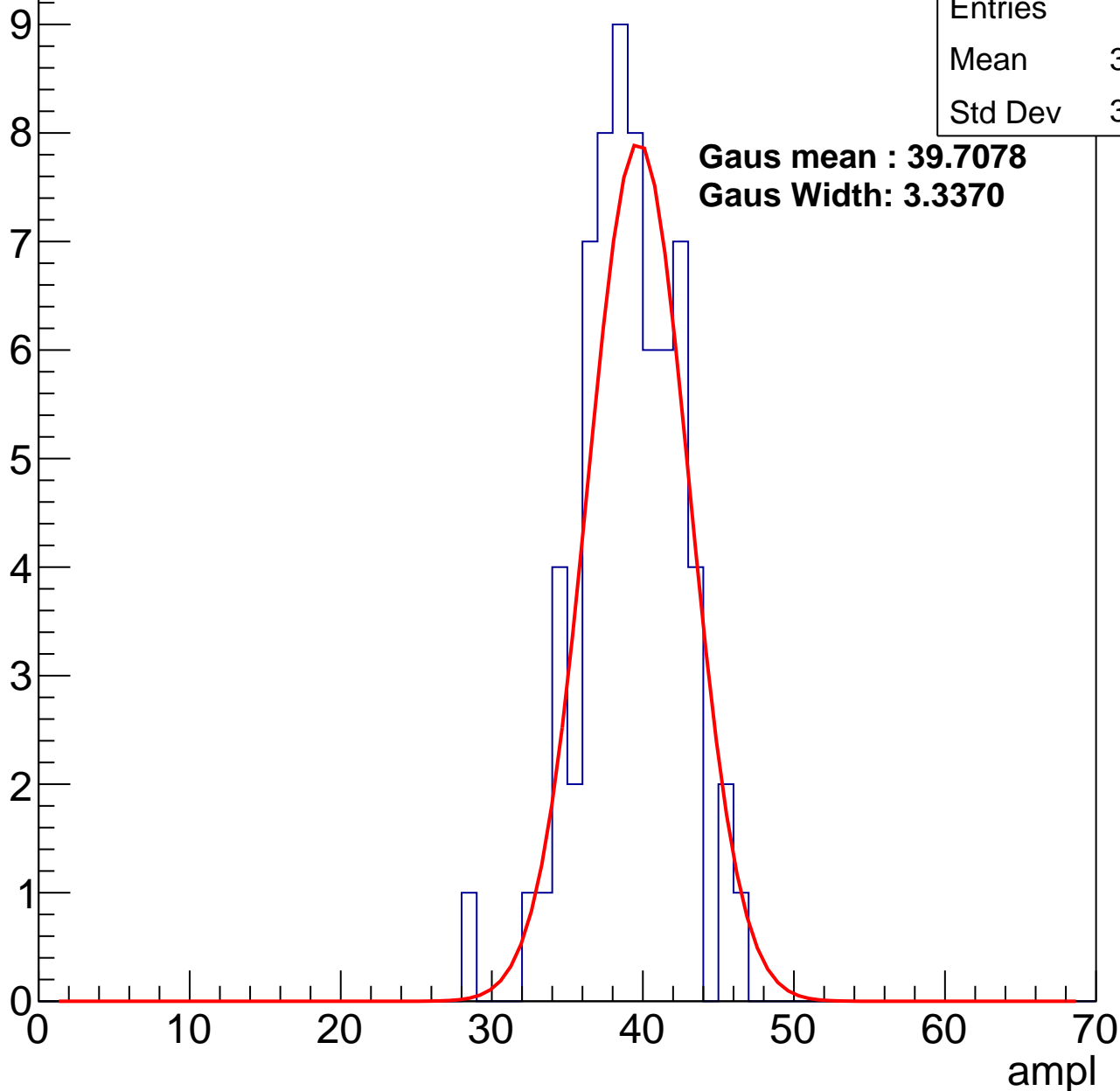
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	38.64
Std Dev	3.259

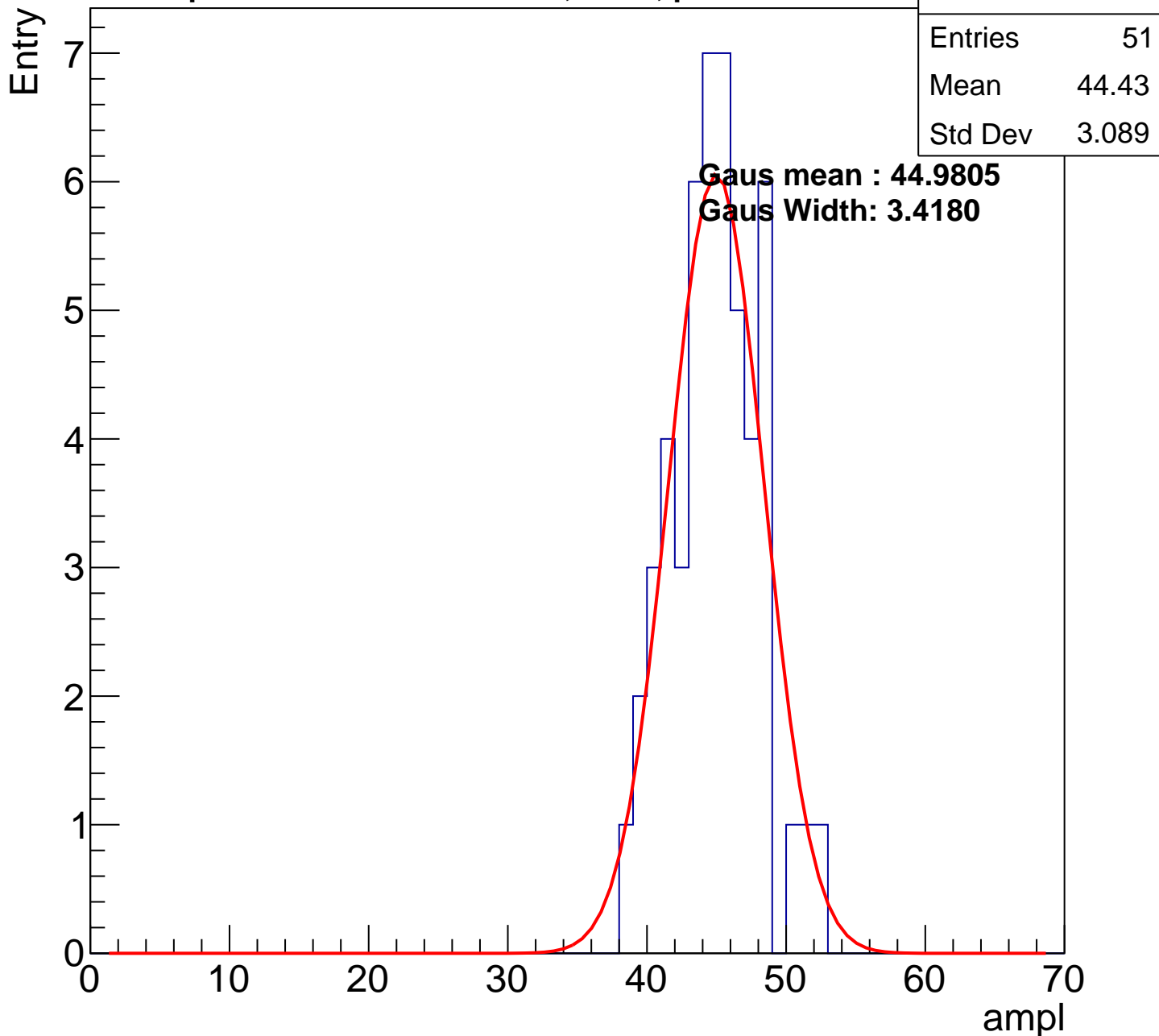
Gaus mean : 39.7078

Gaus Width: 3.3370



B1L101S, U6-ch71, adc2

calib_packv5_042523_0143.root, FC#0, port D2

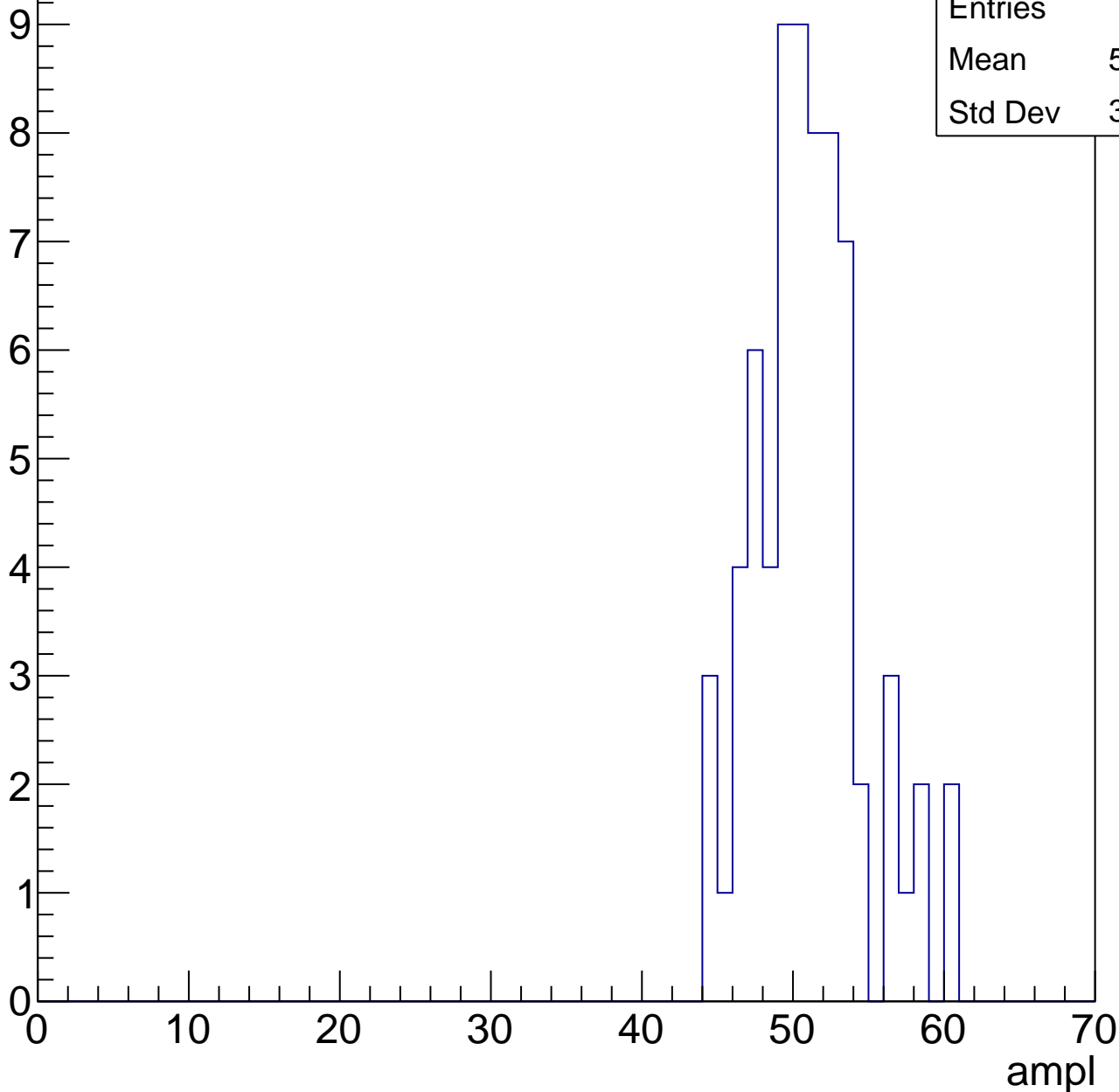


B1L101S, U6-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.58
Std Dev	3.557



B1L101S, U6-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	56
Mean	56.54
Std Dev	2.878

Entry

10

8

6

4

2

0

0

10

20

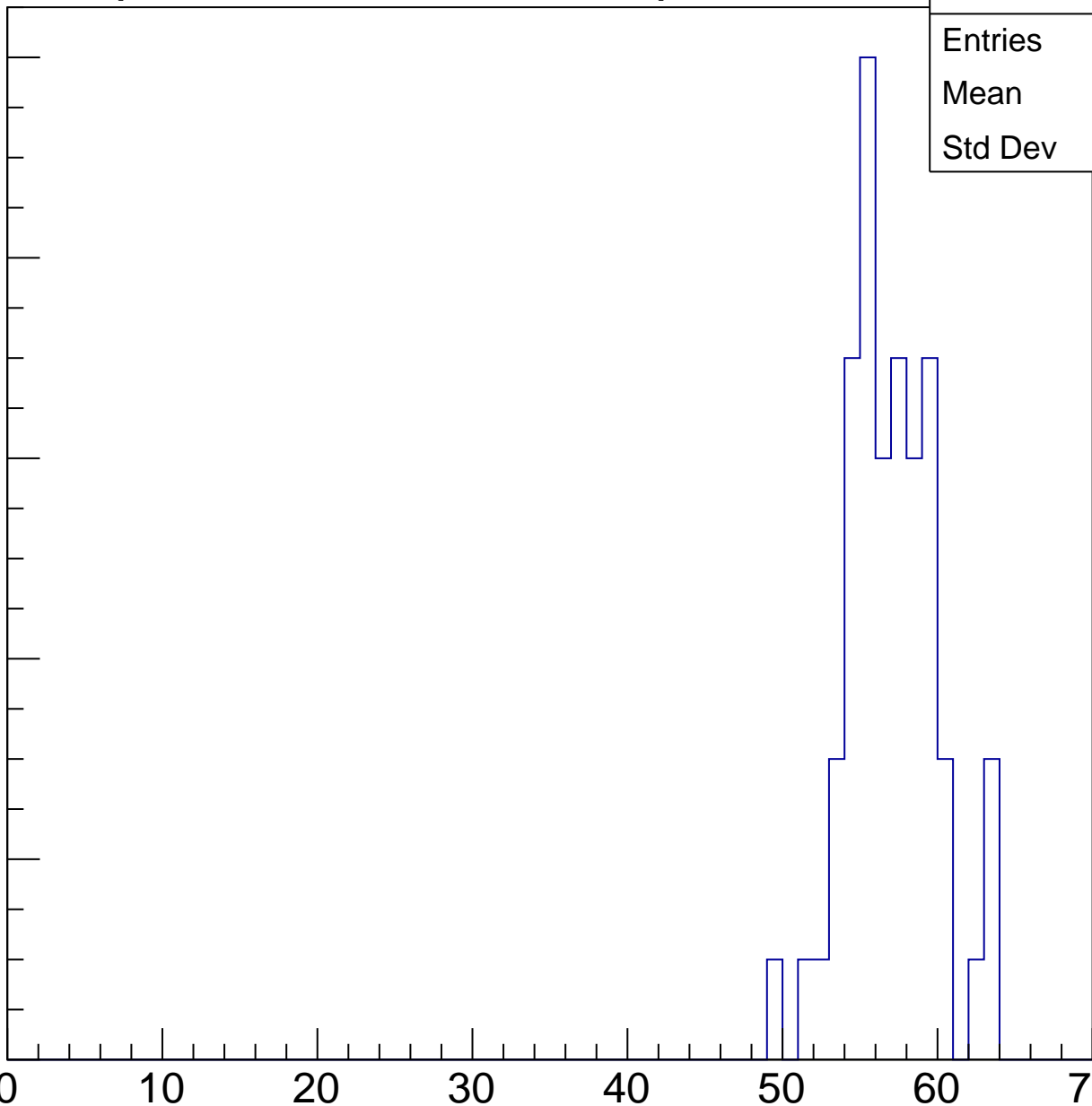
30

40

50

60

ampl



B1L101S, U6-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	39
Mean	59
Std Dev	9.79

0

2

4

6

8

10

B1L101S, U6-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch72, adc0

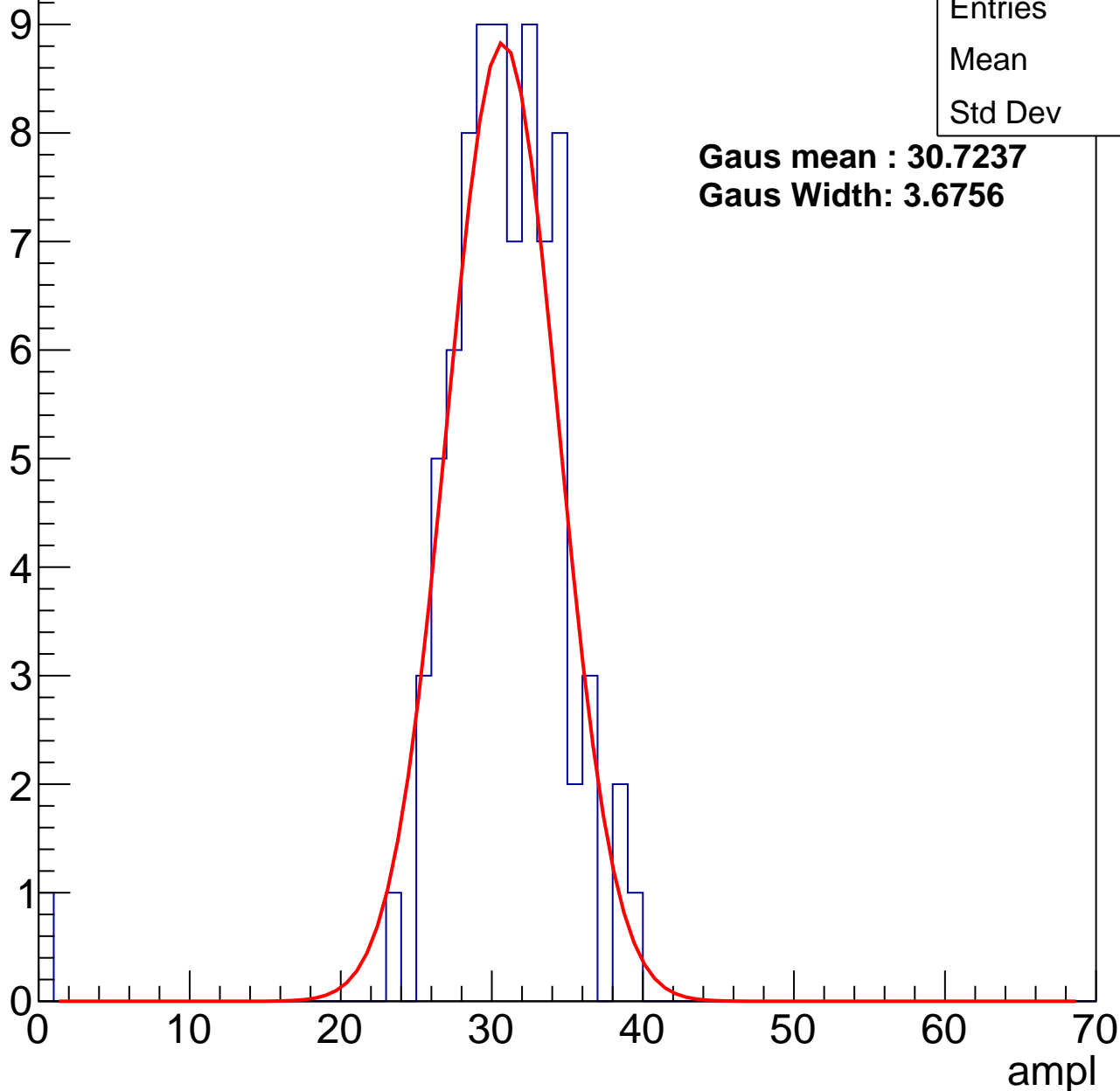
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.2
Std Dev	4.71

Gaus mean : 30.7237

Gaus Width: 3.6756



B1L101S, U6-ch72, adc1

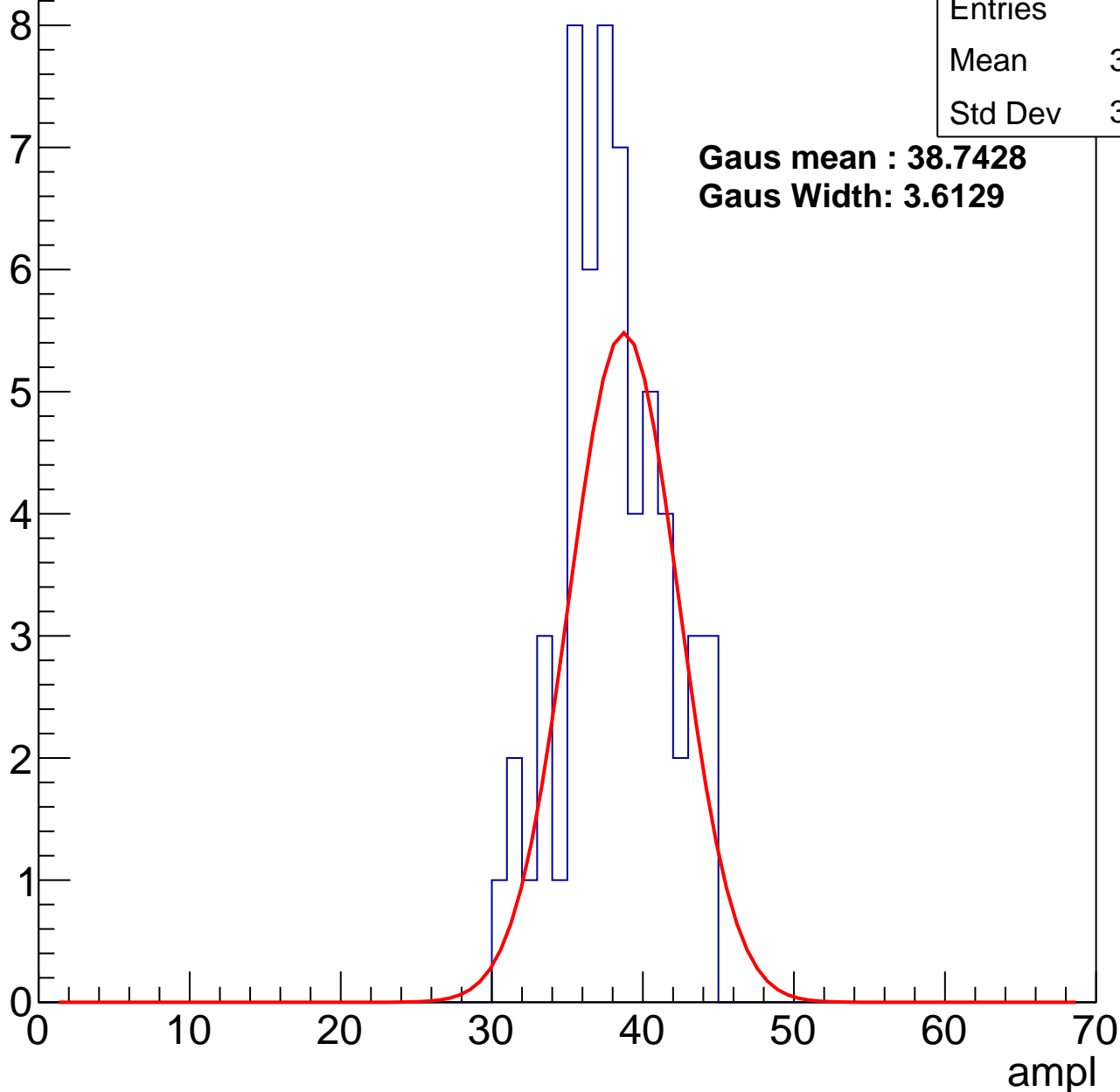
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.59
Std Dev	3.368

Gaus mean : 38.7428

Gaus Width: 3.6129



B1L101S, U6-ch72, adc2

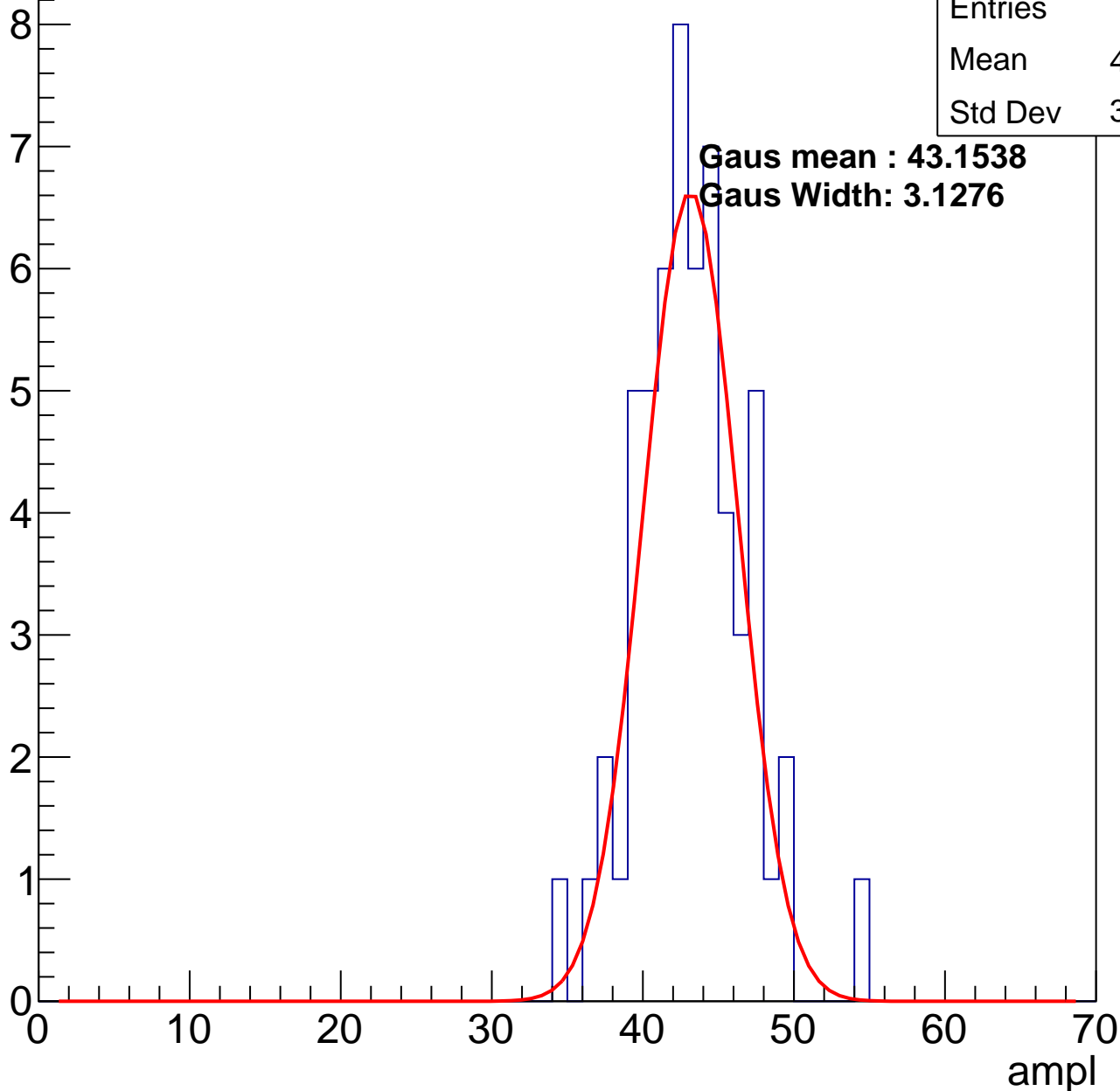
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.72
Std Dev	3.542

Gaus mean : 43.1538

Gaus Width: 3.1276

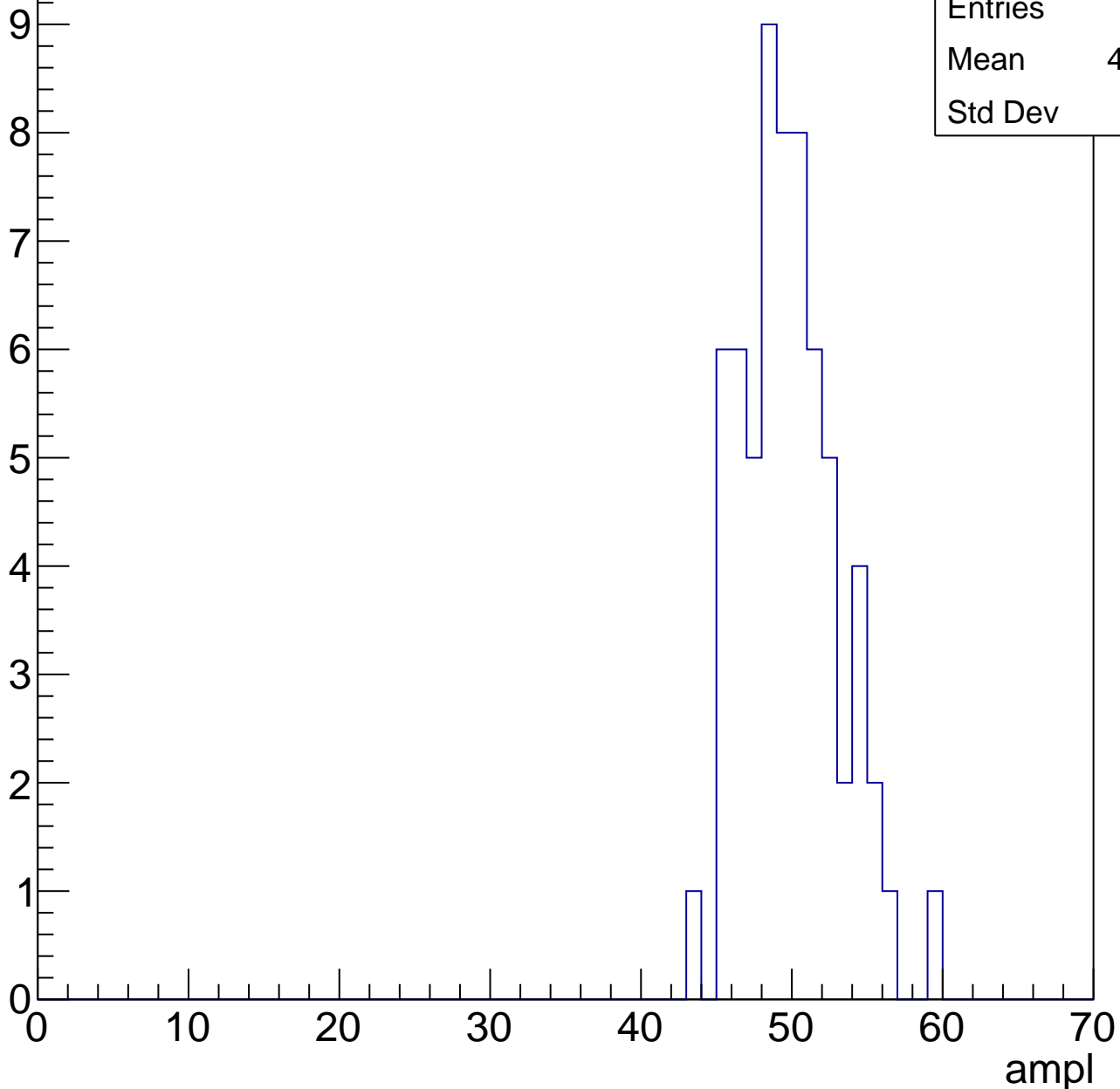


B1L101S, U6-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

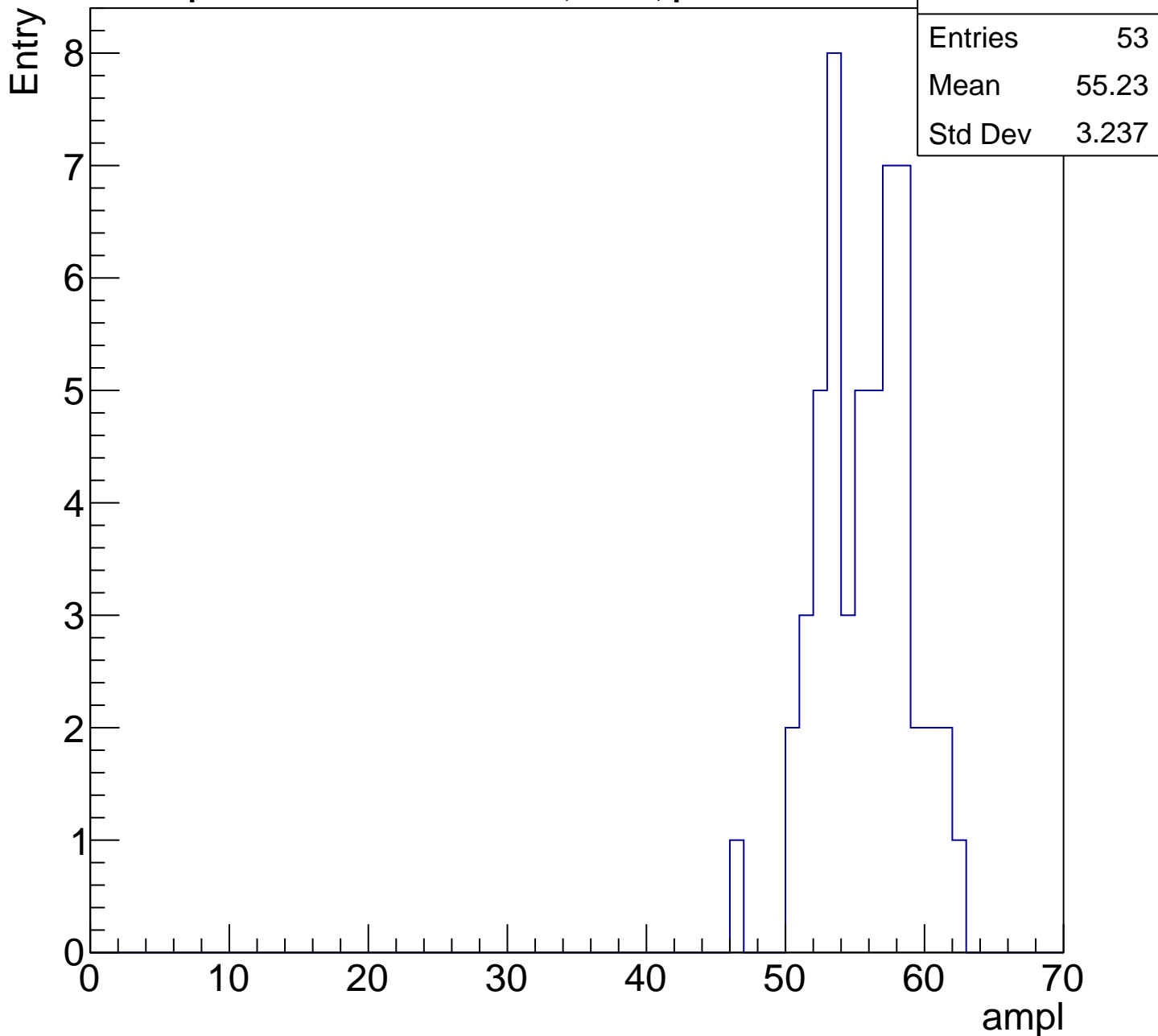
Entry

Entries	64
Mean	49.39
Std Dev	3.15



B1L101S, U6-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

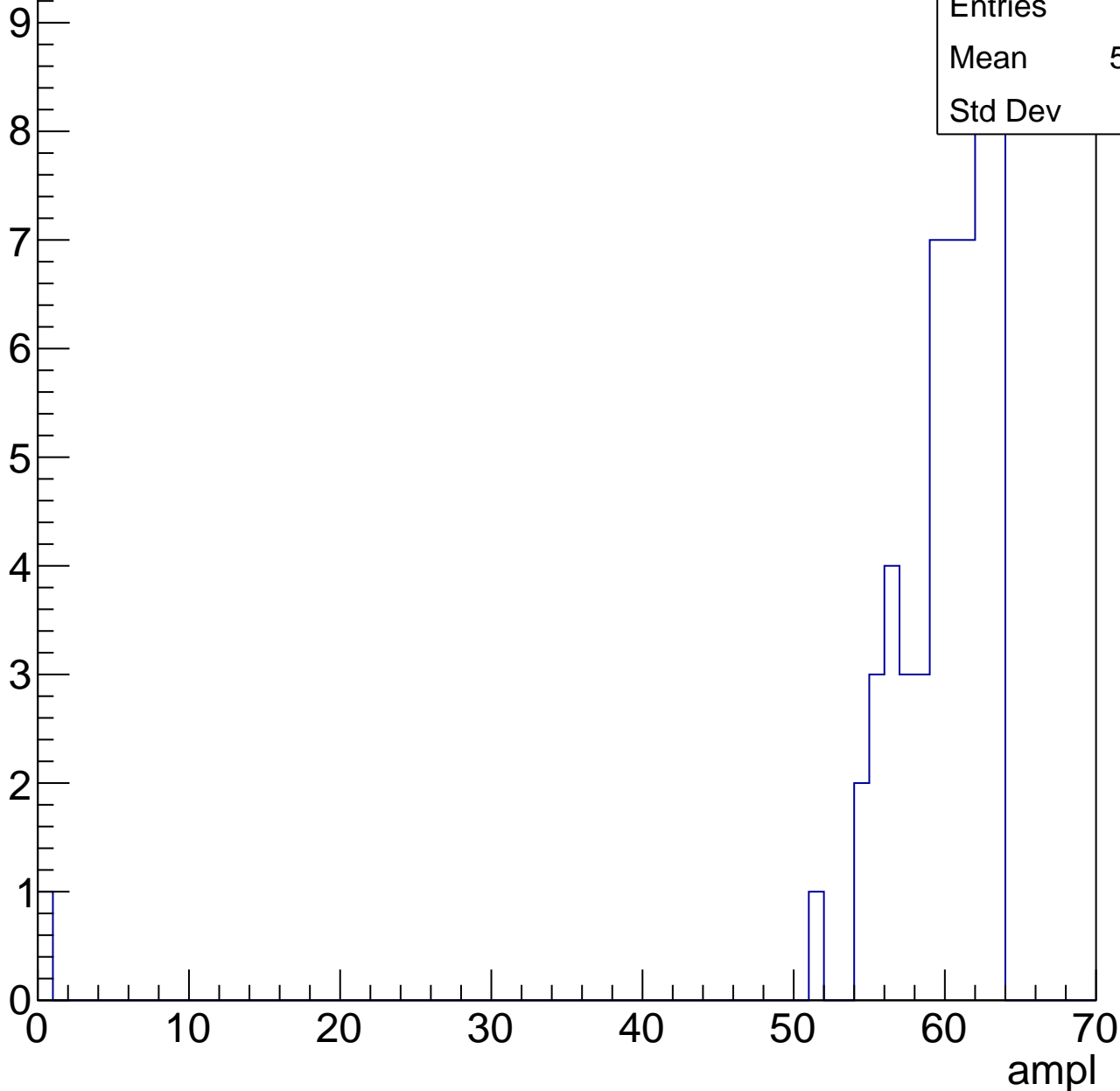


B1L101S, U6-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

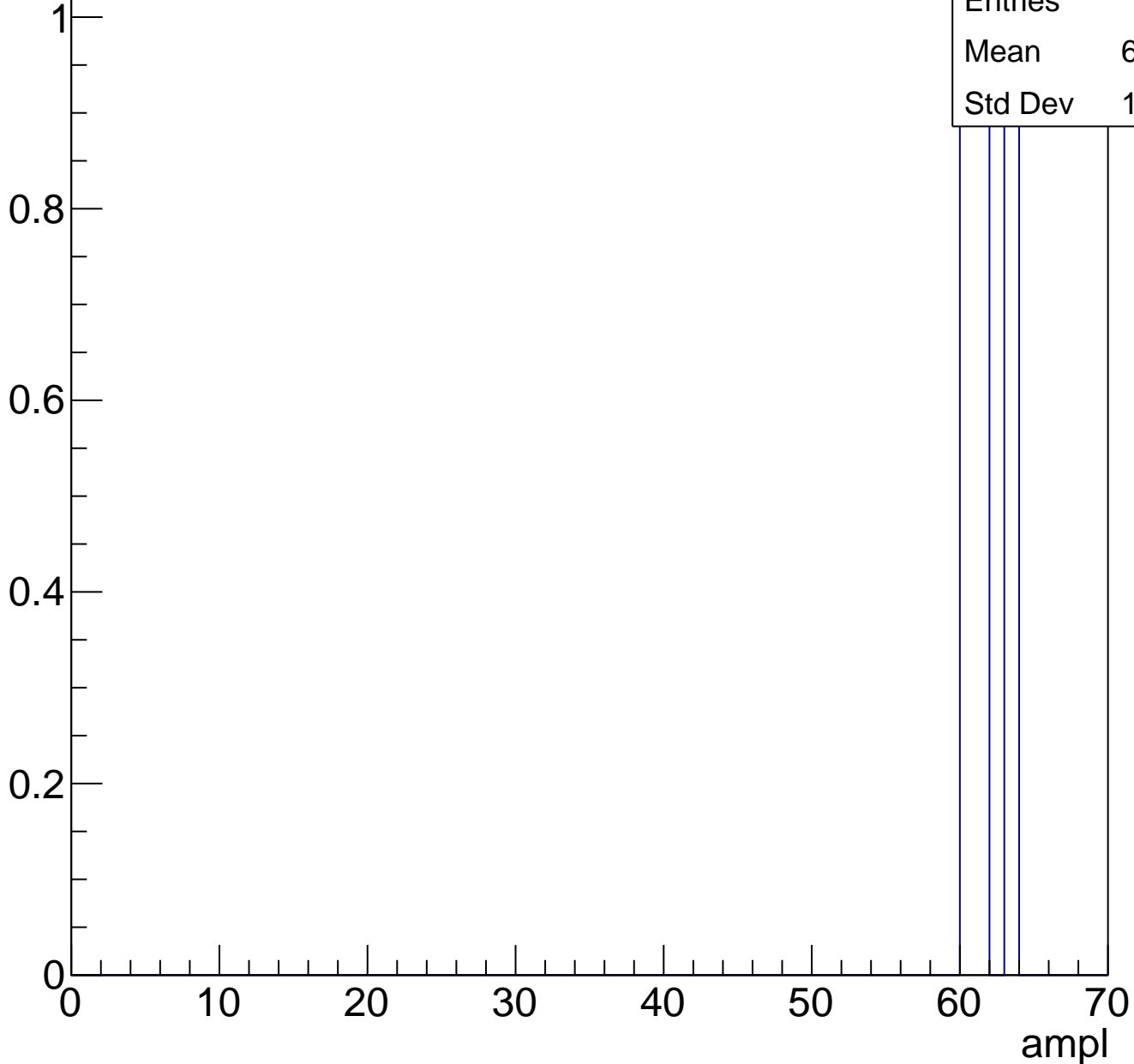
Entries	56
Mean	58.54
Std Dev	8.39



B1L101S, U6-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch73, adc0

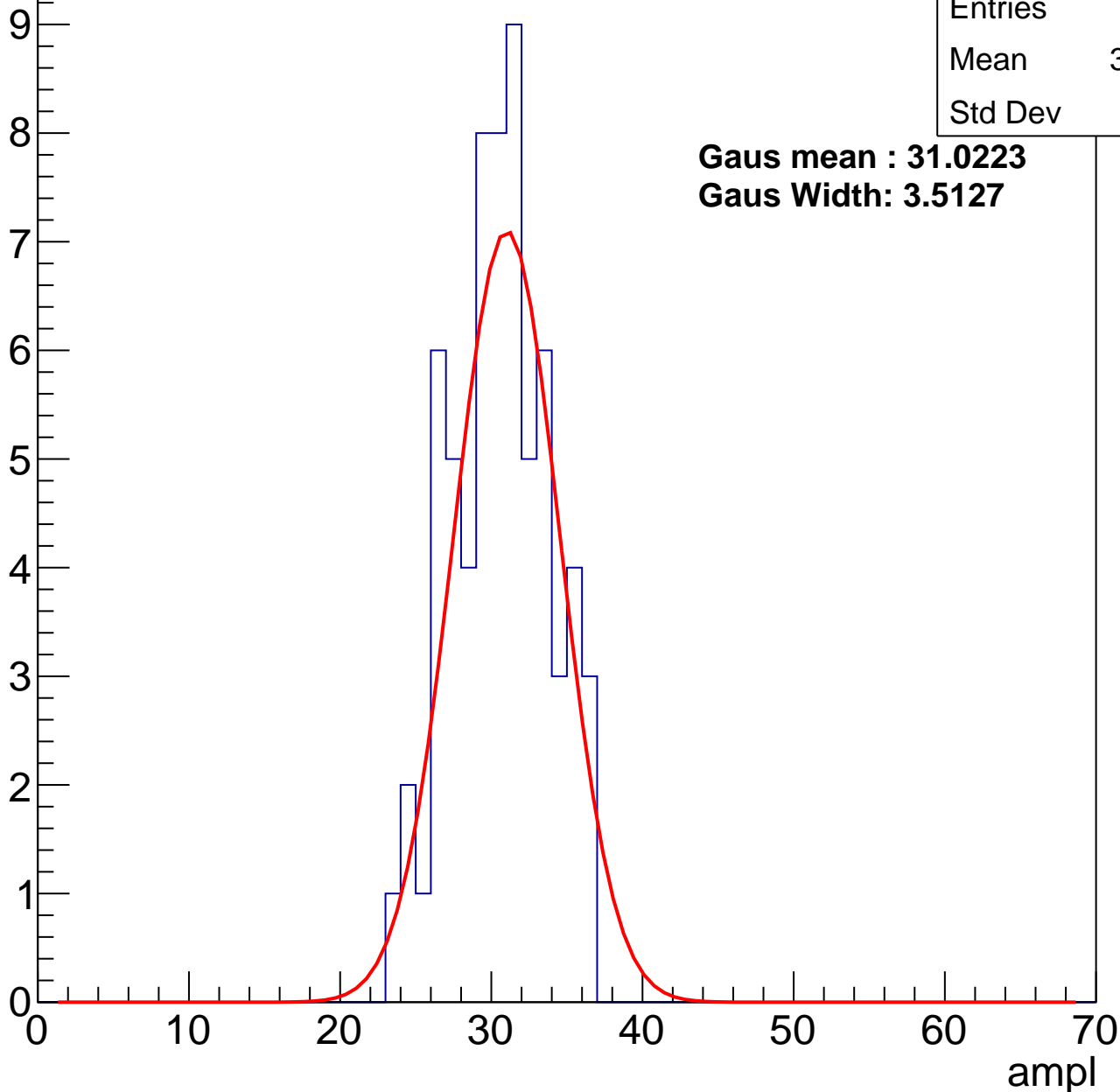
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.12
Std Dev	3.16

Gaus mean : 31.0223

Gaus Width: 3.5127



B1L101S, U6-ch73, adc1

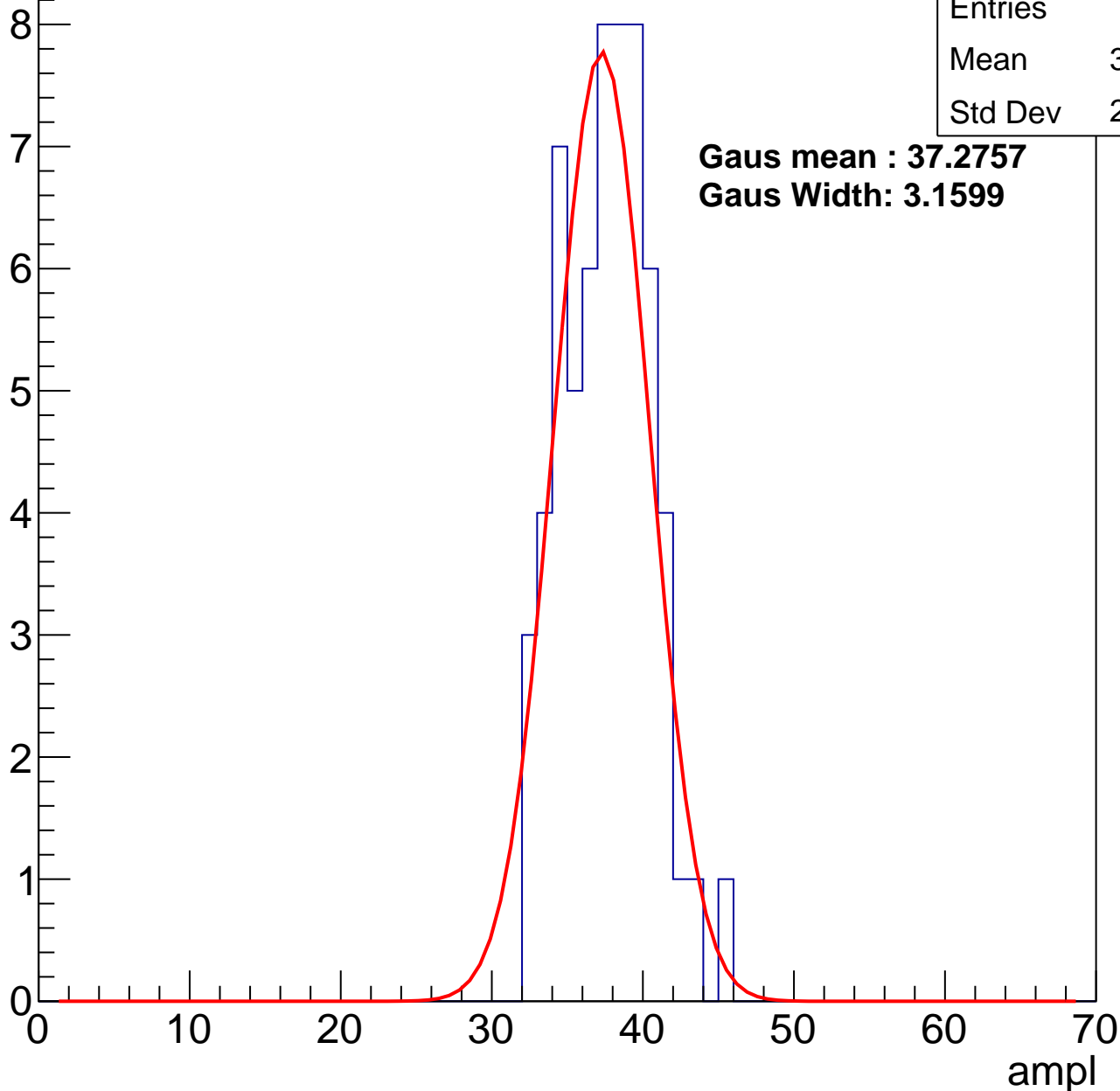
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.15
Std Dev	2.867

Gaus mean : 37.2757

Gaus Width: 3.1599



B1L101S, U6-ch73, adc2

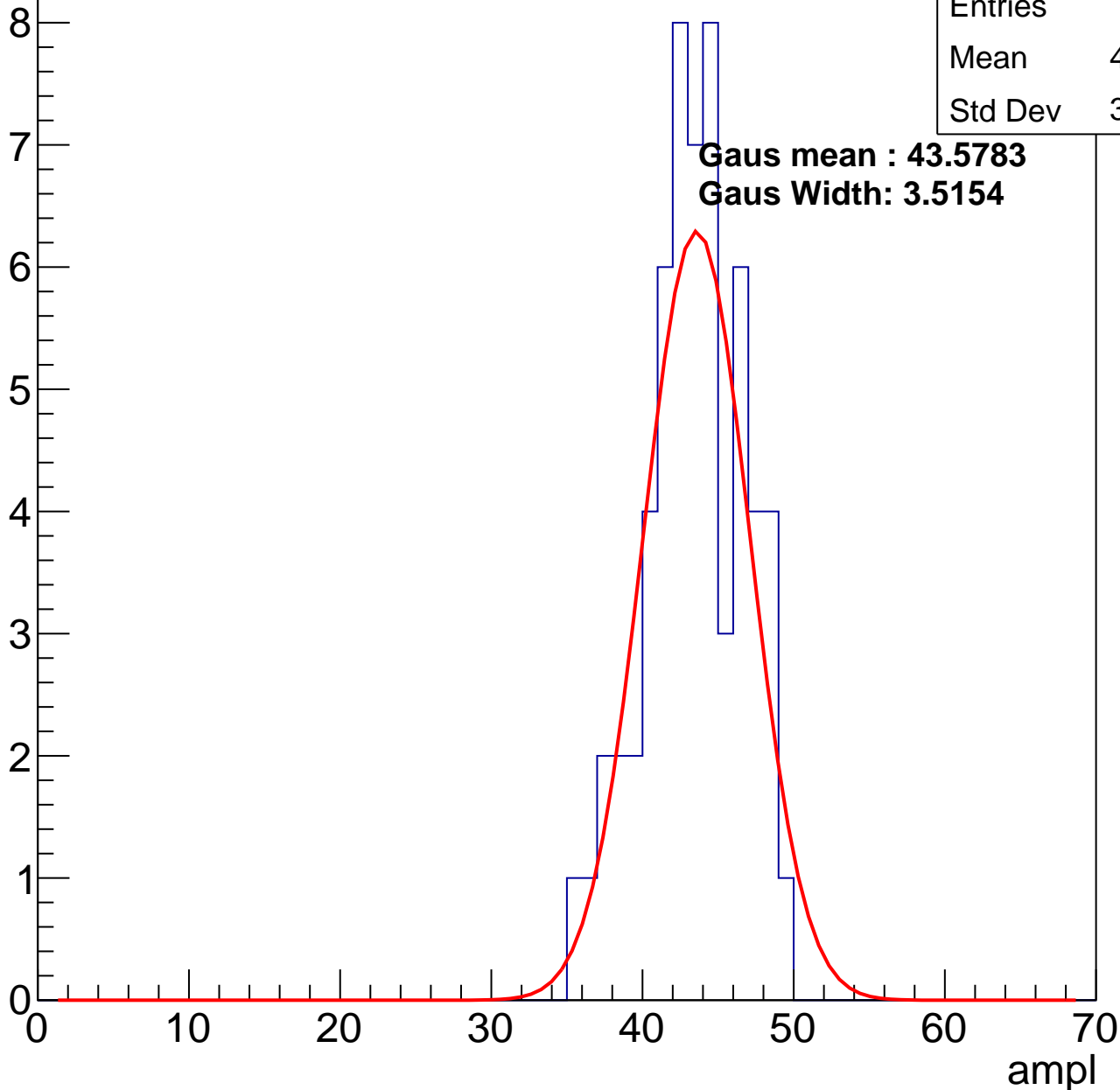
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.95
Std Dev	3.212

Gaus mean : 43.5783

Gaus Width: 3.5154



B1L101S, U6-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

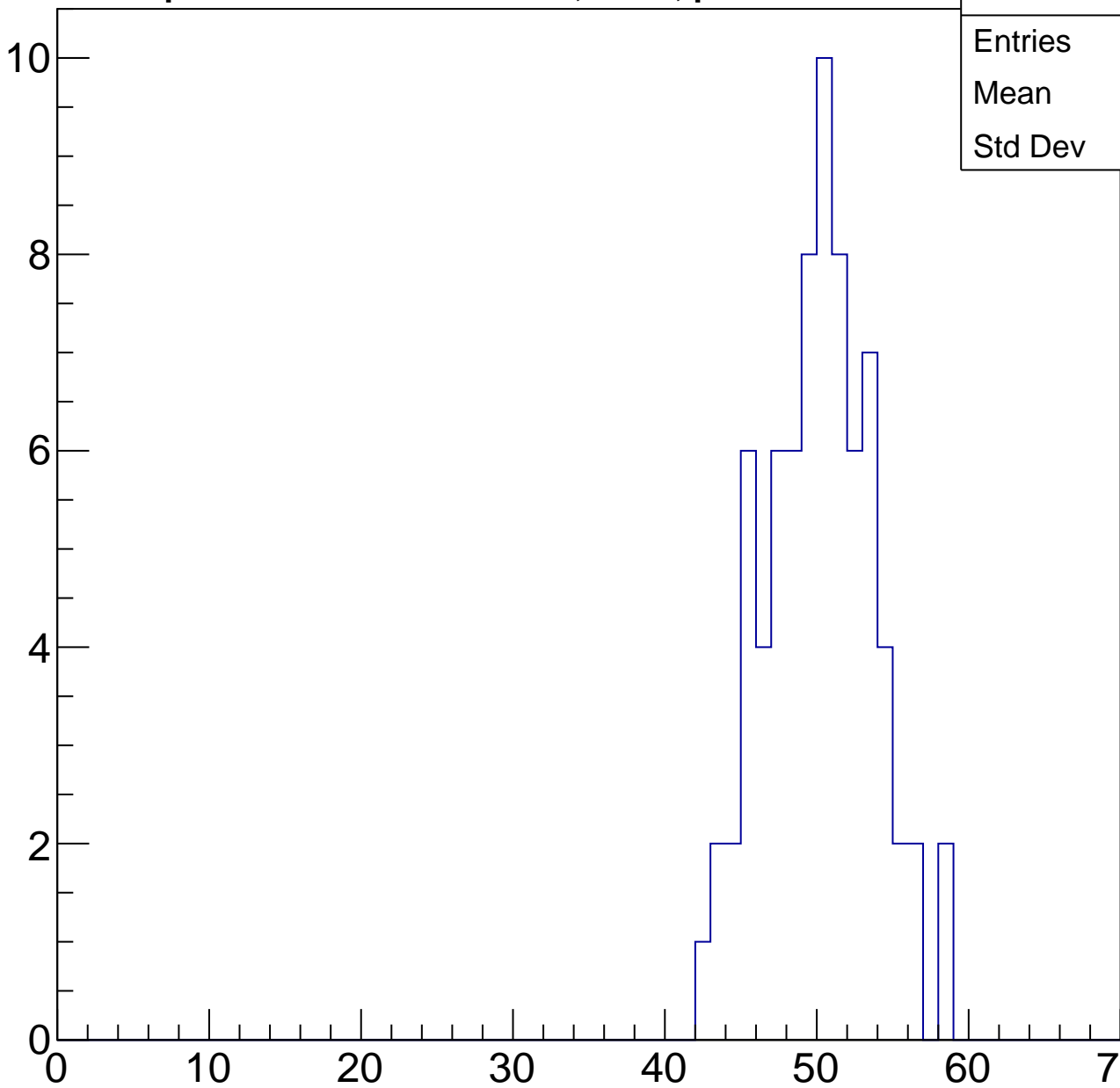
Entries	76
Mean	49.7
Std Dev	3.509

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

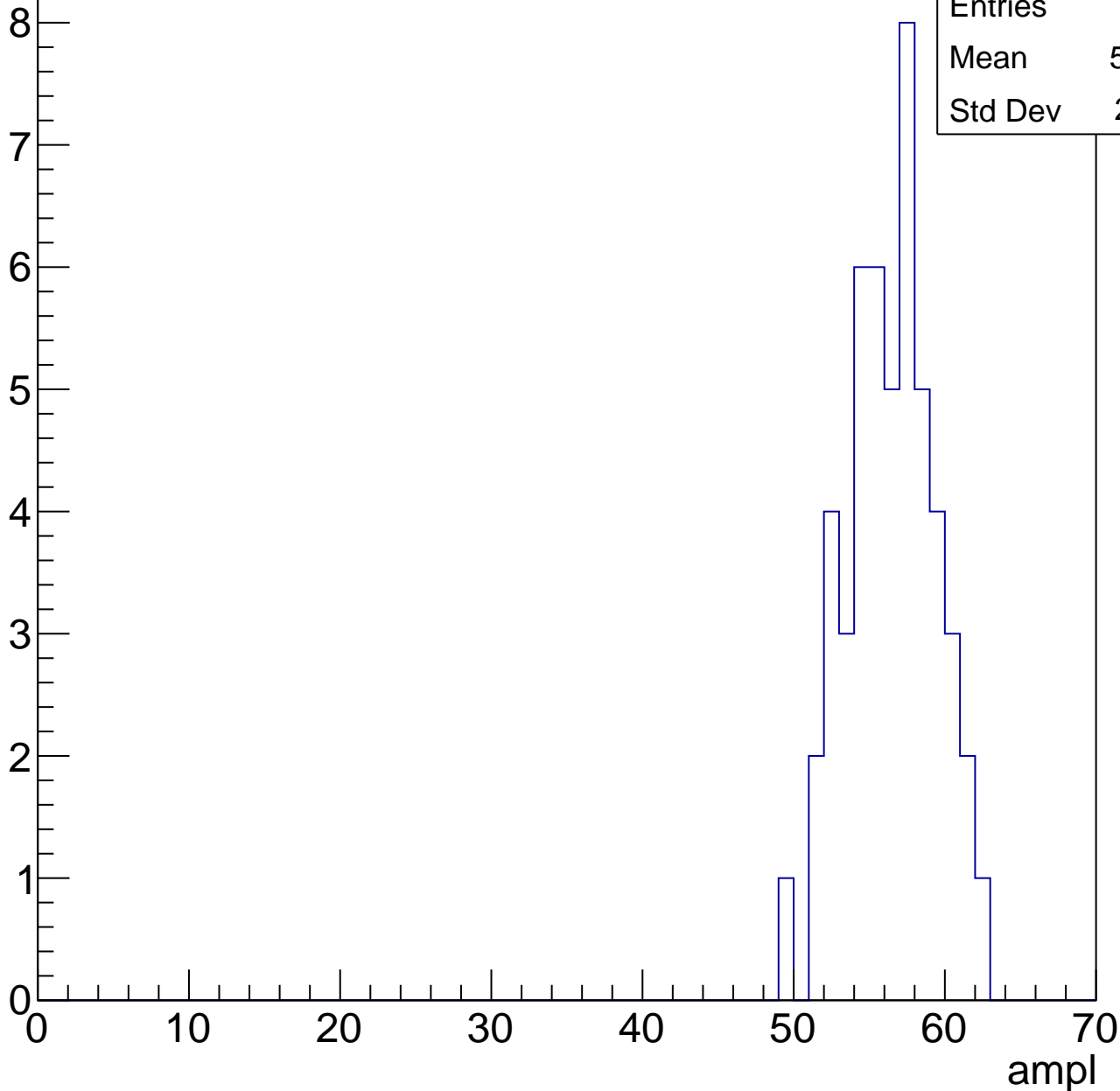


B1L101S, U6-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.96
Std Dev	2.891

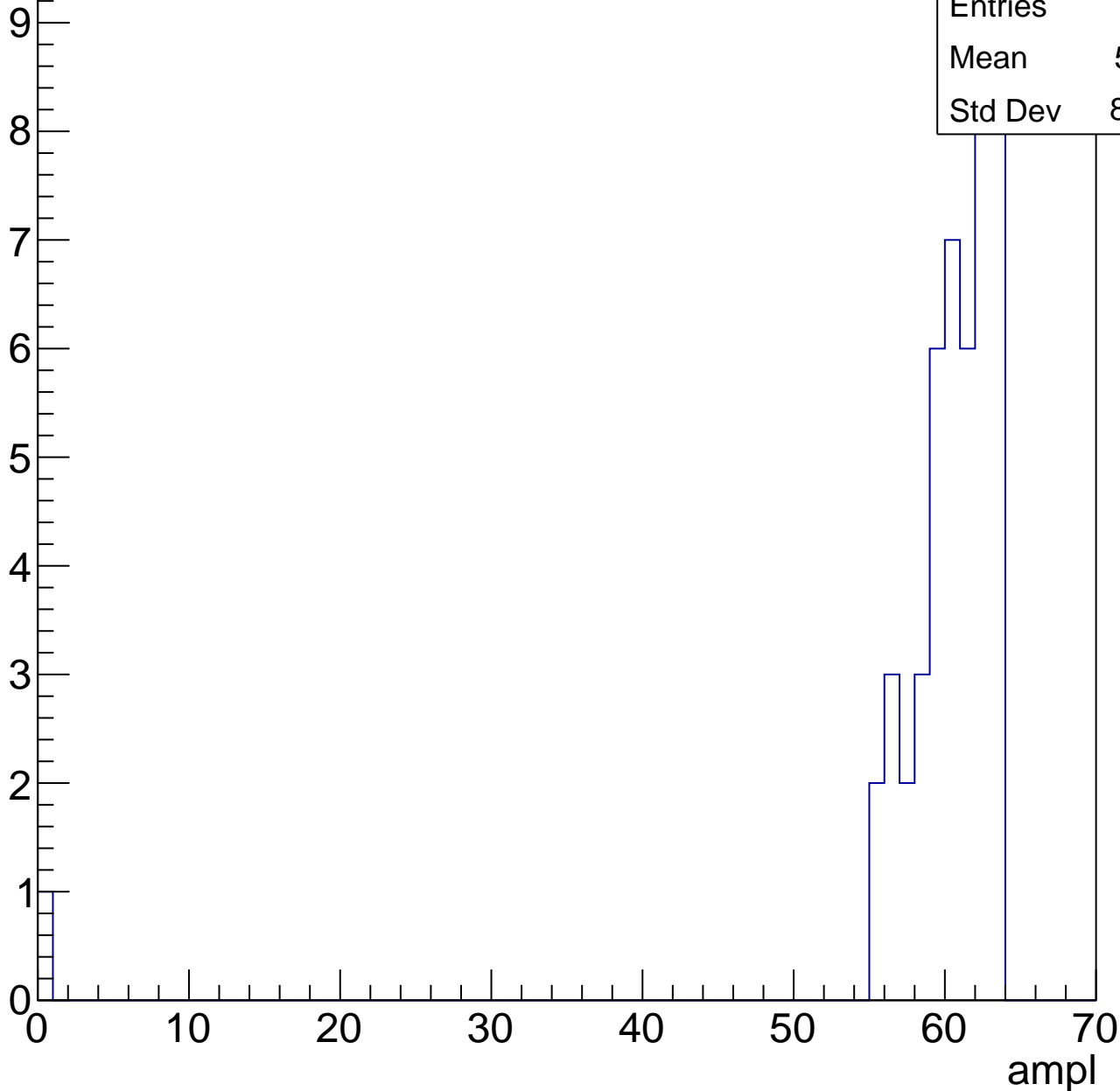


B1L101S, U6-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.91
Std Dev	8.989



B1L101S, U6-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	61.75
Std Dev	0.8292

B1L101S, U6-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L101S, U6-ch74, adc0

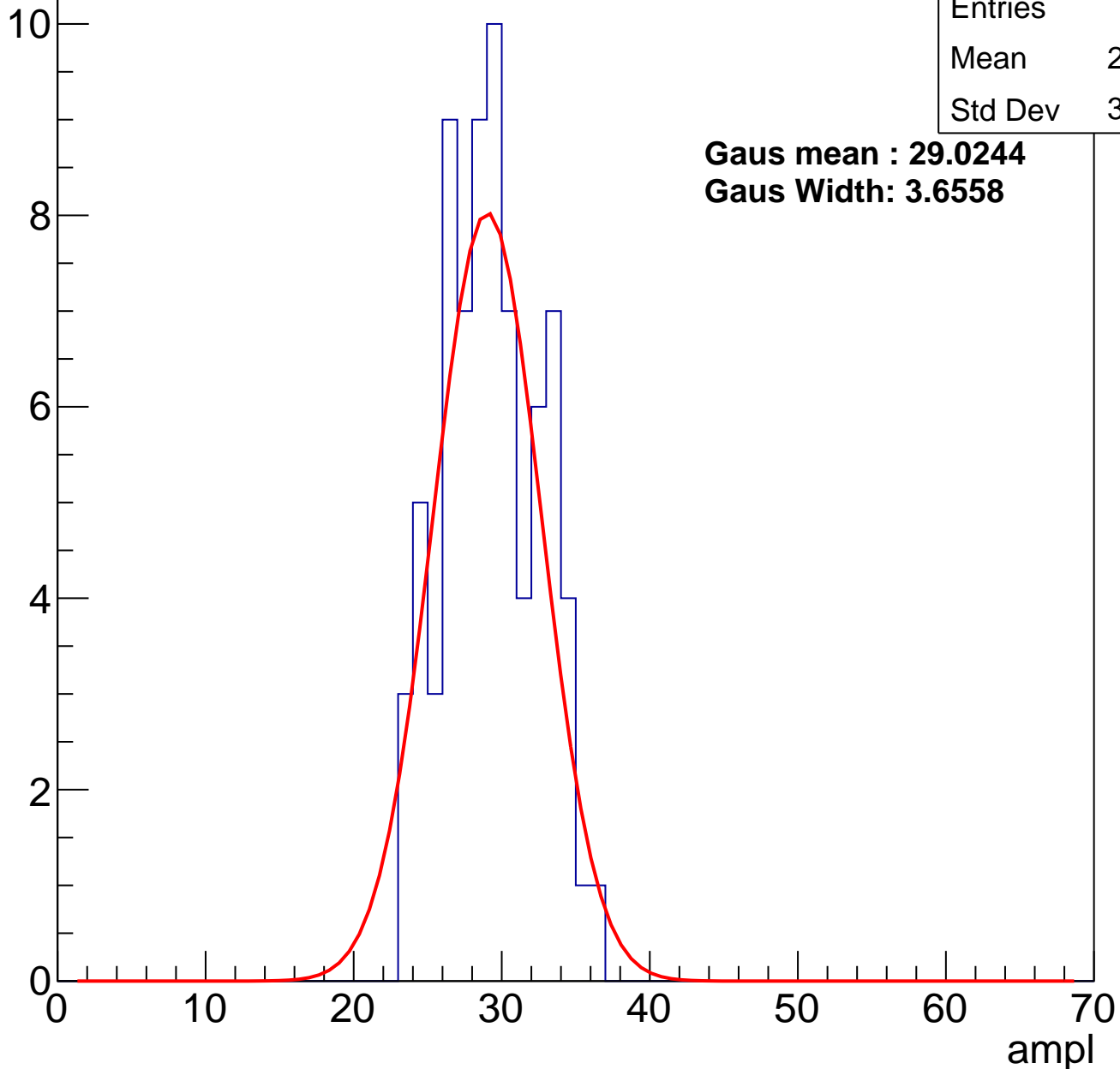
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	28.86
Std Dev	3.186

Gaus mean : 29.0244

Gaus Width: 3.6558

Entry



B1L101S, U6-ch74, adc1

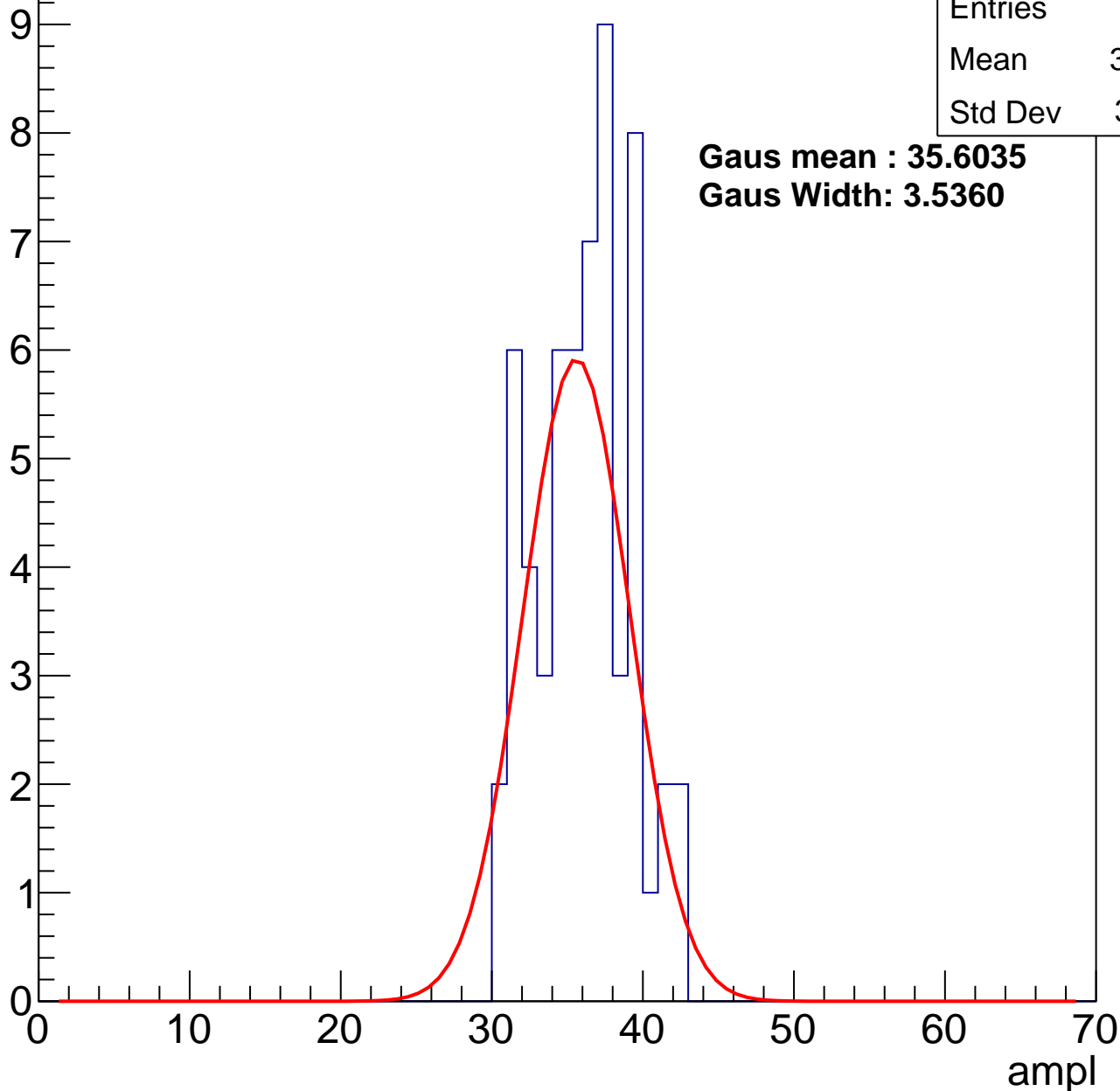
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.66
Std Dev	3.101

Gaus mean : 35.6035

Gaus Width: 3.5360



B1L101S, U6-ch74, adc2

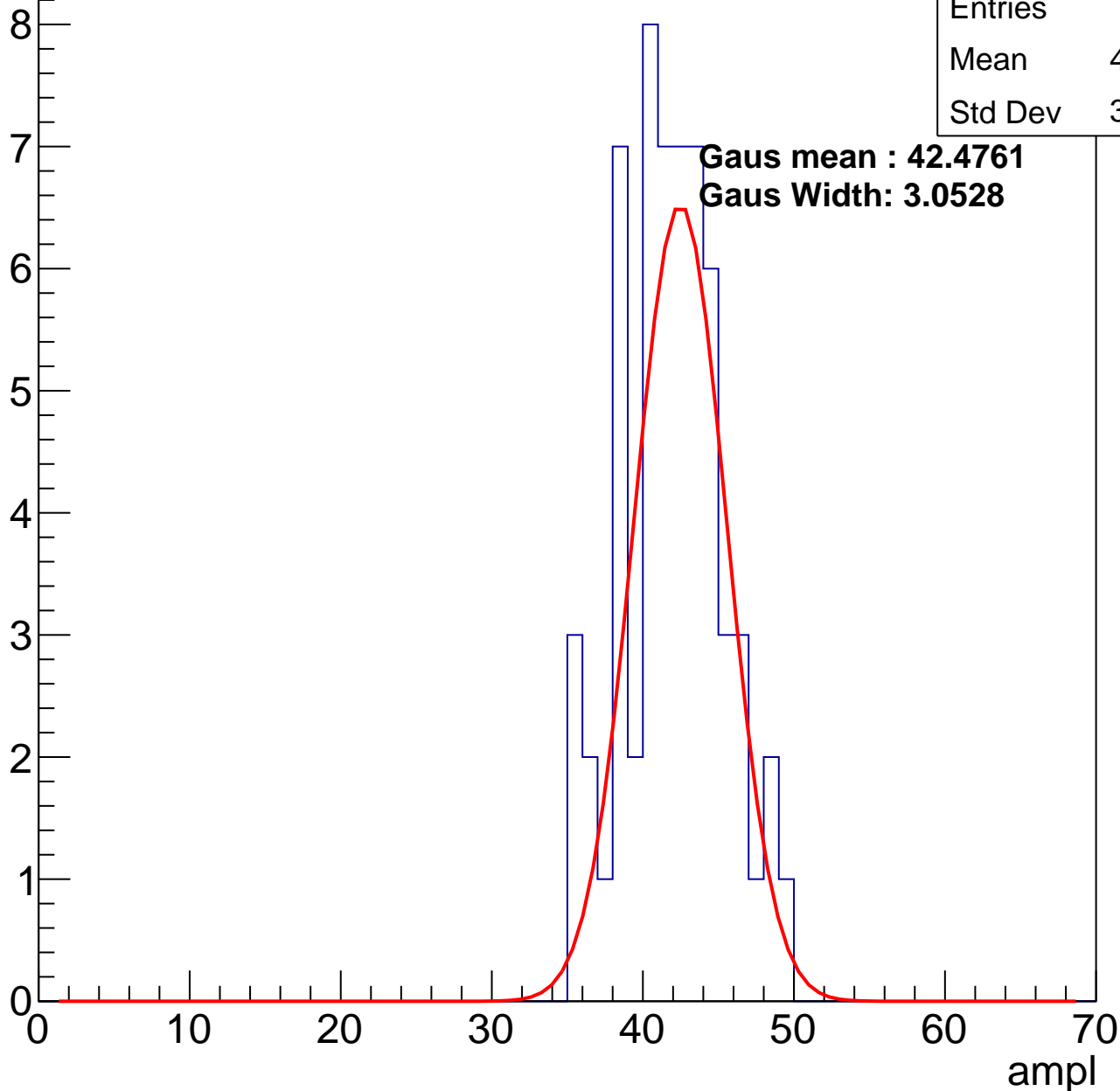
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.48
Std Dev	3.289

Gaus mean : 42.4761

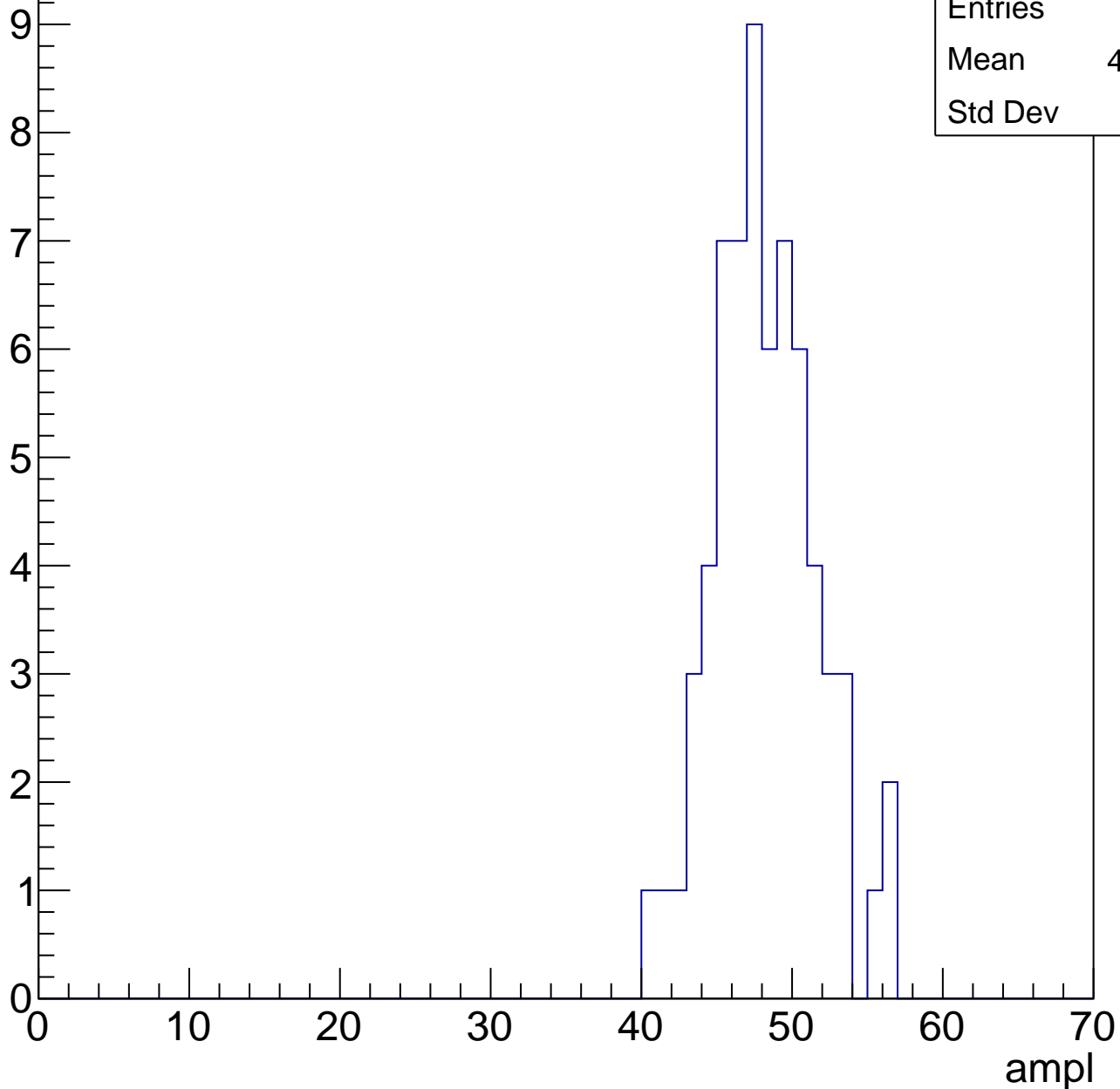
Gaus Width: 3.0528



B1L101S, U6-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



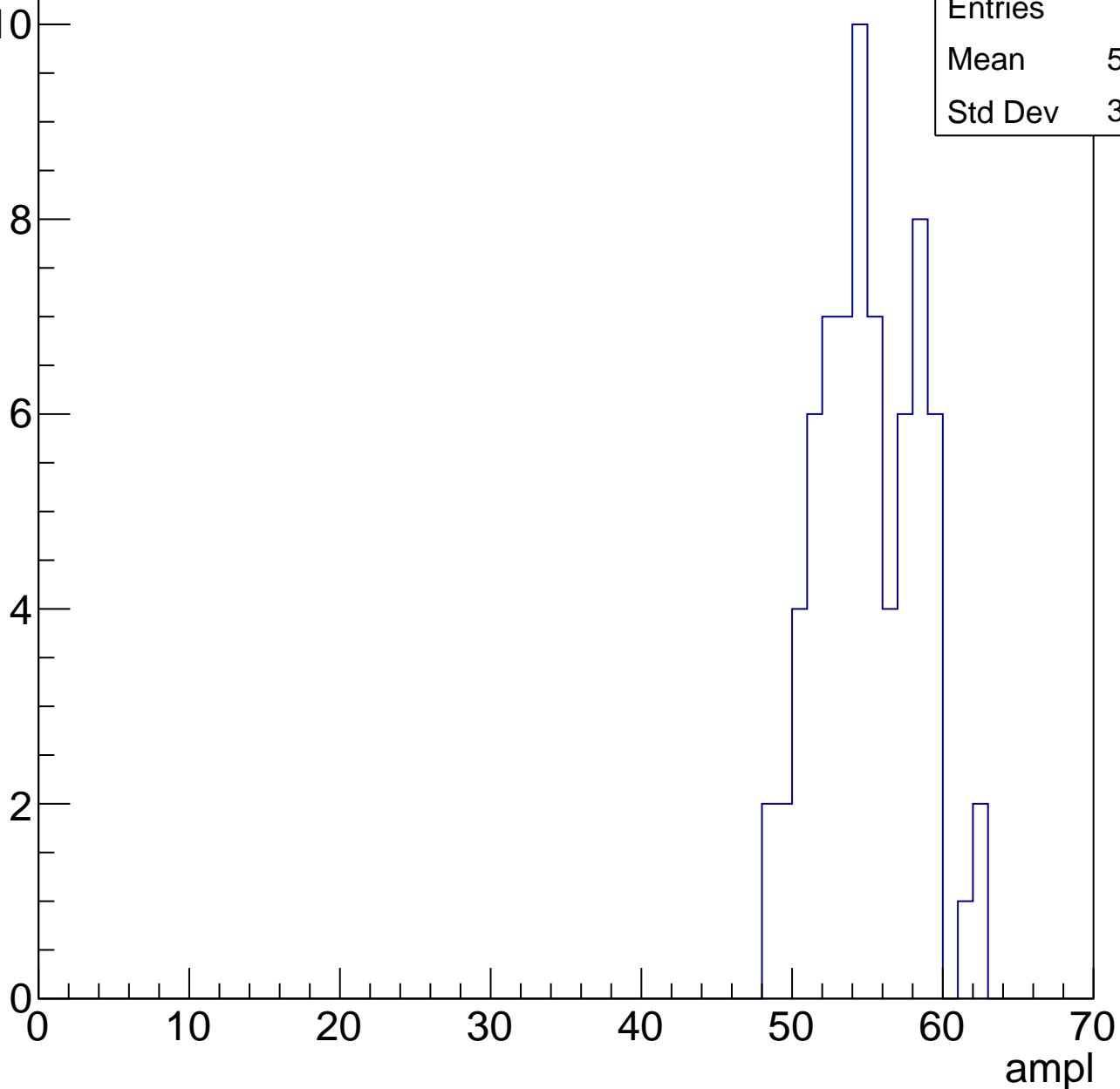
Entries	65
Mean	47.77
Std Dev	3.4

B1L101S, U6-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

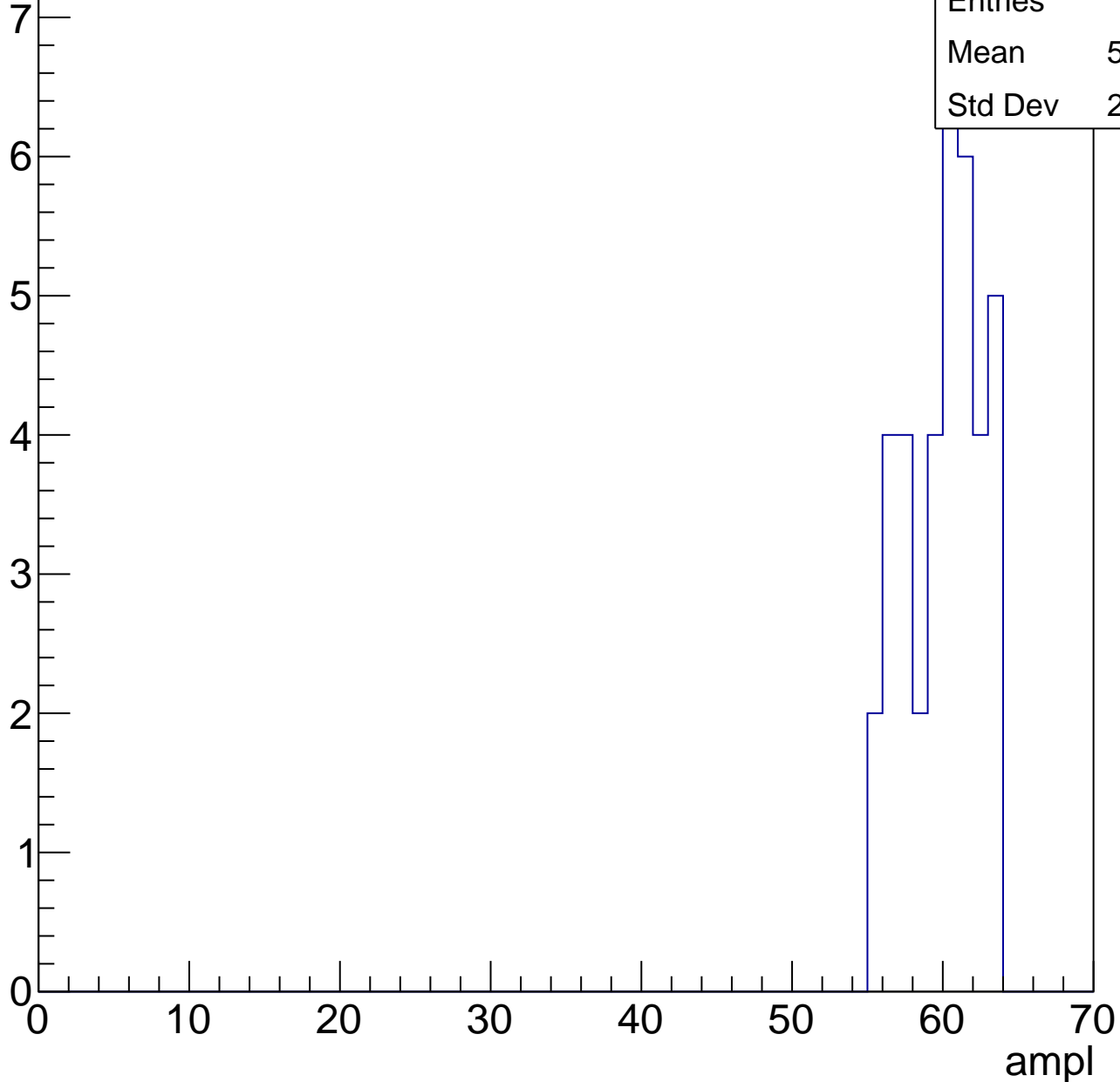
Entries	72
Mean	54.57
Std Dev	3.299



B1L101S, U6-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

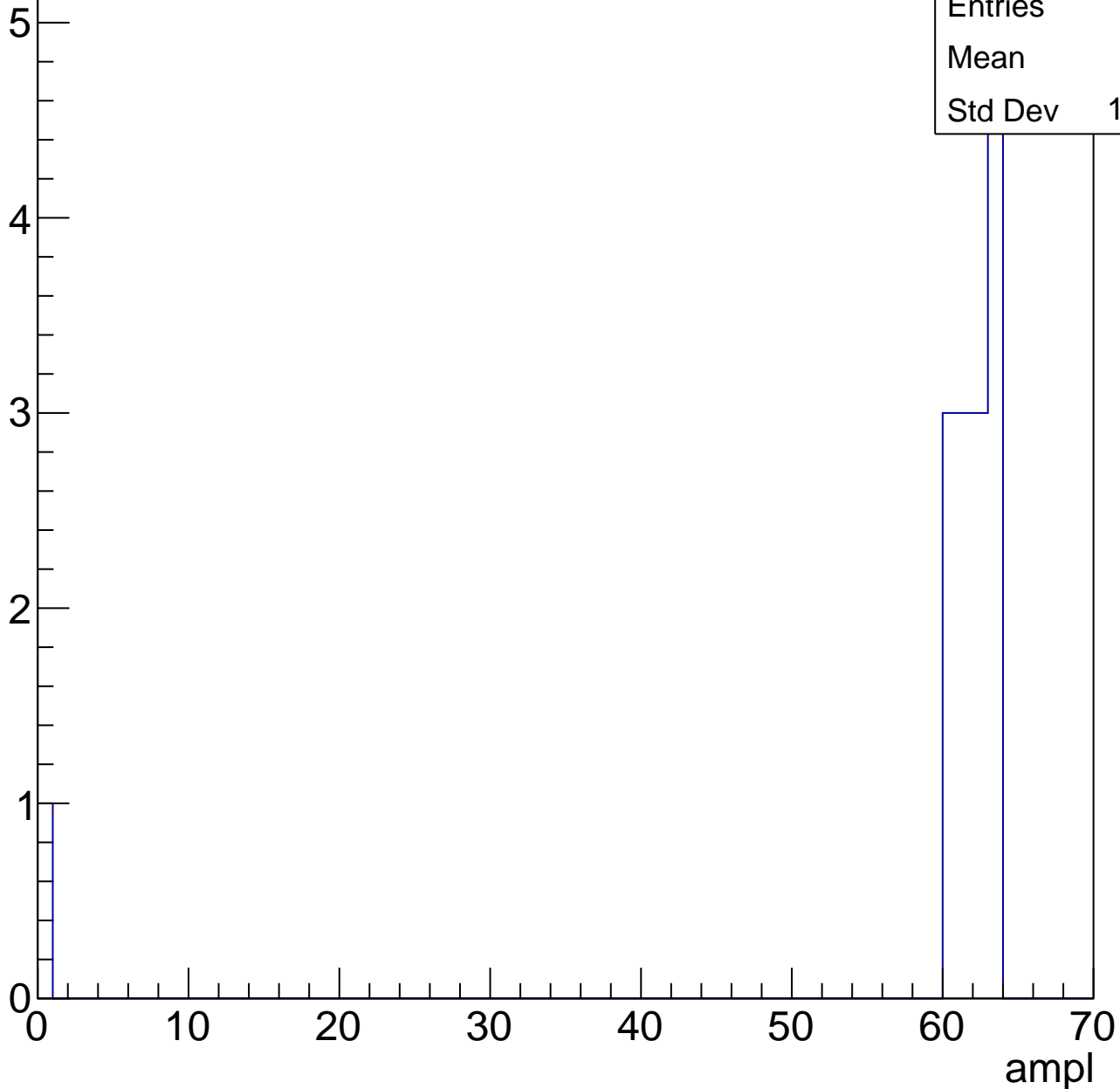


B1L101S, U6-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.6
Std Dev	15.44



B1L101S, U6-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch75, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	30.25
Std Dev	3.121

Gaus mean : 30.5279

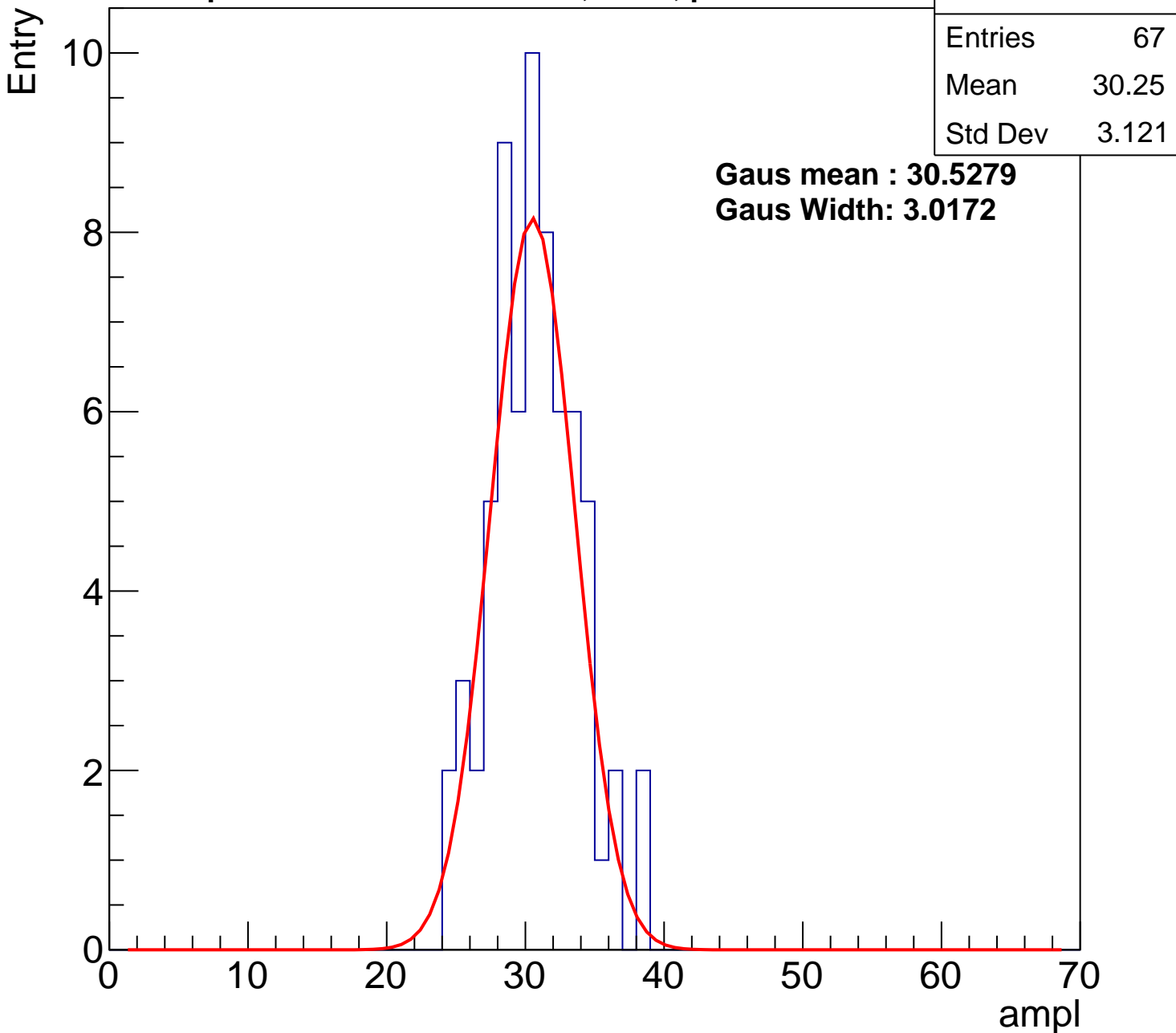
Gaus Width: 3.0172

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch75, adc1

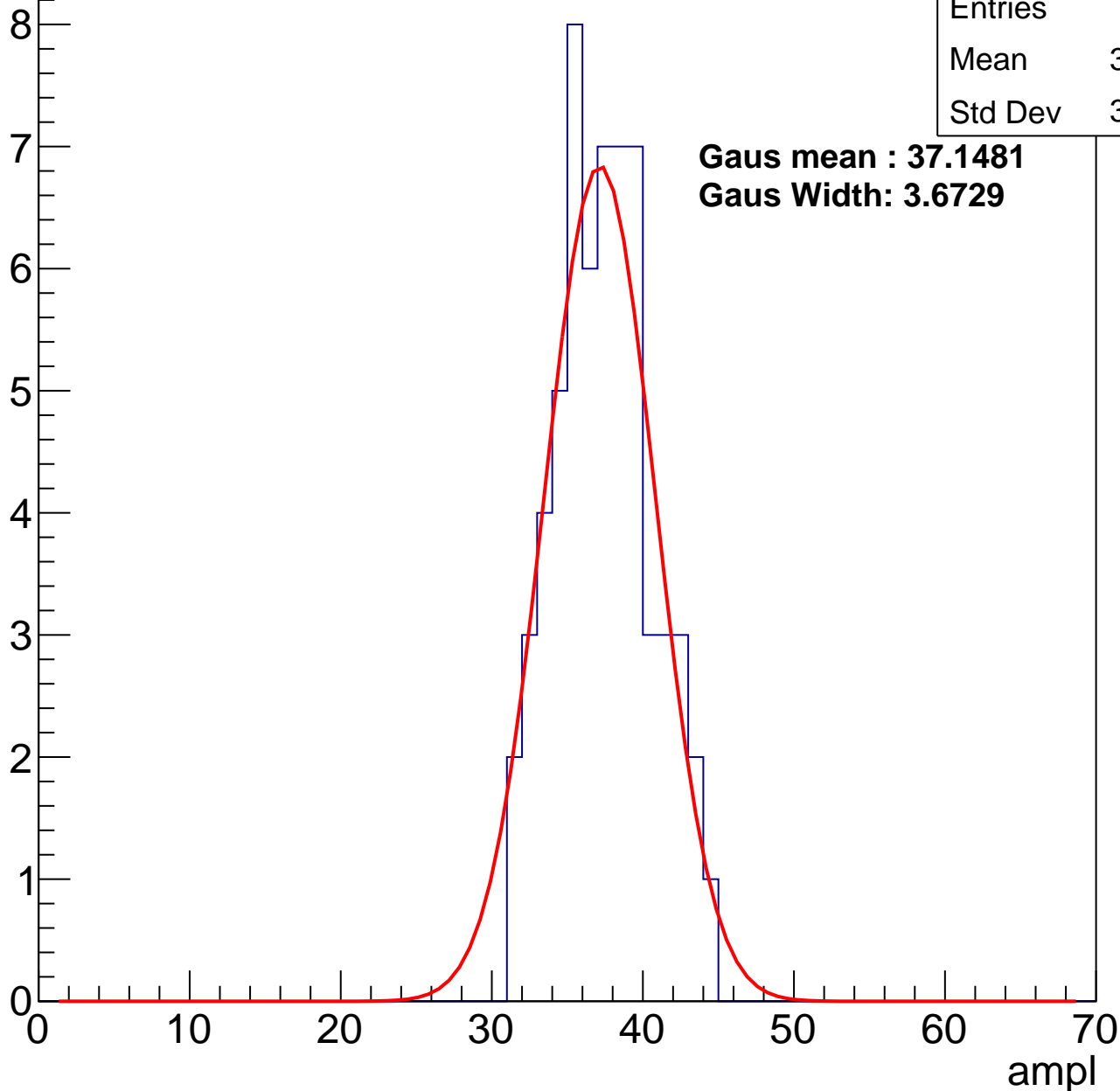
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.93
Std Dev	3.136

Gaus mean : 37.1481

Gaus Width: 3.6729



B1L101S, U6-ch75, adc2

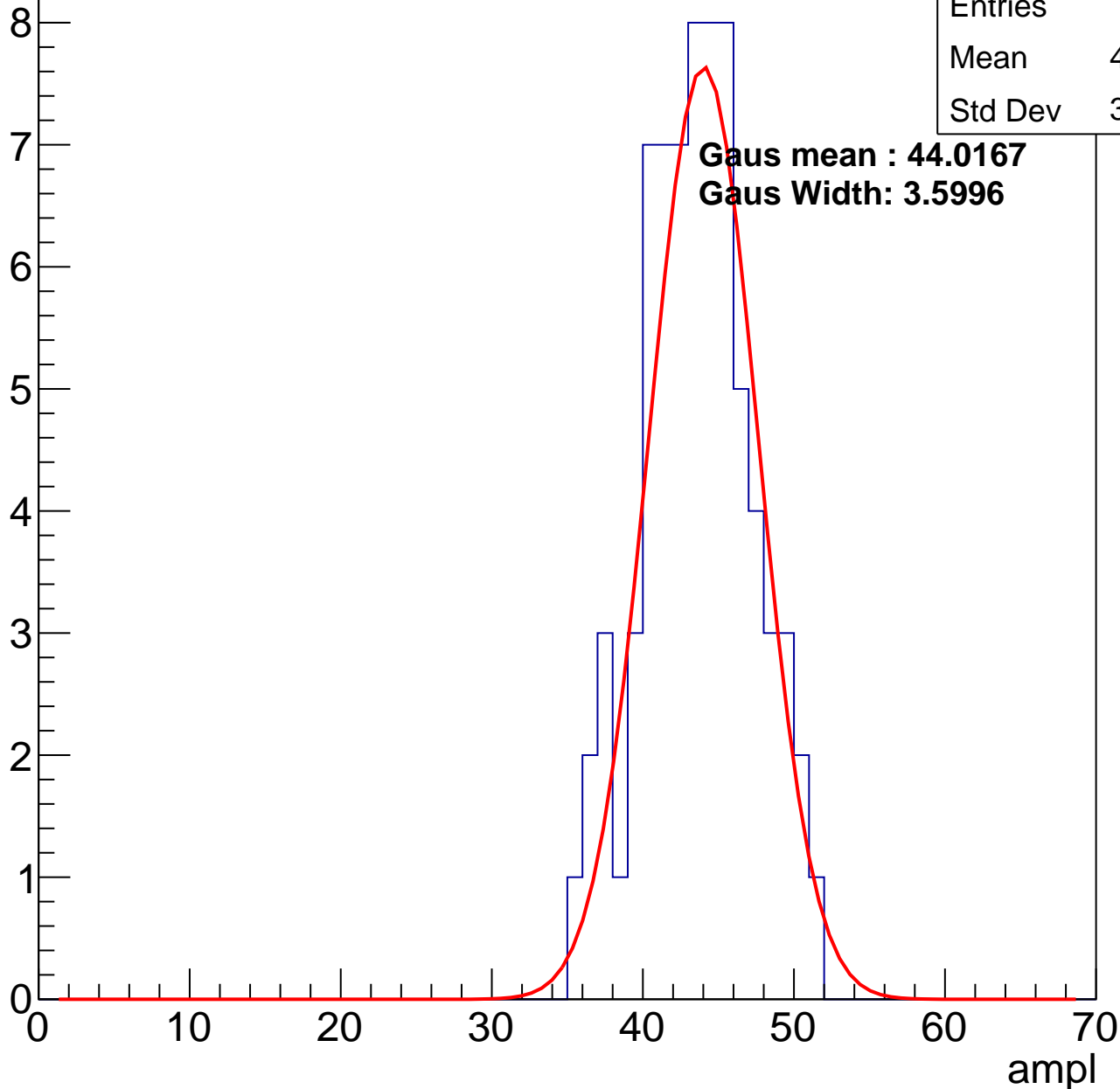
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.15
Std Dev	3.576

Gaus mean : 44.0167

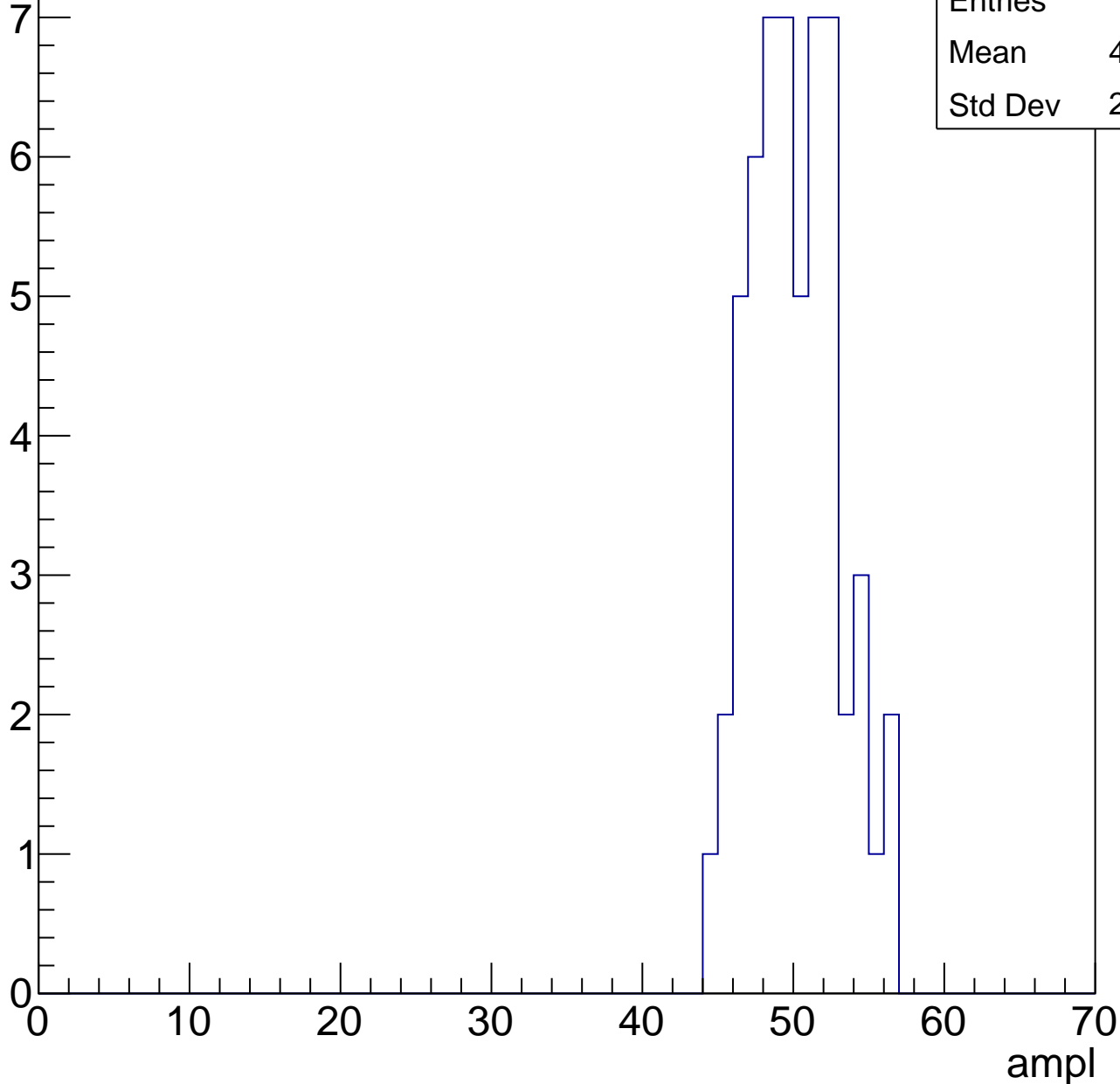
Gaus Width: 3.5996



B1L101S, U6-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



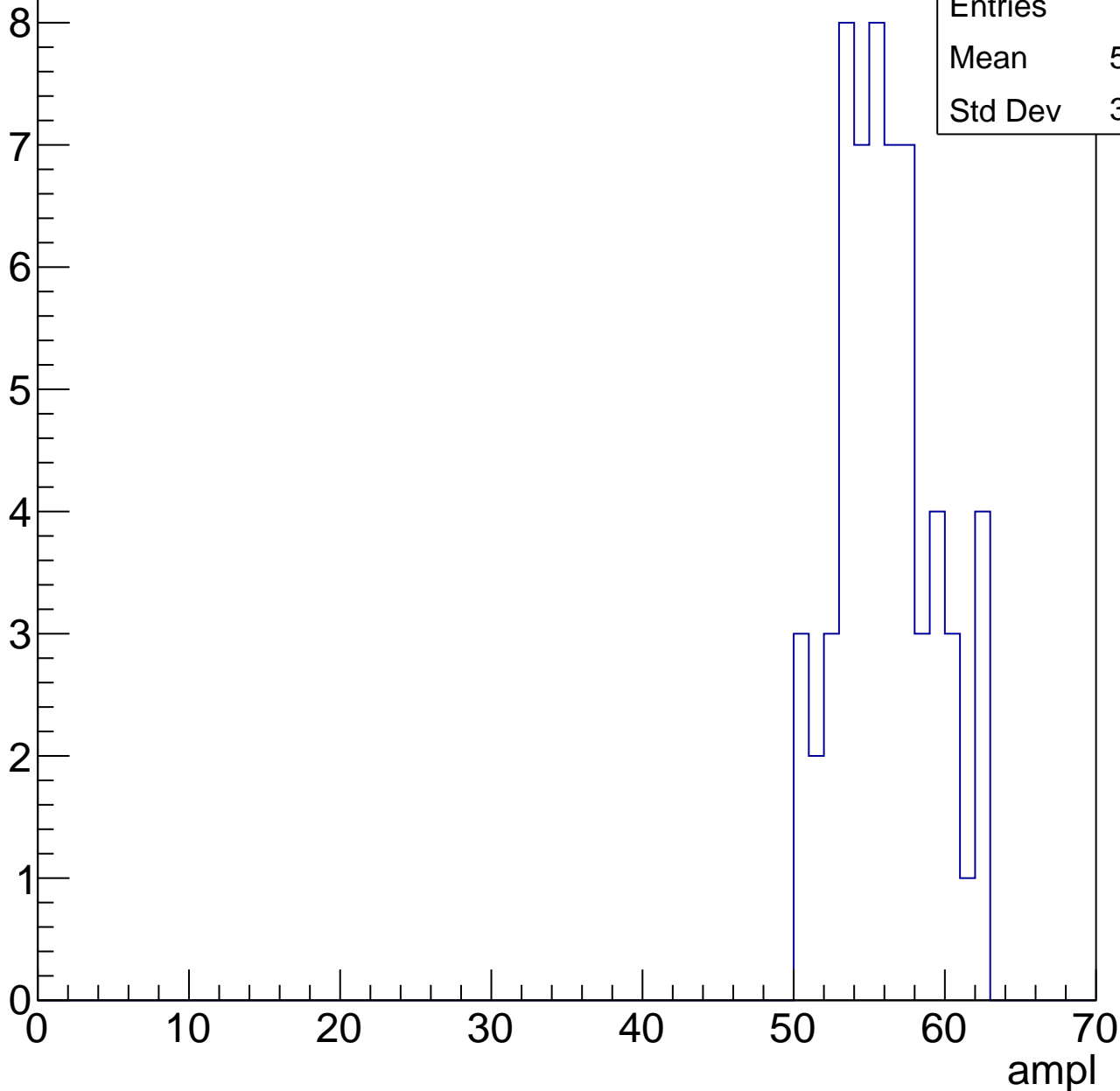
Entries	55
Mean	49.65
Std Dev	2.849

B1L101S, U6-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.67
Std Dev	3.107



B1L101S, U6-ch75, adc5

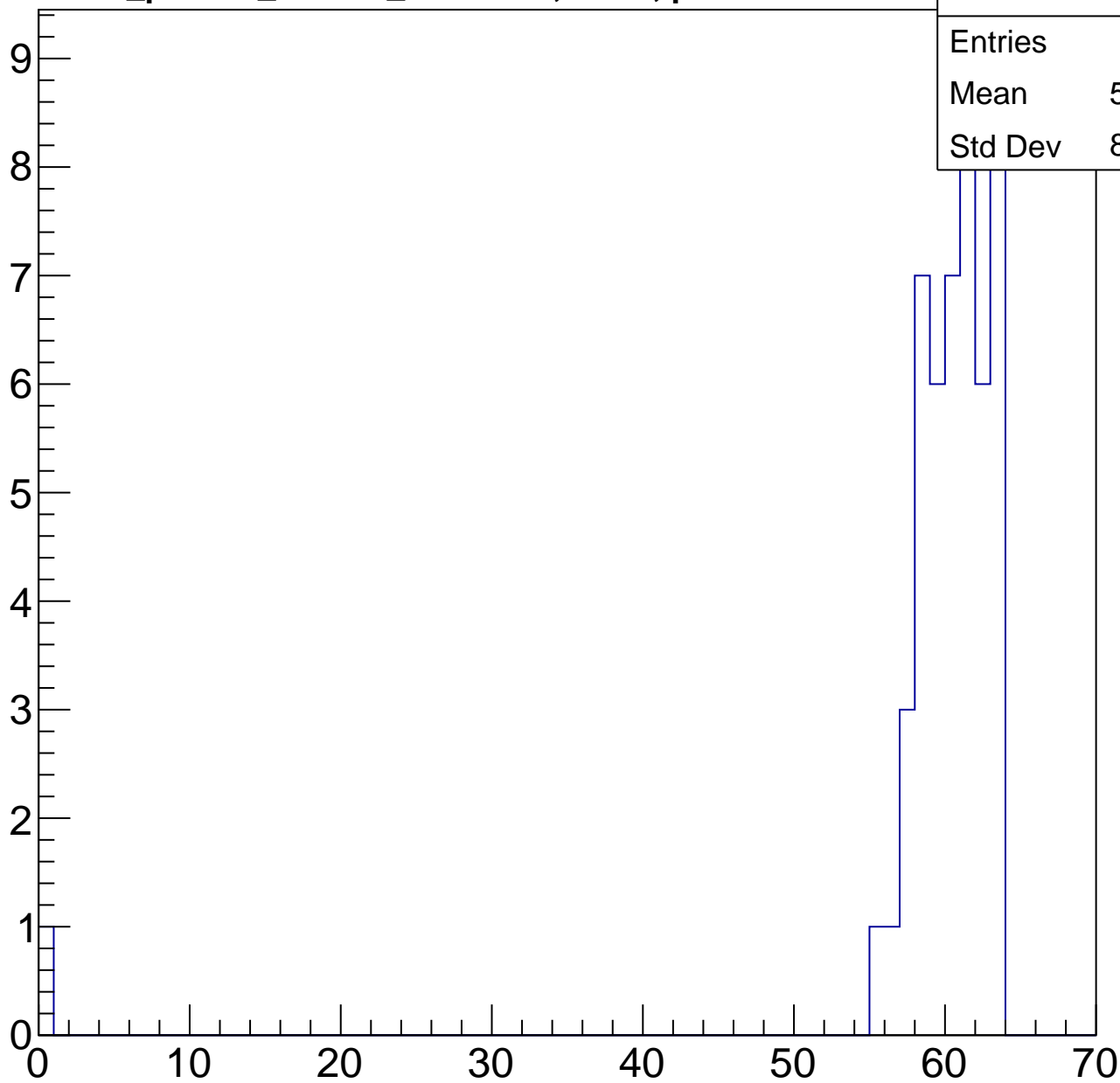
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.92
Std Dev	8.748

ampl



B1L101S, U6-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch76, adc0

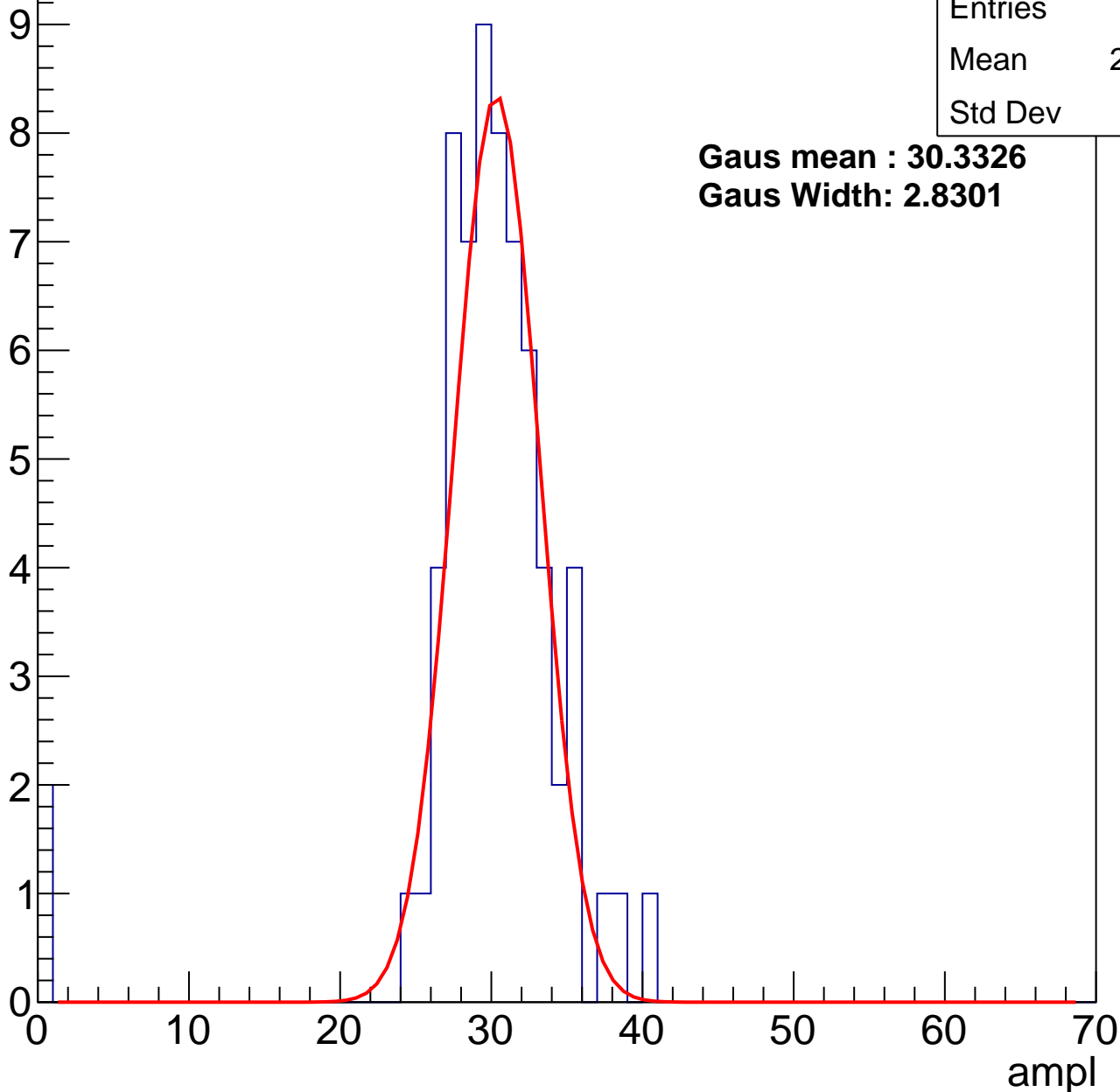
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.24
Std Dev	6.04

Gaus mean : 30.3326

Gaus Width: 2.8301



B1L101S, U6-ch76, adc1

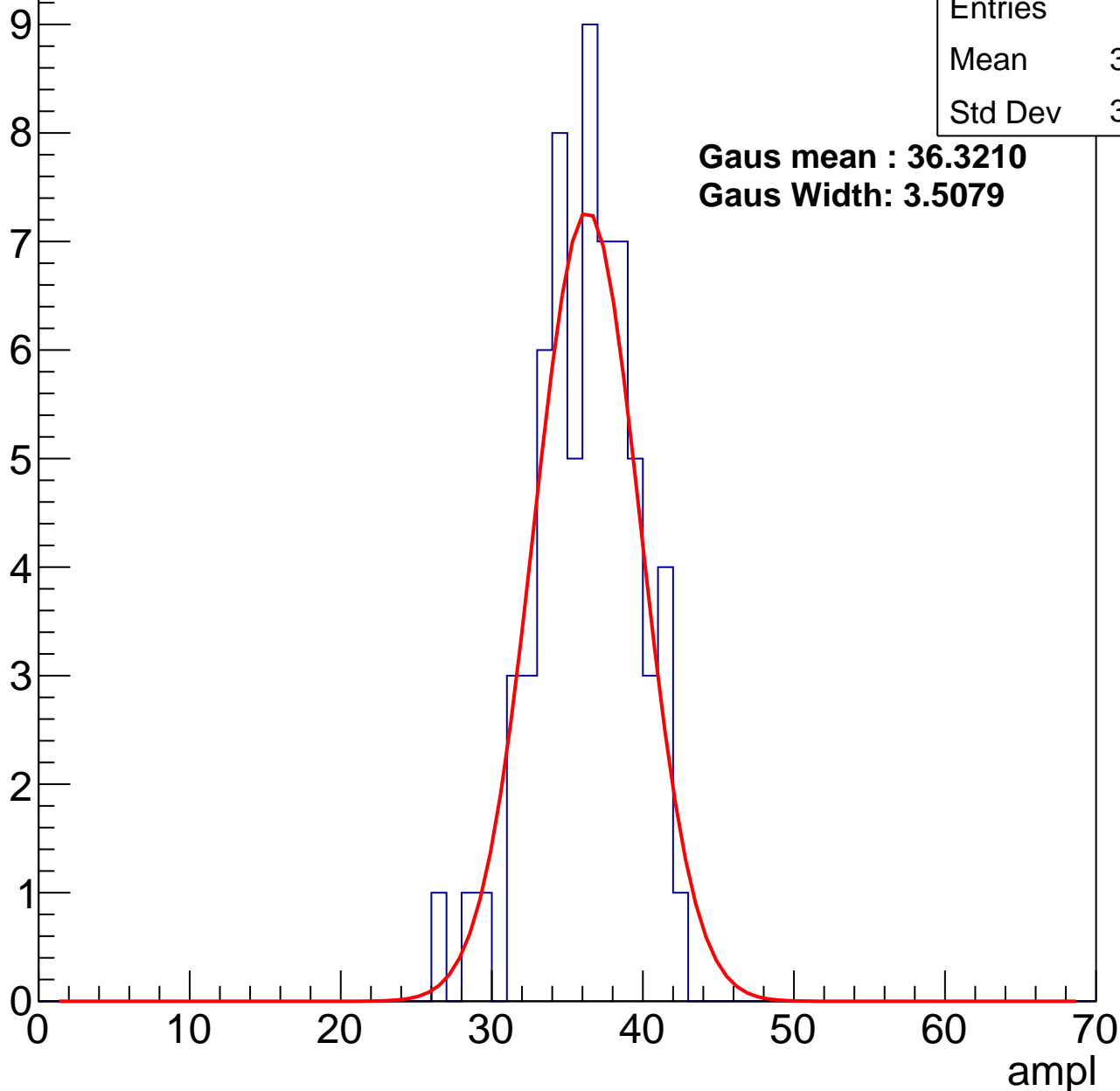
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.73
Std Dev	3.275

Gaus mean : 36.3210

Gaus Width: 3.5079



B1L101S, U6-ch76, adc2

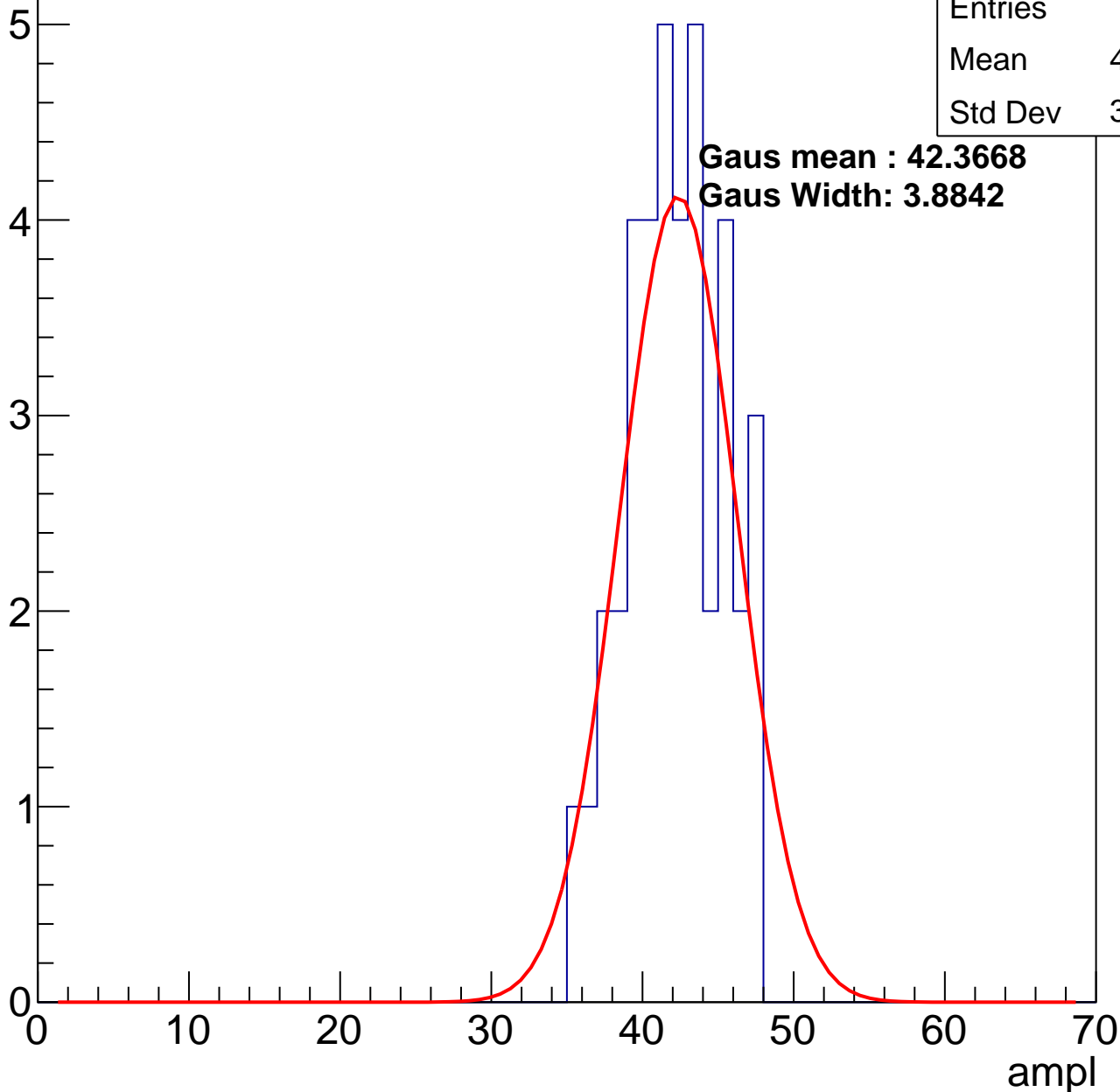
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	41.69
Std Dev	3.106

Gaus mean : 42.3668

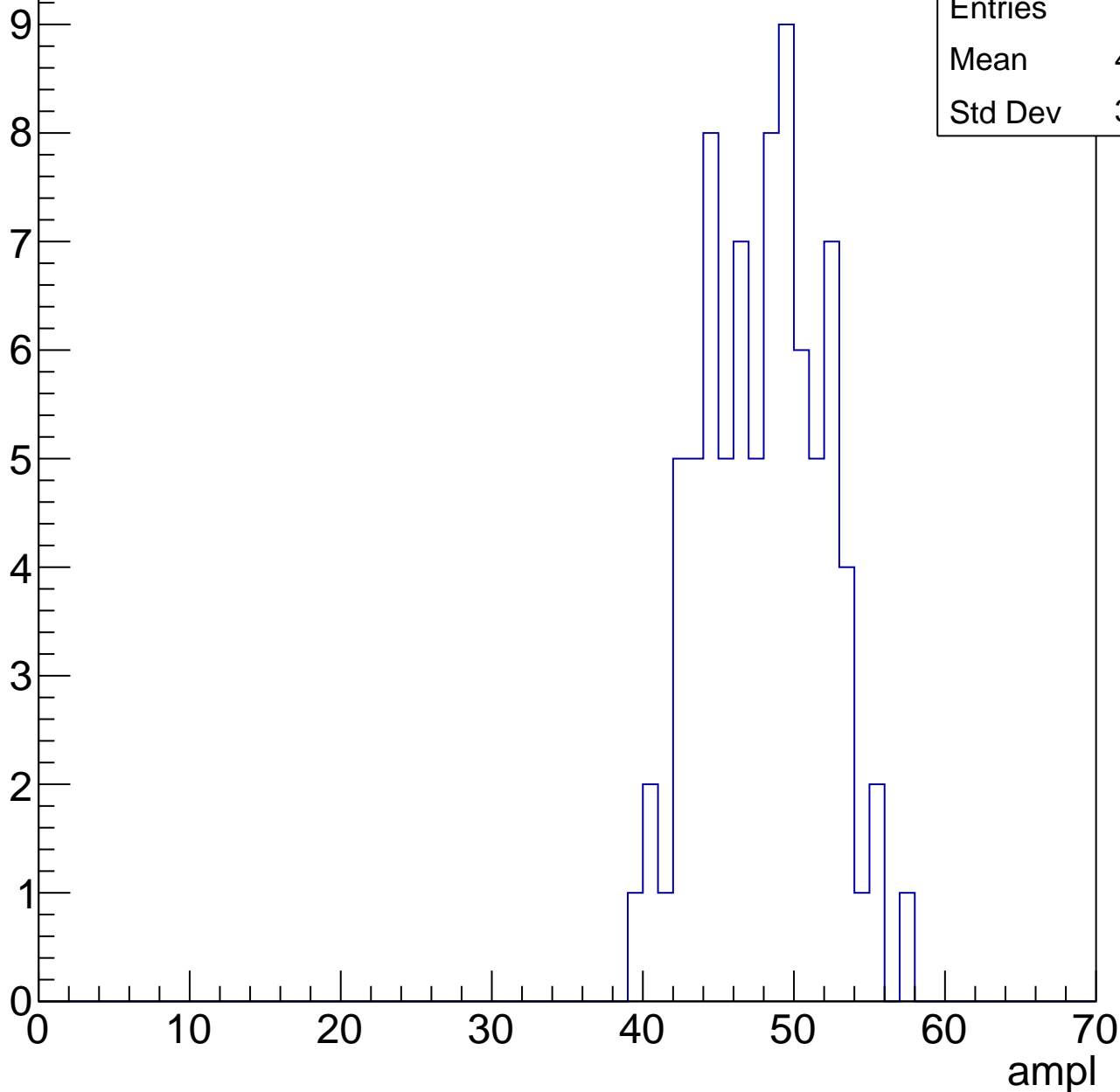
Gaus Width: 3.8842



B1L101S, U6-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



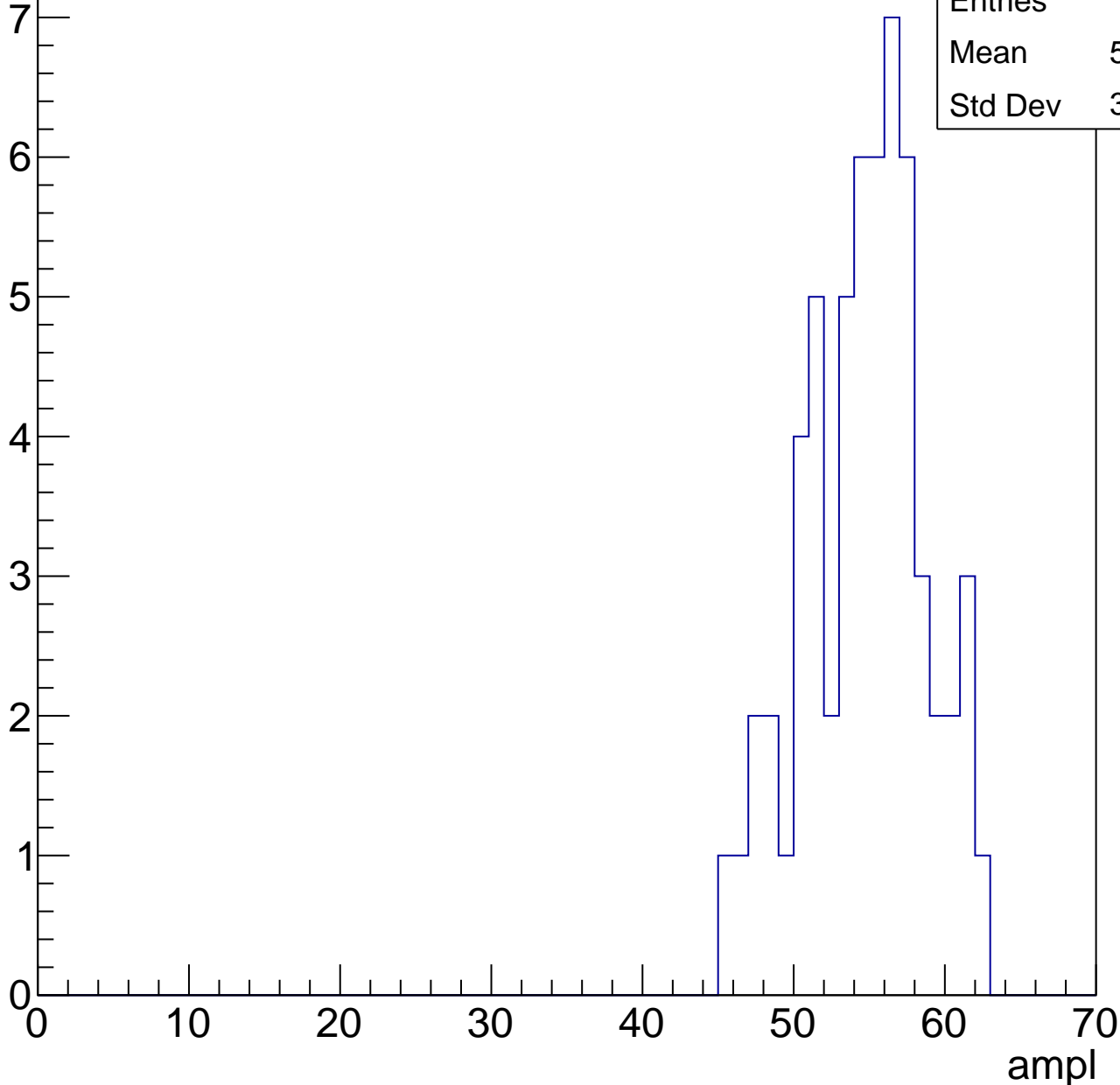
Entries	82
Mean	47.51
Std Dev	3.911

B1L101S, U6-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.22
Std Dev	3.966



B1L101S, U6-ch76, adc5

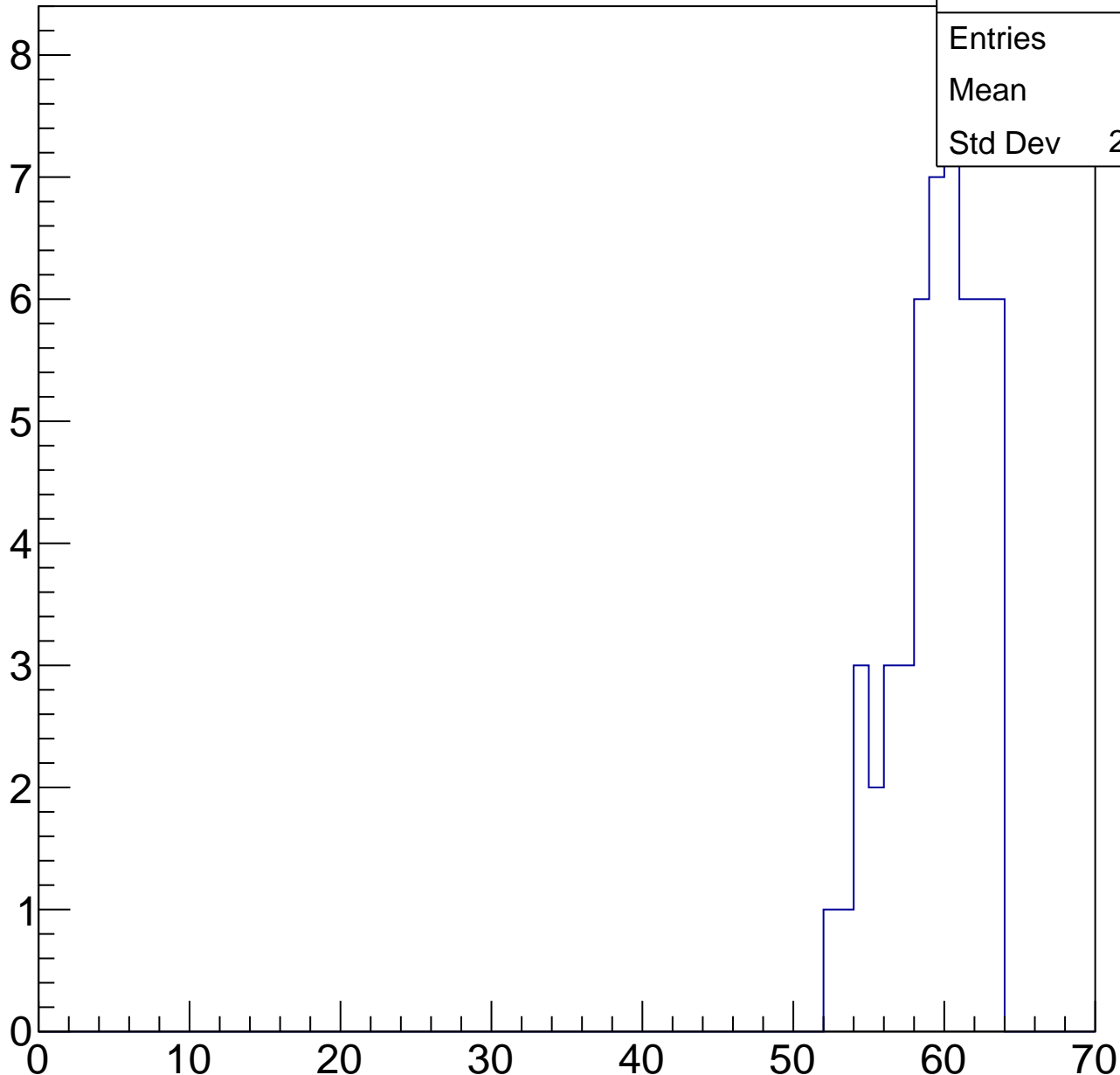
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.1
Std Dev	2.837

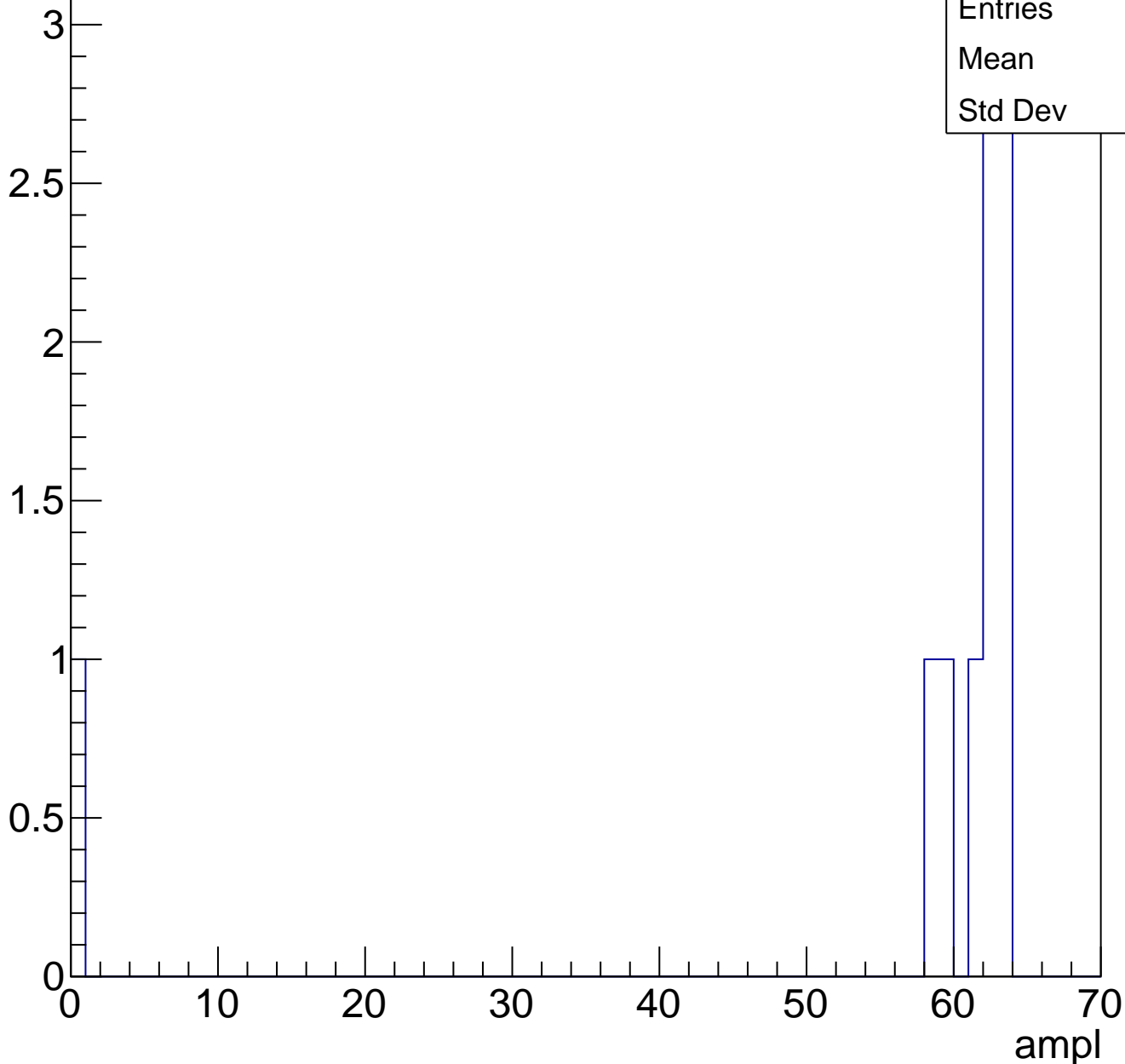
ampl



B1L101S, U6-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U6-ch77, adc0

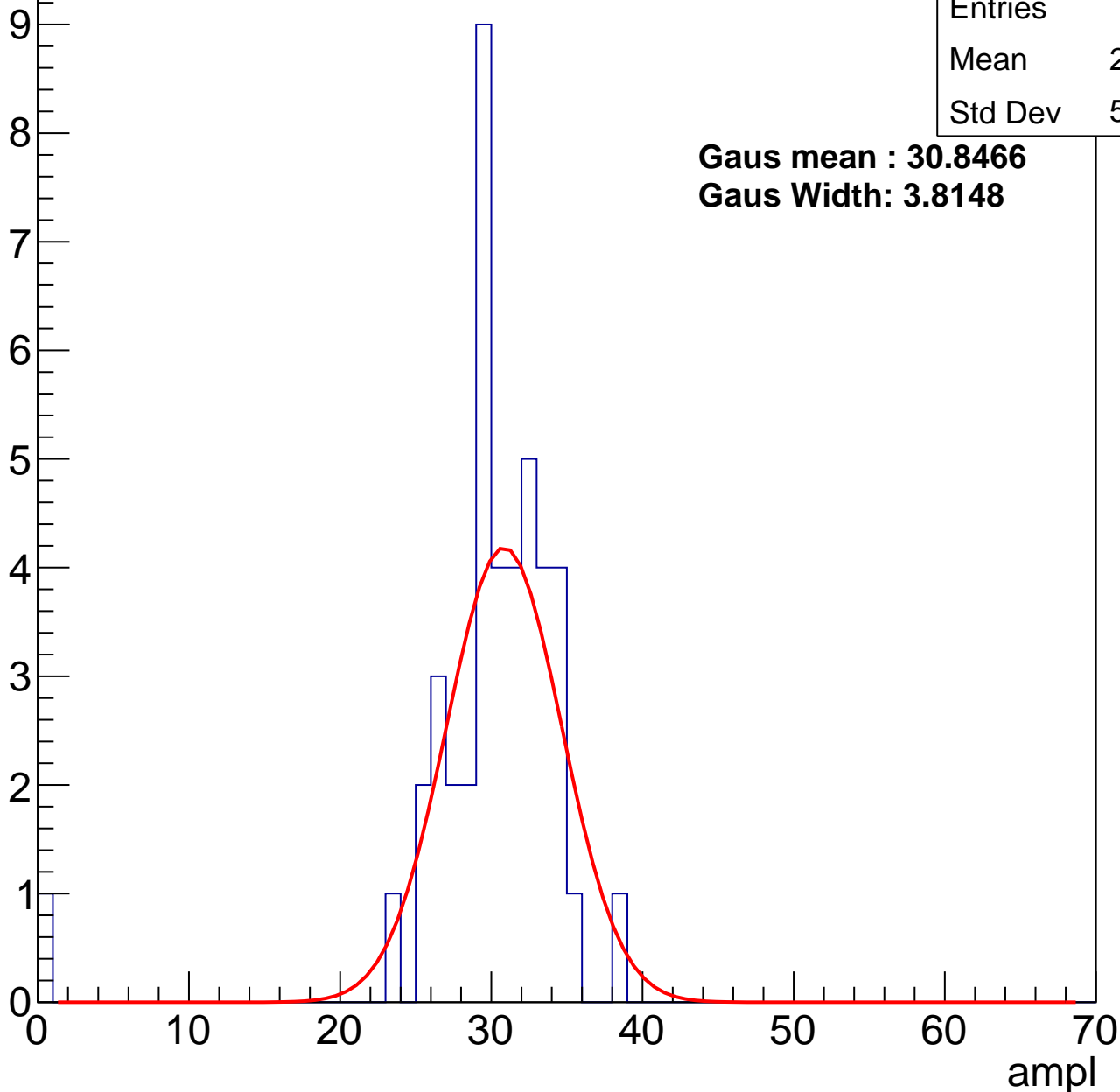
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	29.47
Std Dev	5.462

Gaus mean : 30.8466

Gaus Width: 3.8148



B1L101S, U6-ch77, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	36.29
Std Dev	3.436

Gaus mean : 36.8256

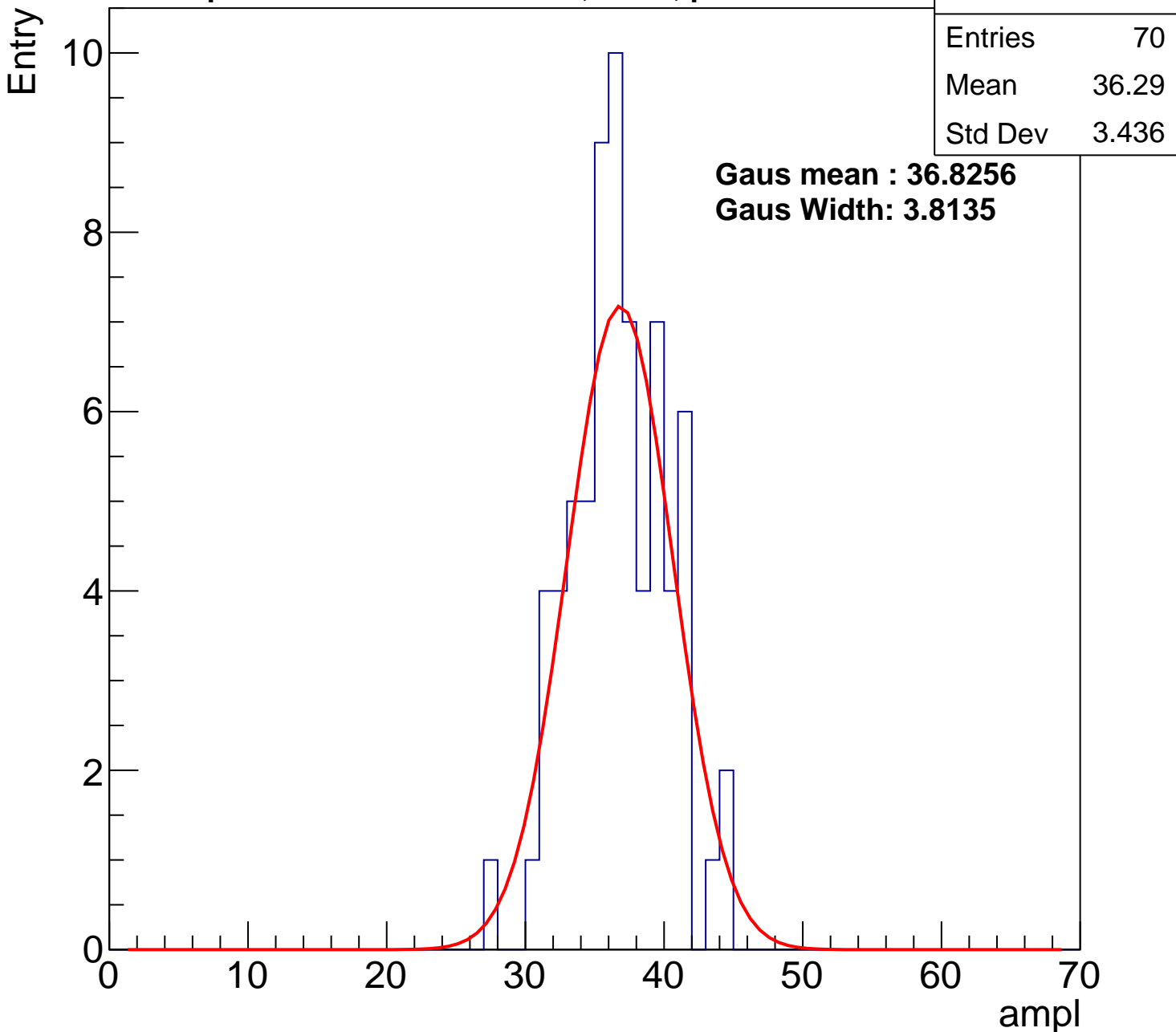
Gaus Width: 3.8135

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch77, adc2

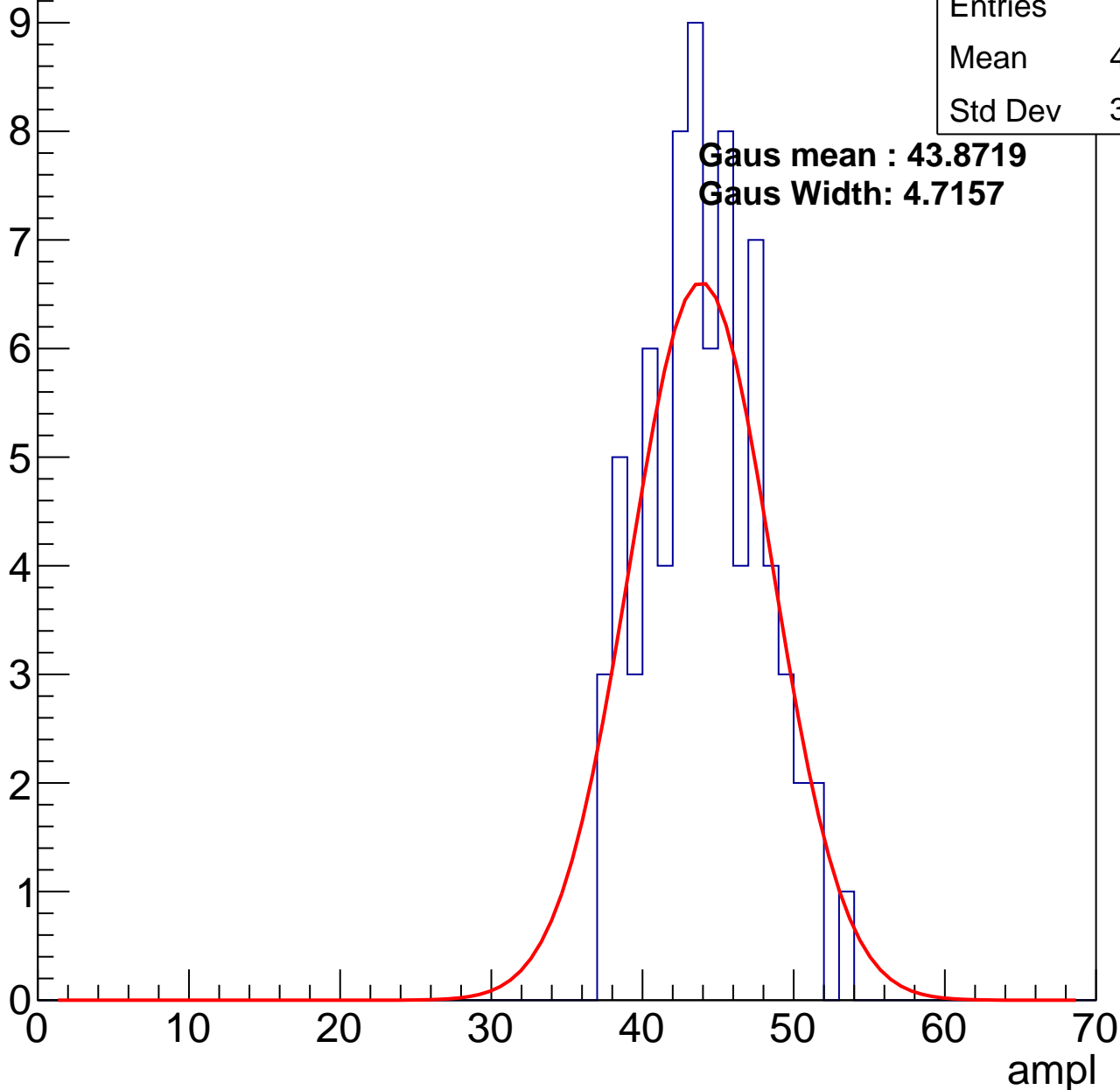
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.68
Std Dev	3.732

Gaus mean : 43.8719

Gaus Width: 4.7157

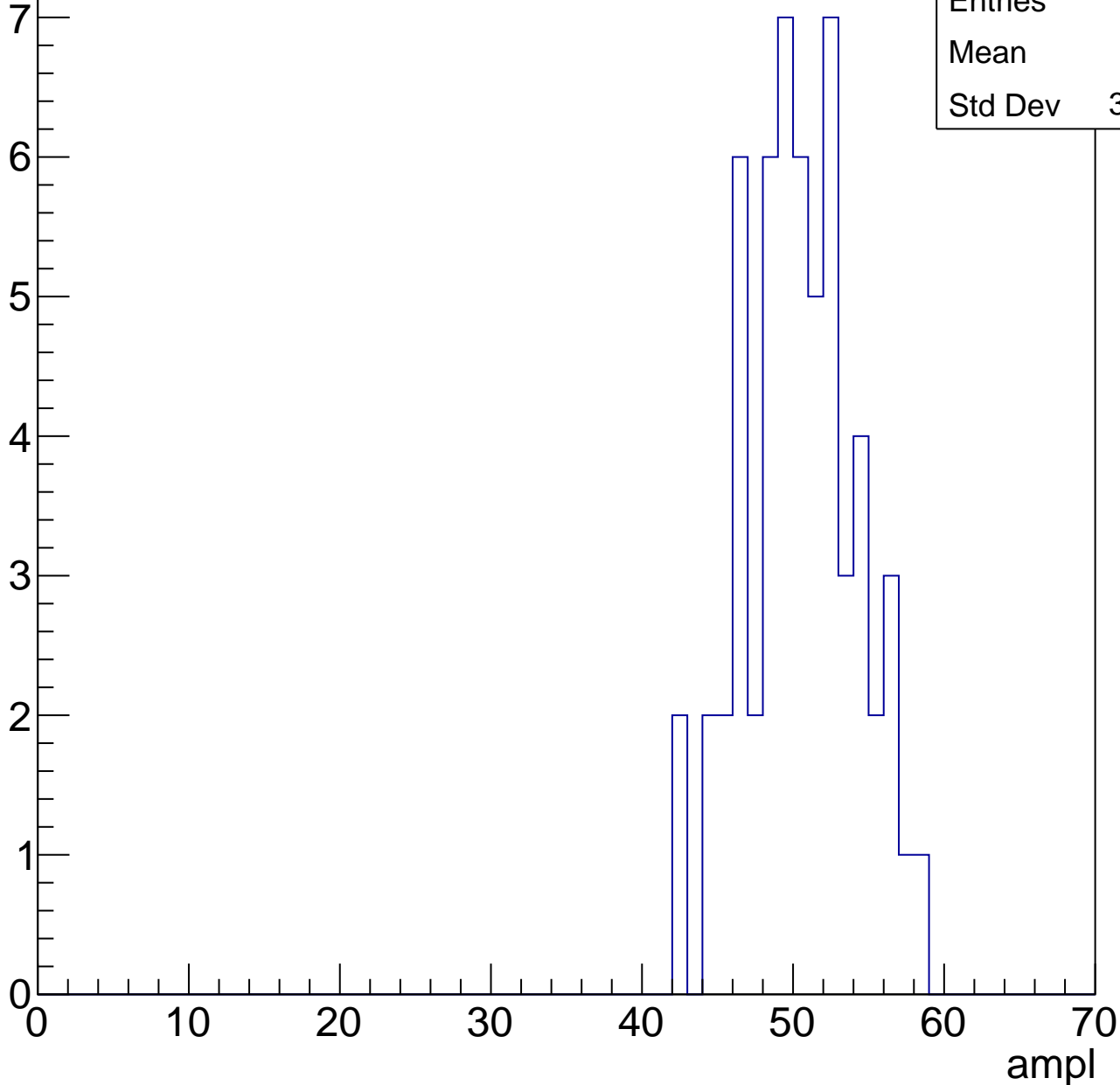


B1L101S, U6-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50
Std Dev	3.659

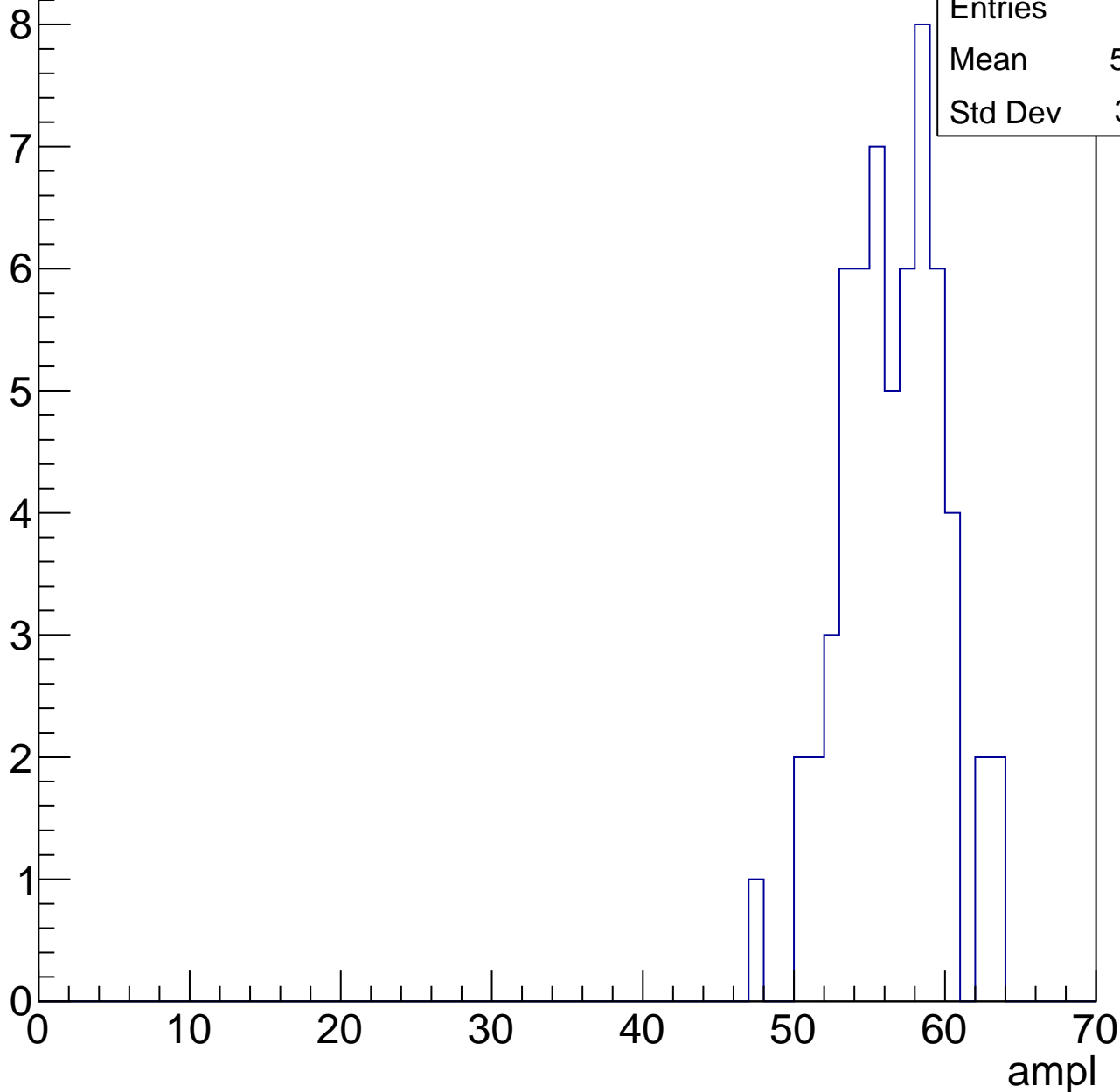


B1L101S, U6-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56.03
Std Dev	3.321

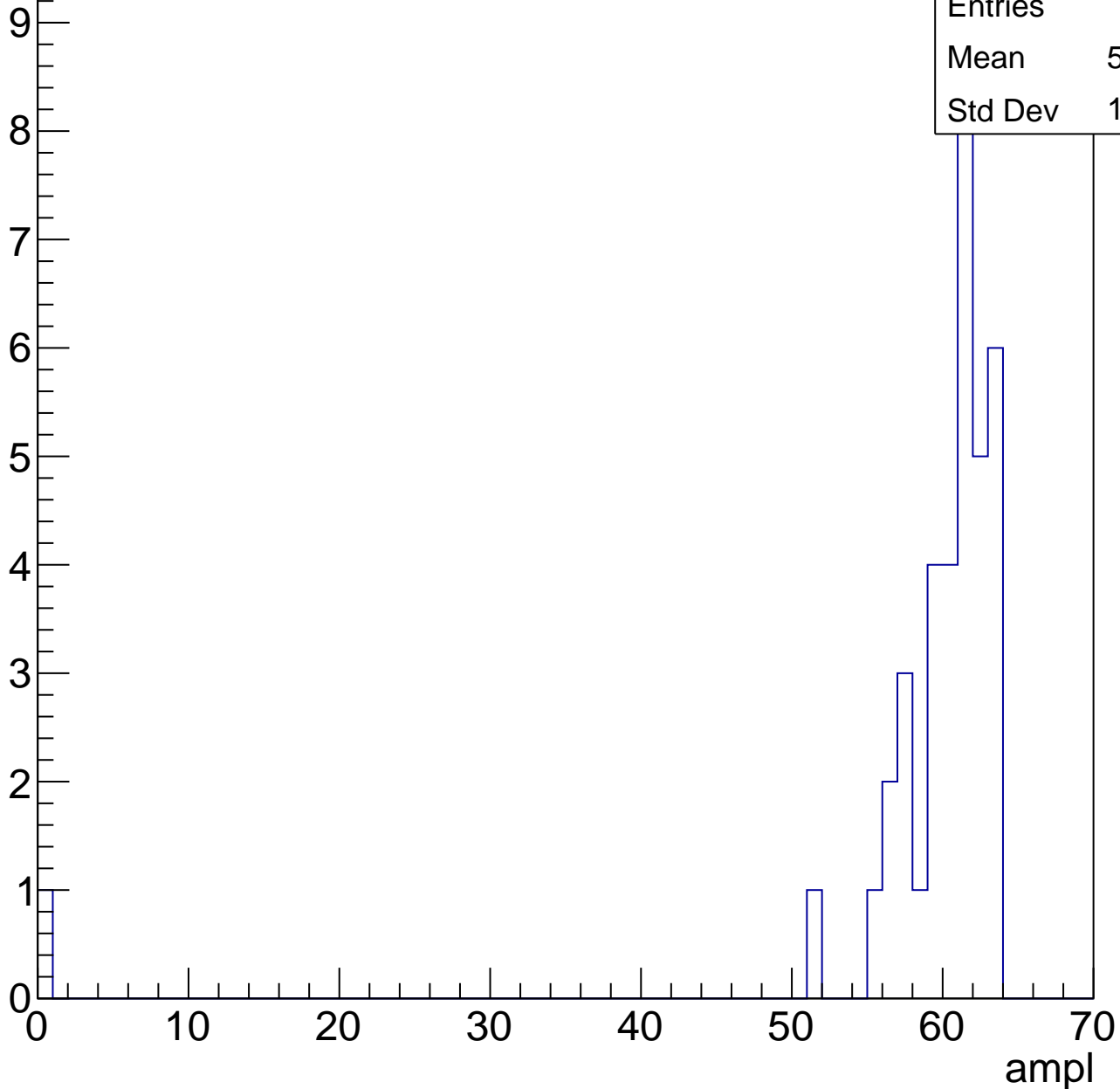


B1L101S, U6-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.38
Std Dev	10.08



B1L101S, U6-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.57
Std Dev	1.178

ampl

B1L101S, U6-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch78, adc0

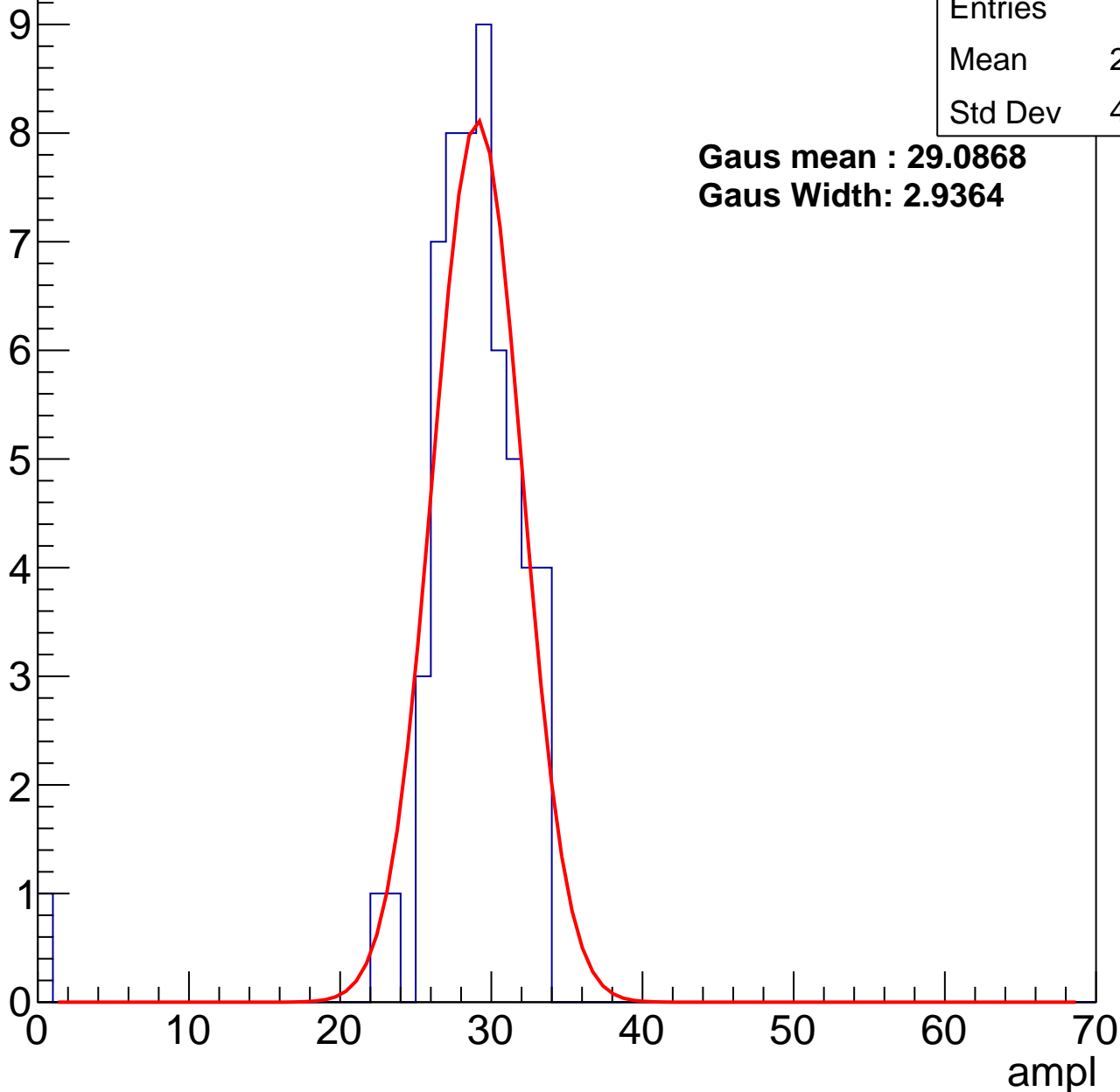
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.04
Std Dev	4.492

Gaus mean : 29.0868

Gaus Width: 2.9364



B1L101S, U6-ch78, adc1

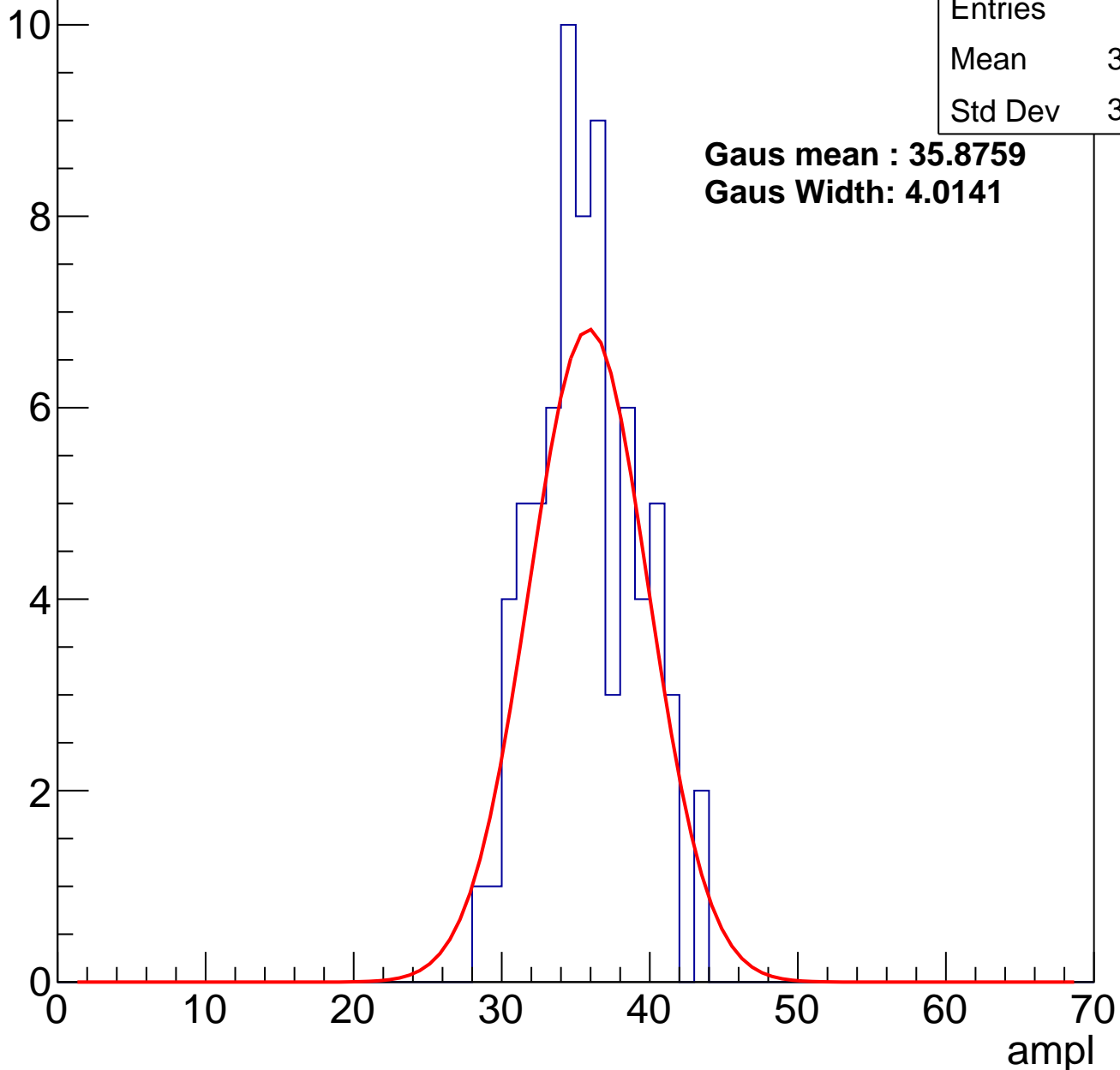
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	35.25
Std Dev	3.415

Gaus mean : 35.8759

Gaus Width: 4.0141

Entry



B1L101S, U6-ch78, adc2

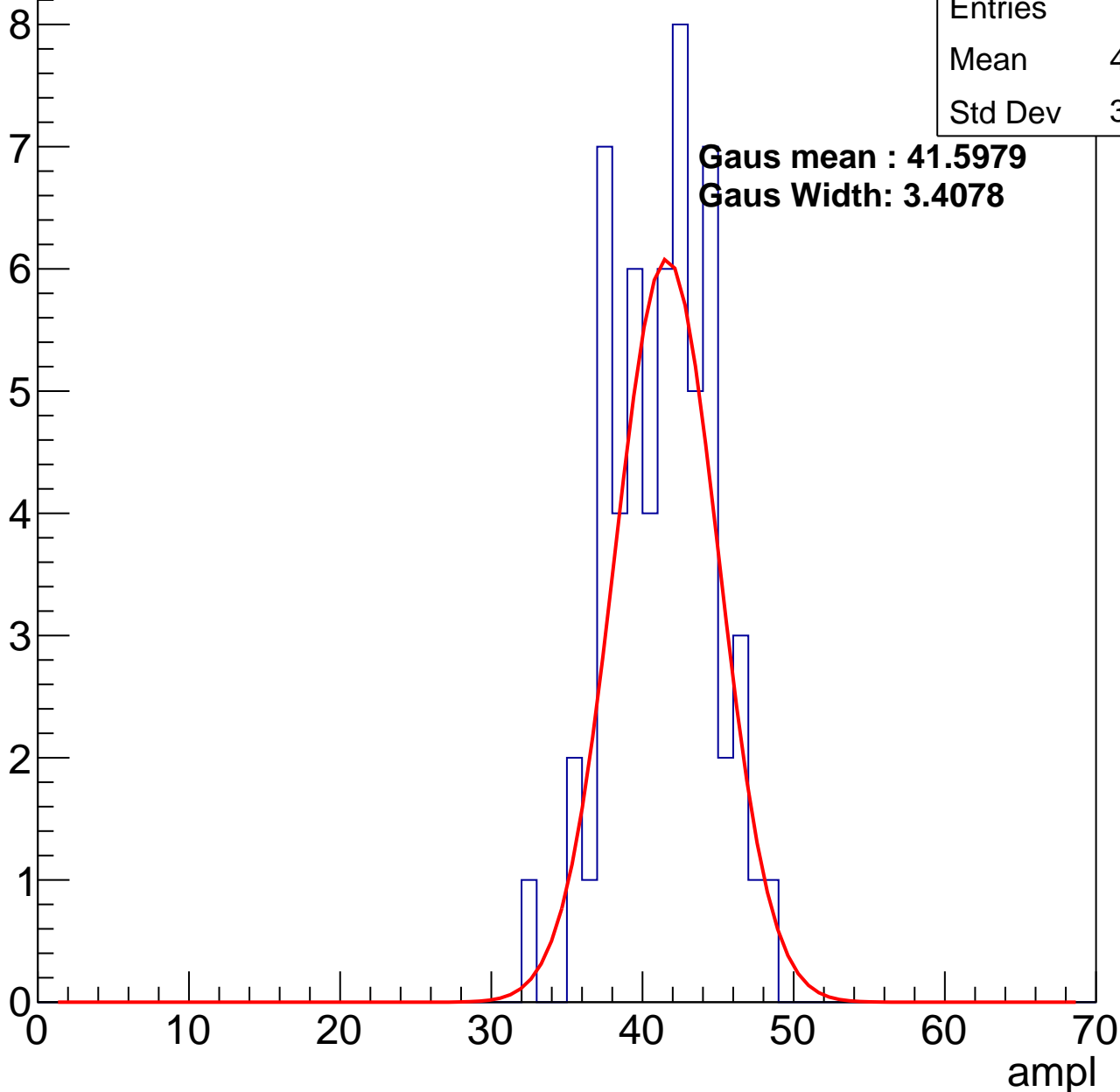
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	40.88
Std Dev	3.312

Gaus mean : 41.5979

Gaus Width: 3.4078



B1L101S, U6-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	48.05
Std Dev	3.392

Entry

10

8

6

4

2

0

0

10

20

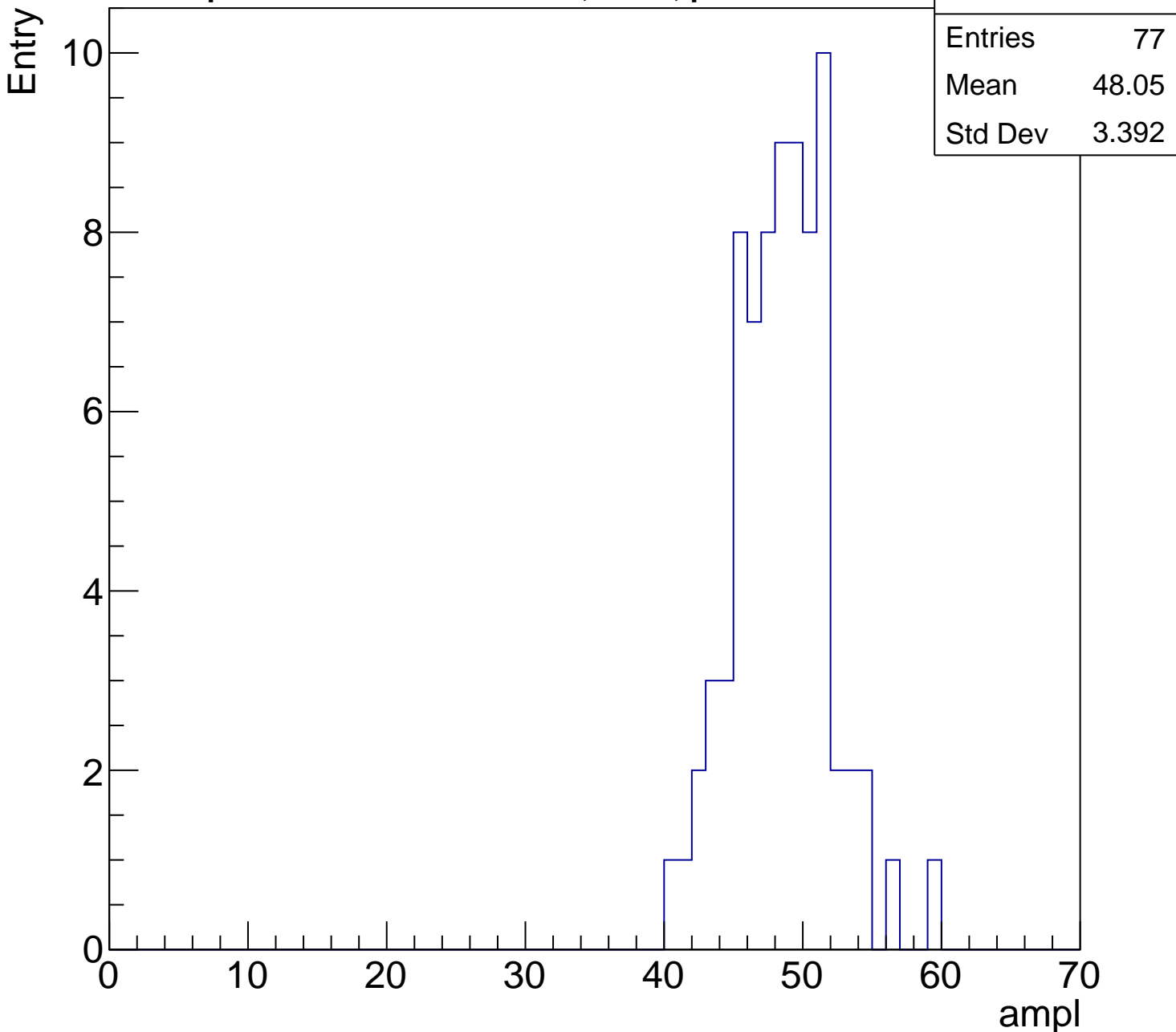
30

40

50

60

ampl

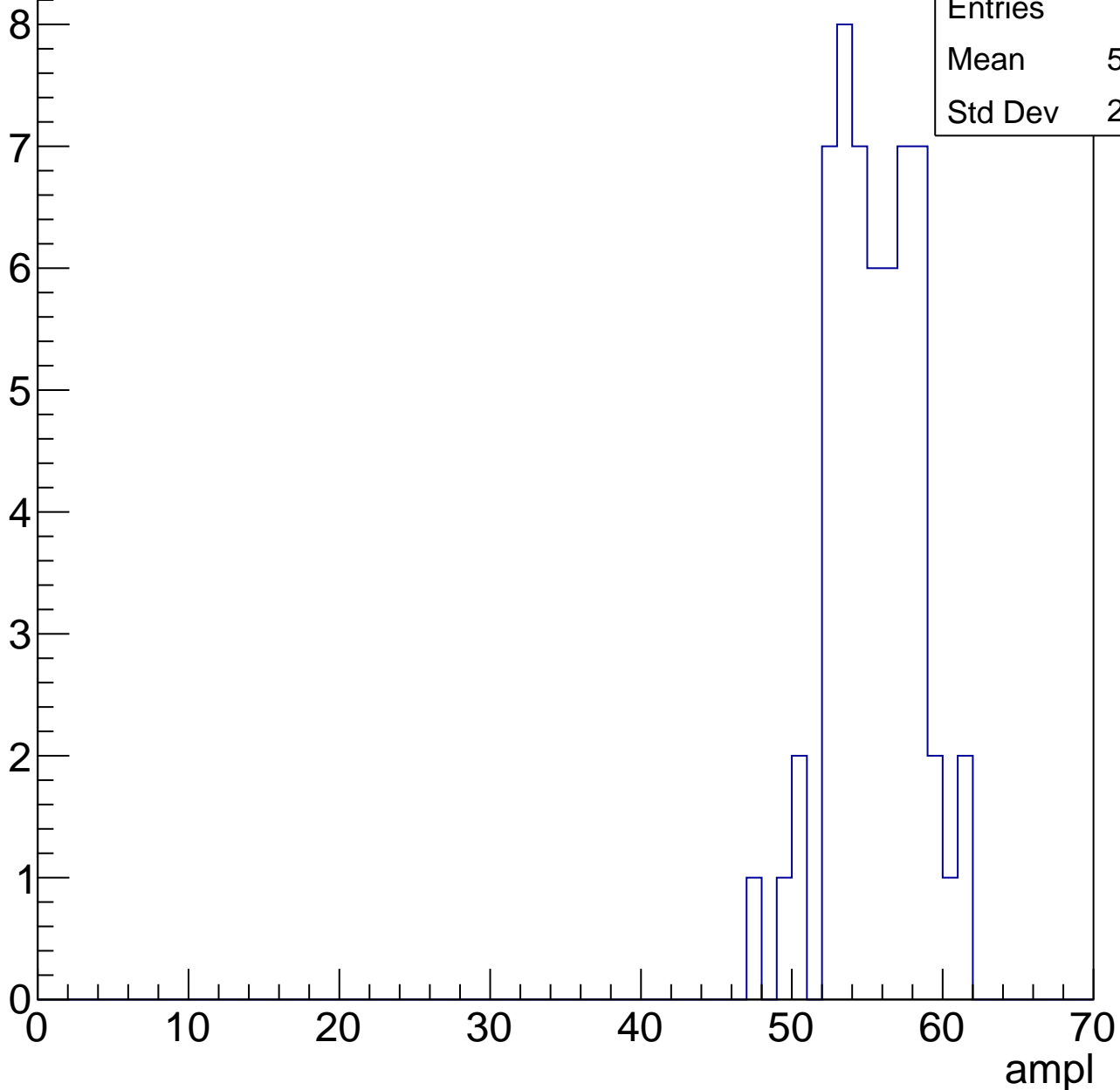


B1L101S, U6-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

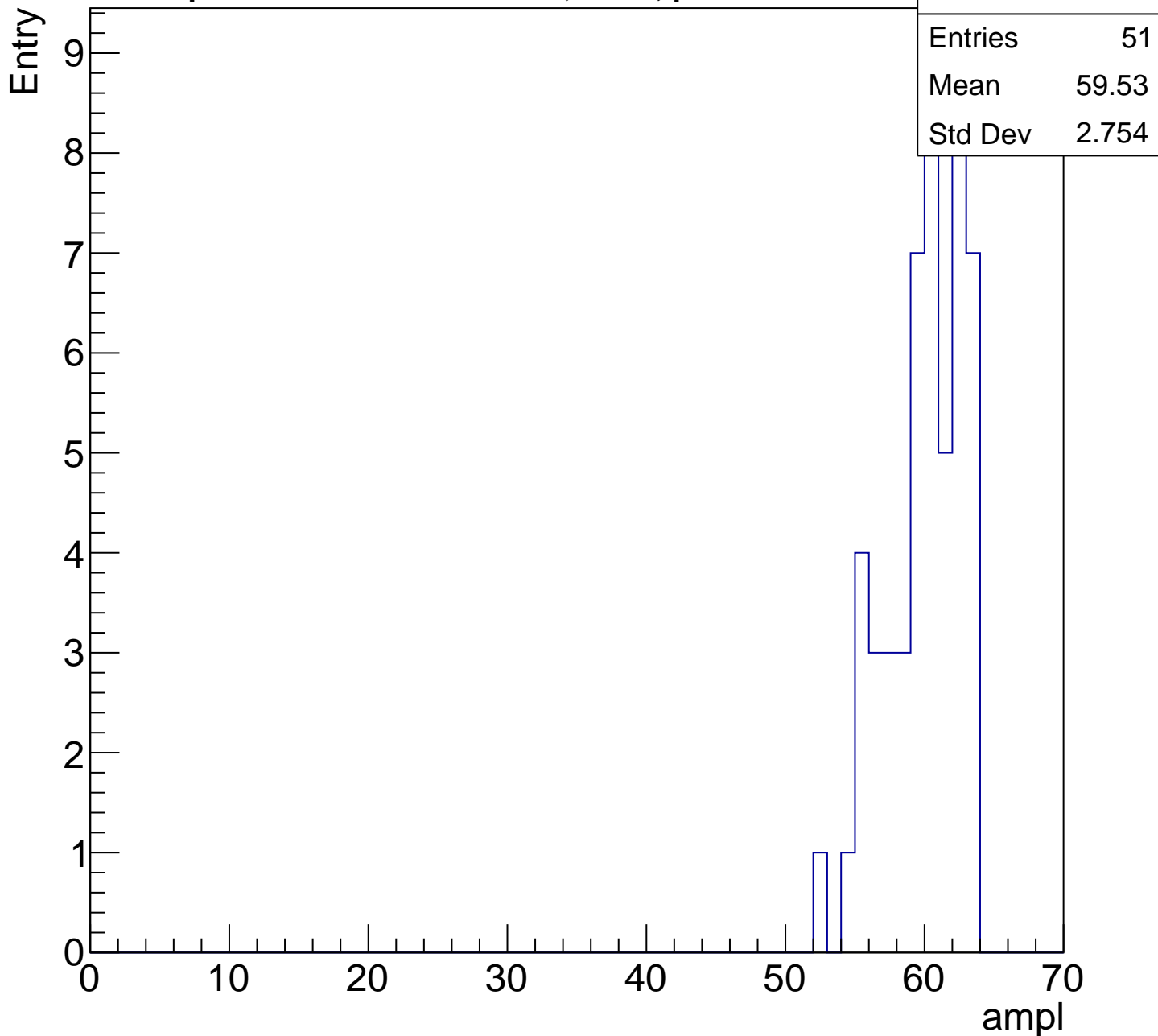
Entry

Entries	57
Mean	54.96
Std Dev	2.896



B1L101S, U6-ch78, adc5

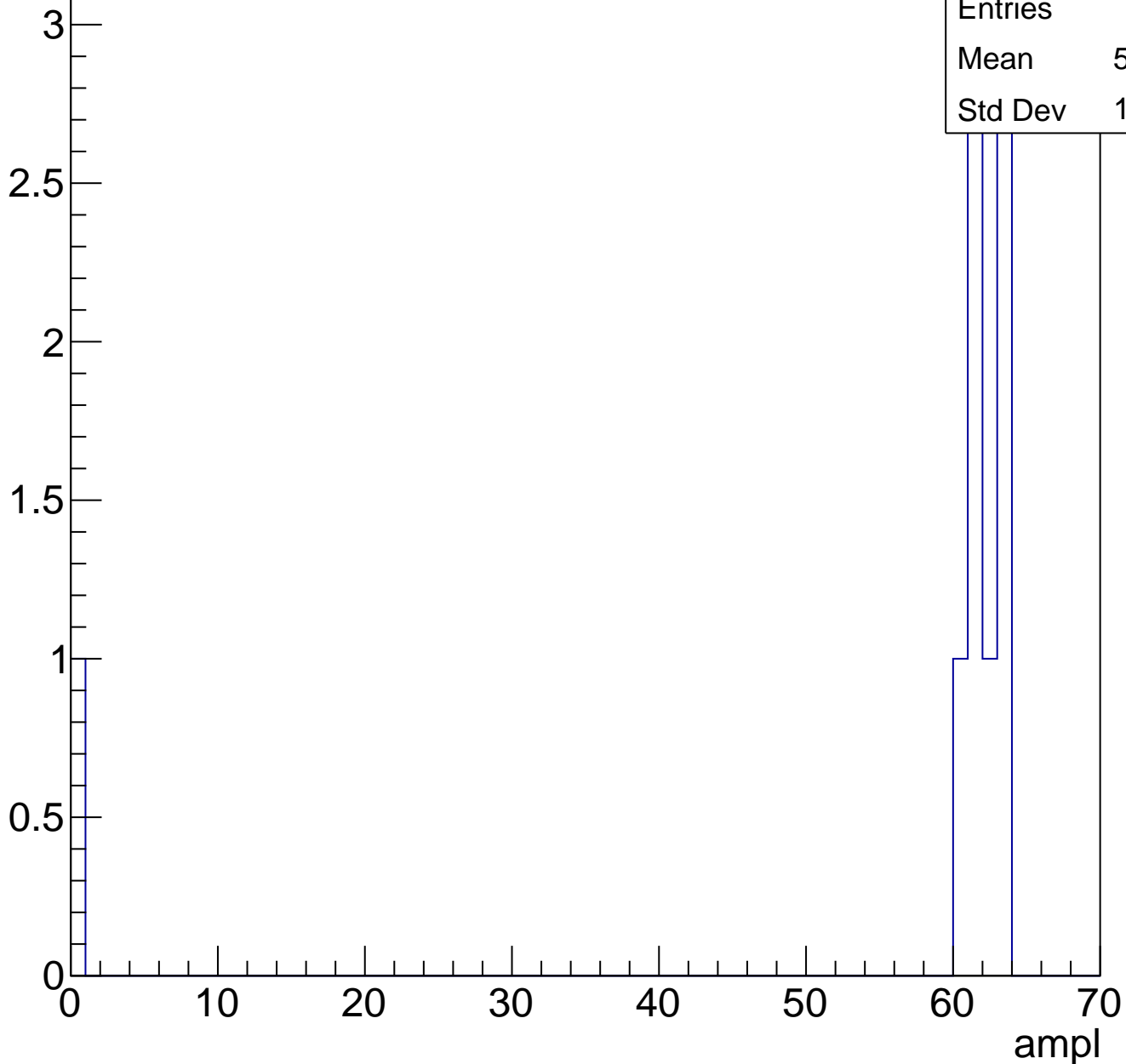
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L101S, U6-ch79, adc0

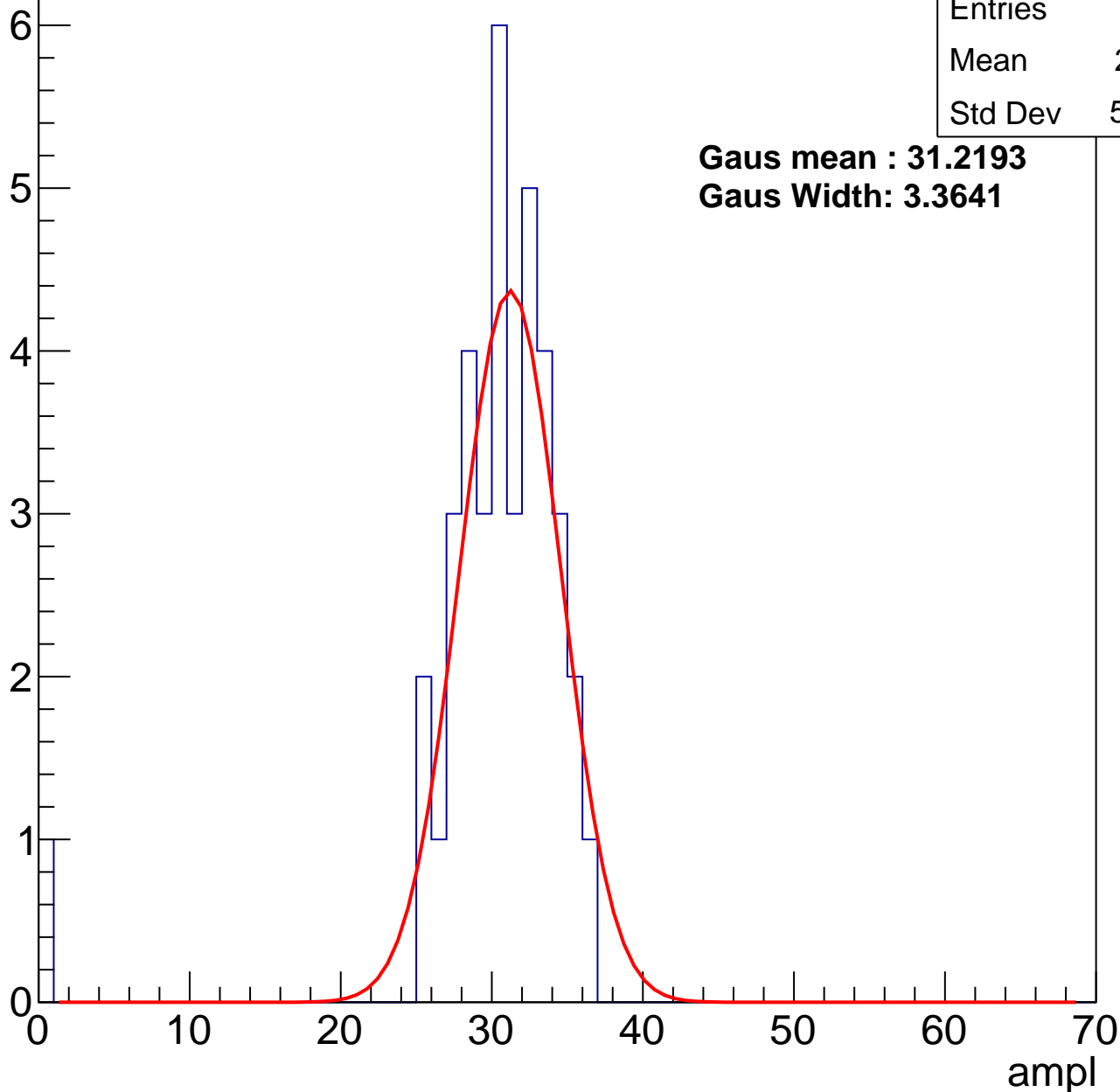
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	29.71
Std Dev	5.624

Gaus mean : 31.2193

Gaus Width: 3.3641



B1L101S, U6-ch79, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	36.52
Std Dev	3.772

Gaus mean : 36.9770

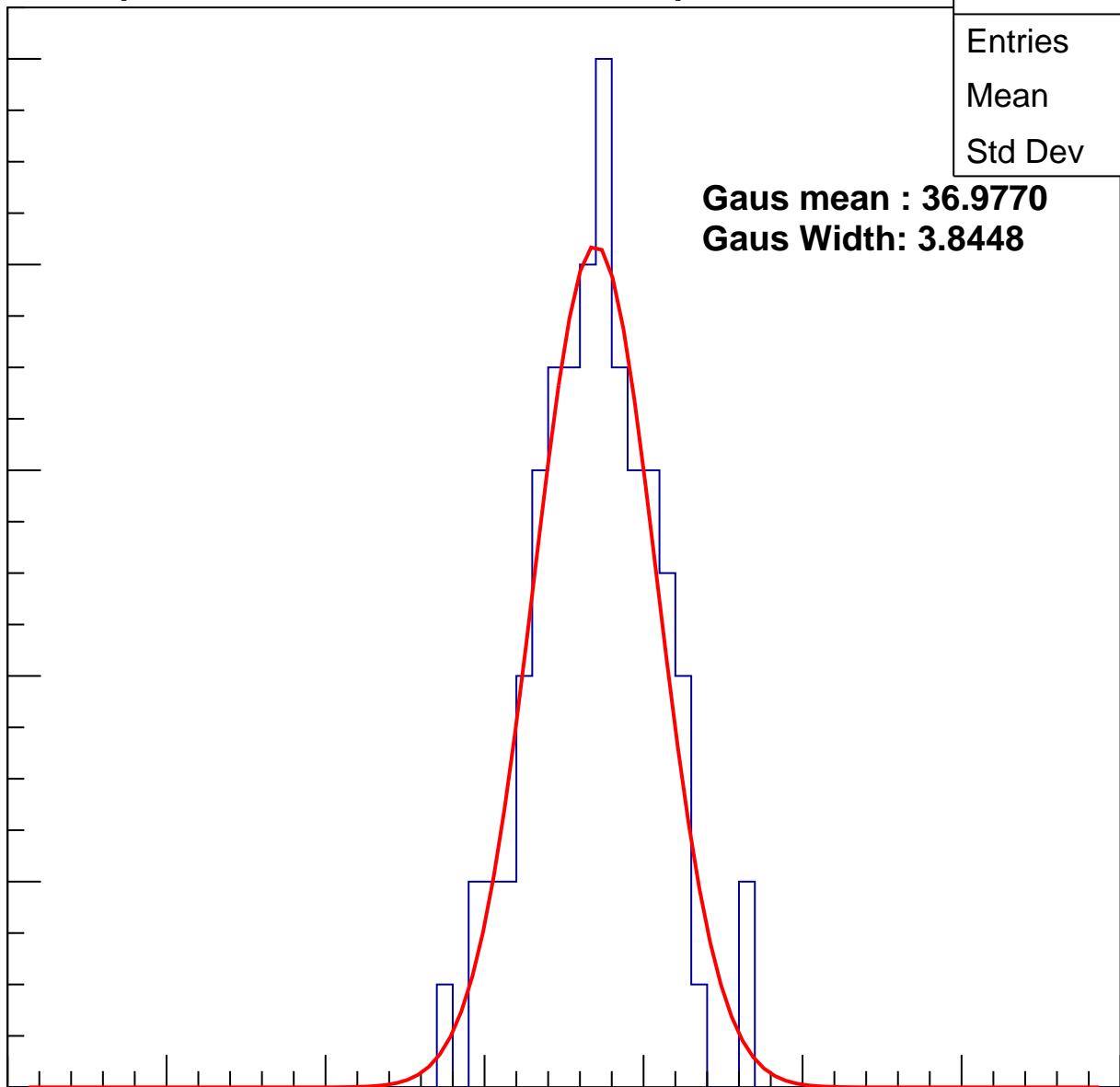
Gaus Width: 3.8448

Entry

10
8
6
4
2
0

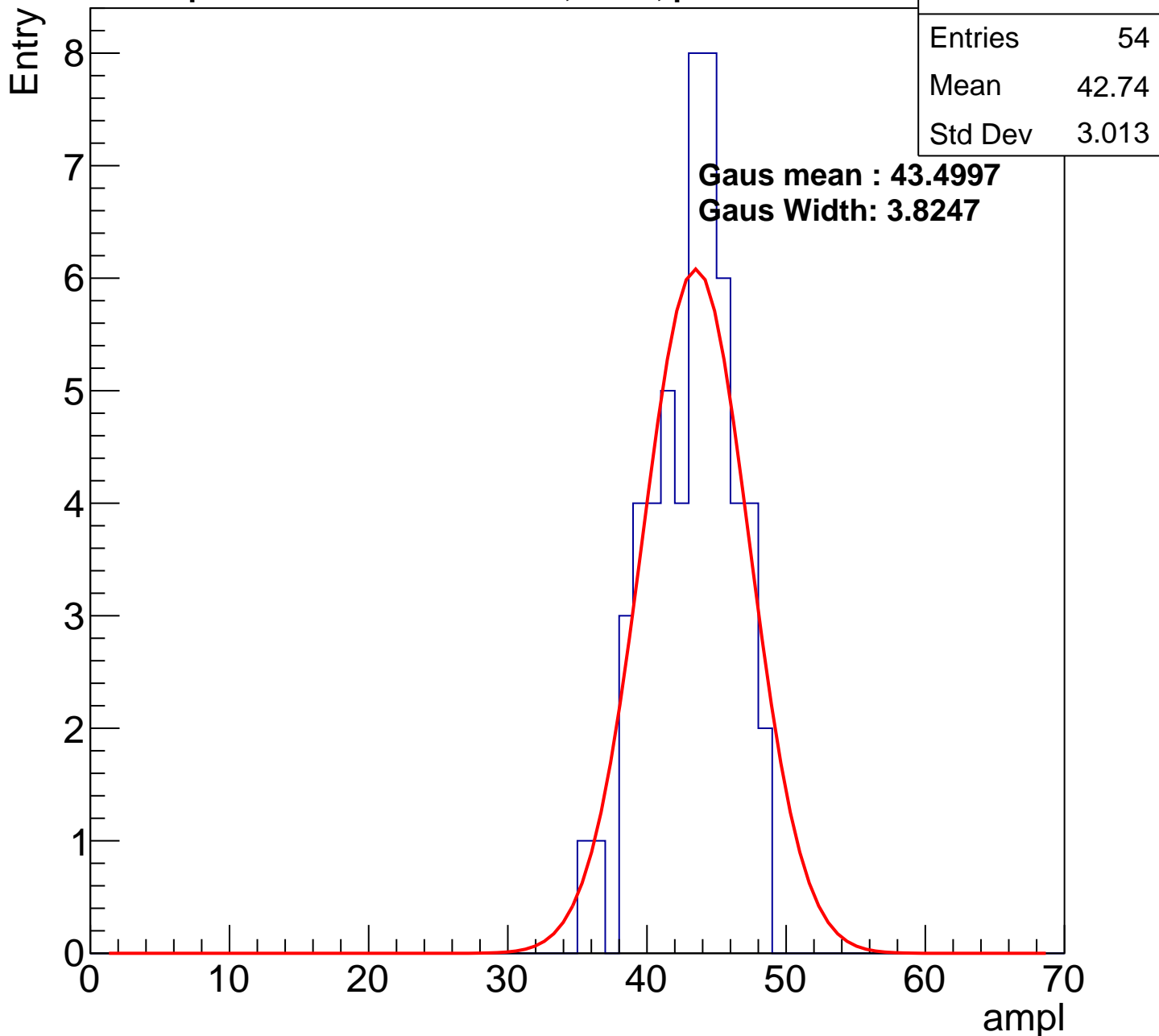
0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch79, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

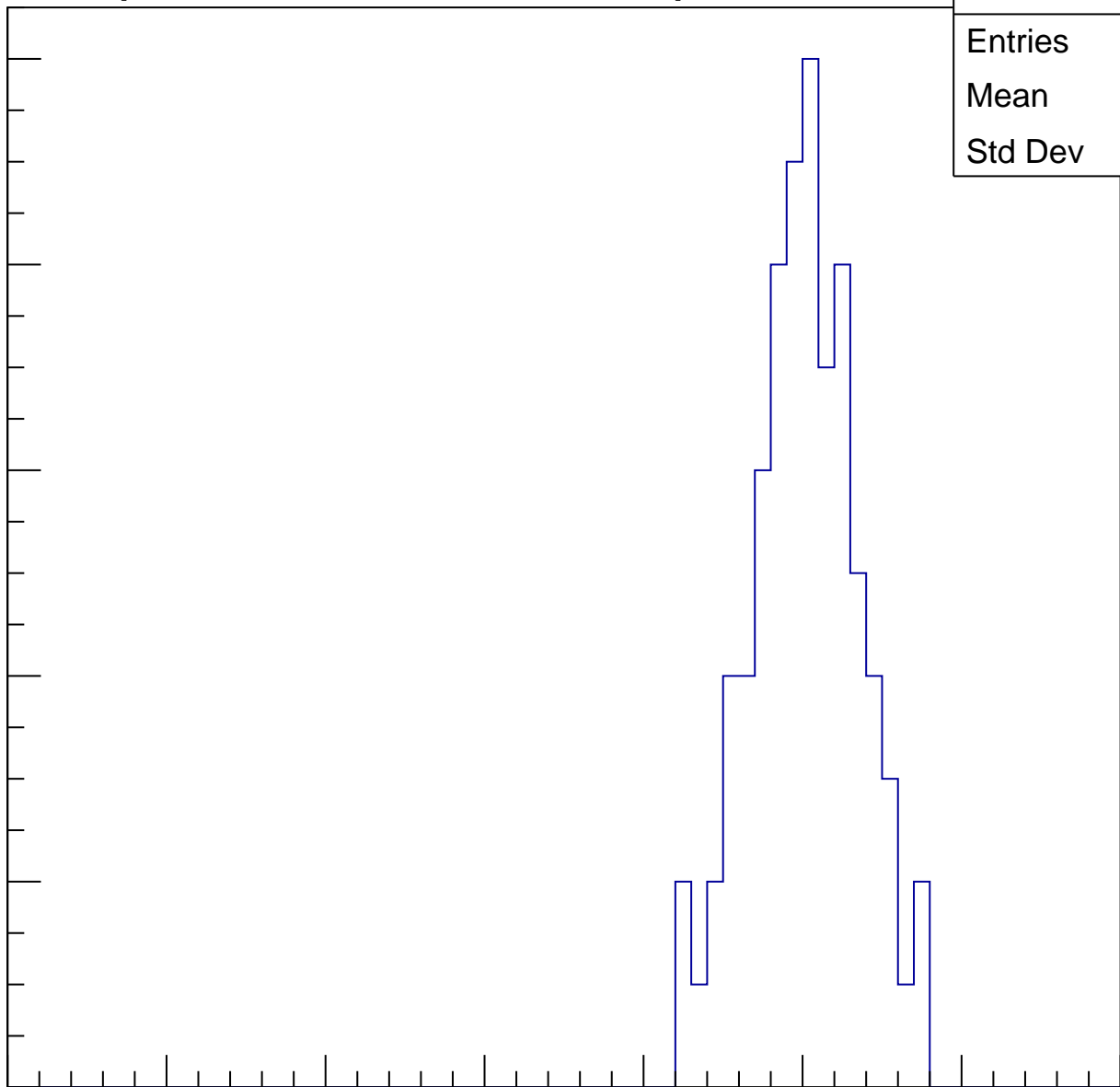
Entries	76
Mean	49.67
Std Dev	3.358

Entry

10
8
6
4
2
0

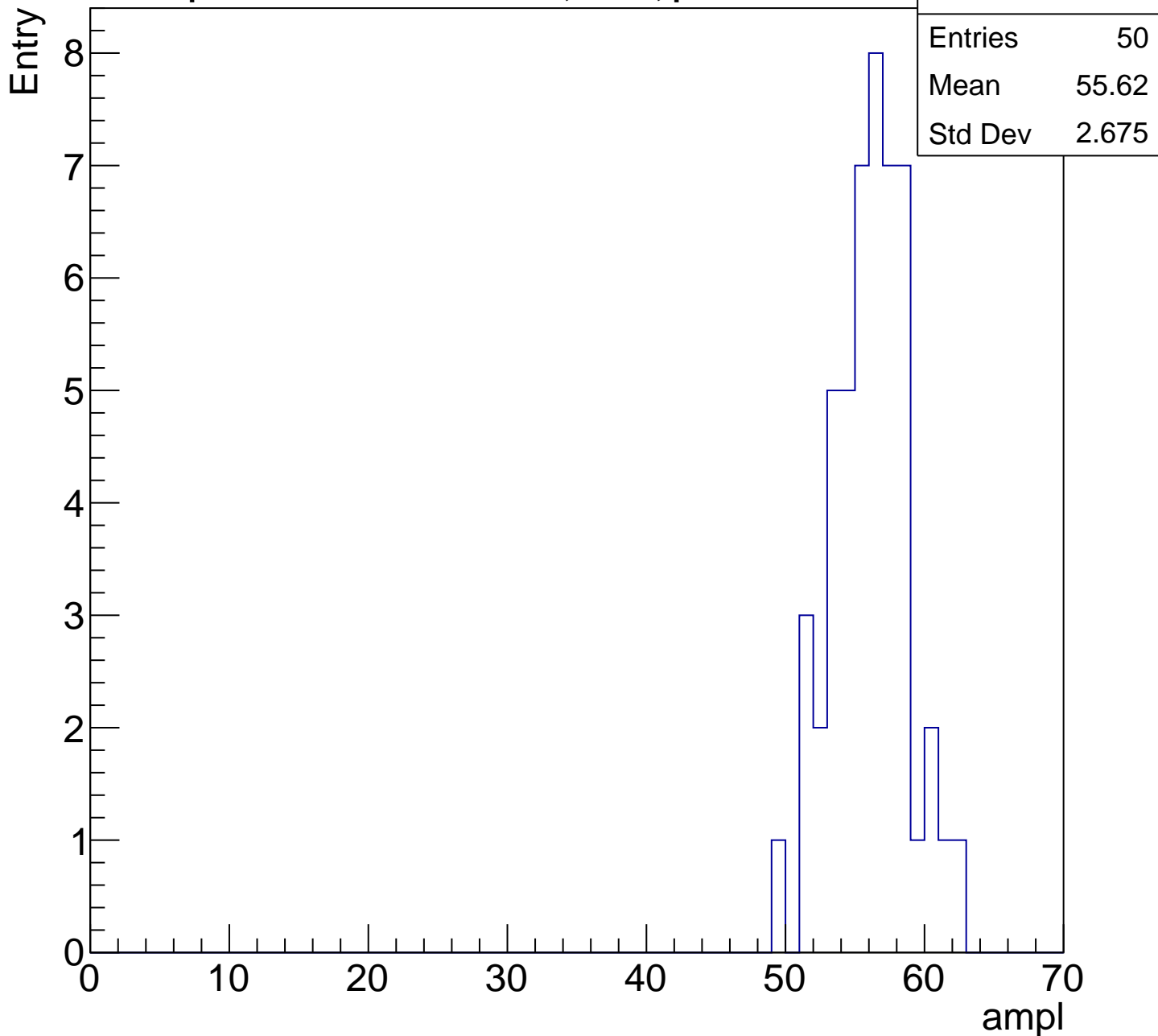
0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

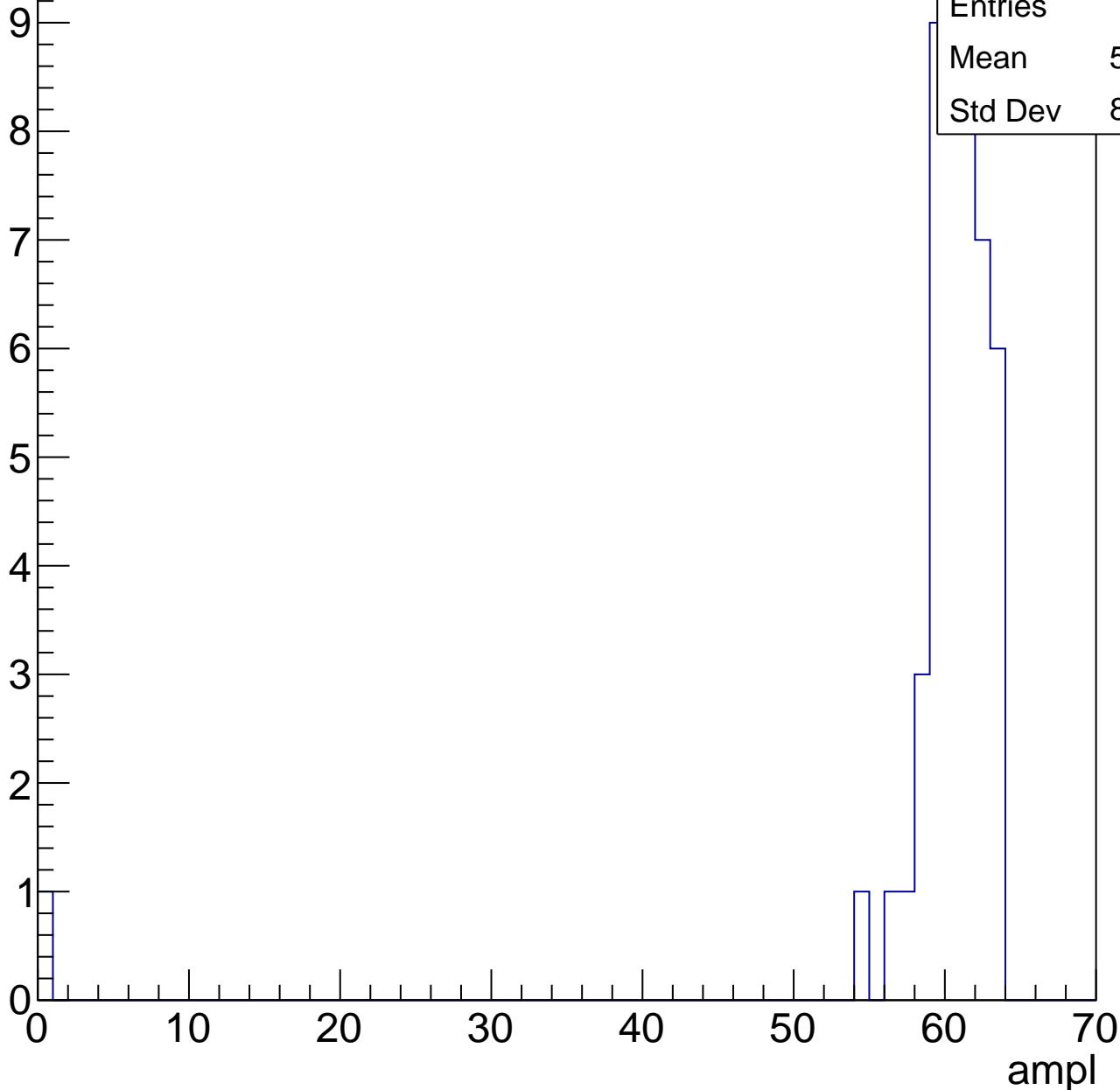


B1L101S, U6-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

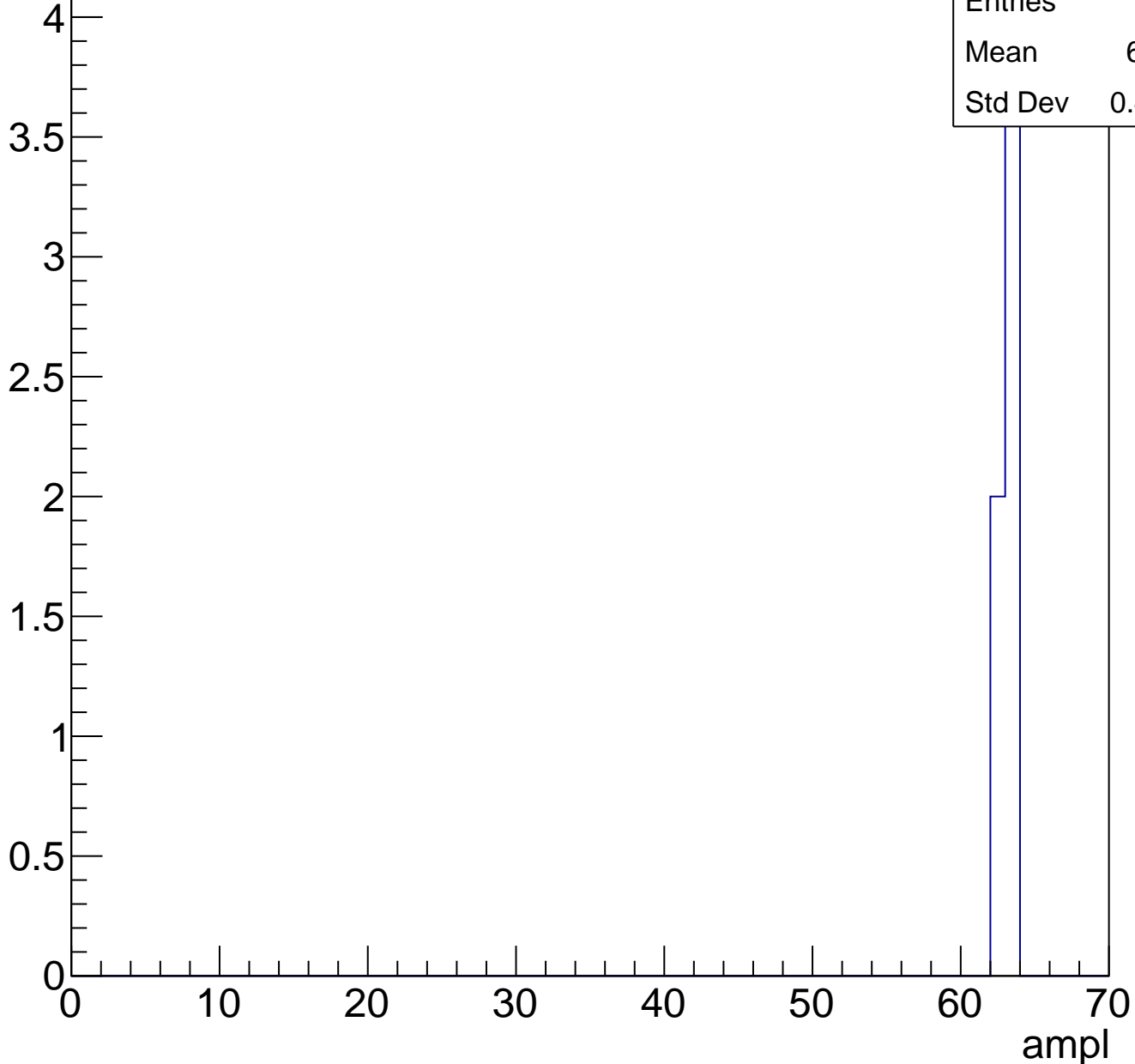
Entries	46
Mean	58.98
Std Dev	8.999



B1L101S, U6-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch80, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	26.64
Std Dev	5.722

Gaus mean : 27.4829

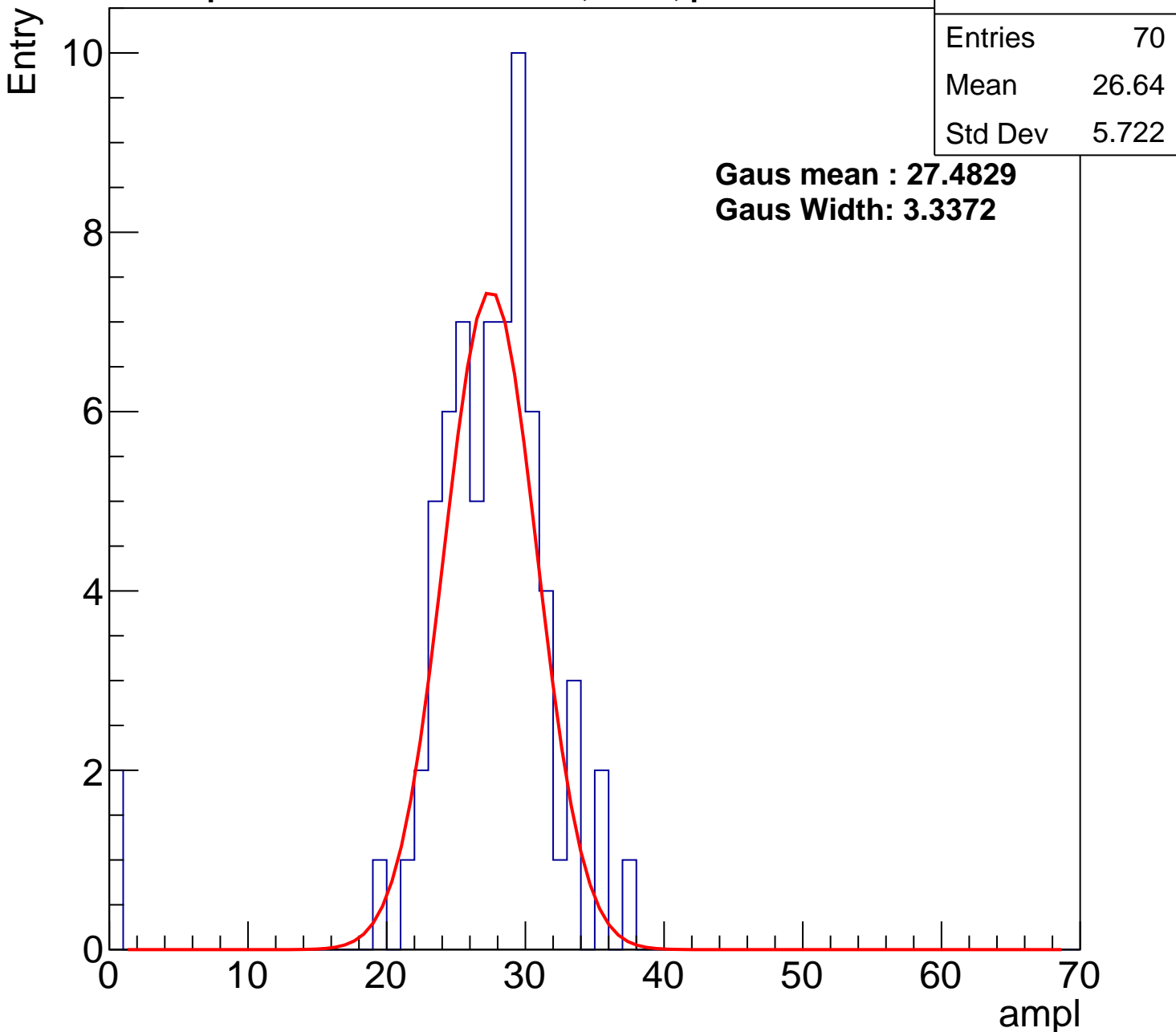
Gaus Width: 3.3372

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch80, adc1

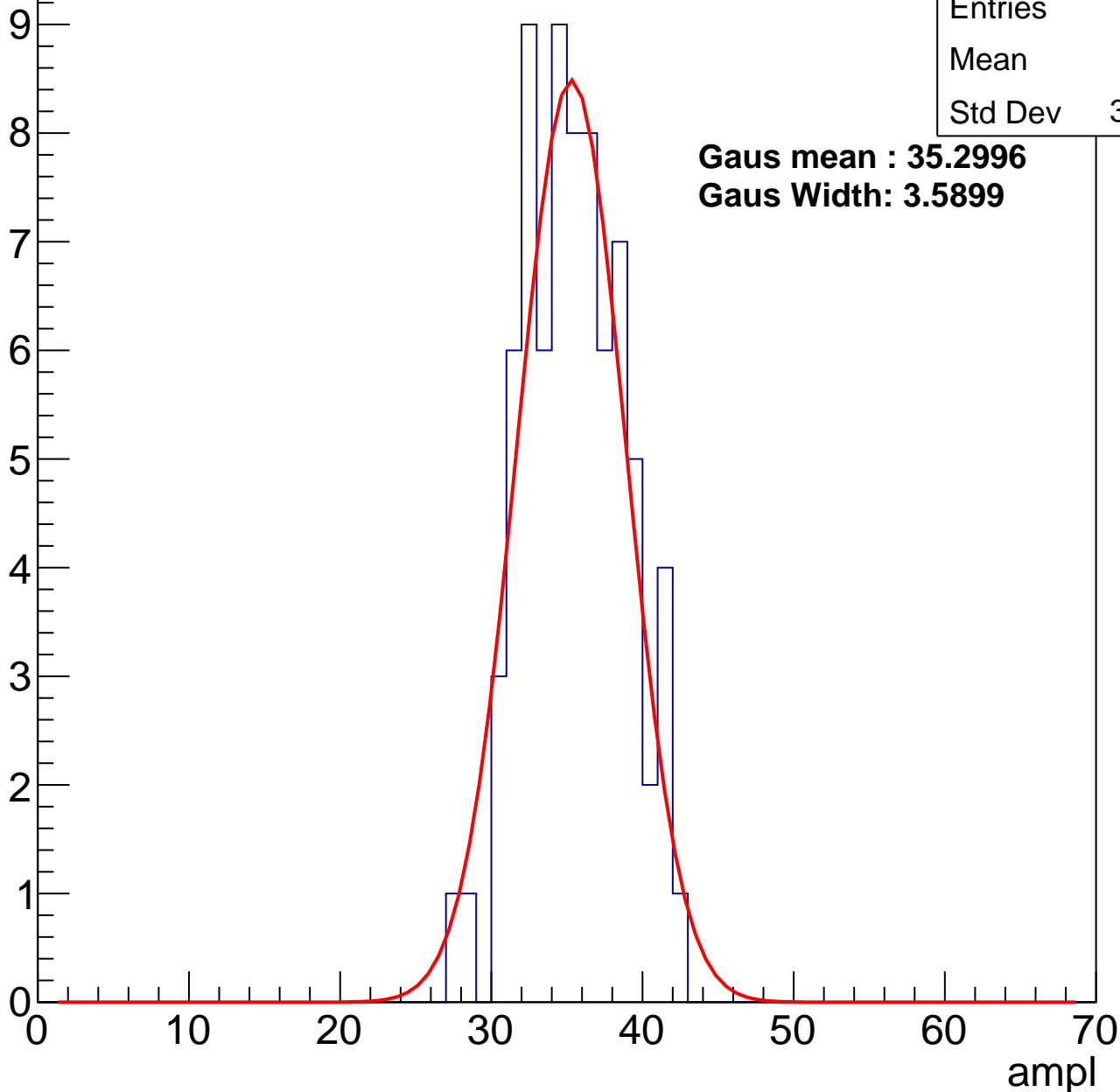
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35
Std Dev	3.277

Gaus mean : 35.2996

Gaus Width: 3.5899



B1L101S, U6-ch80, adc2

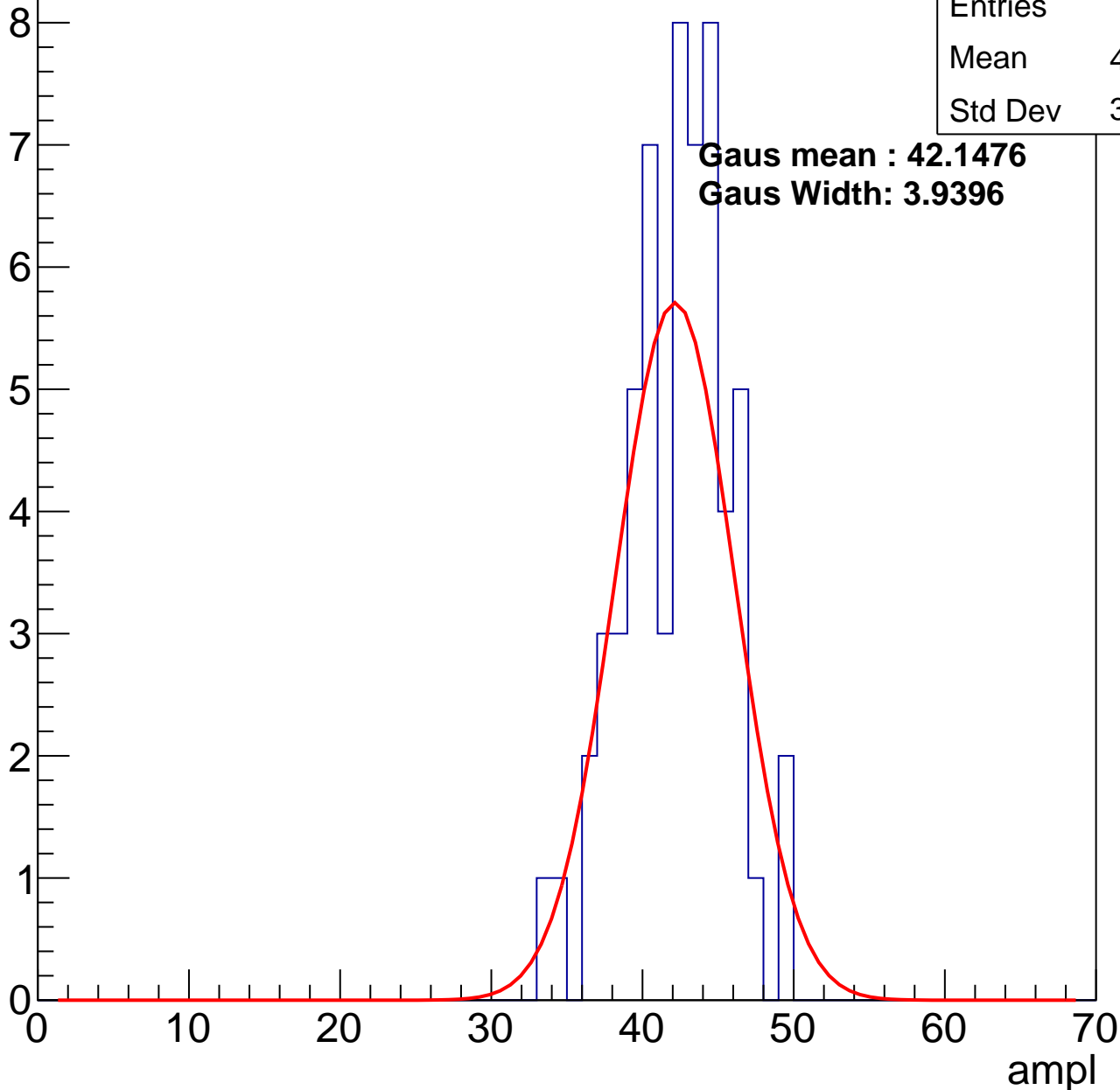
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.77
Std Dev	3.393

Gaus mean : 42.1476

Gaus Width: 3.9396

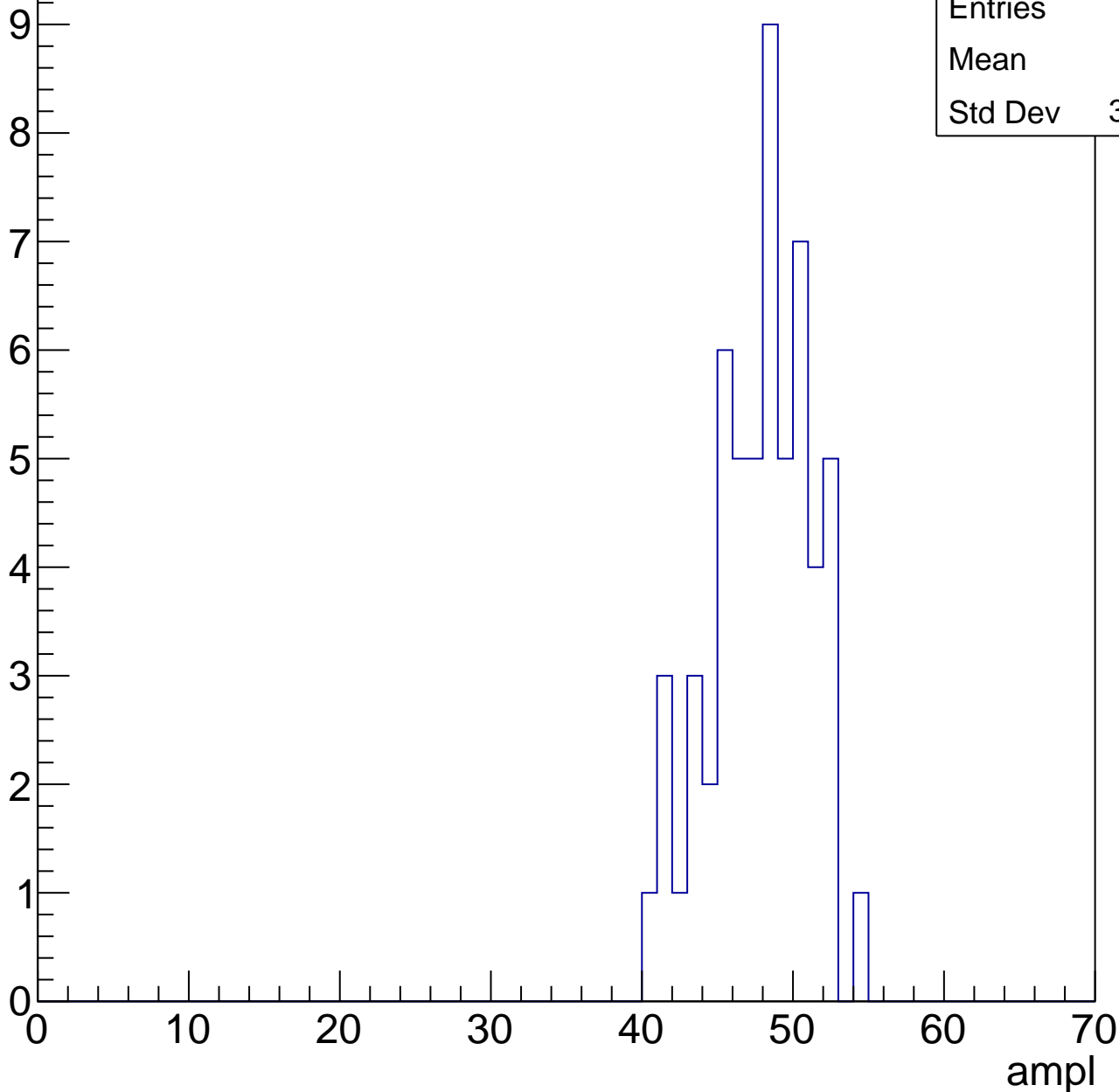


B1L101S, U6-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

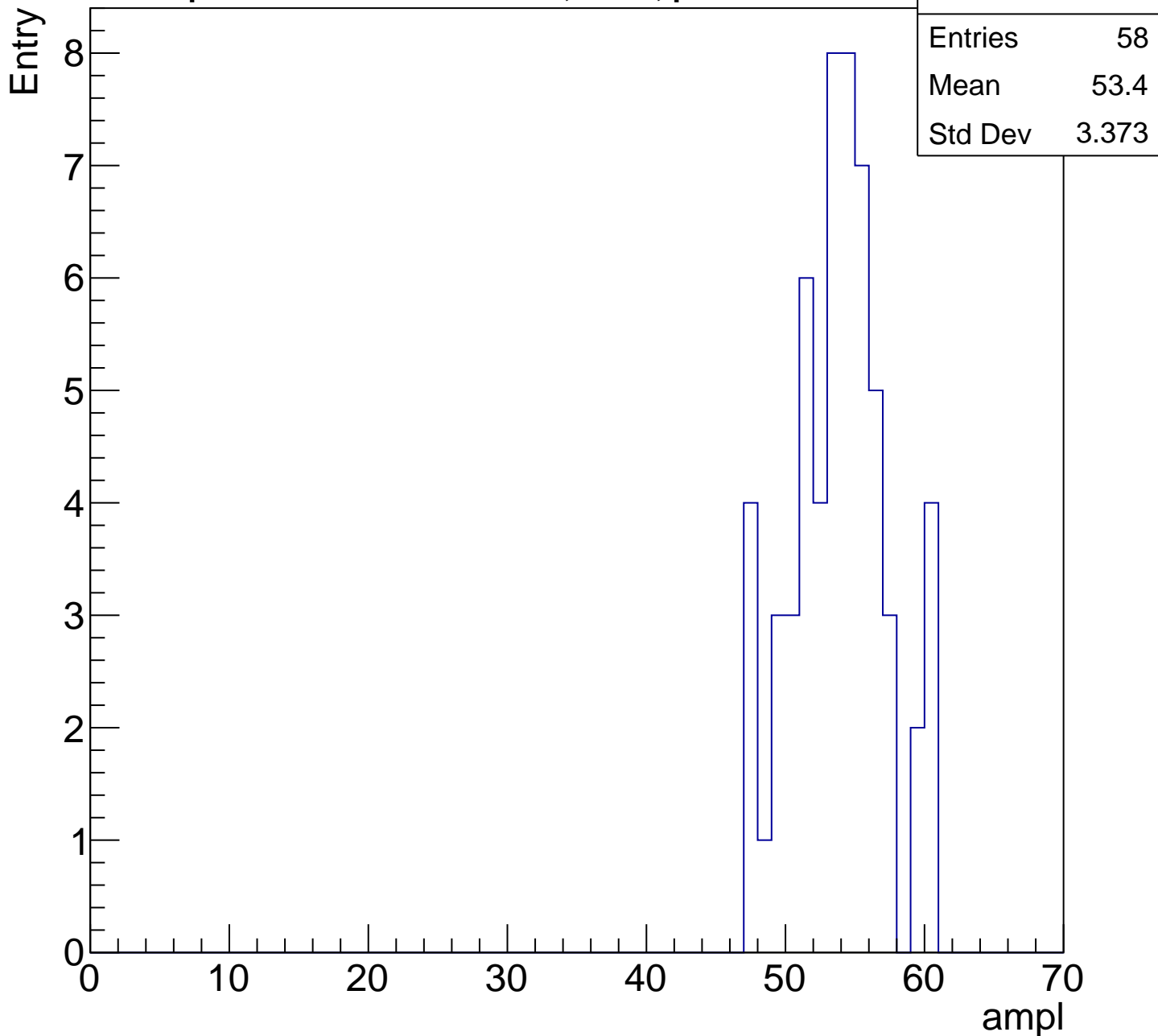
Entry

Entries	57
Mean	47.4
Std Dev	3.238



B1L101S, U6-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

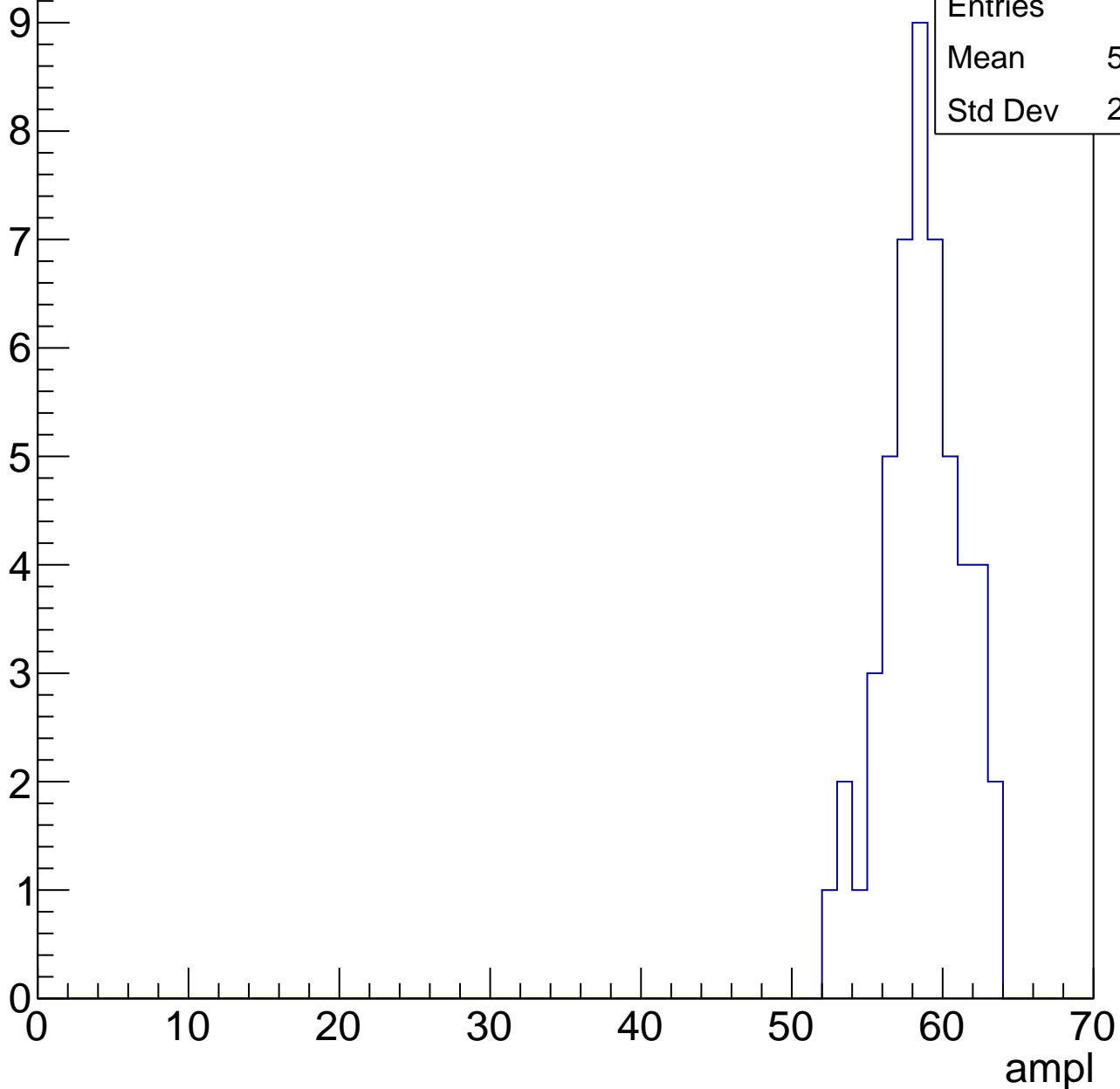


B1L101S, U6-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.18
Std Dev	2.574

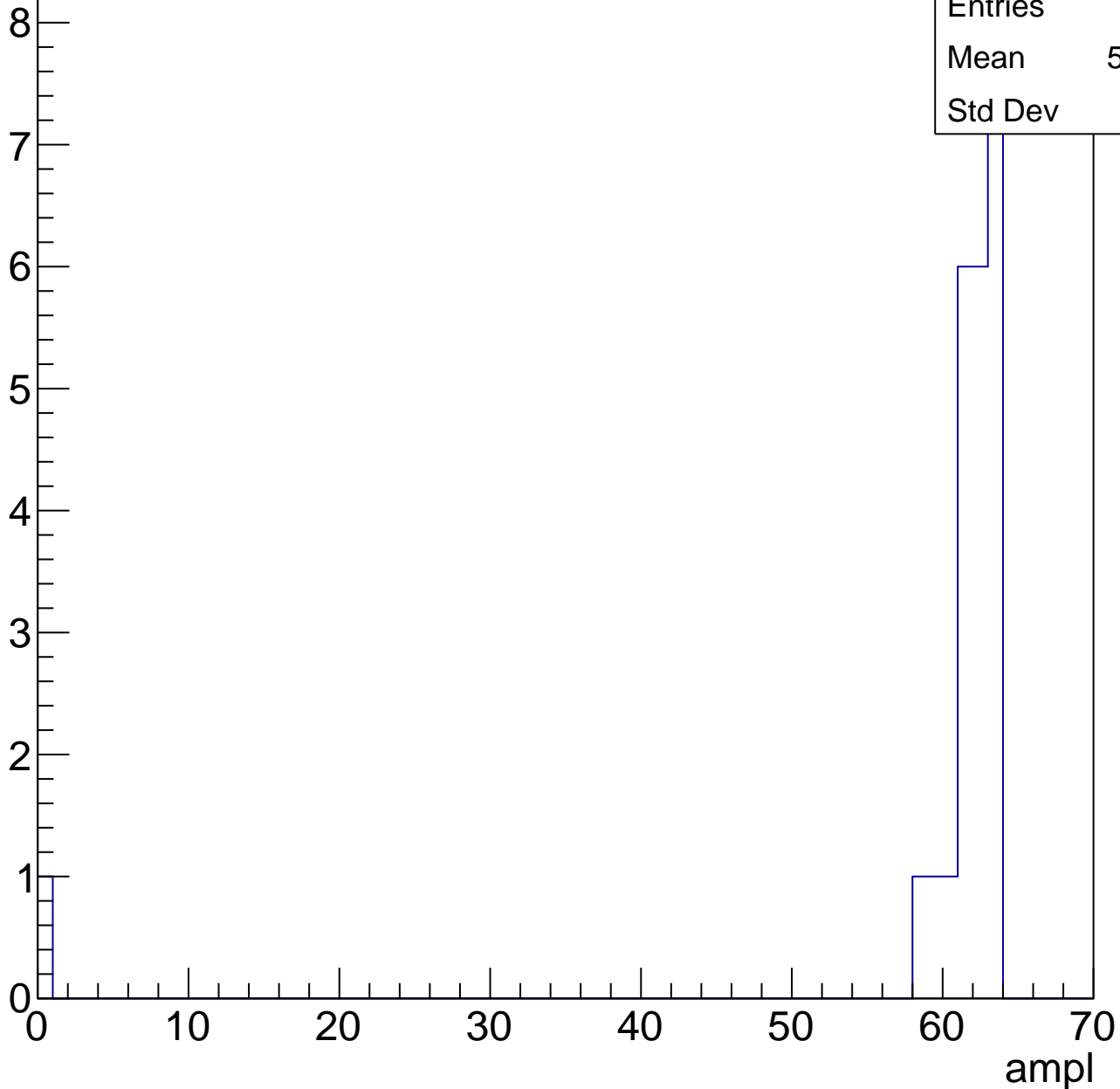


B1L101S, U6-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	59.12
Std Dev	12.4



B1L101S, U6-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch81, adc0

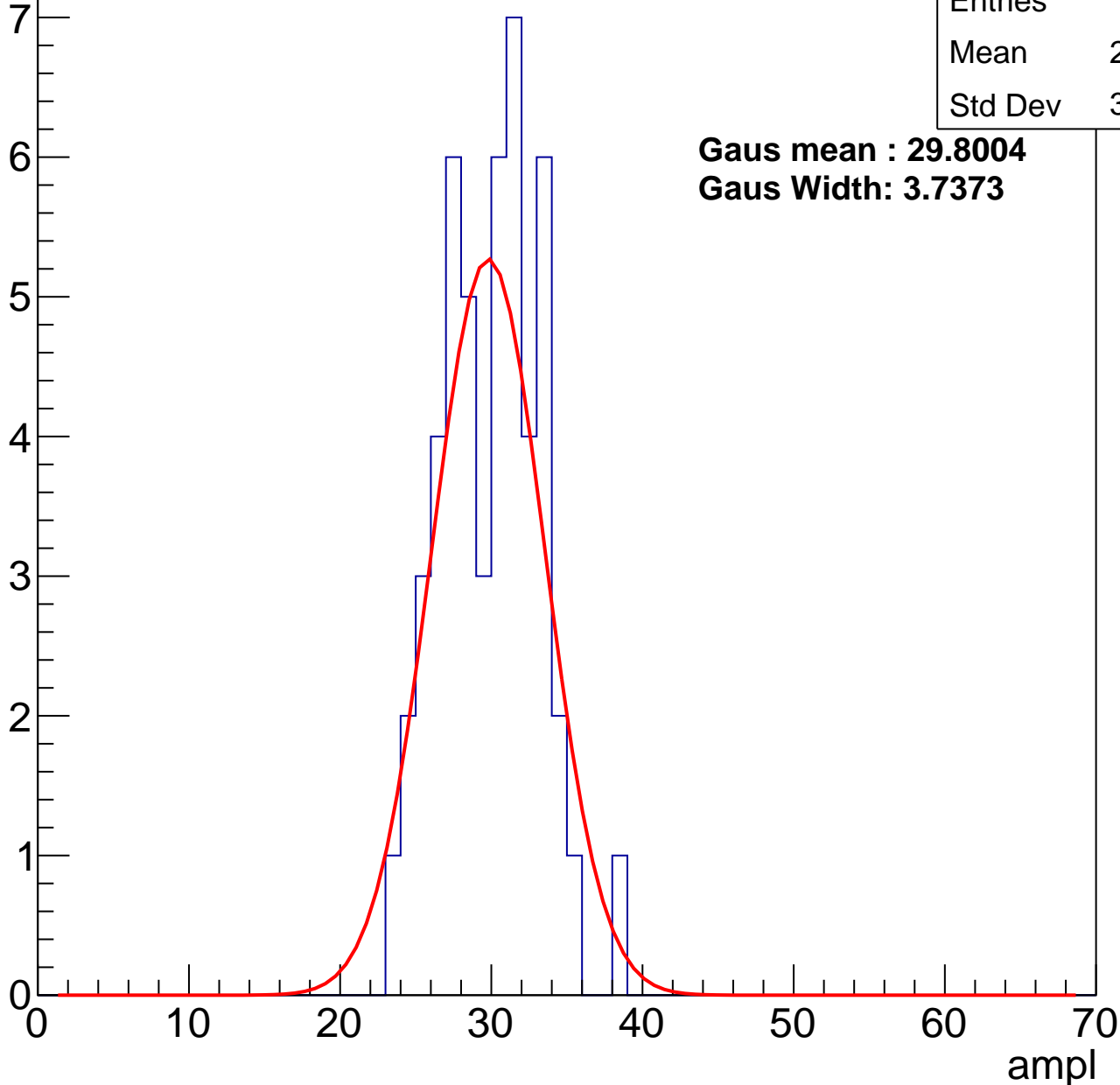
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	29.47
Std Dev	3.195

Gaus mean : 29.8004

Gaus Width: 3.7373



B1L101S, U6-ch81, adc1

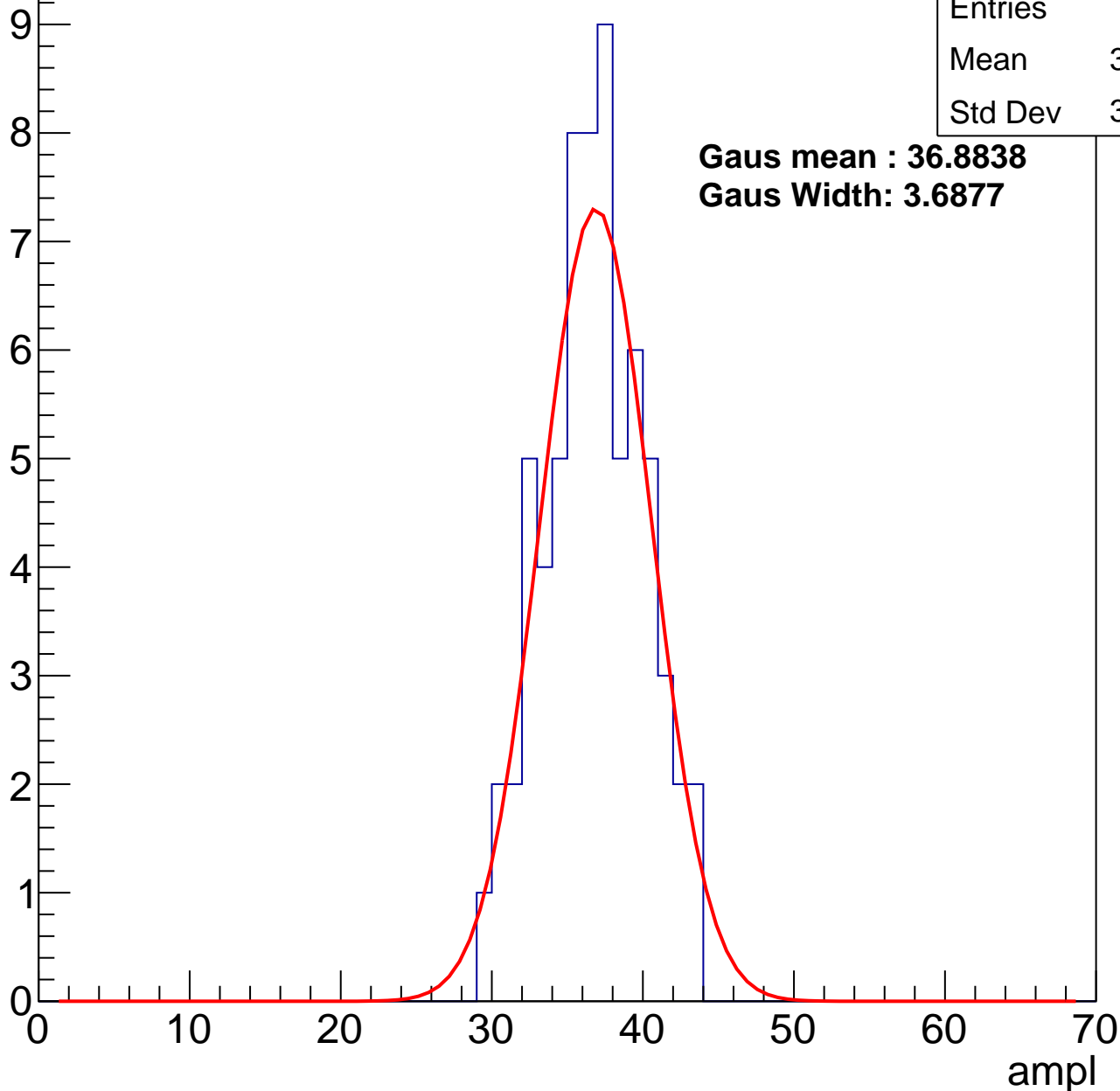
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.28
Std Dev	3.273

Gaus mean : 36.8838

Gaus Width: 3.6877



B1L101S, U6-ch81, adc2

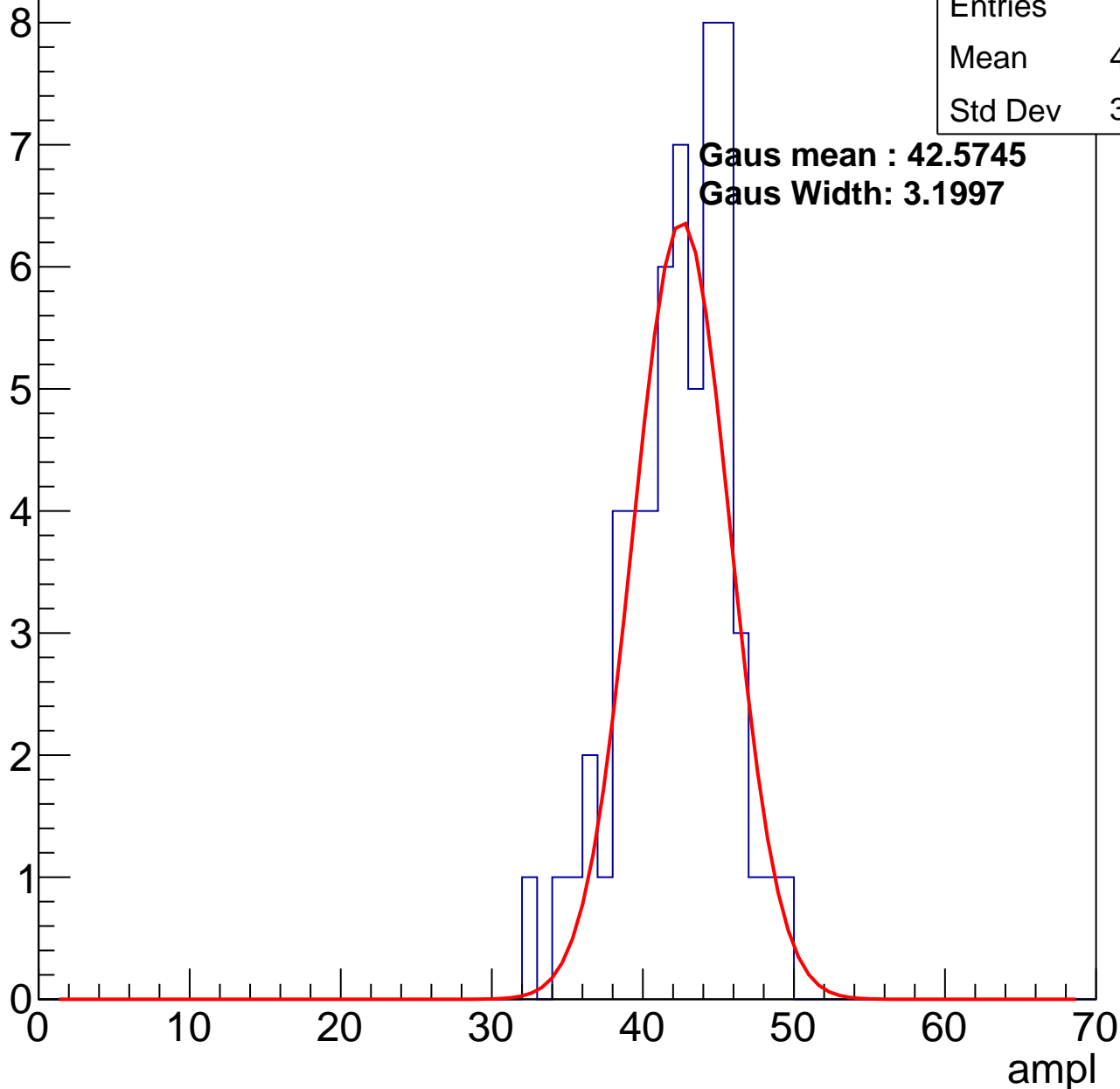
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.84
Std Dev	3.468

Gaus mean : 42.5745

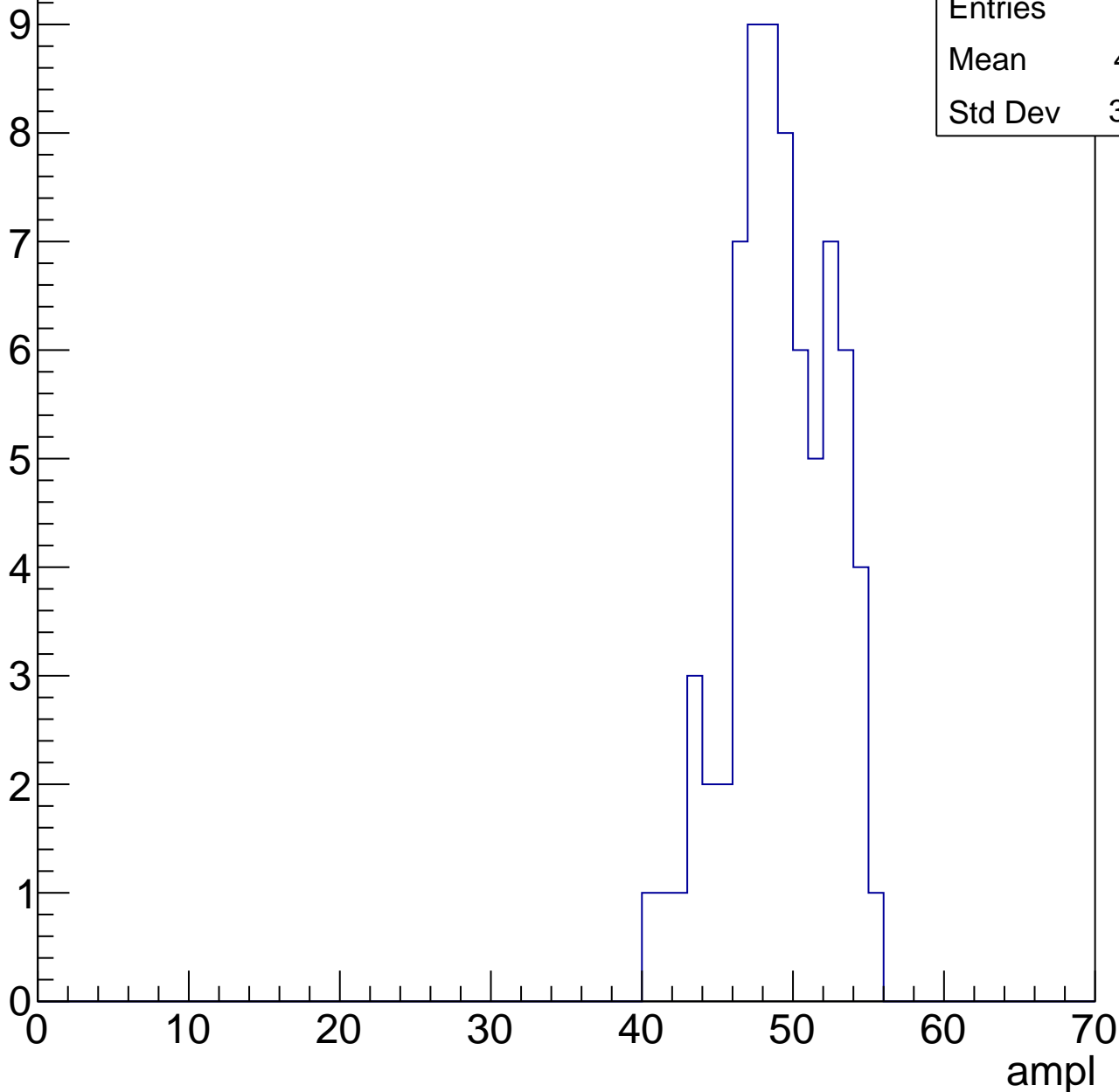
Gaus Width: 3.1997



B1L101S, U6-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	72
Mean	48.71
Std Dev	3.348

B1L101S, U6-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

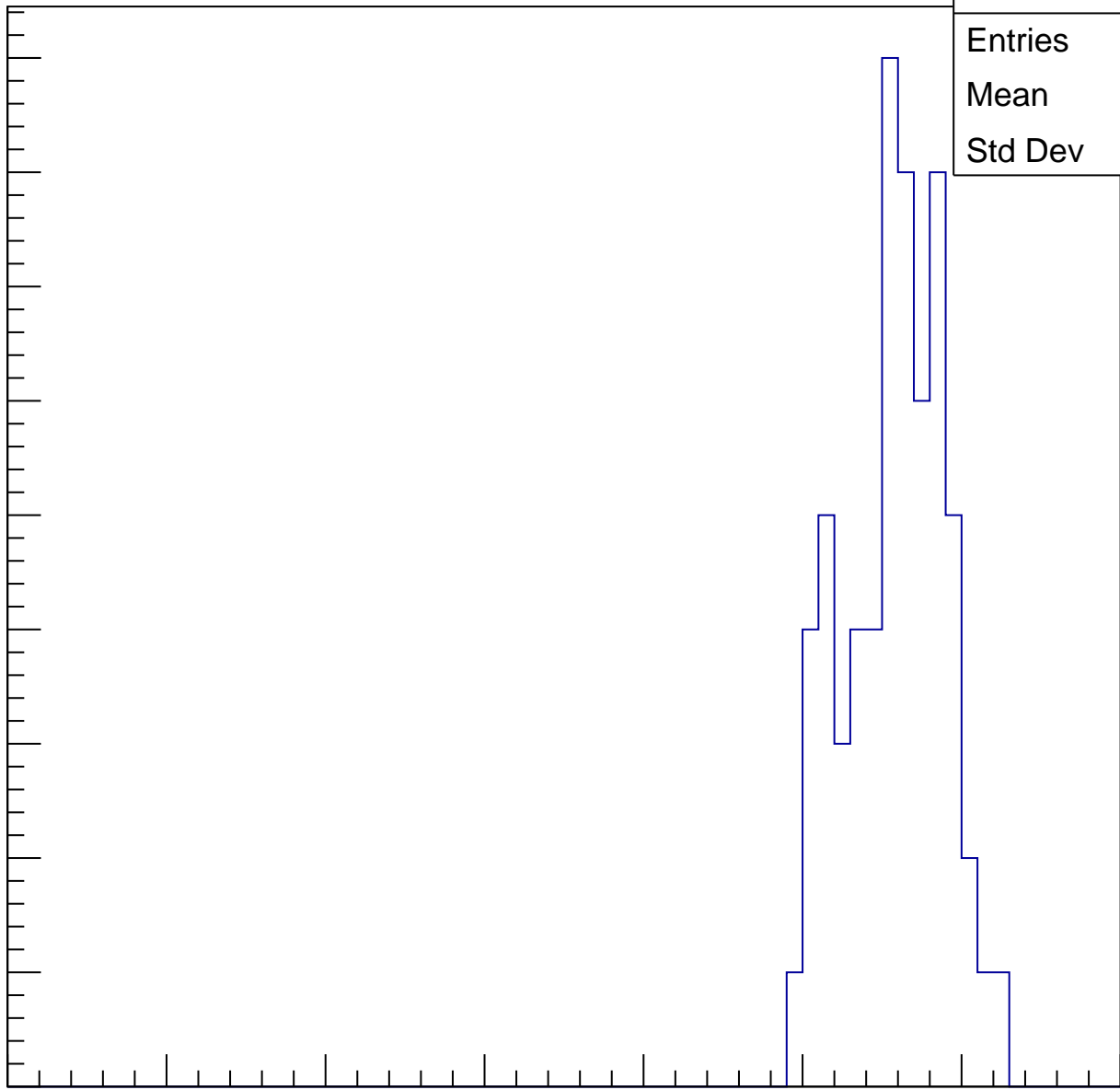
Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	55.33
Std Dev	3.071

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch81, adc5

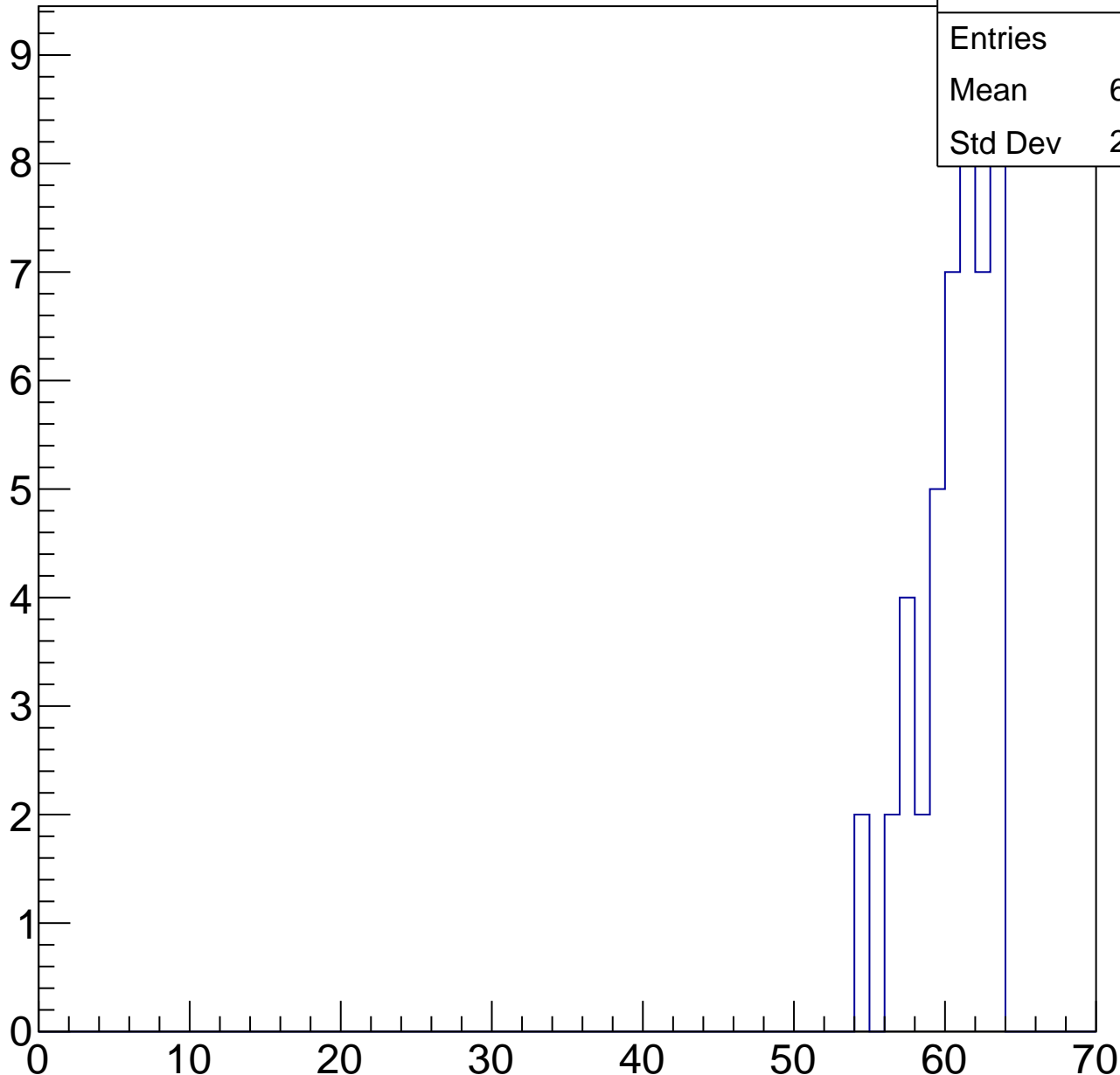
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.13
Std Dev	2.383

ampl



B1L101S, U6-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

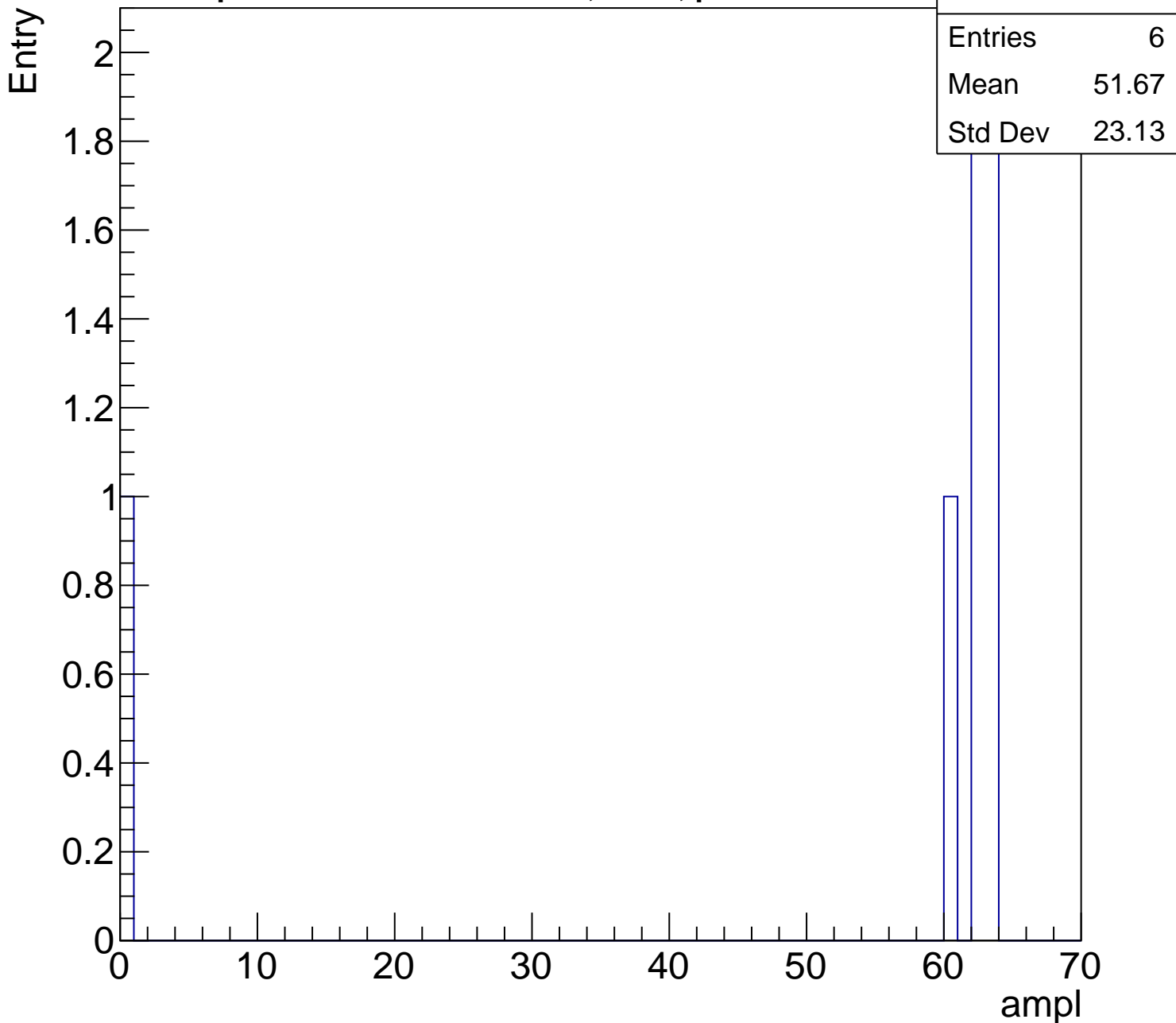
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.13

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch82, adc0

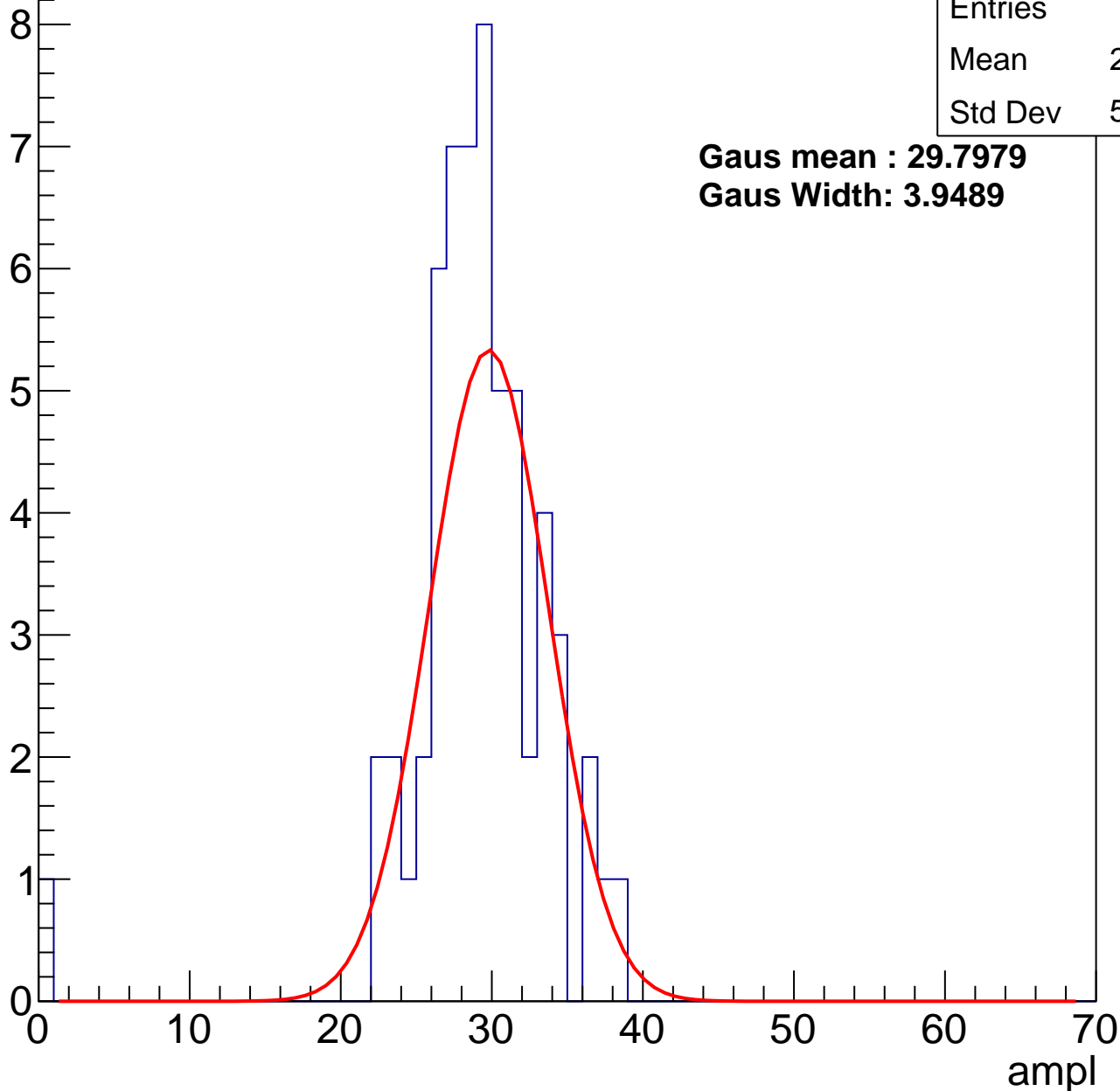
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.59
Std Dev	5.162

Gaus mean : 29.7979

Gaus Width: 3.9489



B1L101S, U6-ch82, adc1

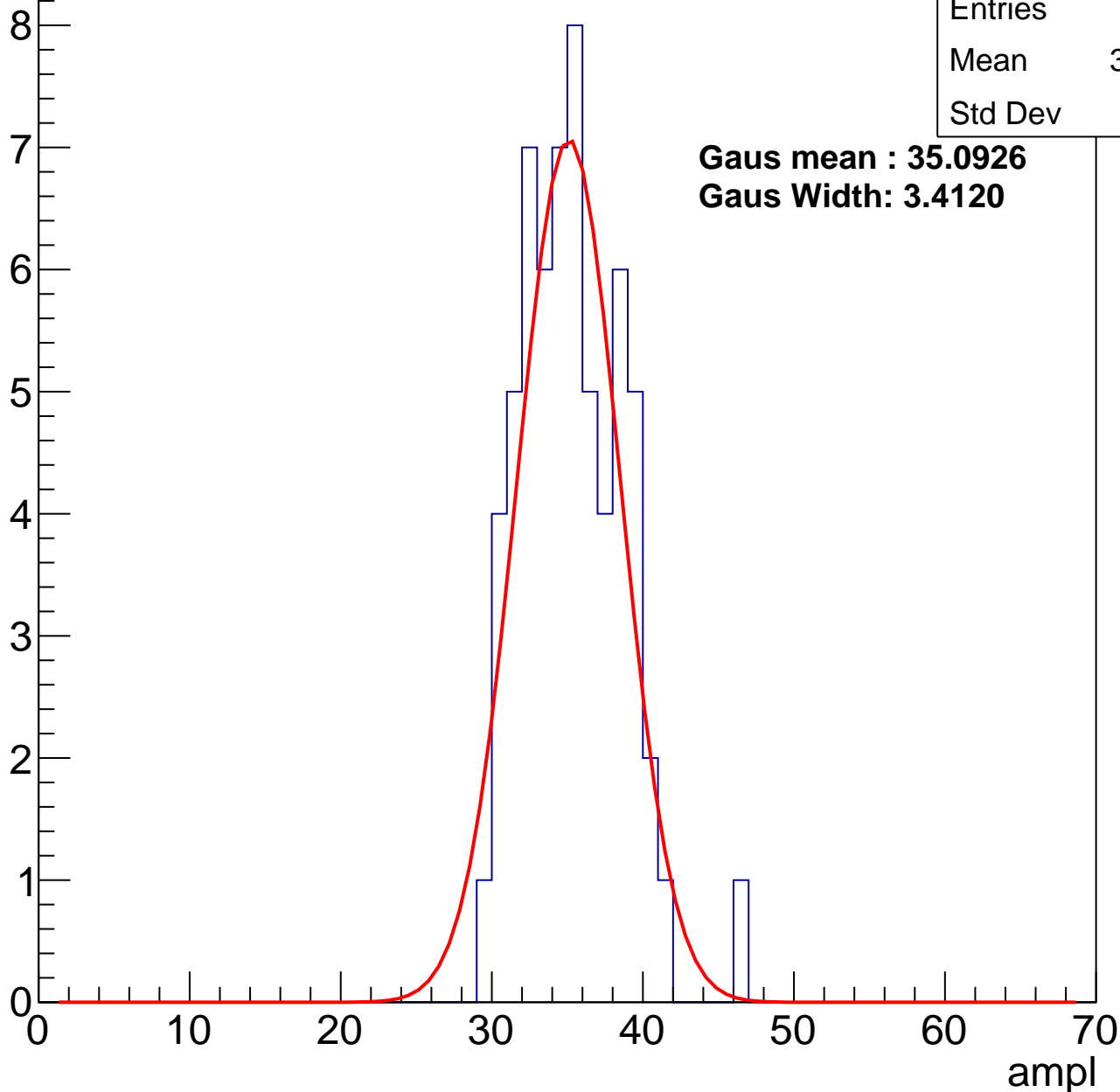
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	34.87
Std Dev	3.29

Gaus mean : 35.0926

Gaus Width: 3.4120



B1L101S, U6-ch82, adc2

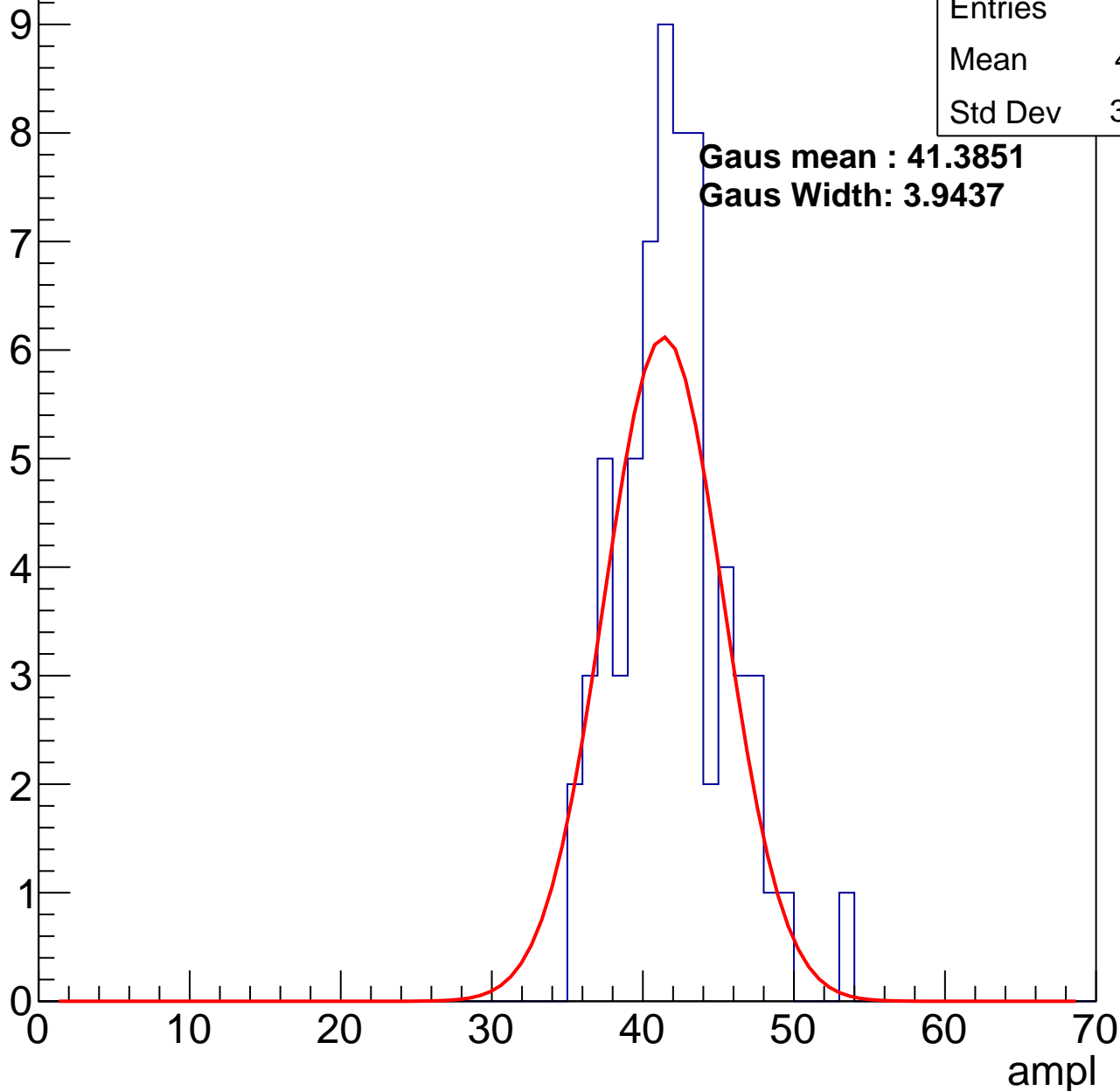
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.51
Std Dev	3.565

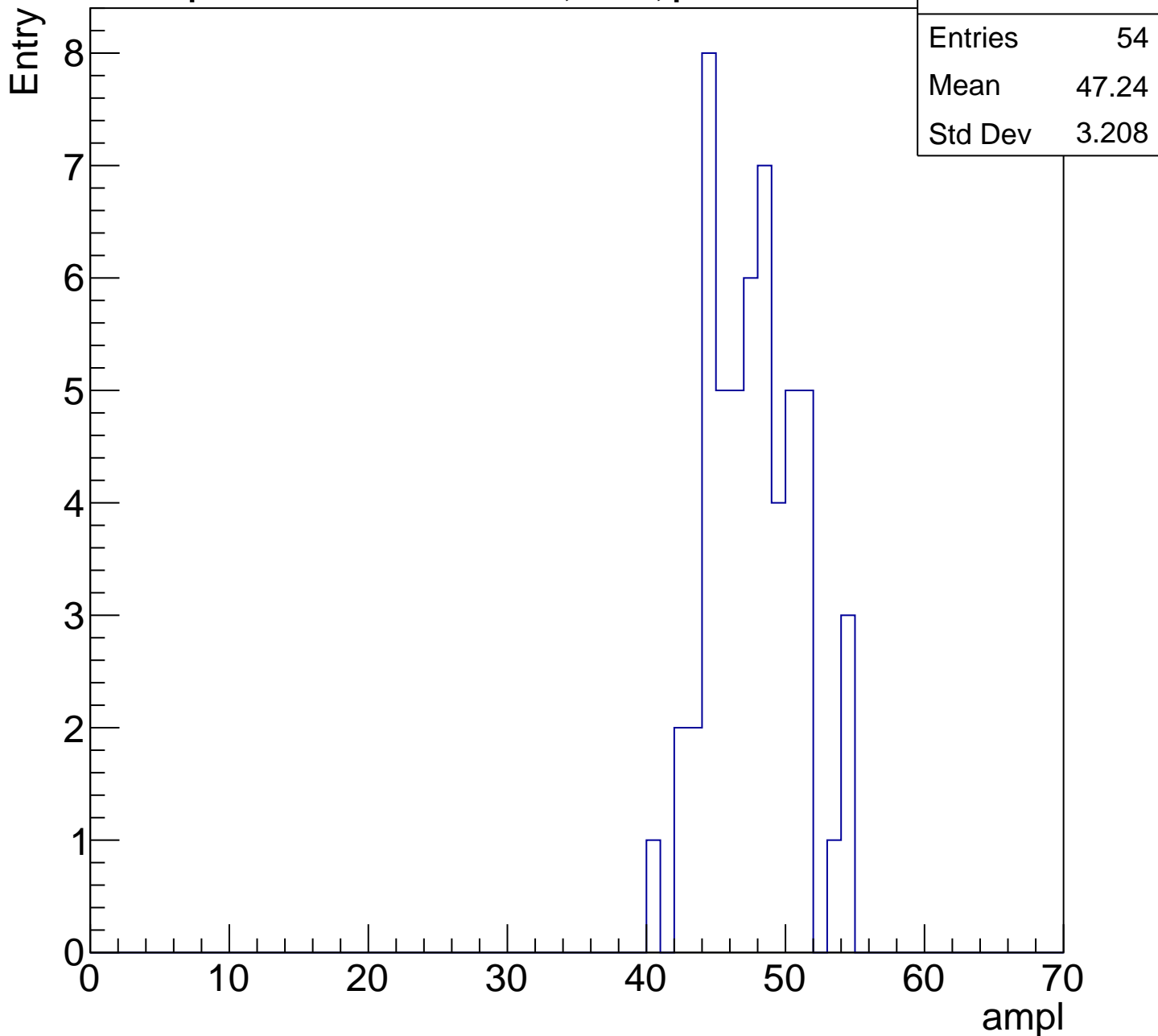
Gaus mean : 41.3851

Gaus Width: 3.9437



B1L101S, U6-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

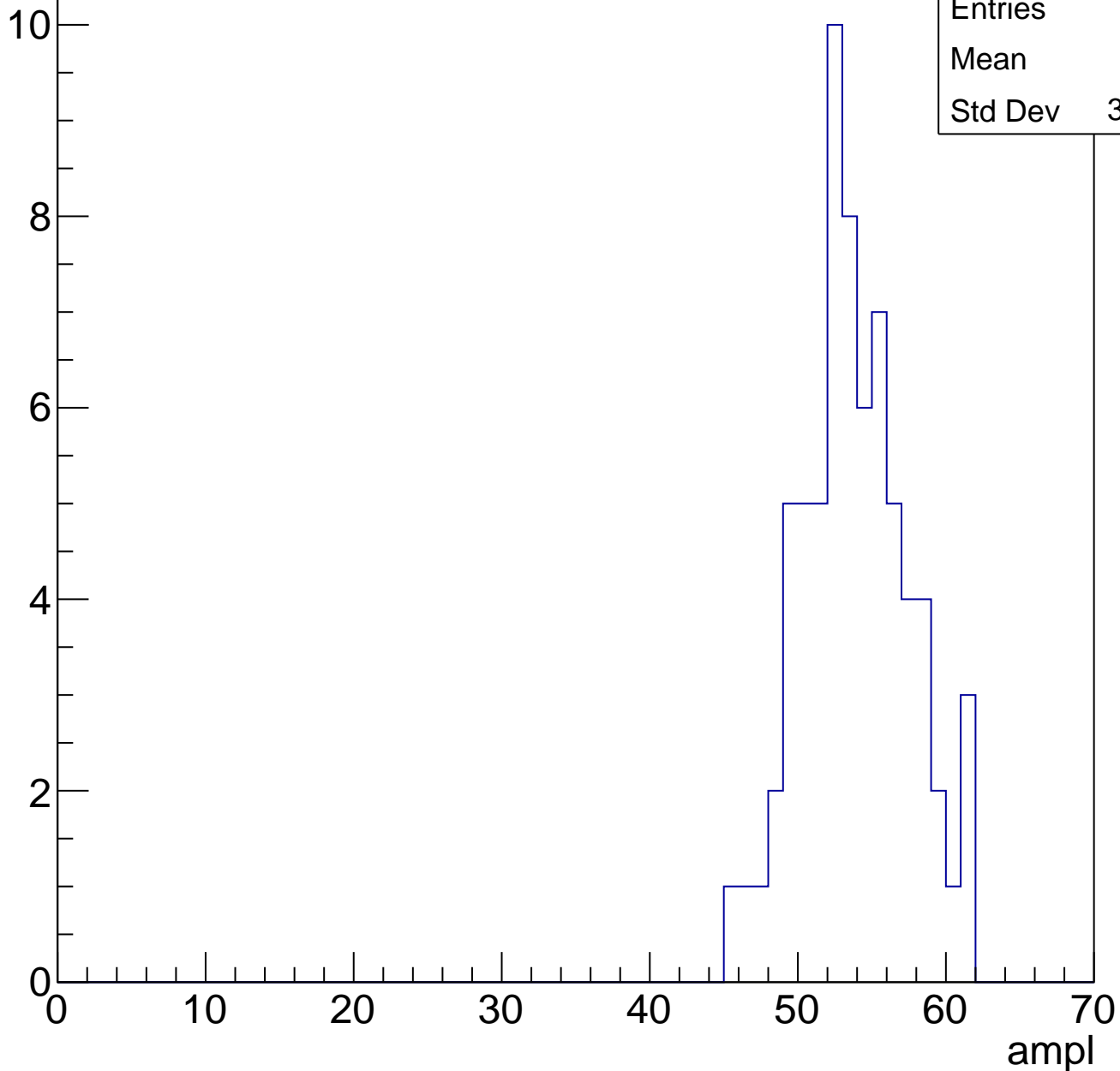


B1L101S, U6-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	53.4
Std Dev	3.583

Entry

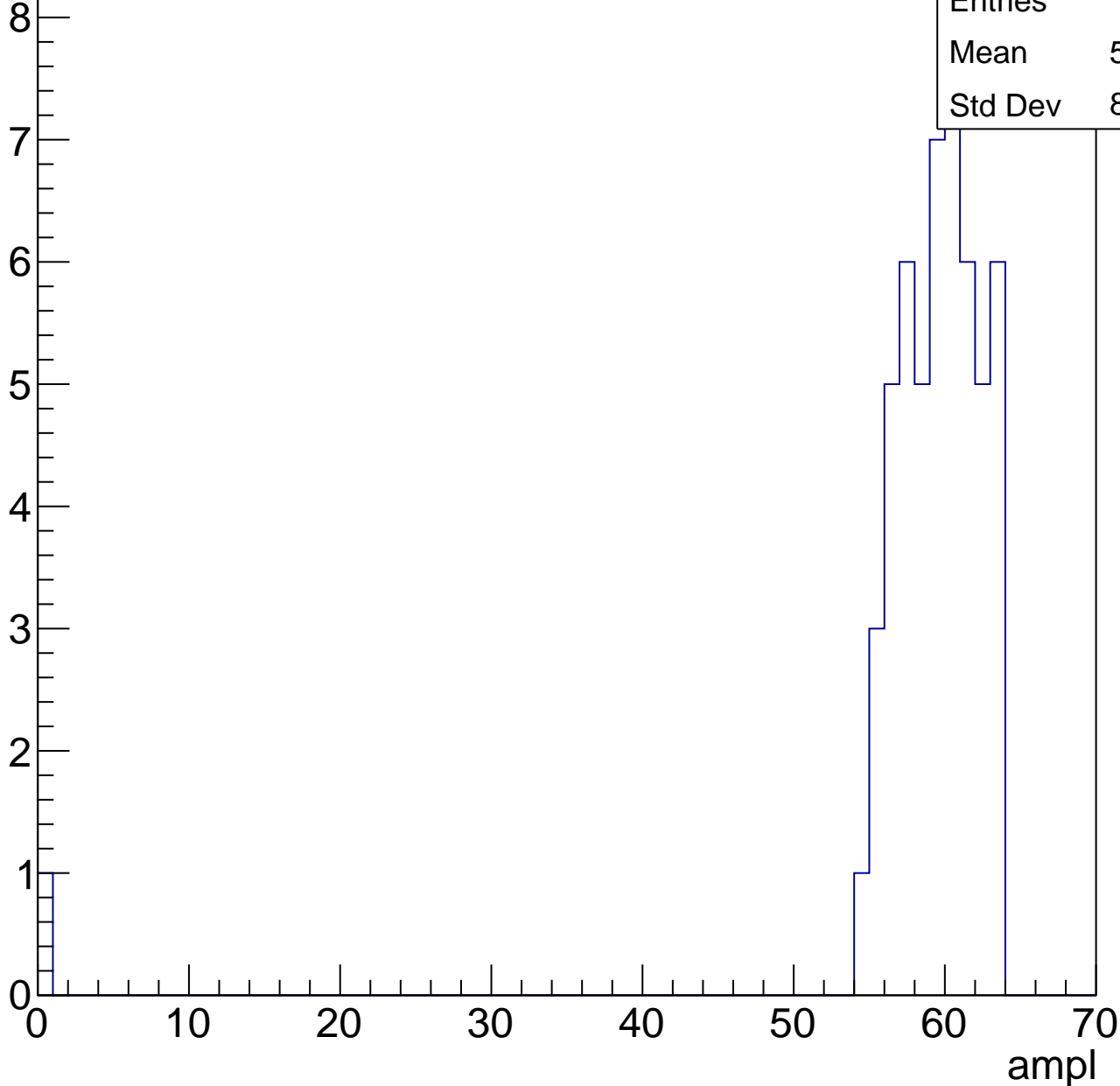


B1L101S, U6-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.08
Std Dev	8.418

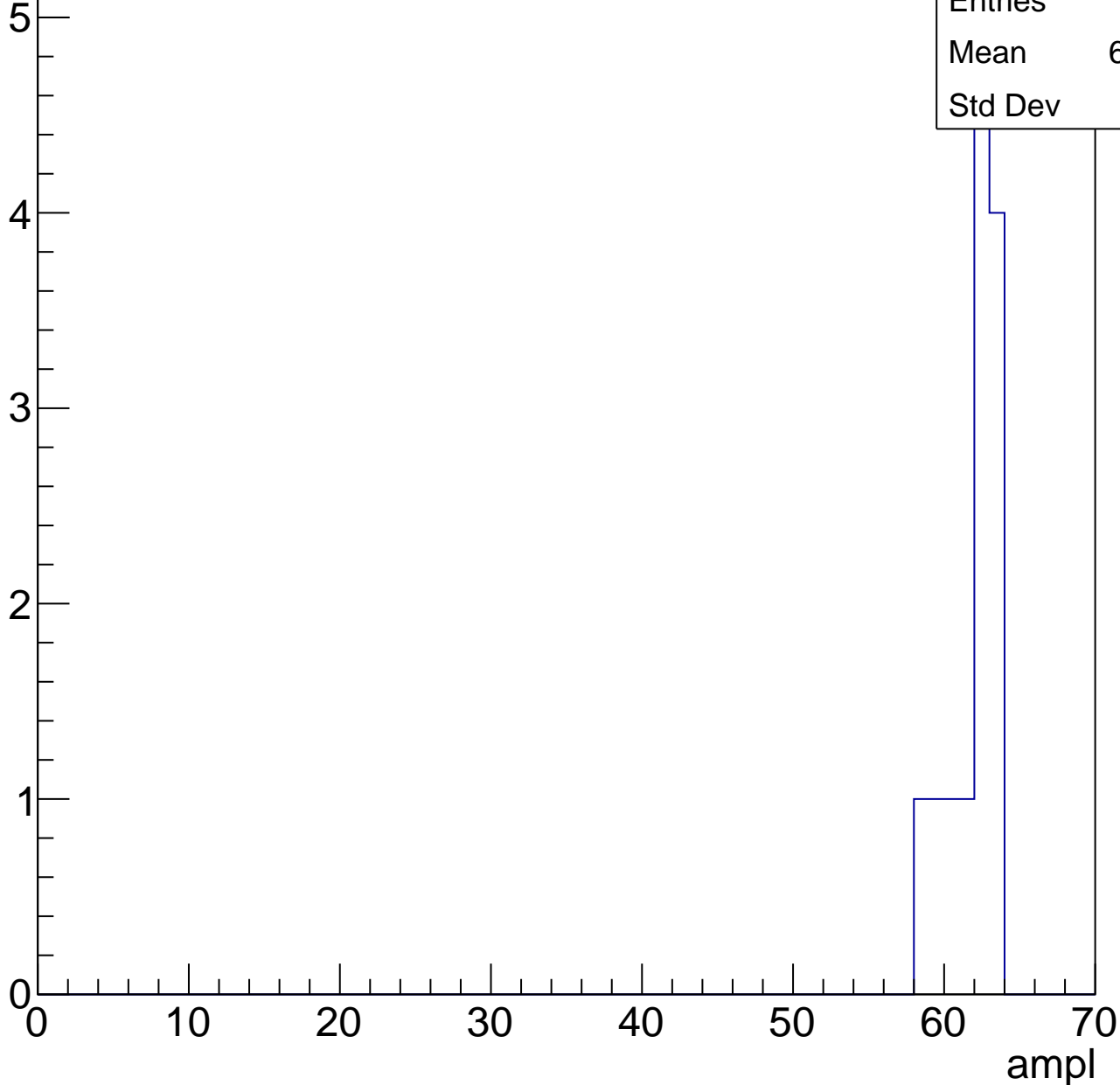


B1L101S, U6-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.54
Std Dev	1.55



B1L101S, U6-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch83, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	29.35
Std Dev	4.888

Gaus mean : 30.3298

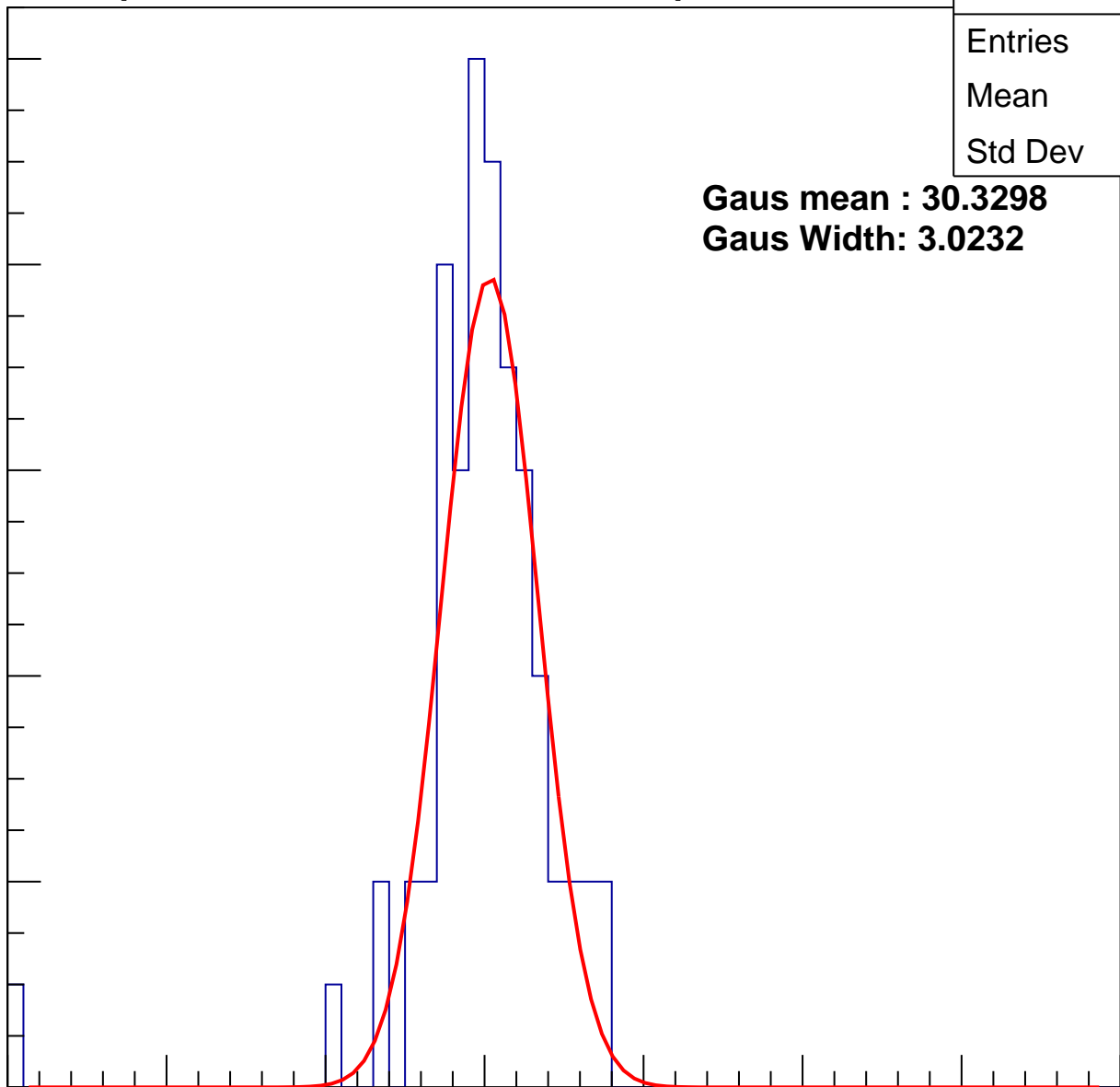
Gaus Width: 3.0232

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch83, adc1

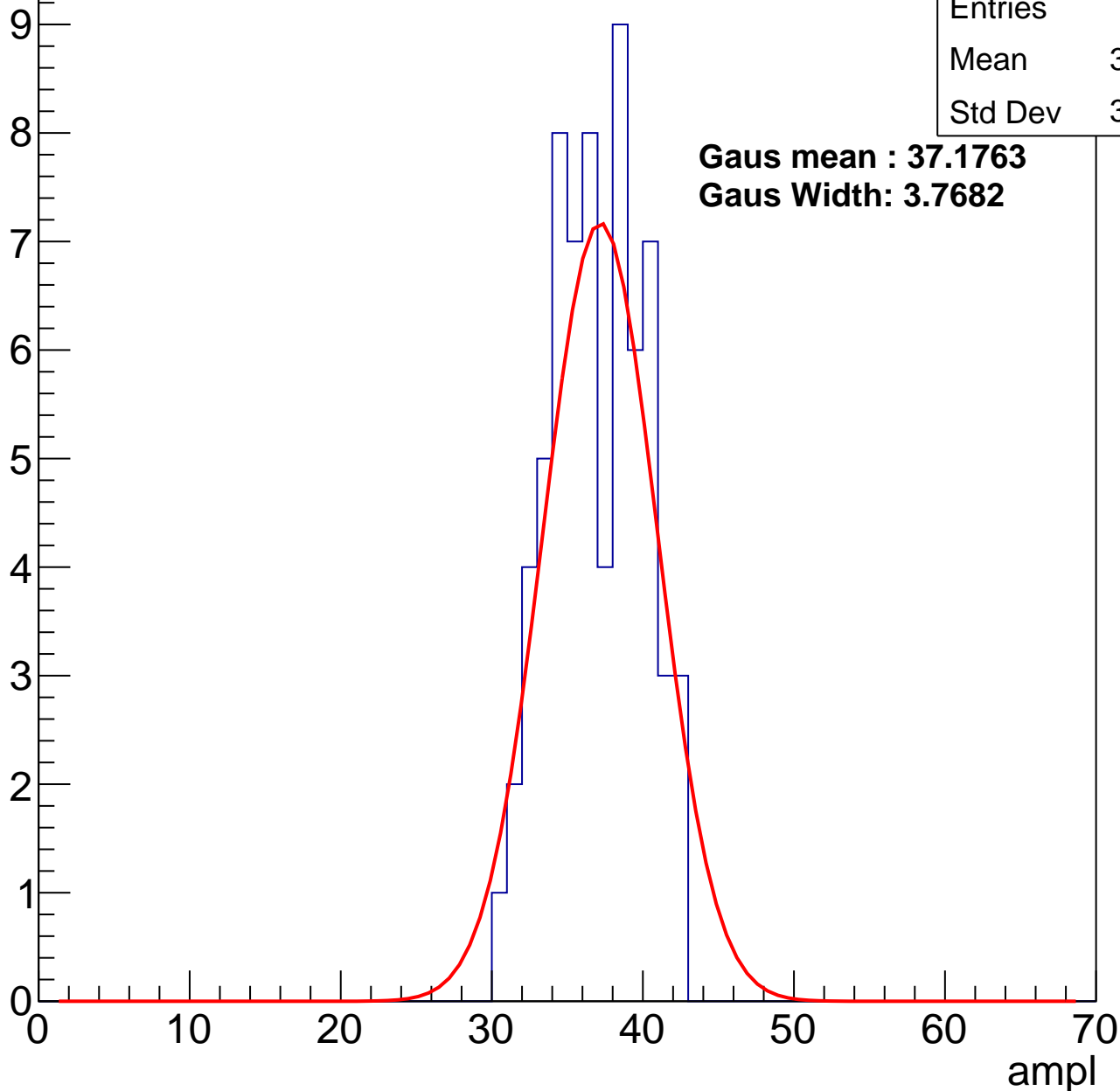
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.46
Std Dev	3.014

Gaus mean : 37.1763

Gaus Width: 3.7682



B1L101S, U6-ch83, adc2

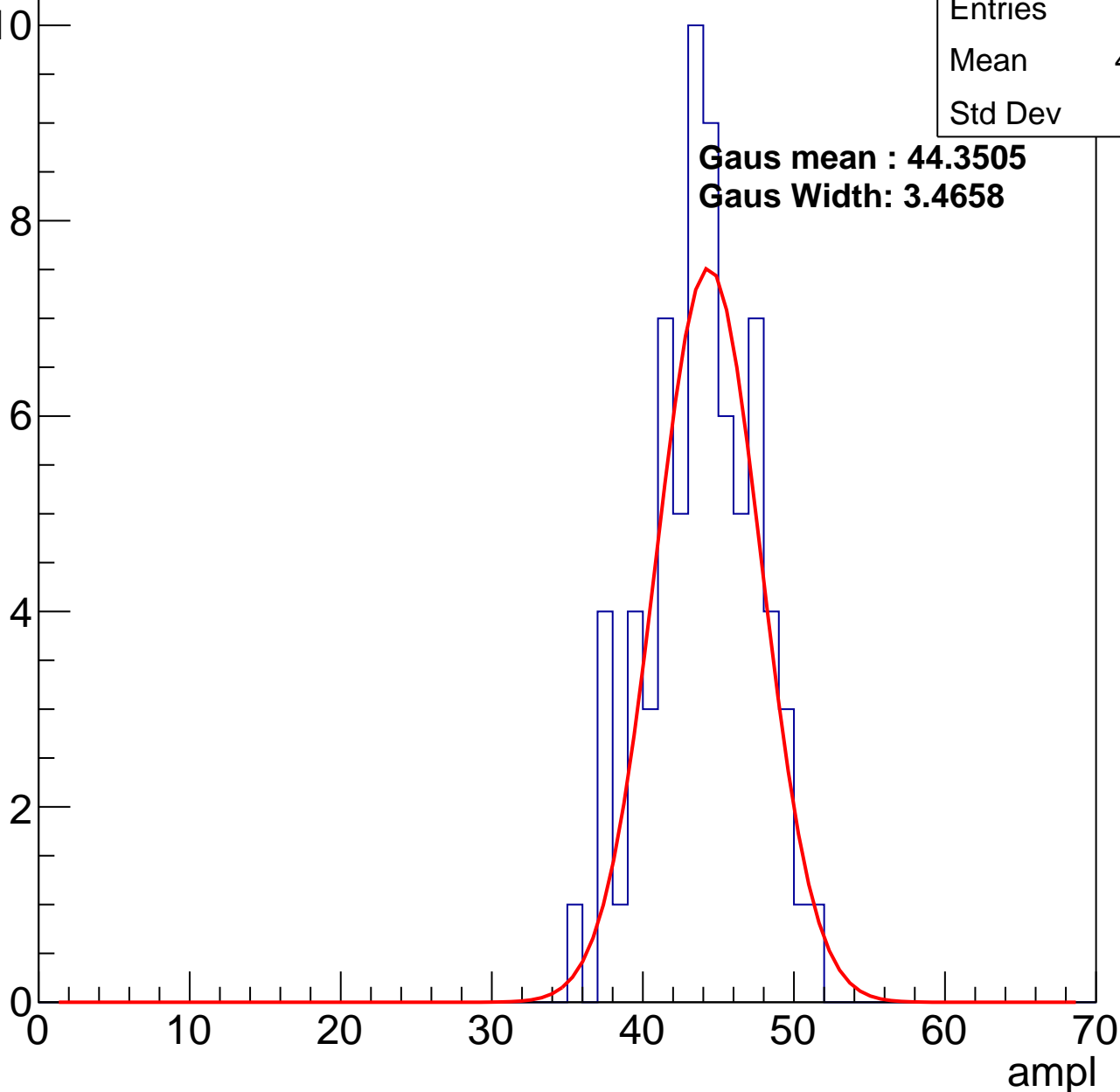
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.51
Std Dev	3.46

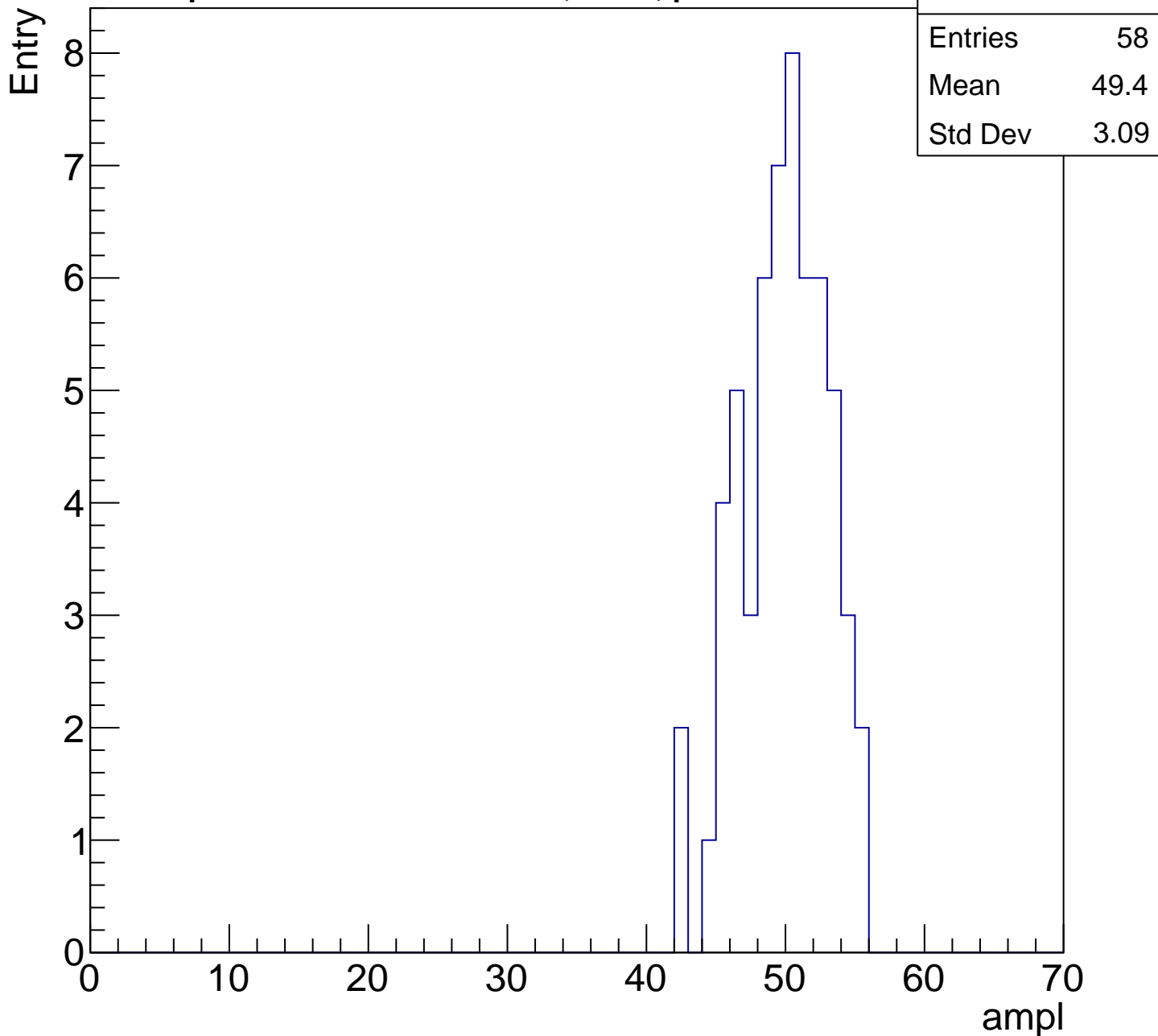
Gaus mean : 44.3505

Gaus Width: 3.4658



B1L101S, U6-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

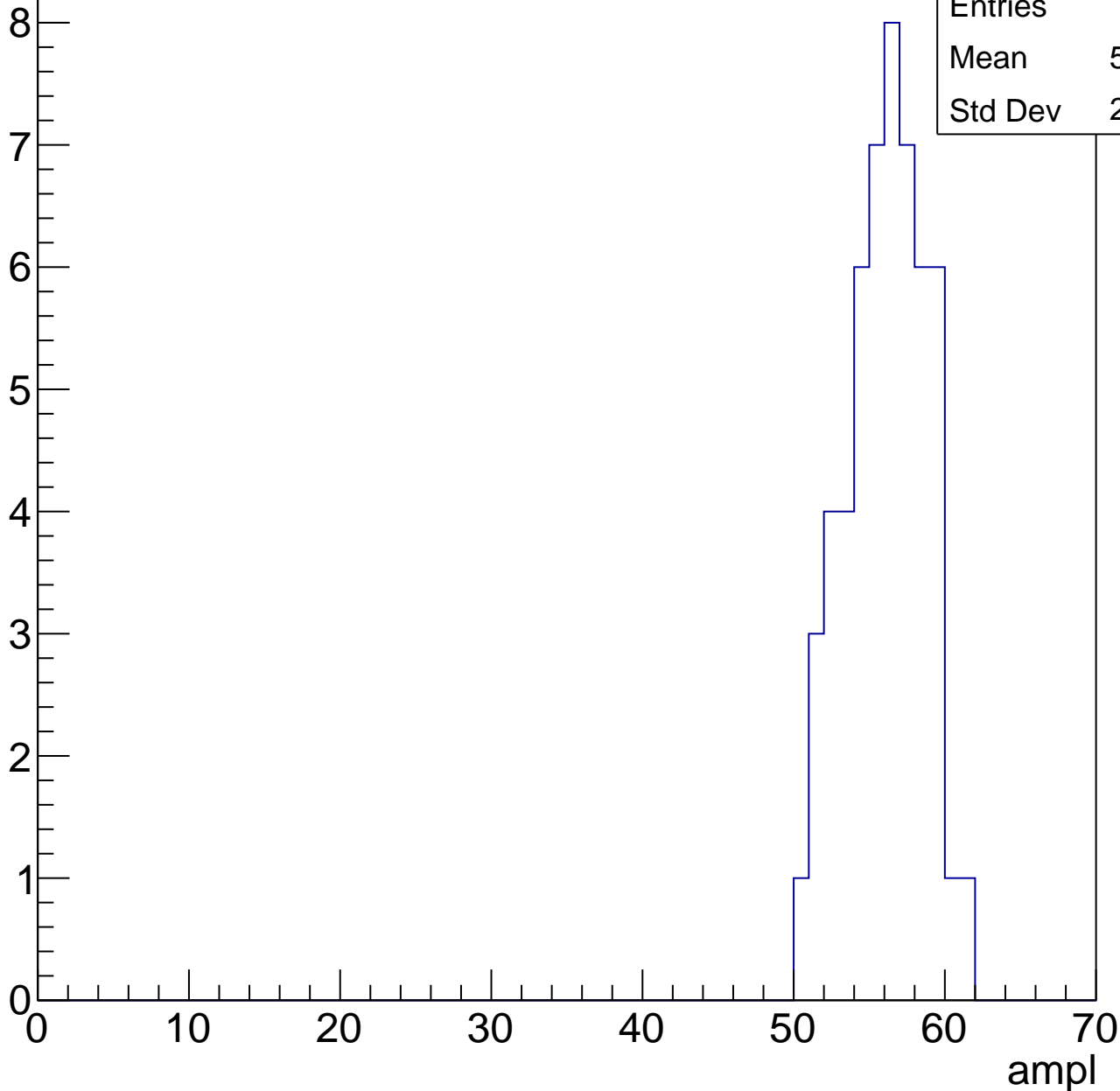


B1L101S, U6-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.59
Std Dev	2.579



B1L101S, U6-ch83, adc5

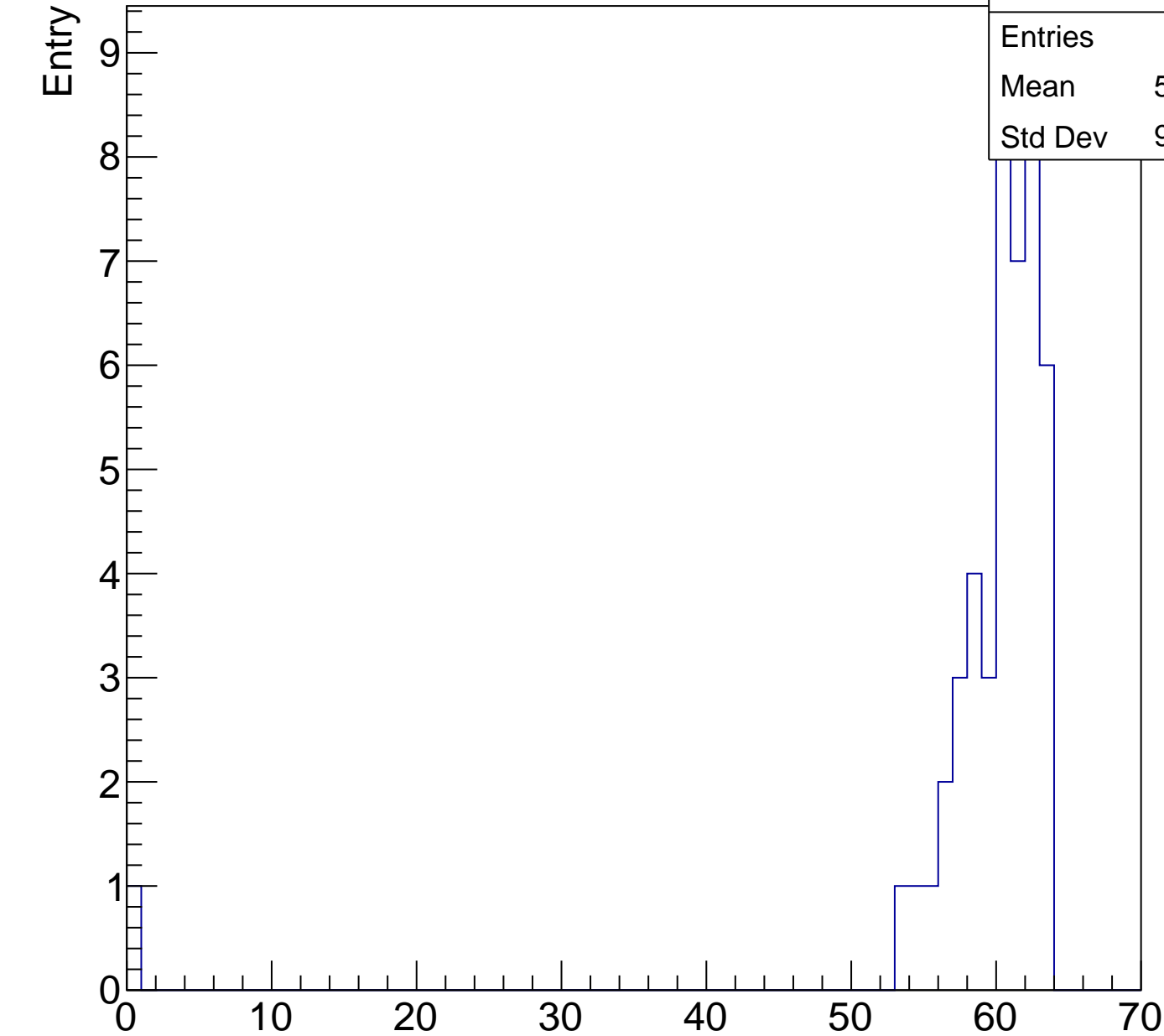
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.59
Std Dev	9.076

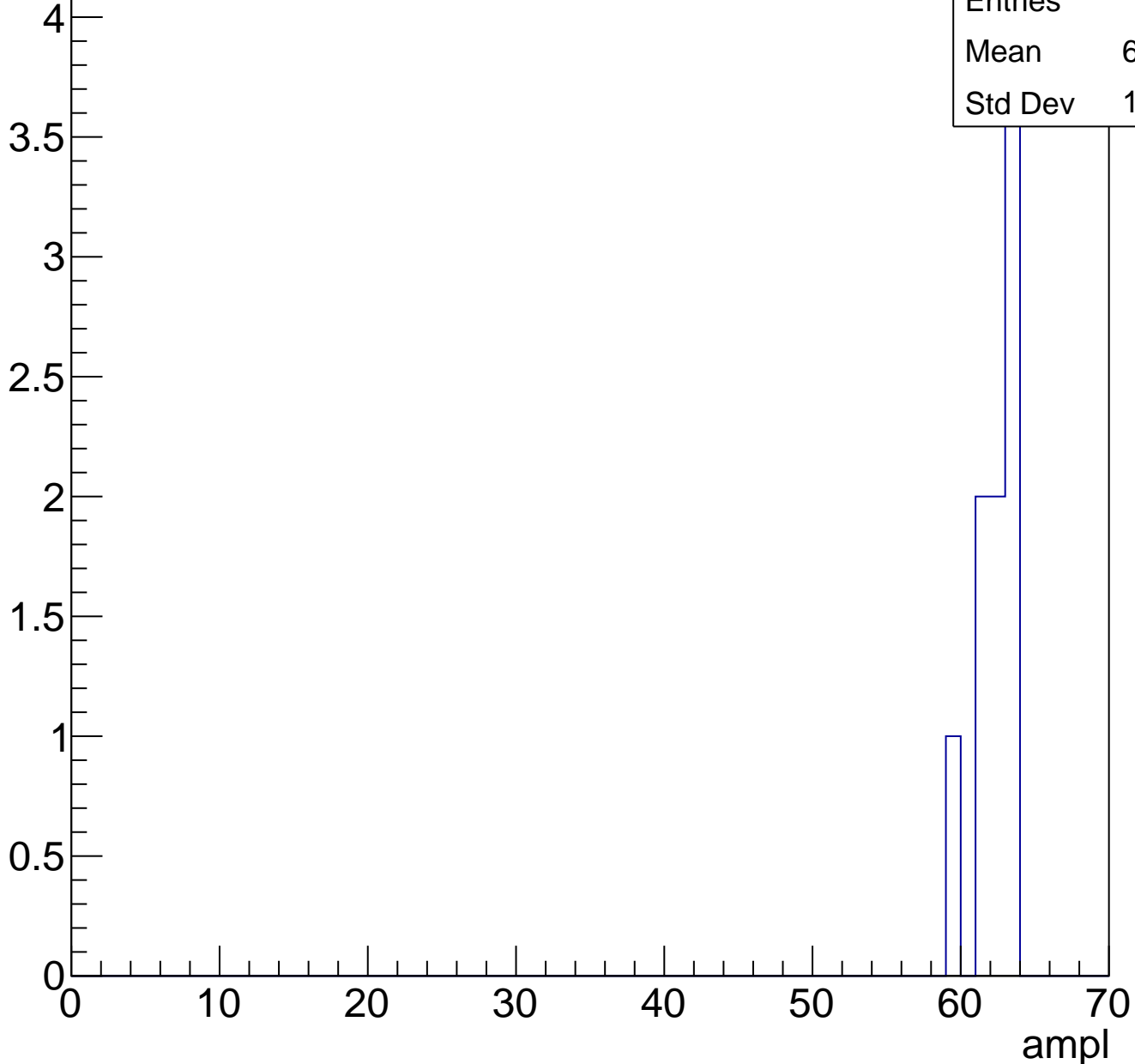
ampl



B1L101S, U6-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch84, adc0

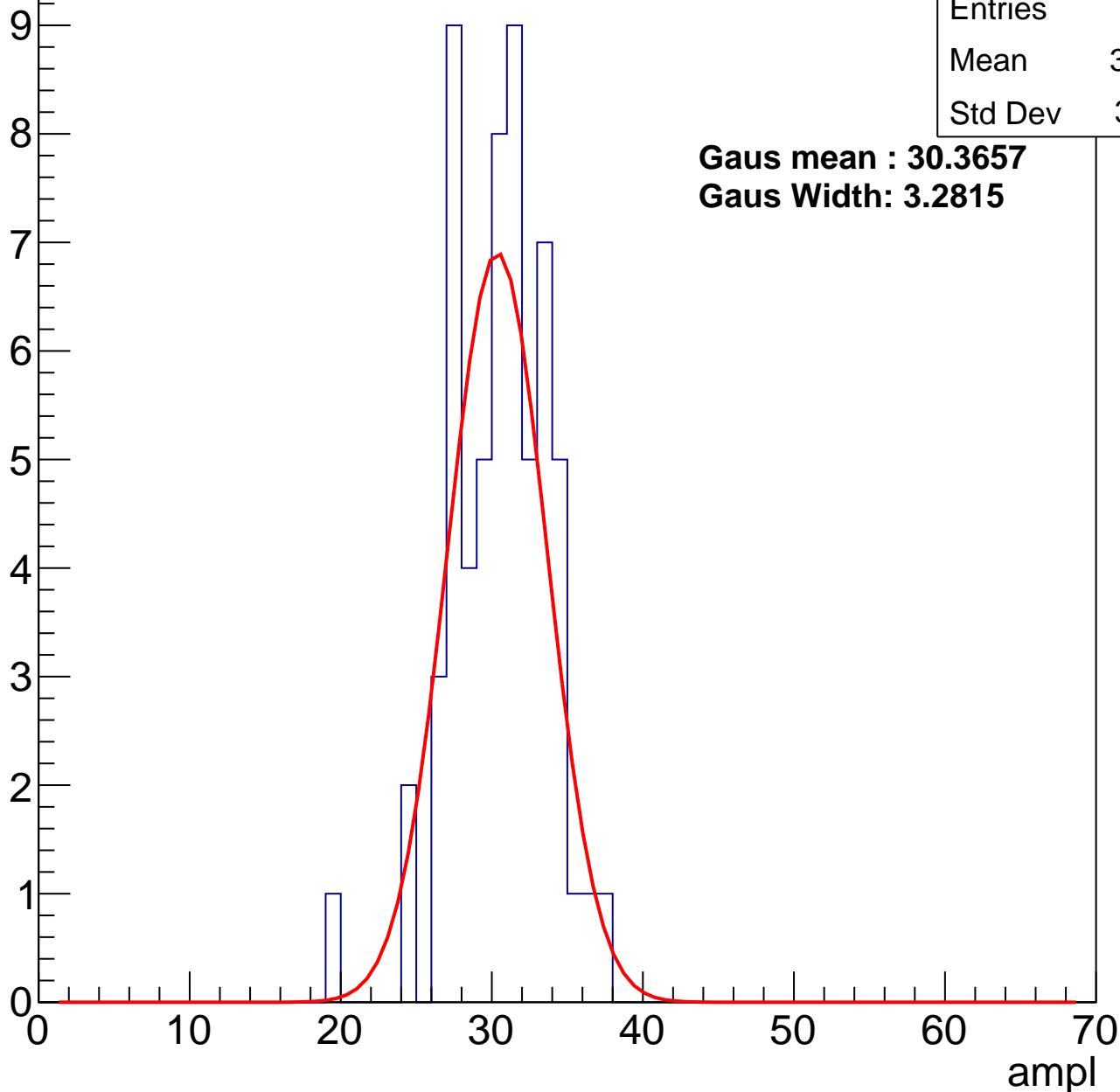
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	30.05
Std Dev	3.201

Gaus mean : 30.3657

Gaus Width: 3.2815



B1L101S, U6-ch84, adc1

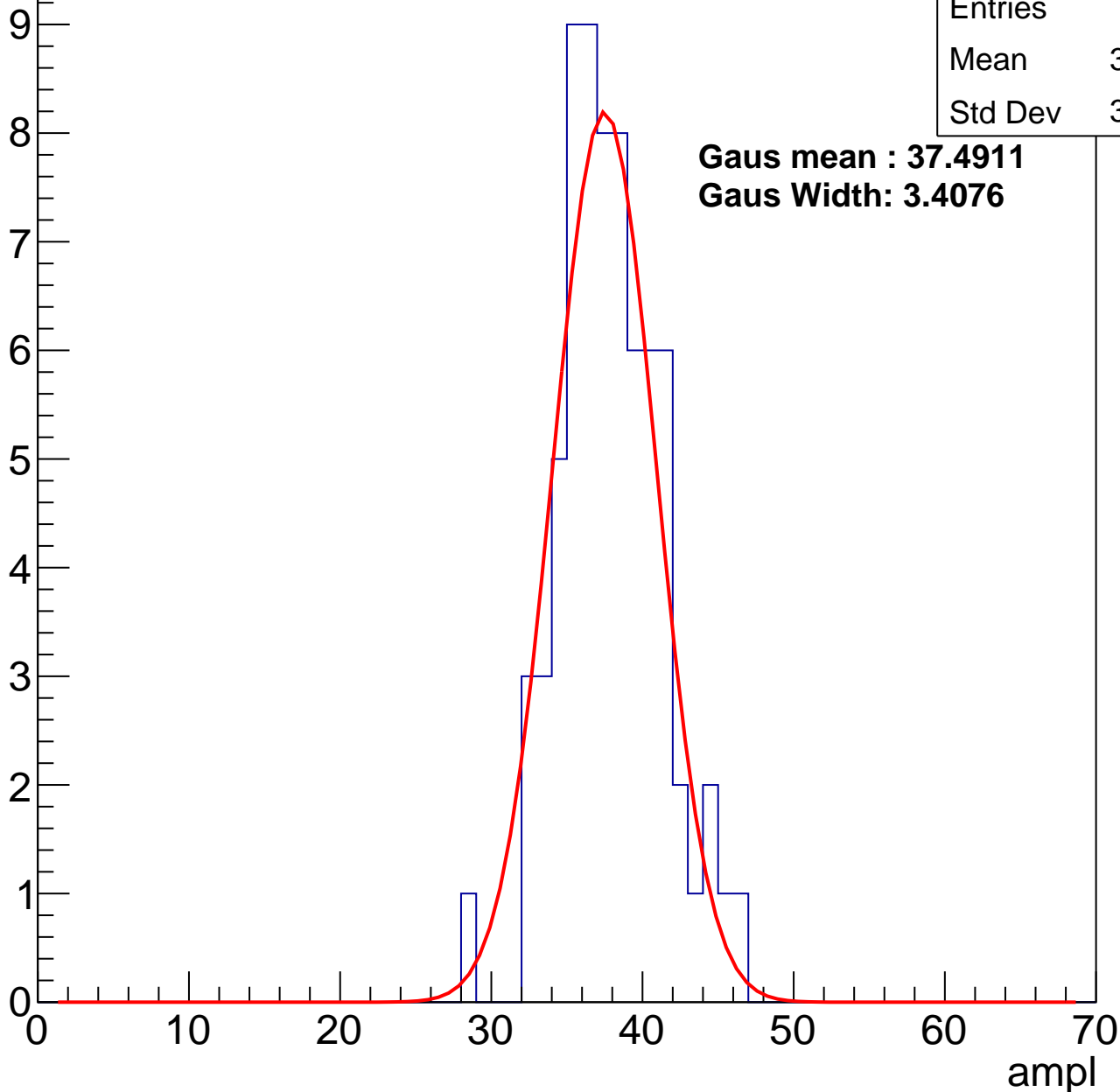
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.44
Std Dev	3.339

Gaus mean : 37.4911

Gaus Width: 3.4076



B1L101S, U6-ch84, adc2

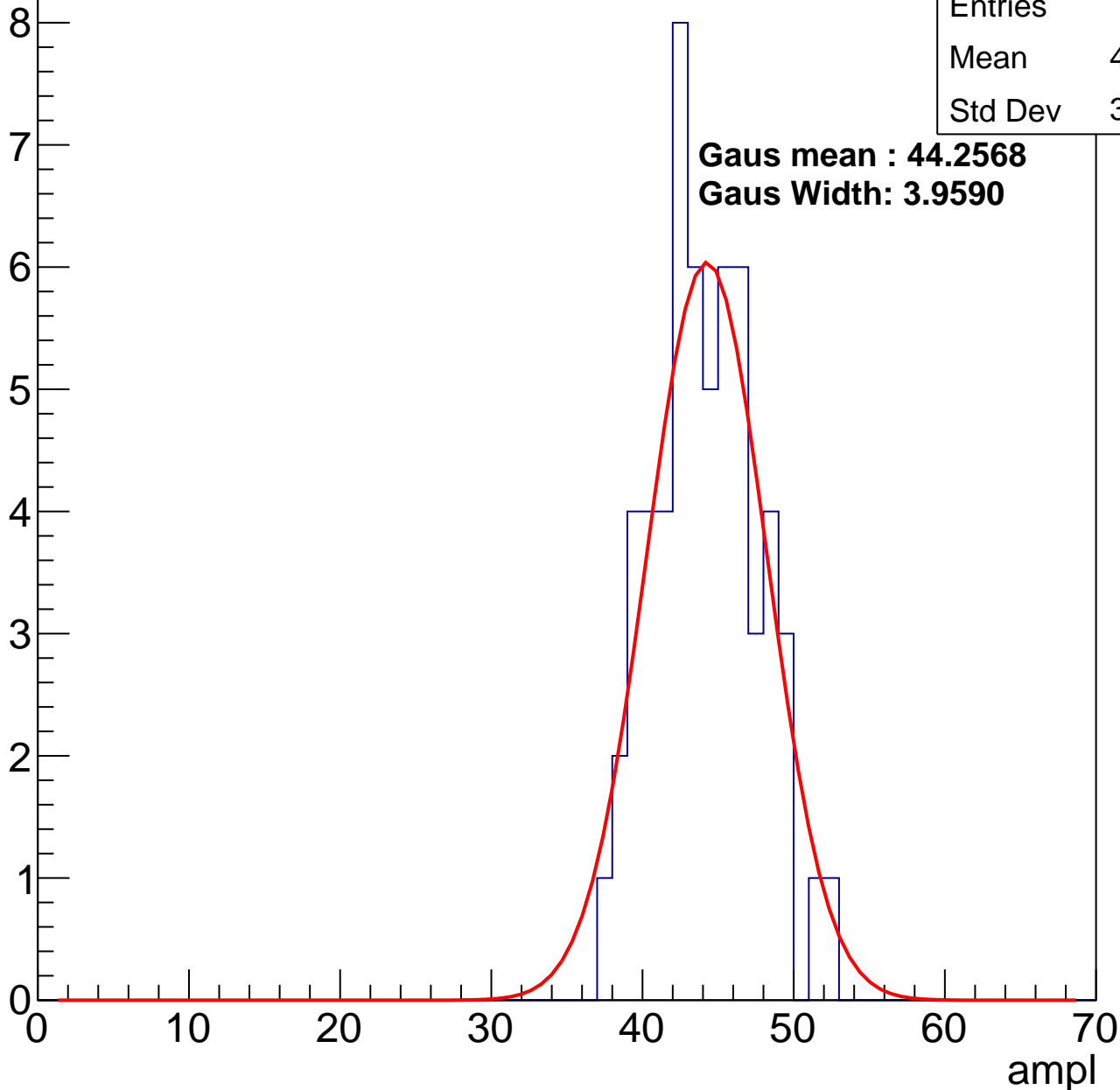
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.72
Std Dev	3.382

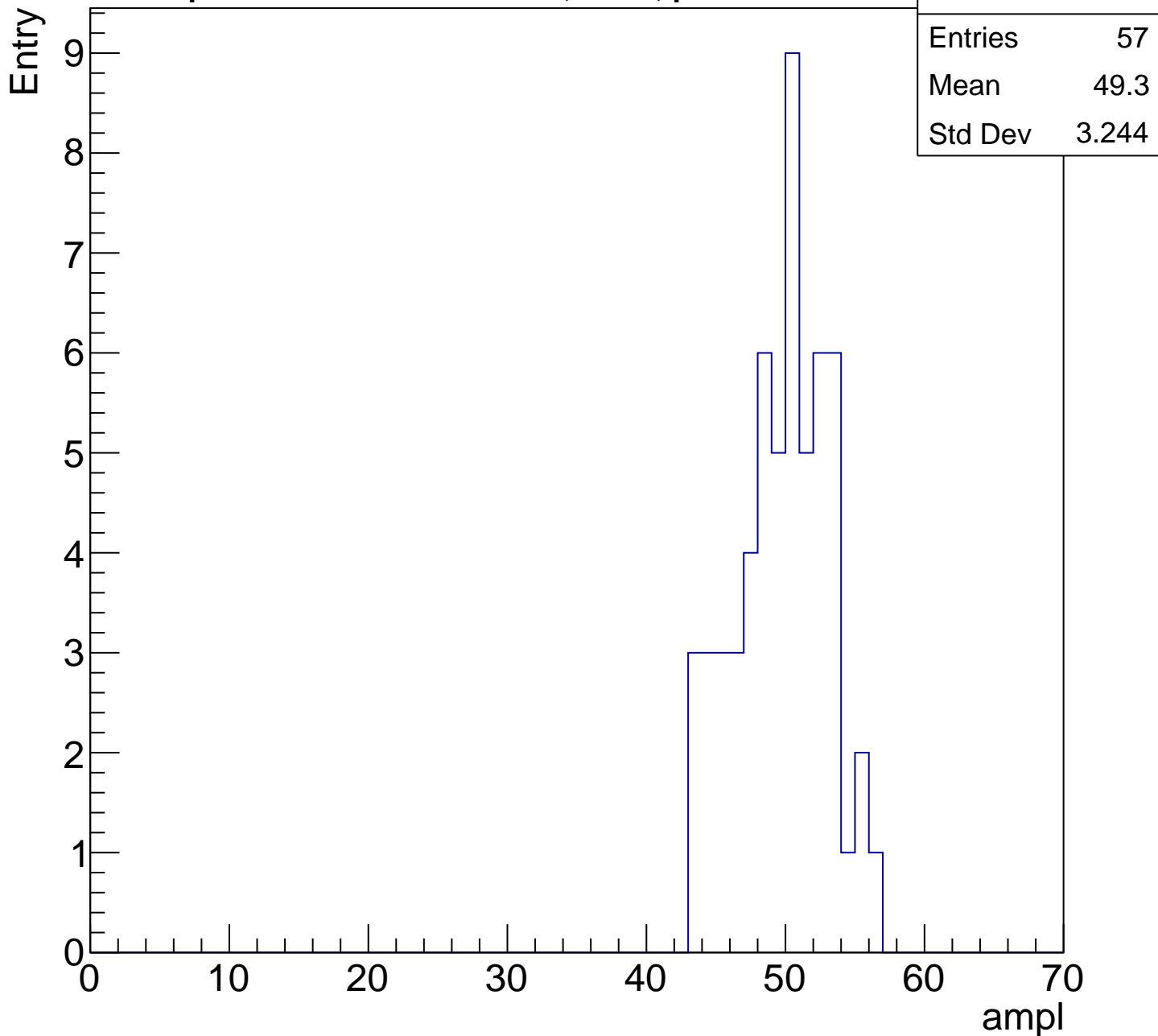
Gaus mean : 44.2568

Gaus Width: 3.9590



B1L101S, U6-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

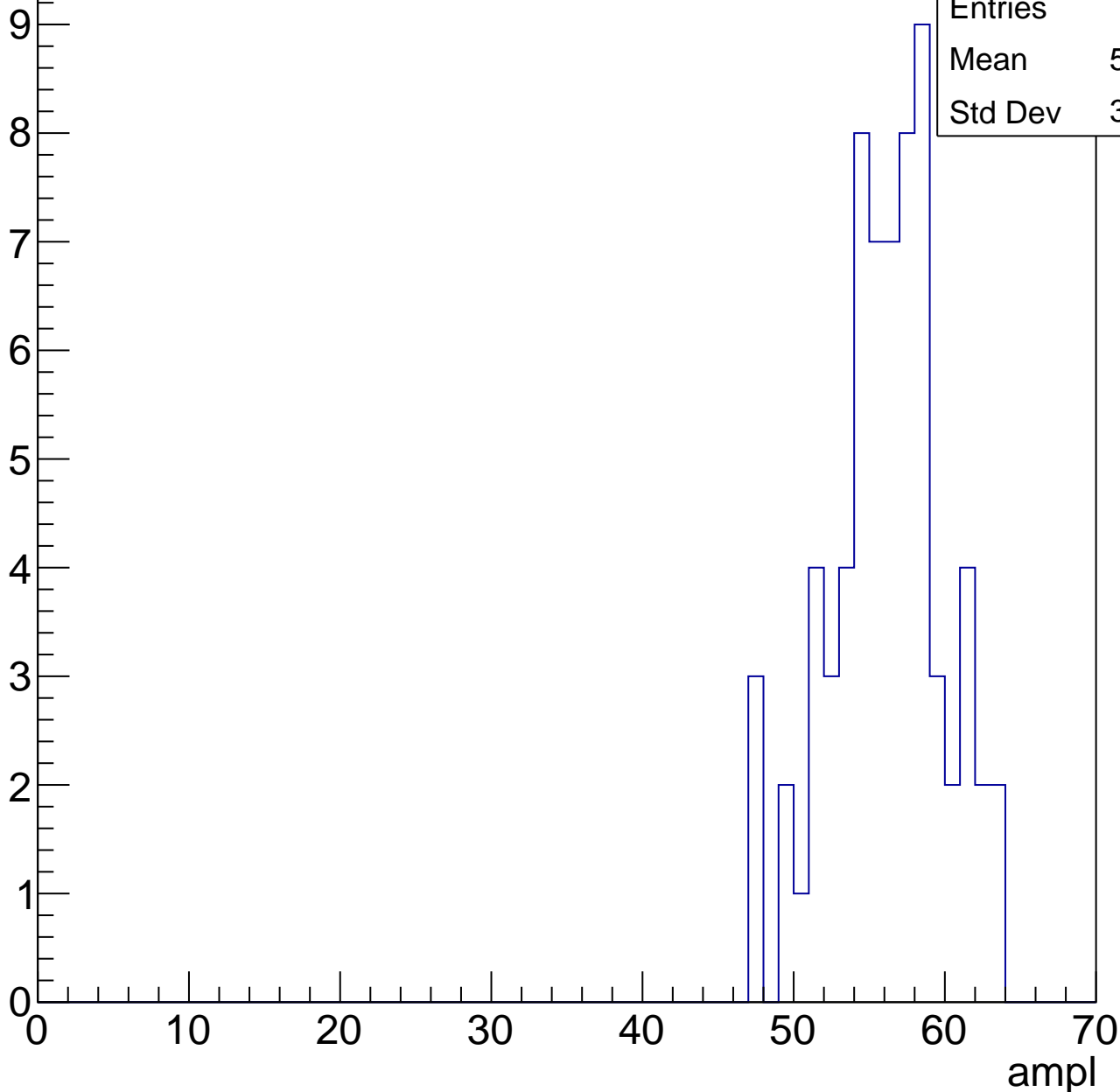


B1L101S, U6-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

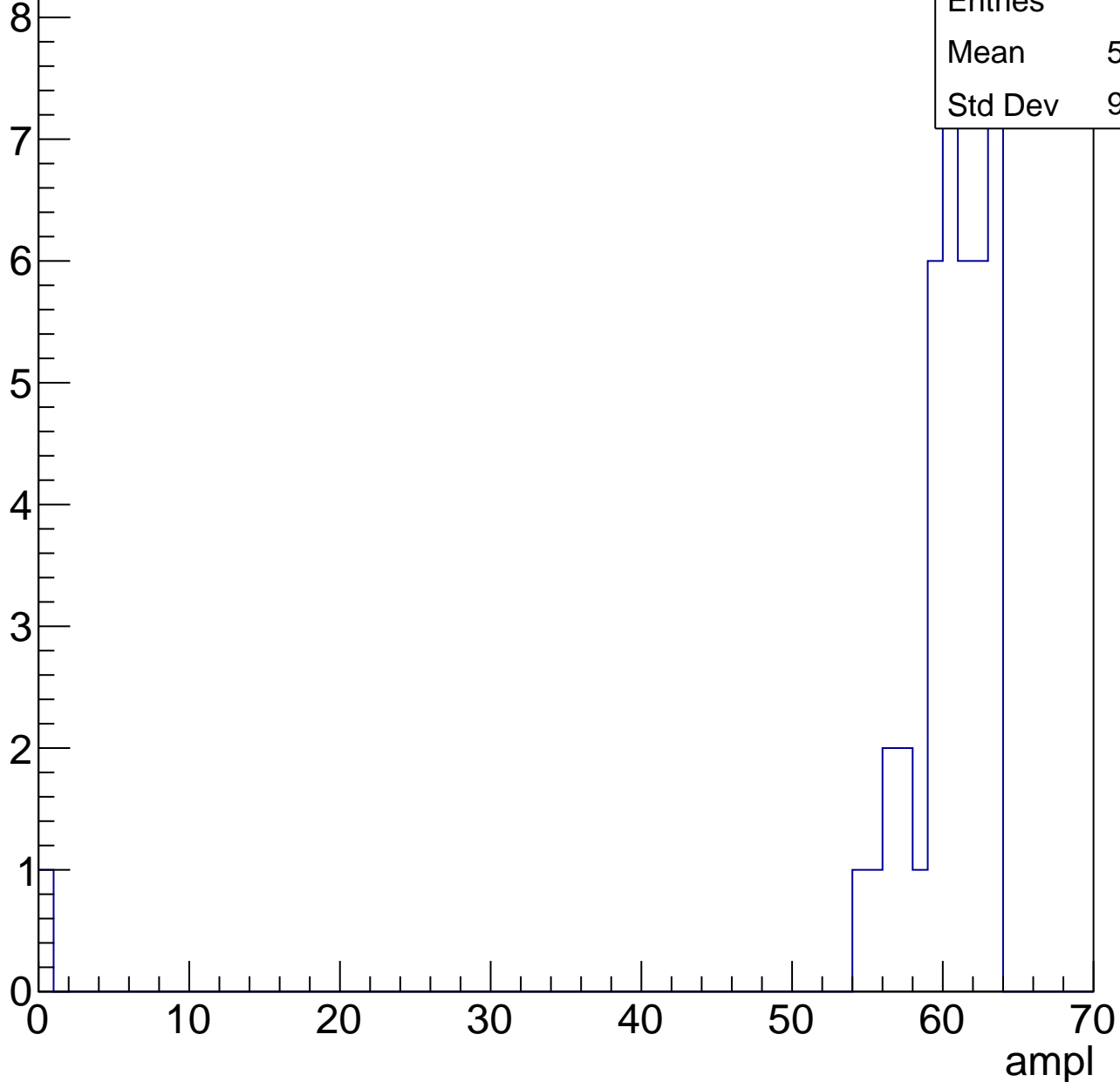
Entries	69
Mean	55.64
Std Dev	3.738



B1L101S, U6-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

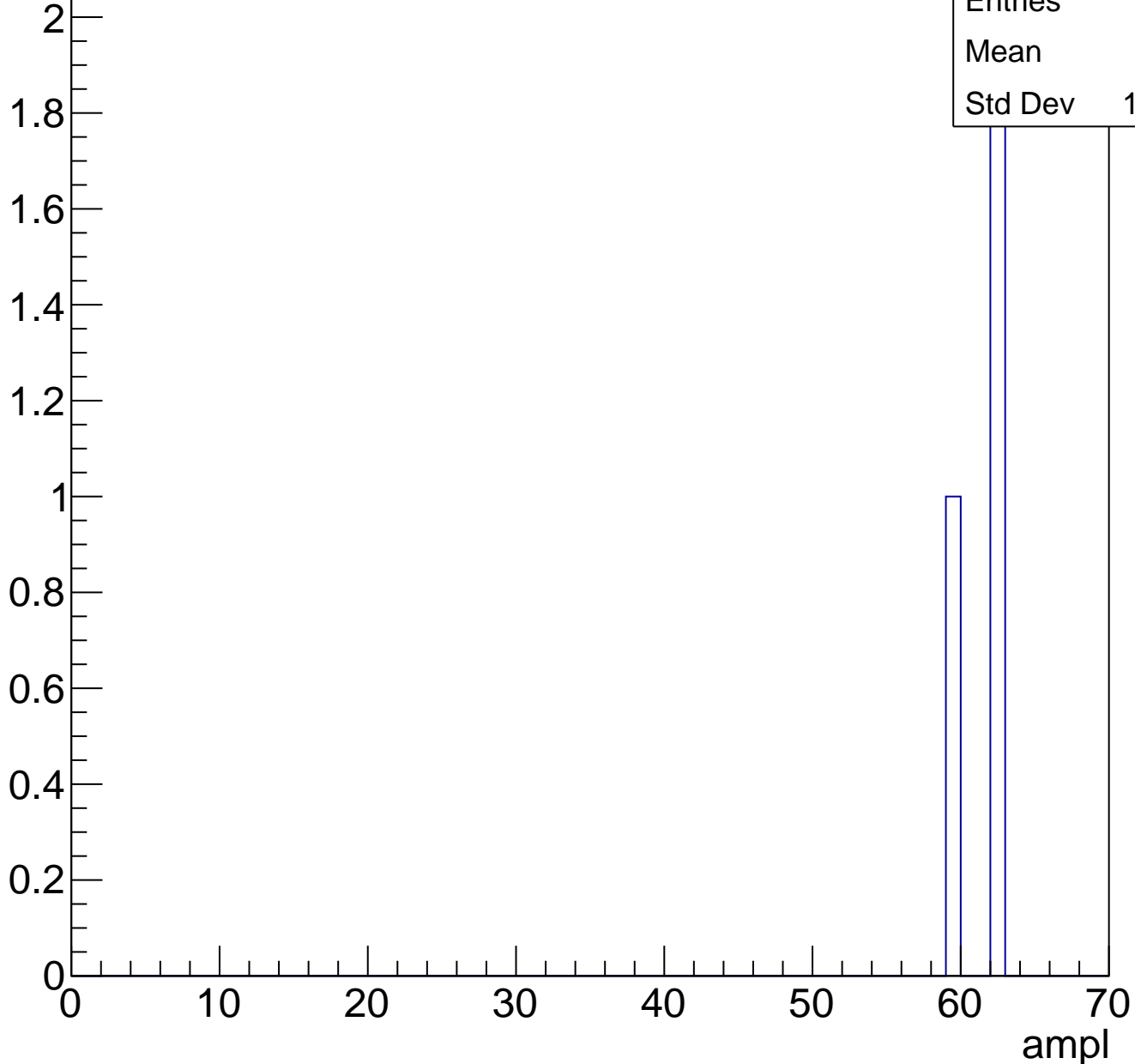
Entry



B1L101S, U6-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch85, adc0

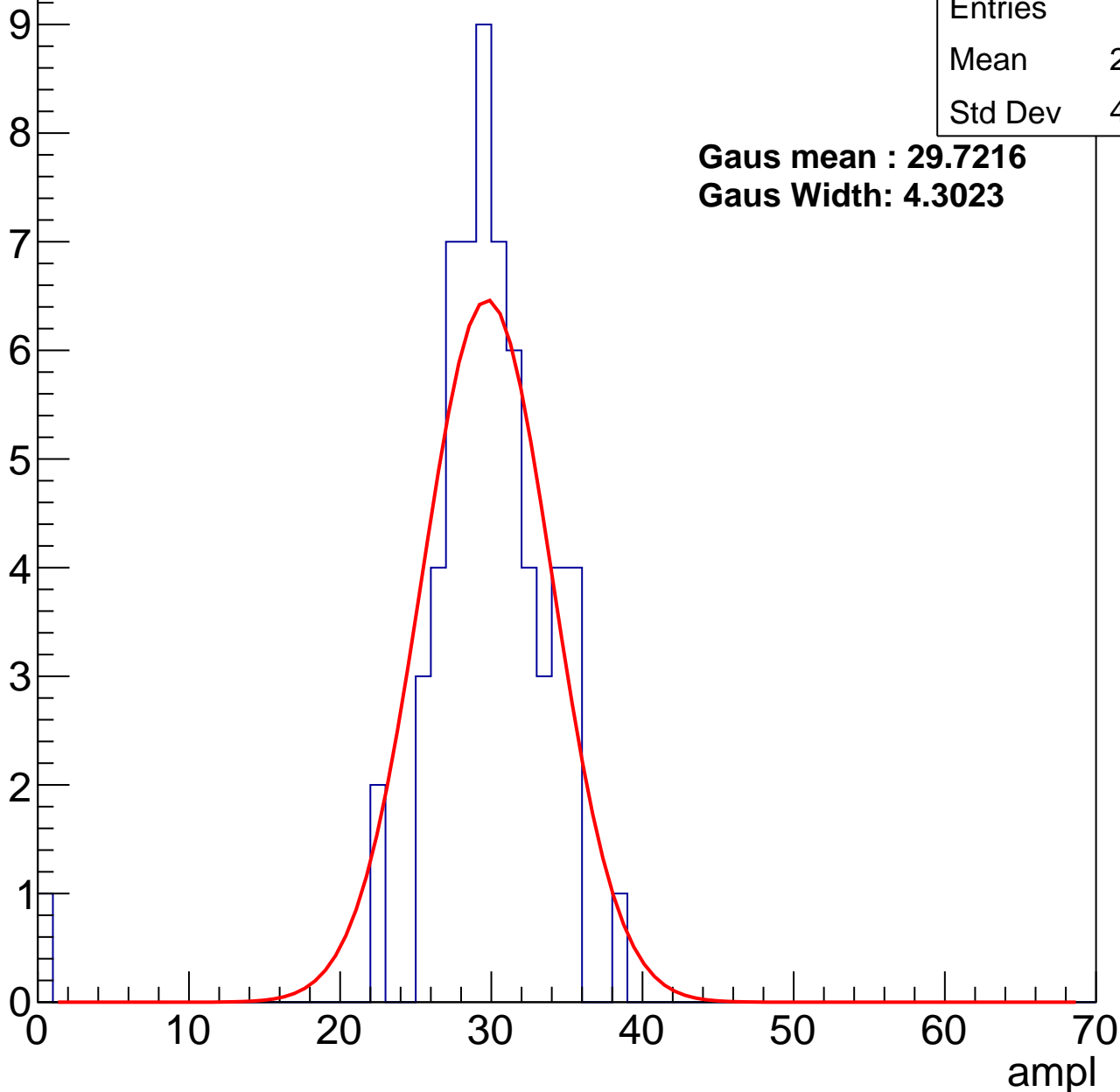
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.13
Std Dev	4.914

Gaus mean : 29.7216

Gaus Width: 4.3023



B1L101S, U6-ch85, adc1

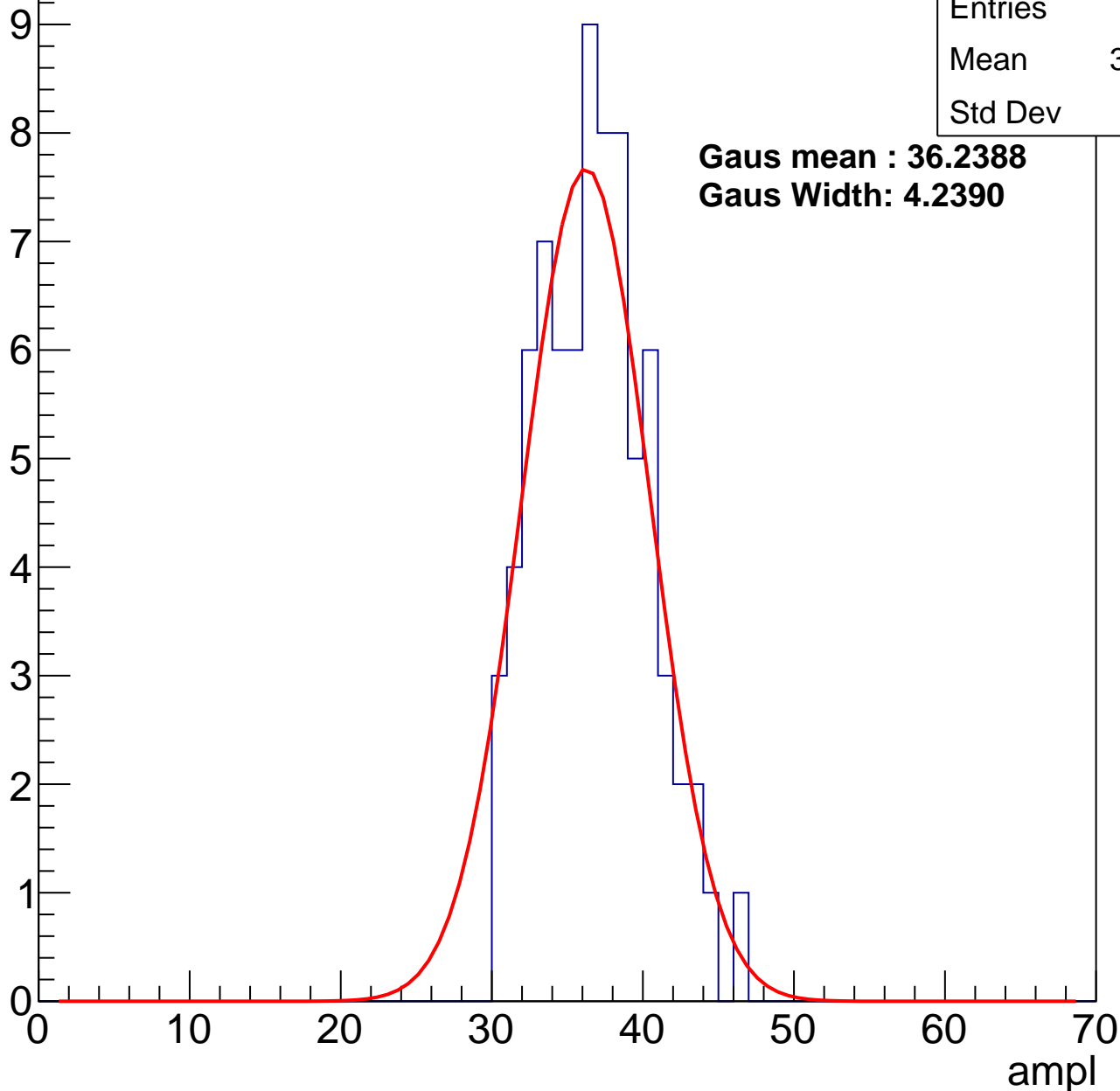
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.27
Std Dev	3.57

Gaus mean : 36.2388

Gaus Width: 4.2390



B1L101S, U6-ch85, adc2

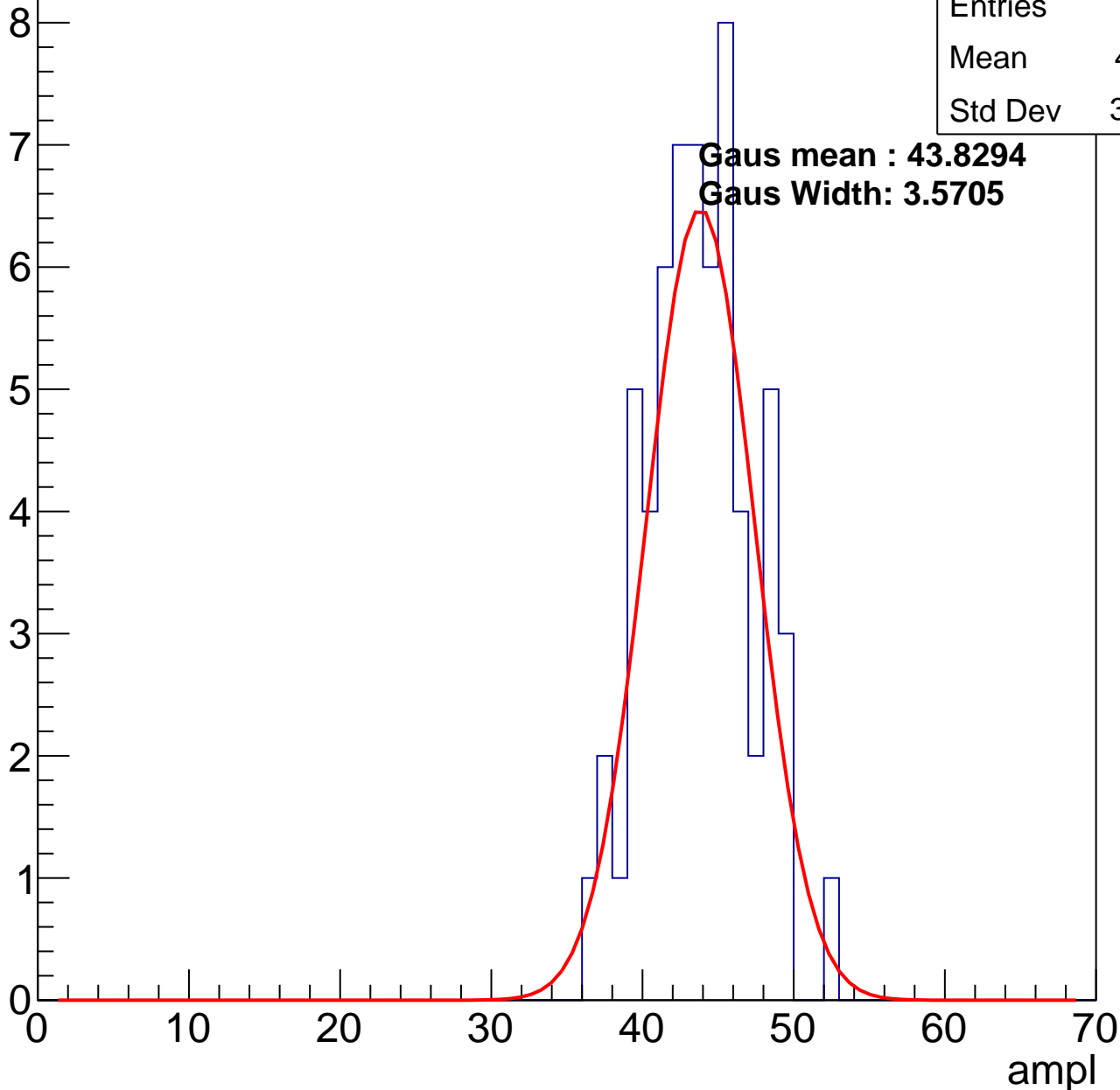
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.31
Std Dev	3.377

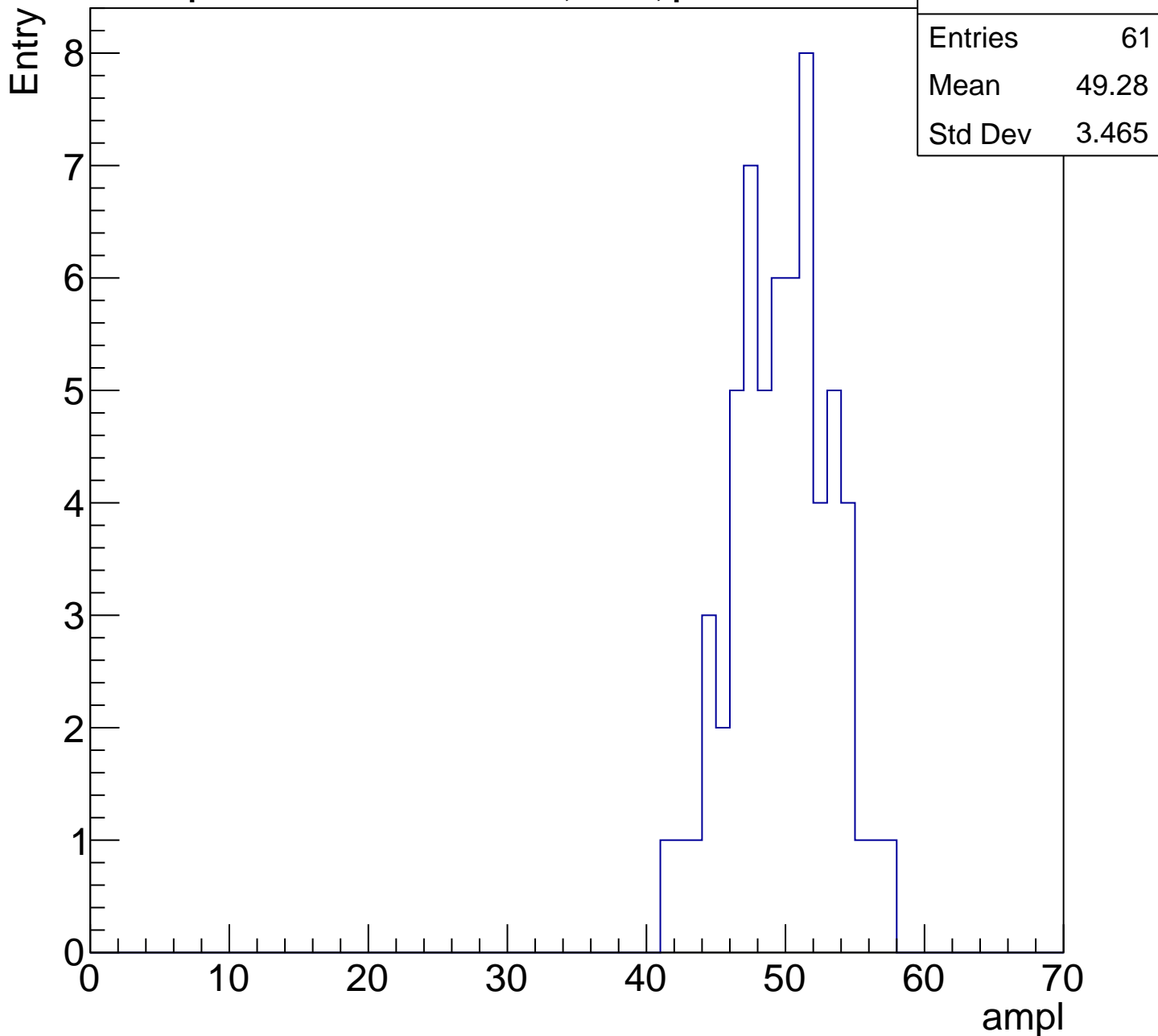
Gaus mean : 43.8294

Gaus Width: 3.5705



B1L101S, U6-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

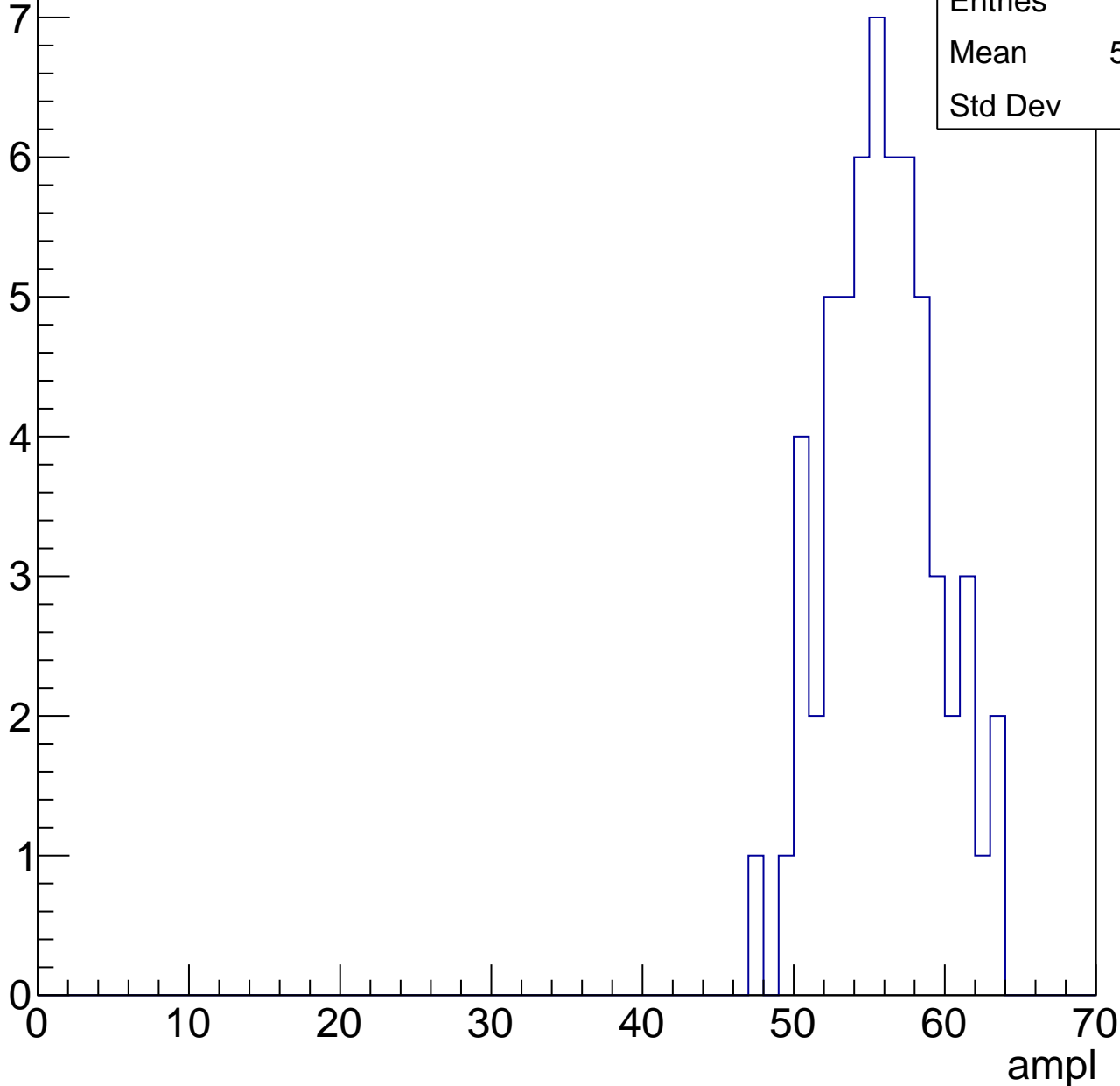


B1L101S, U6-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

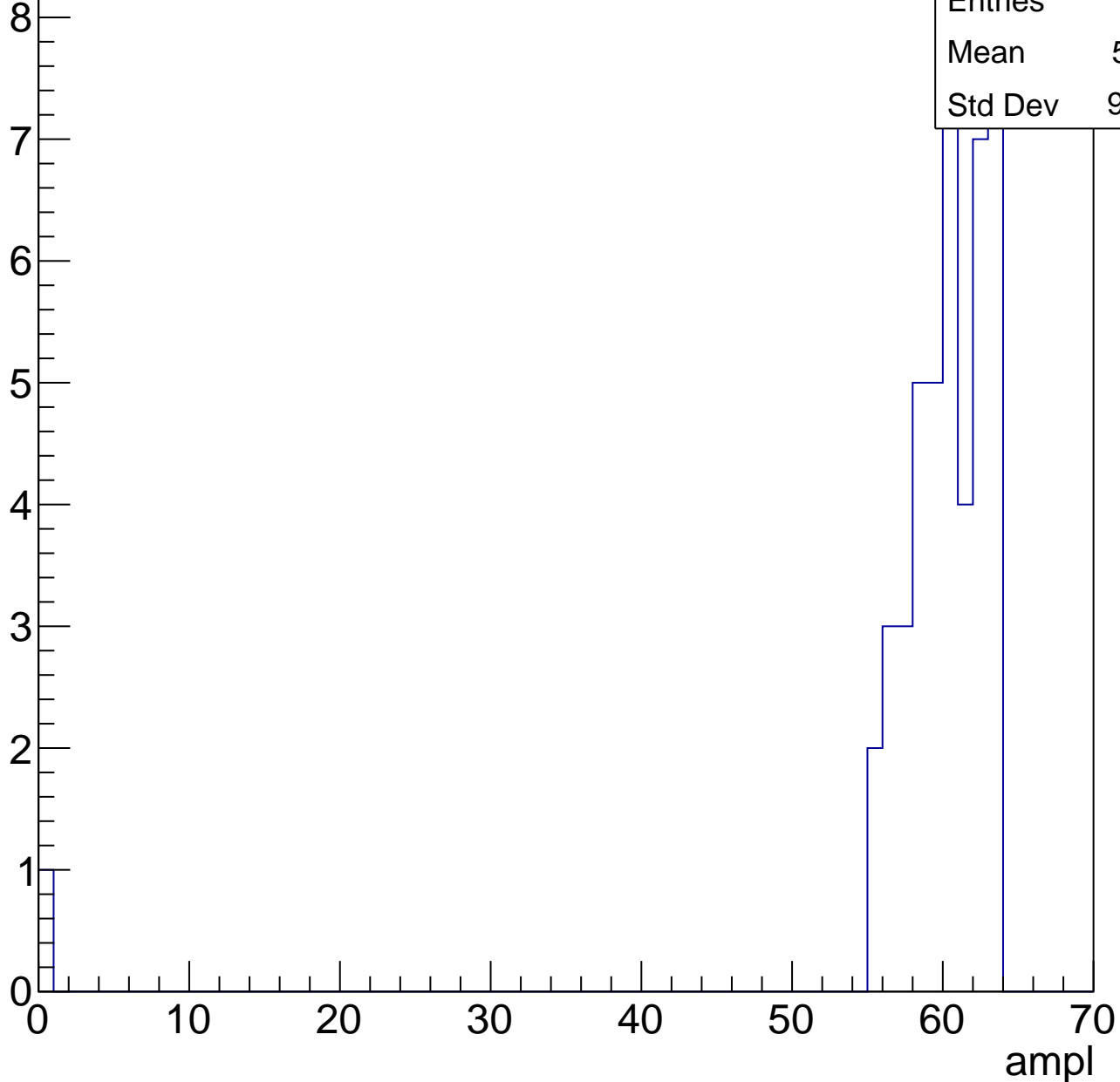
Entries	59
Mean	55.39
Std Dev	3.57



B1L101S, U6-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

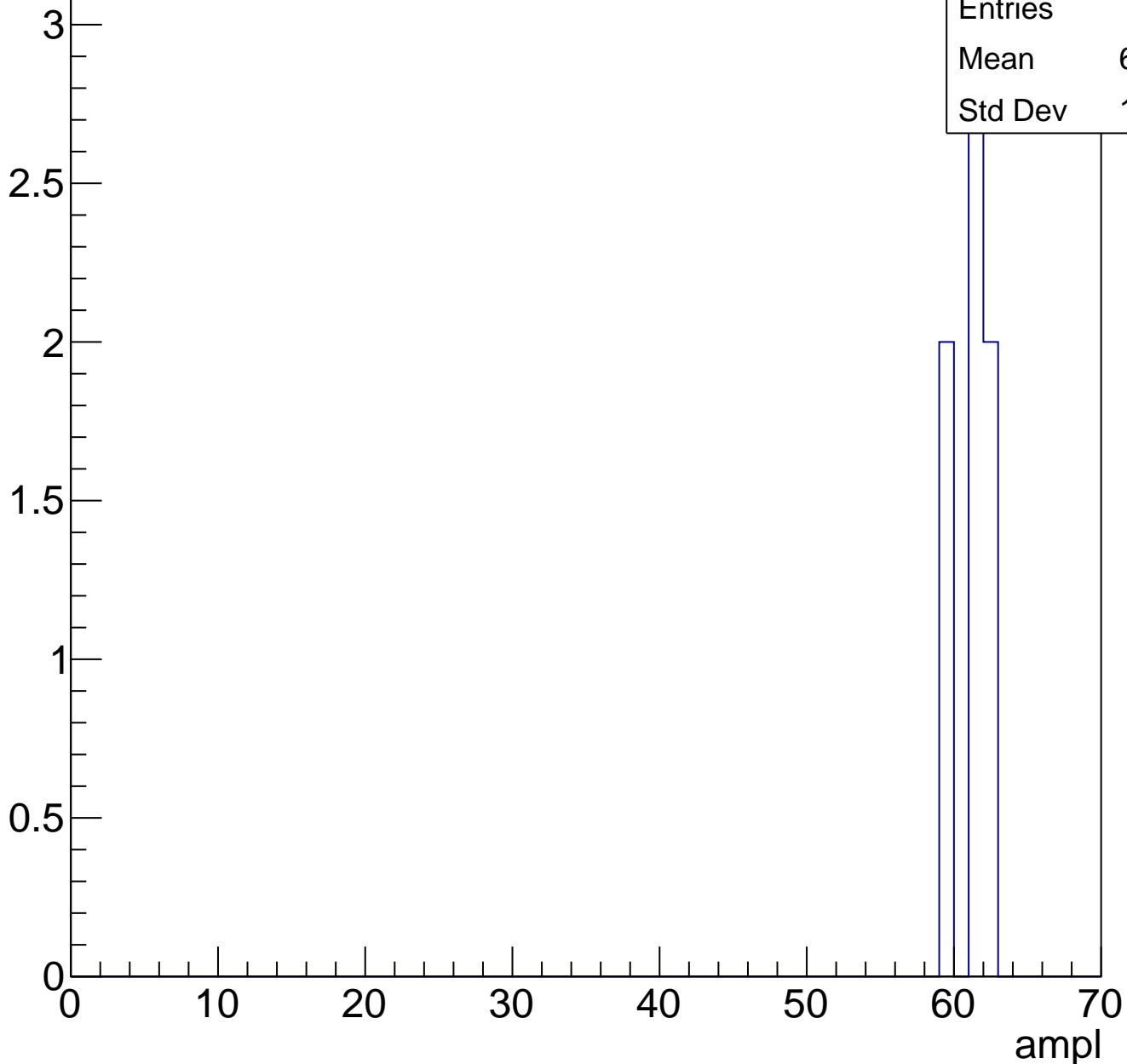
Entry



B1L101S, U6-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch86, adc0

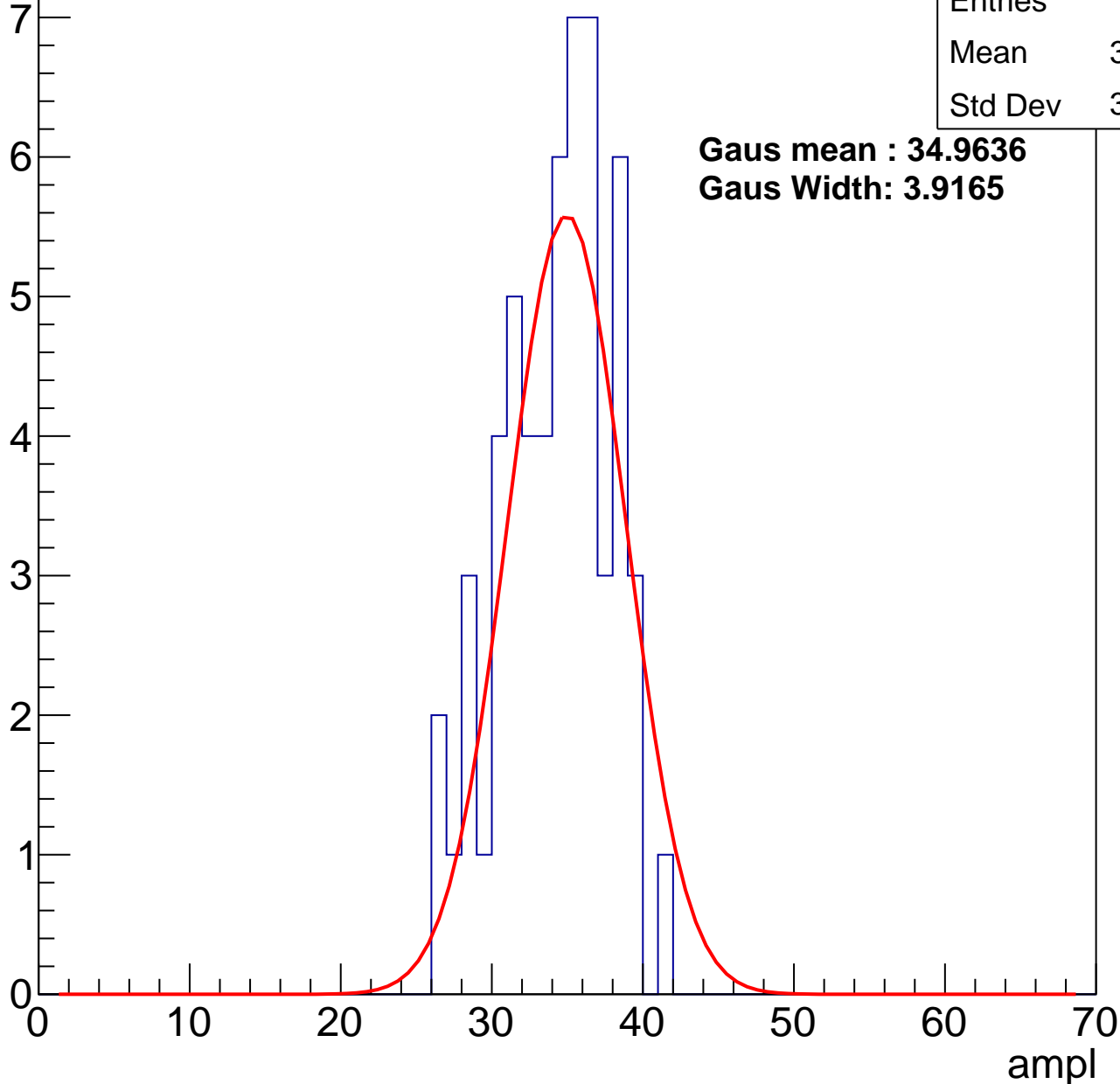
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	33.77
Std Dev	3.549

Gaus mean : 34.9636

Gaus Width: 3.9165



B1L101S, U6-ch86, adc1

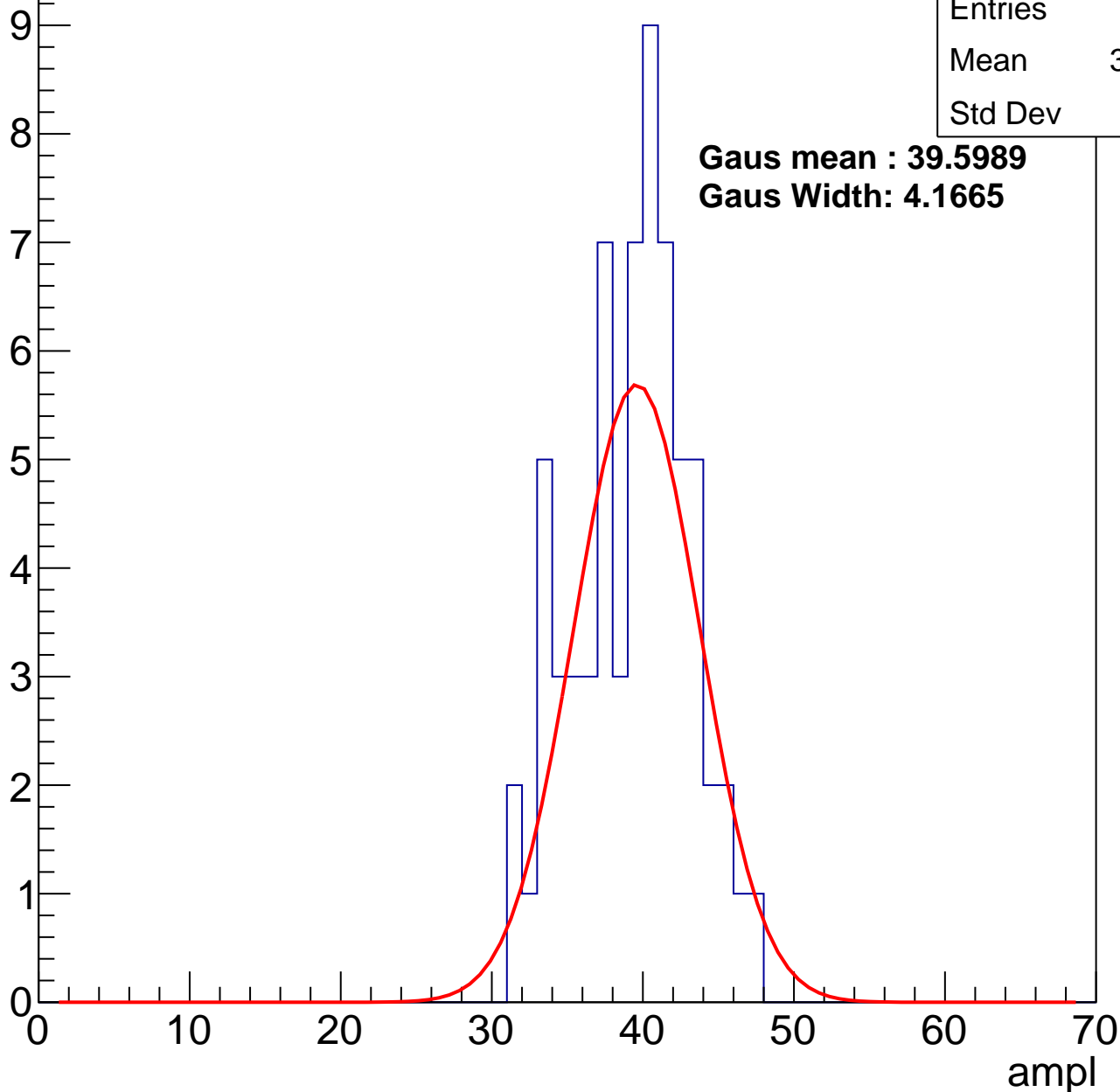
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	38.83
Std Dev	3.76

Gaus mean : 39.5989

Gaus Width: 4.1665



B1L101S, U6-ch86, adc2

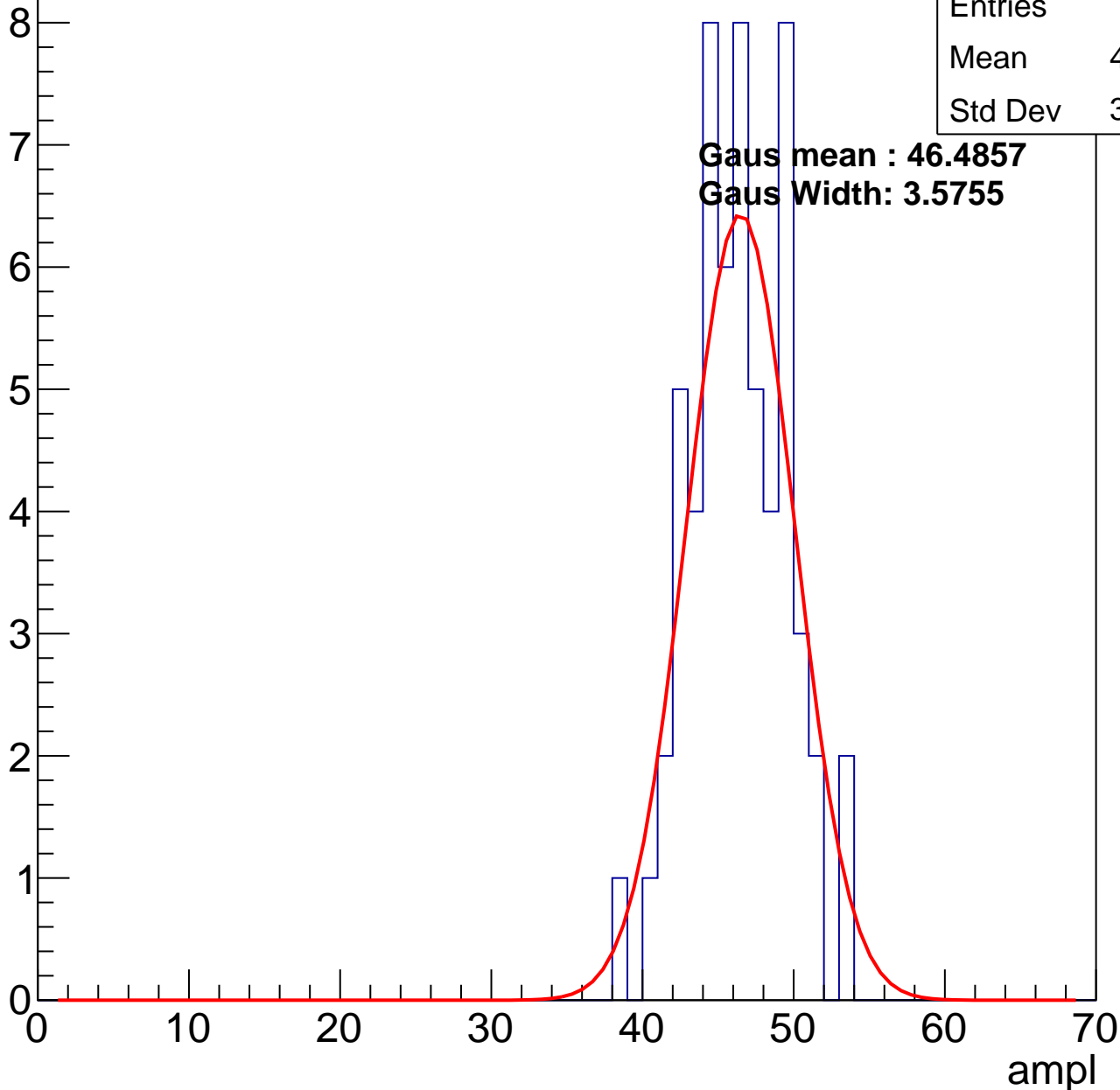
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	45.92
Std Dev	3.169

Gaus mean : 46.4857

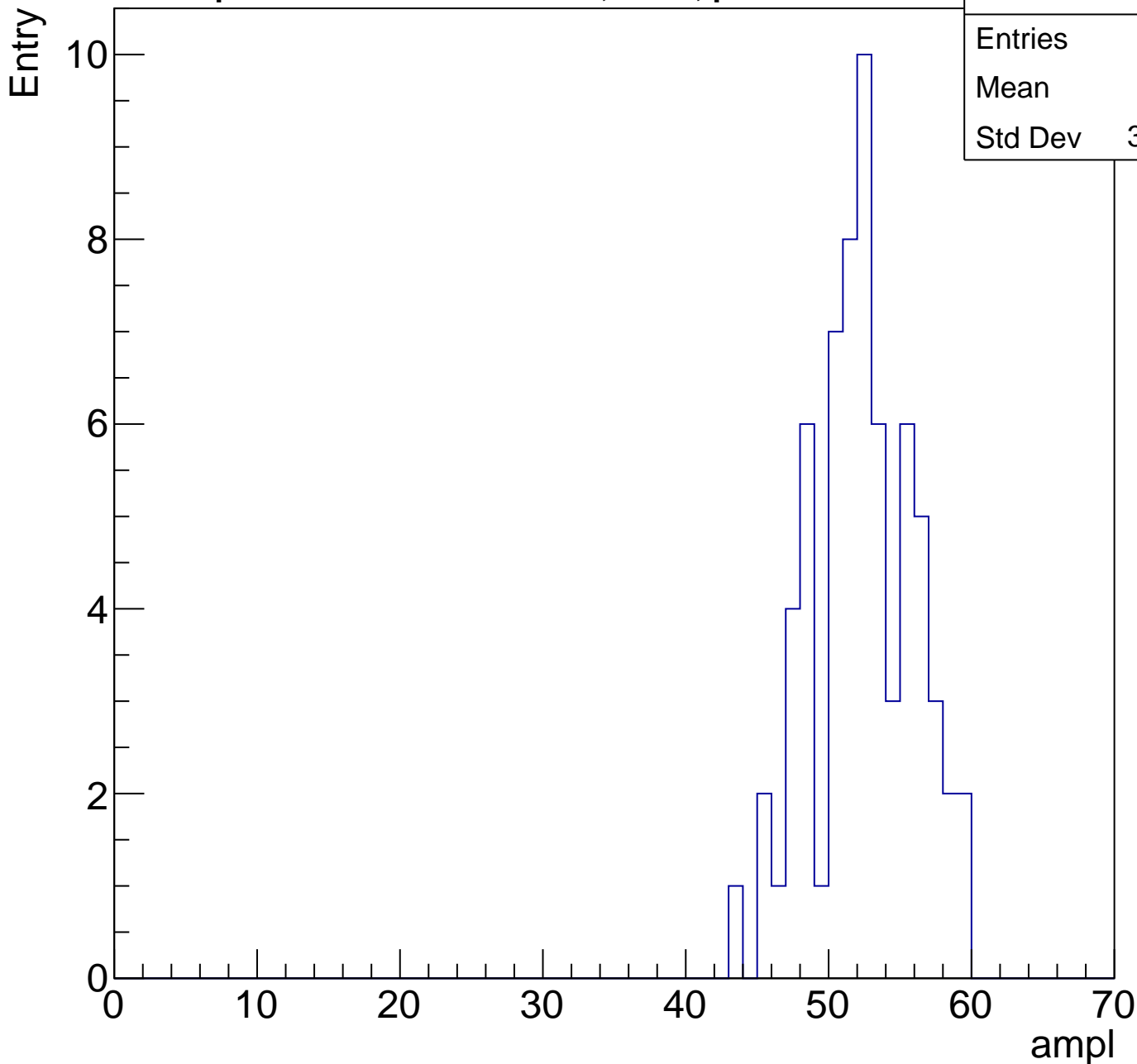
Gaus Width: 3.5755



B1L101S, U6-ch86, adc3

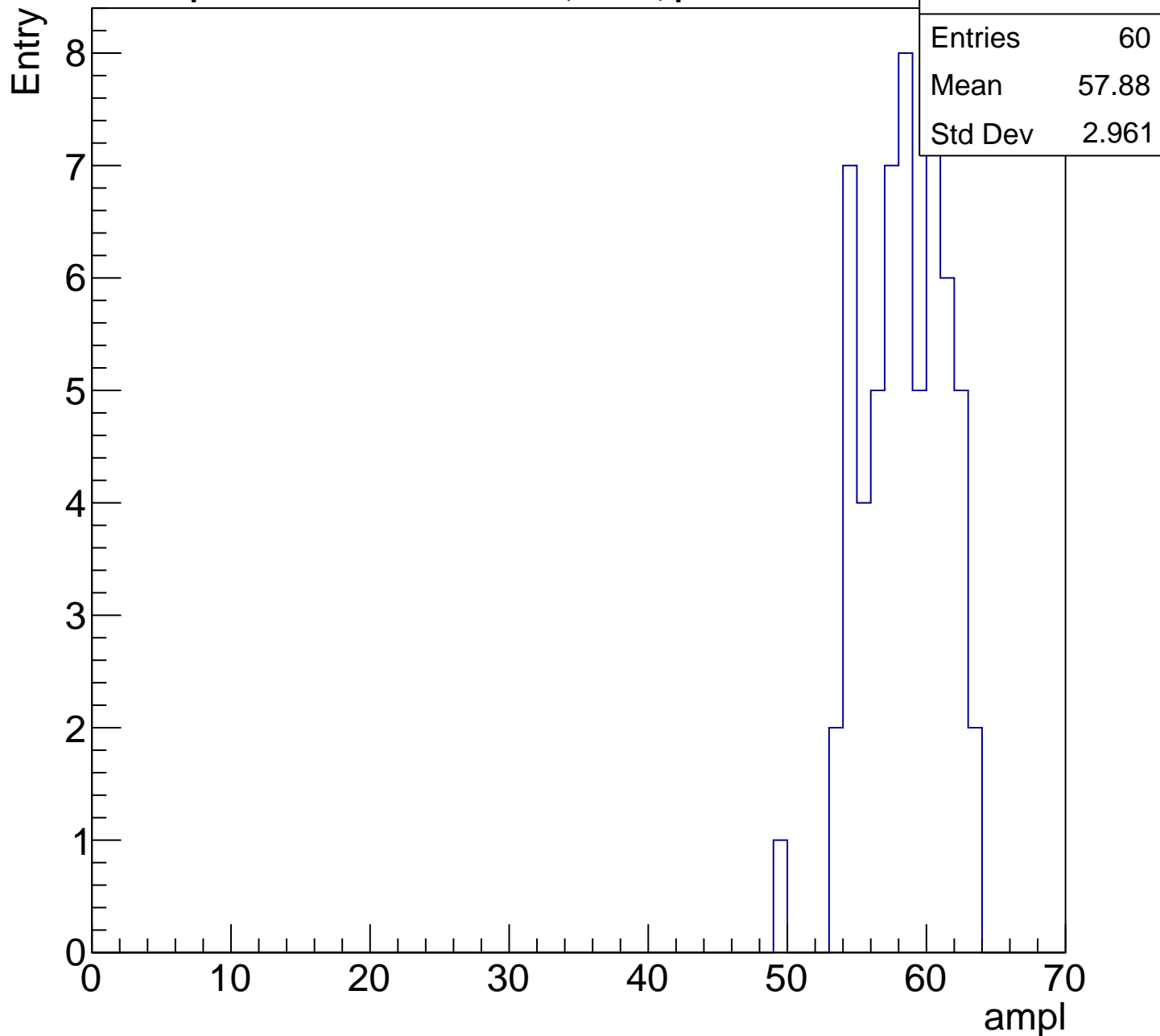
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	51.9
Std Dev	3.567



B1L101S, U6-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

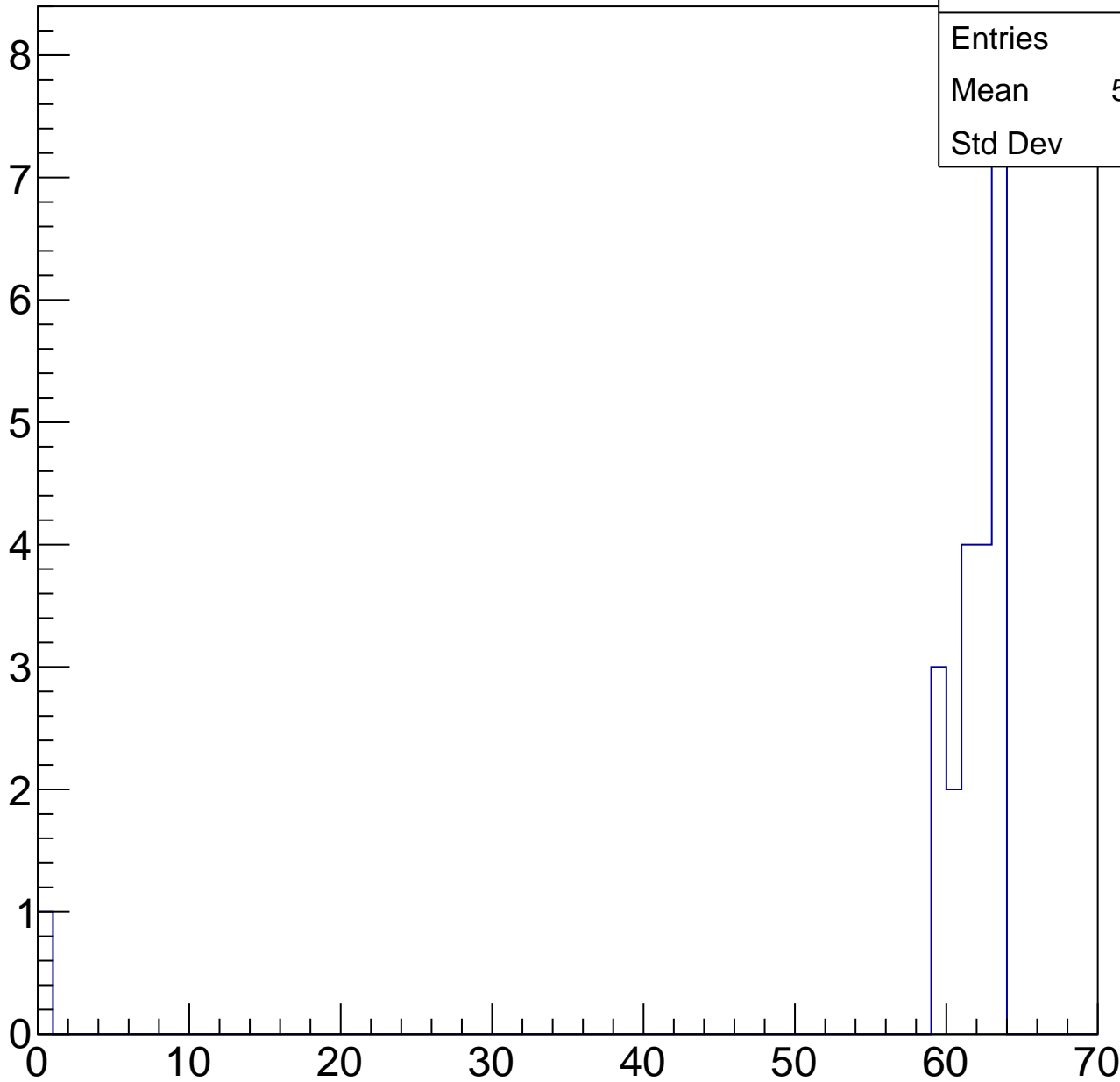
Entry

Entries	22
Mean	58.77
Std Dev	12.9

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U6-ch87, adc0

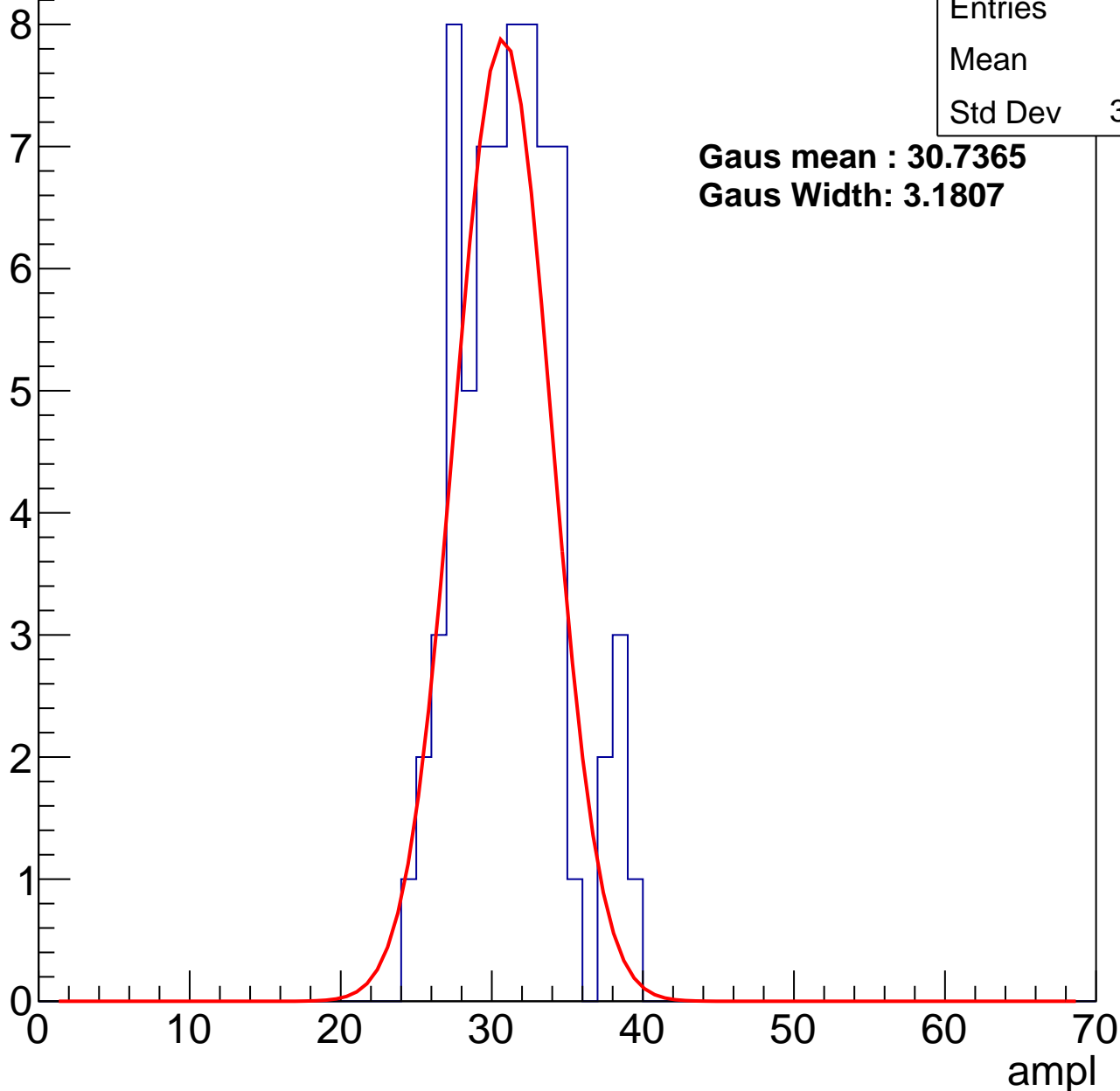
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.8
Std Dev	3.379

Gaus mean : 30.7365

Gaus Width: 3.1807



B1L101S, U6-ch87, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	37.95
Std Dev	3.389

Gaus mean : 38.6011

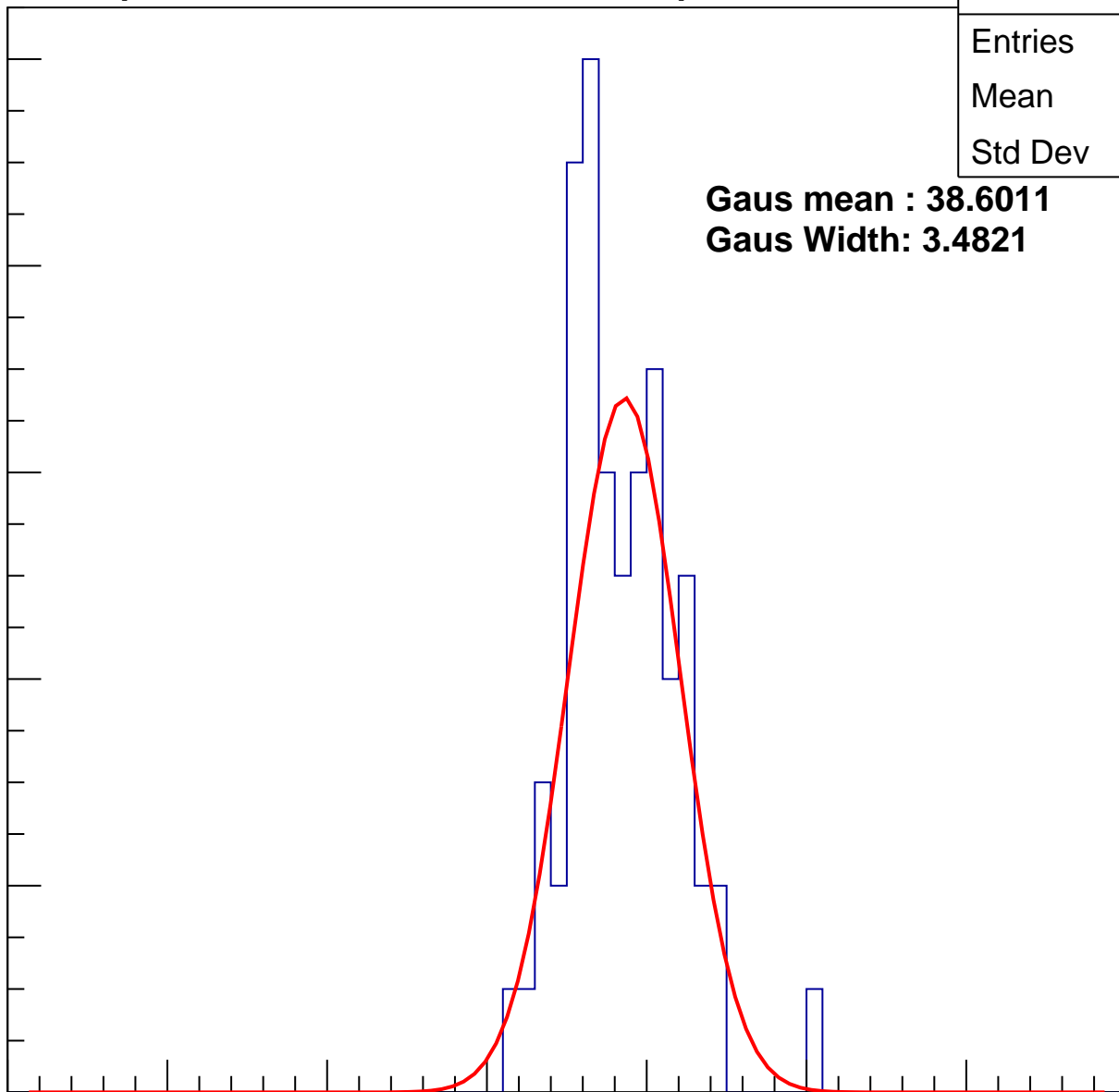
Gaus Width: 3.4821

Entry

10
8
6
4
2
0

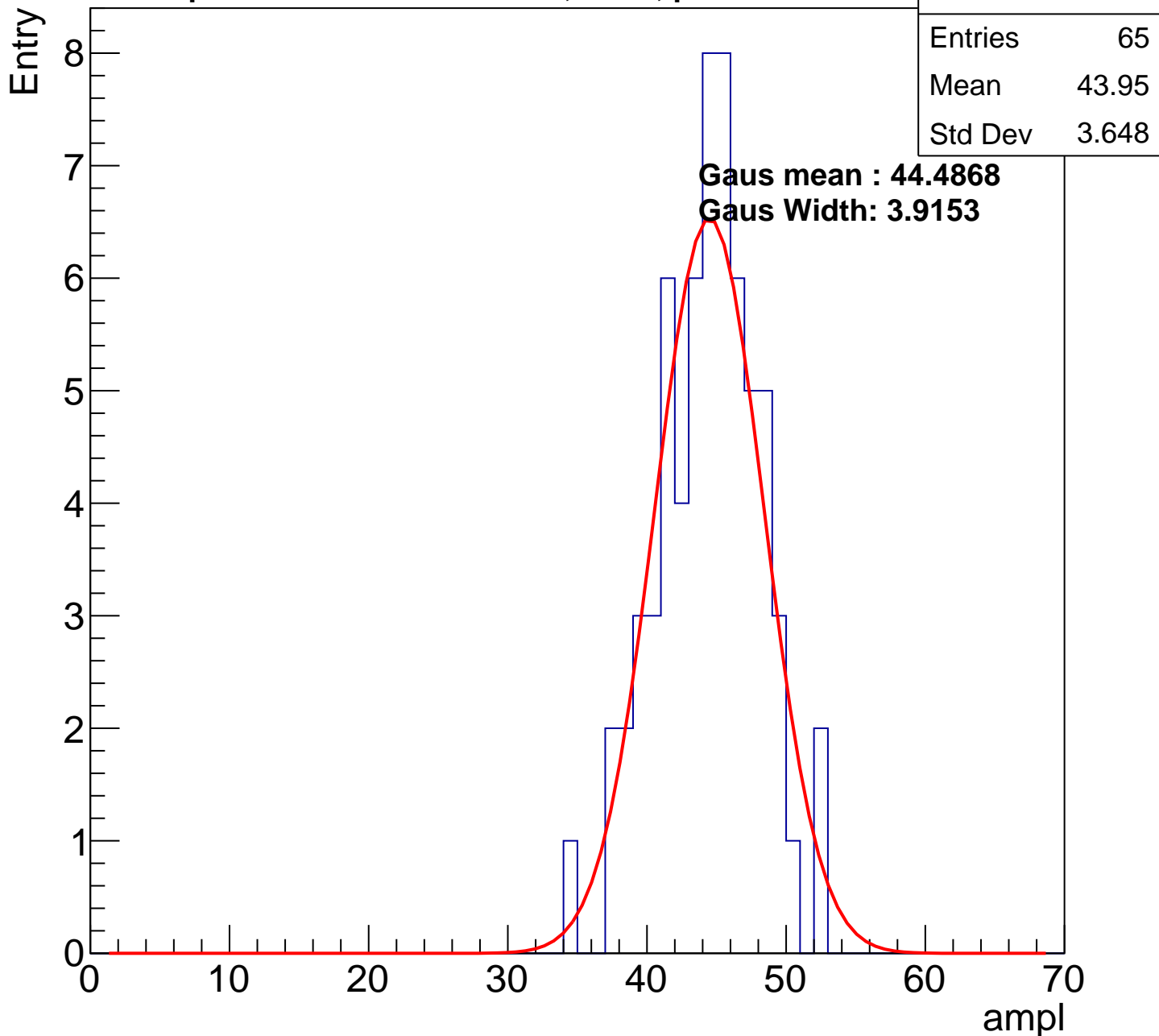
0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch87, adc2

calib_packv5_042523_0143.root, FC#0, port D2

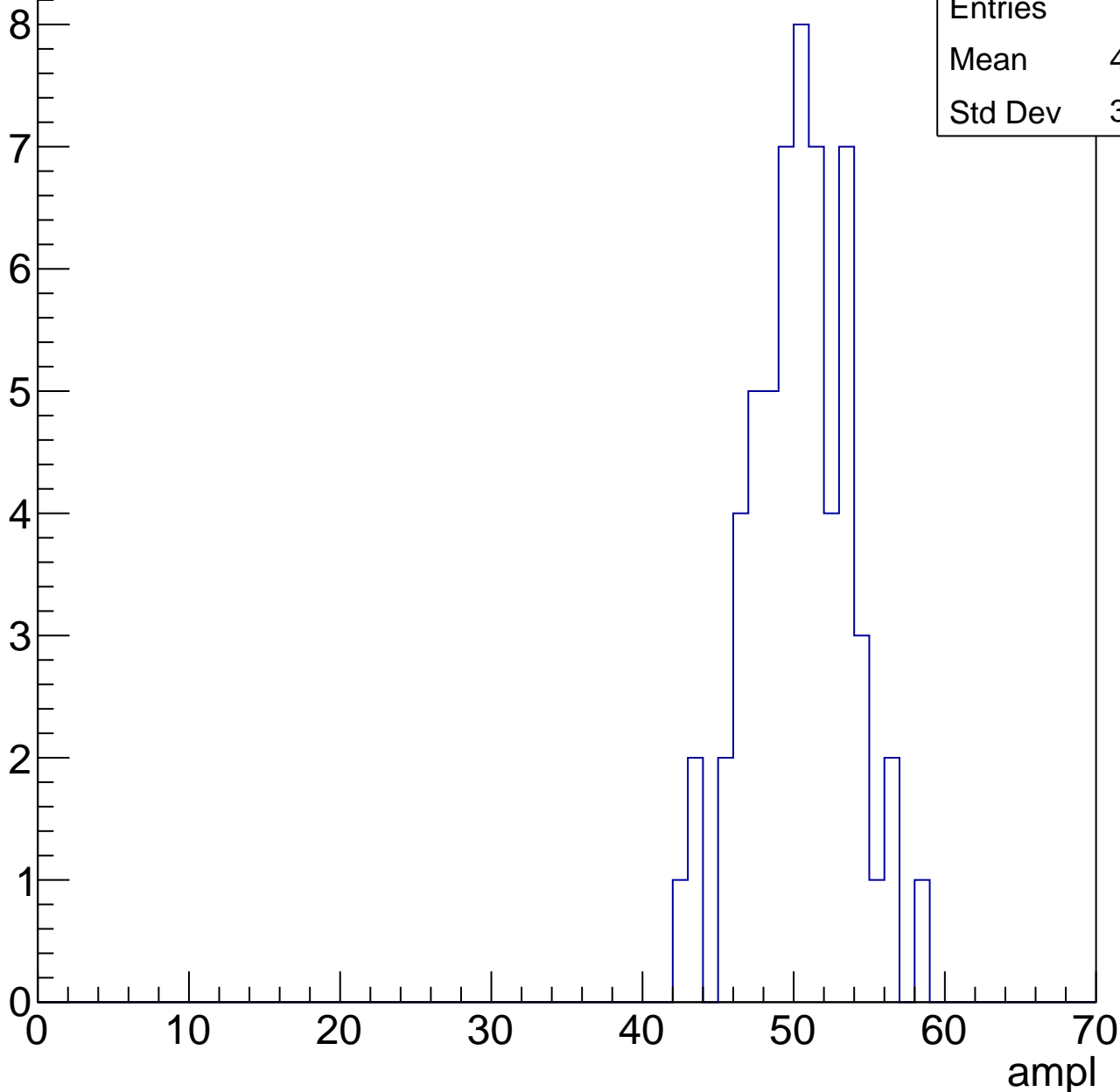


B1L101S, U6-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

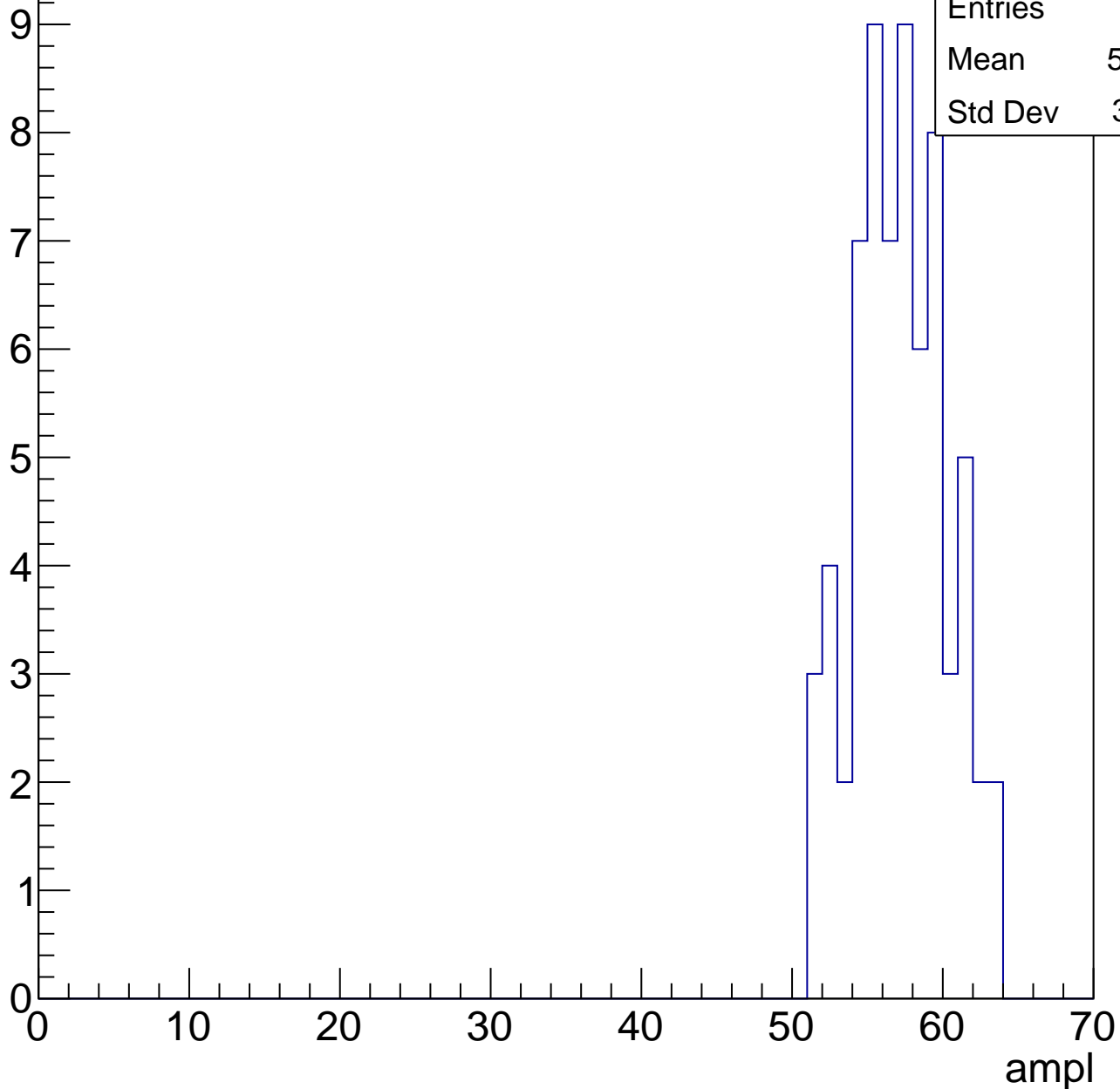
Entries	59
Mean	49.88
Std Dev	3.299



B1L101S, U6-ch87, adc4

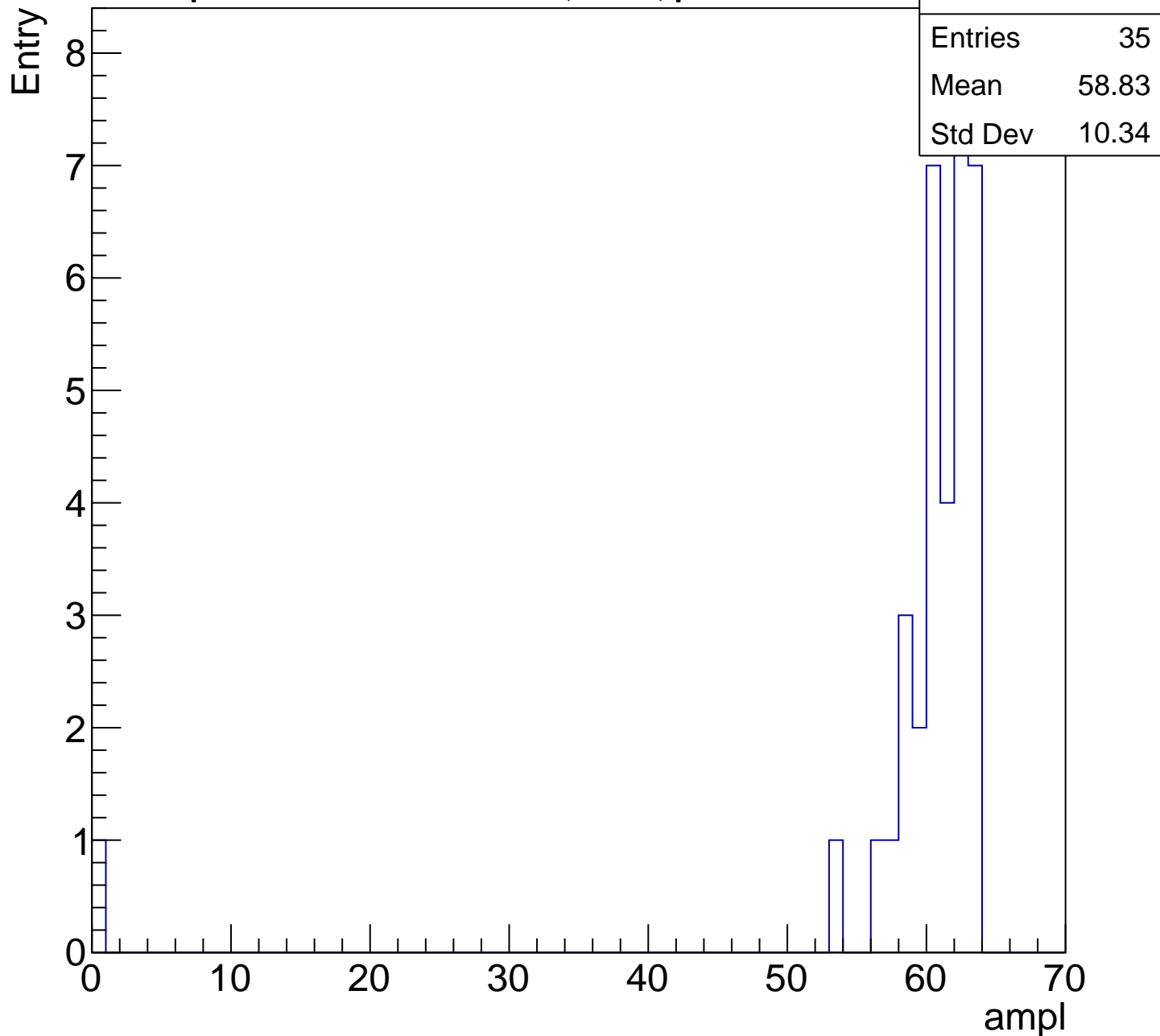
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch87, adc5

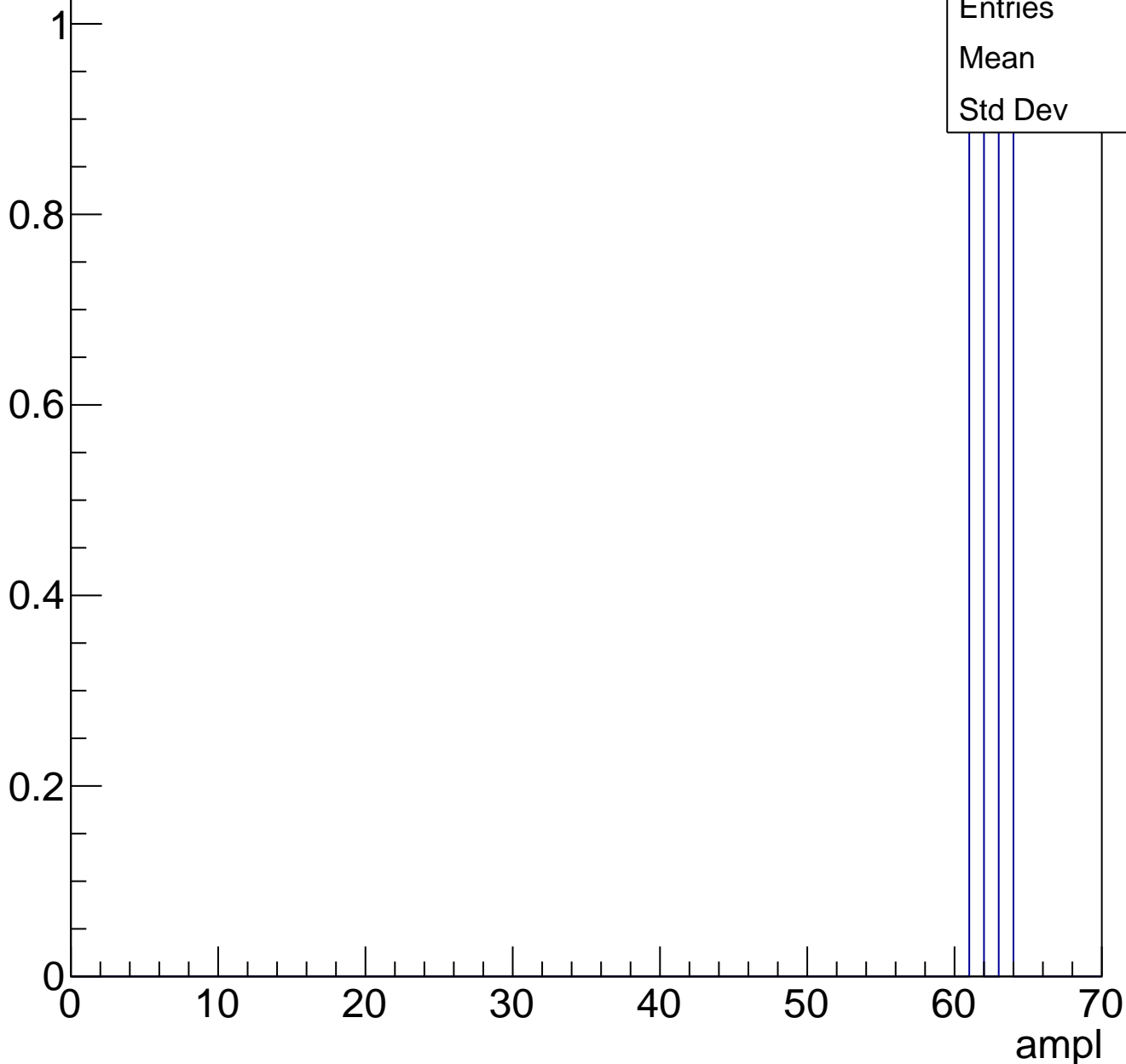
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

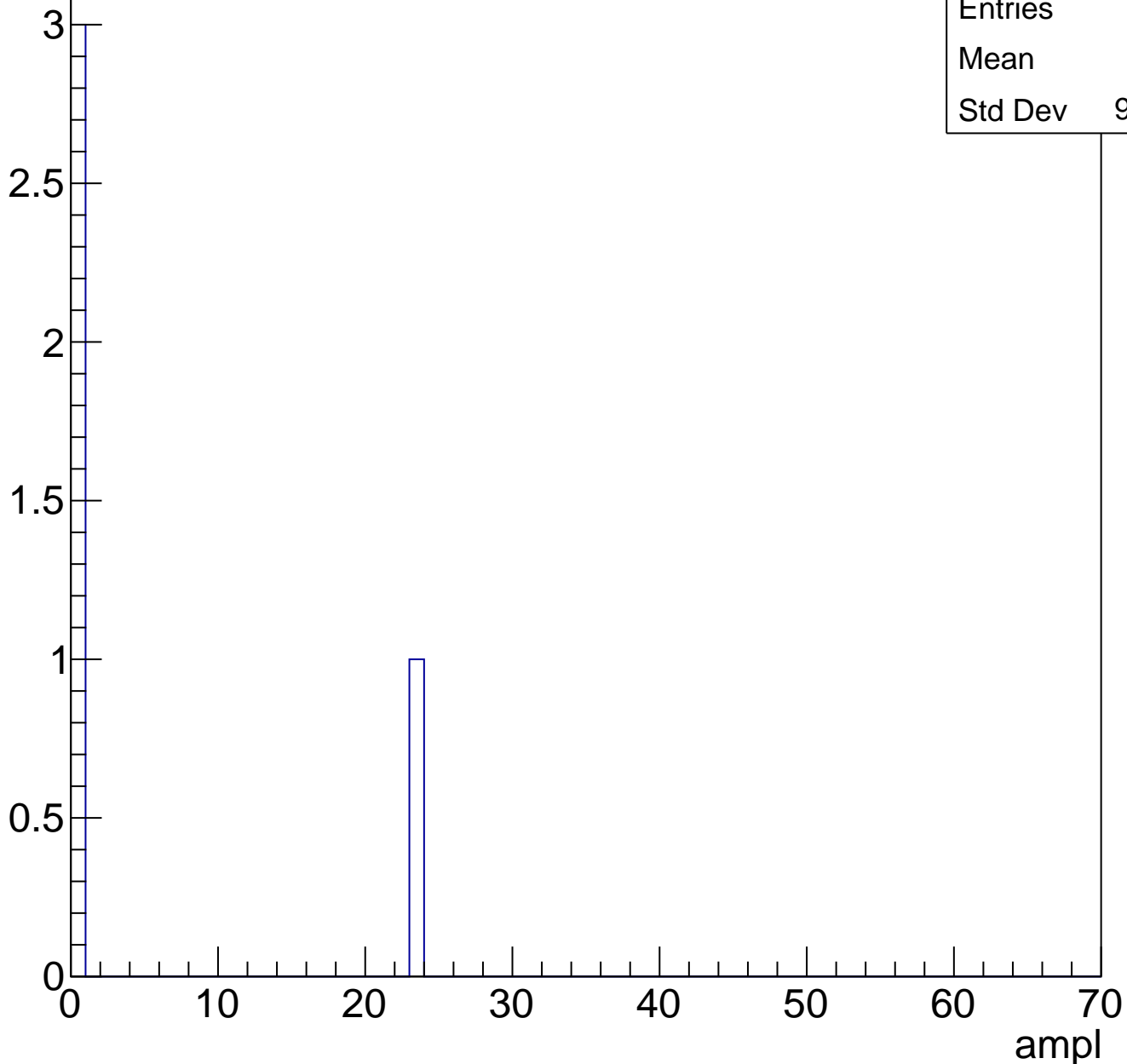
Entry



B1L101S, U6-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch88, adc0

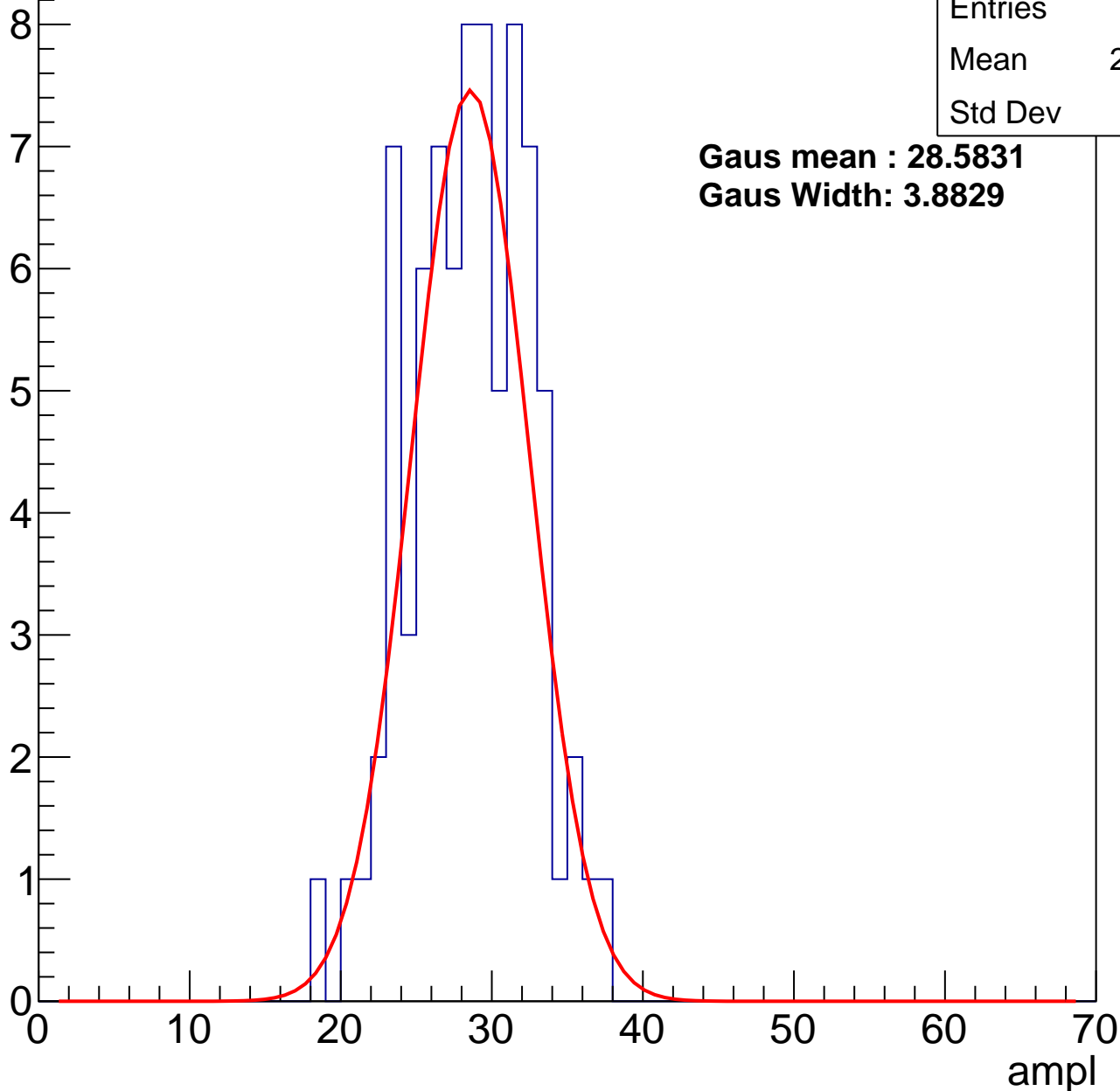
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.12
Std Dev	3.89

Gaus mean : 28.5831

Gaus Width: 3.8829



B1L101S, U6-ch88, adc1

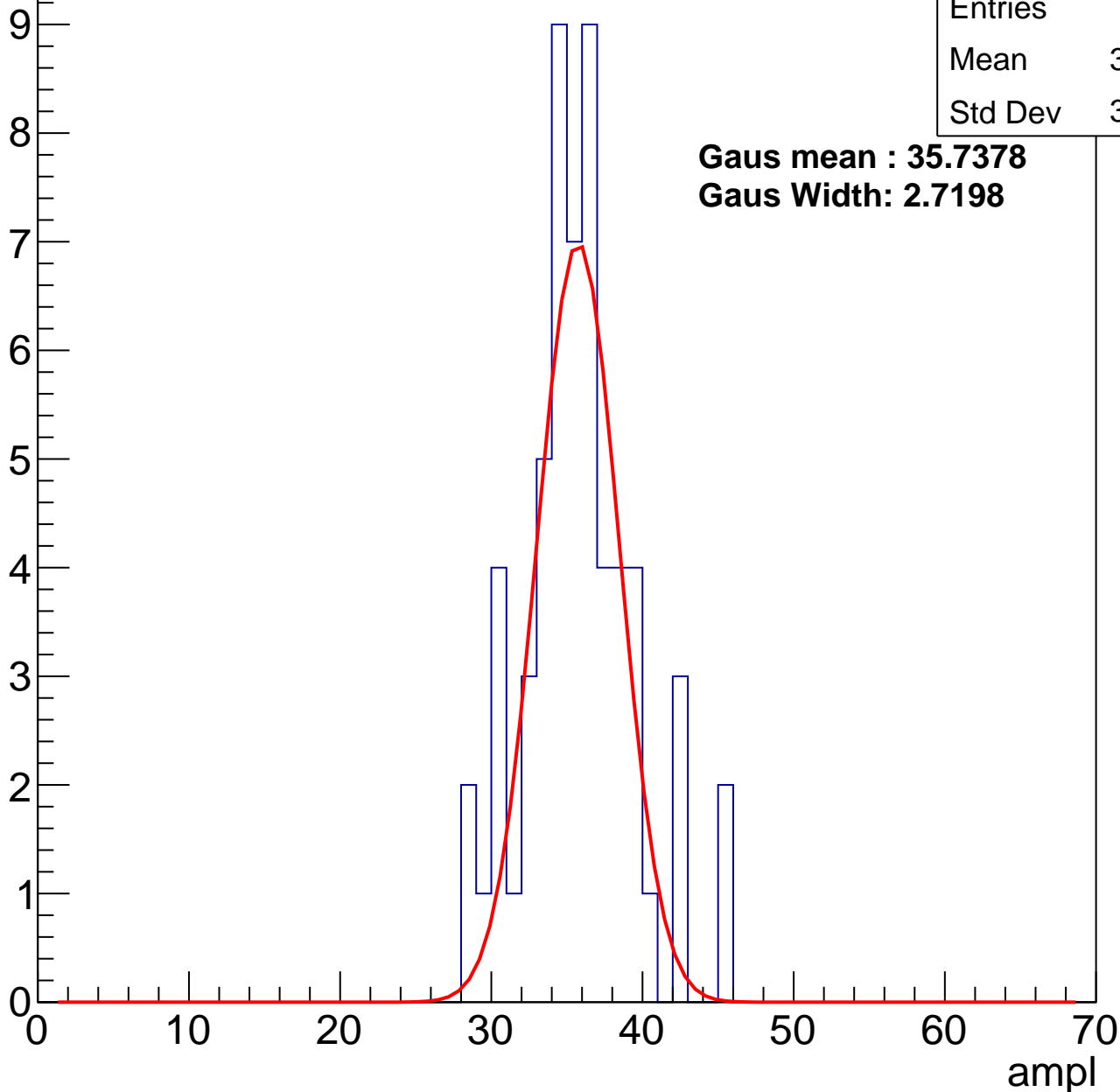
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.32
Std Dev	3.675

Gaus mean : 35.7378

Gaus Width: 2.7198



B1L101S, U6-ch88, adc2

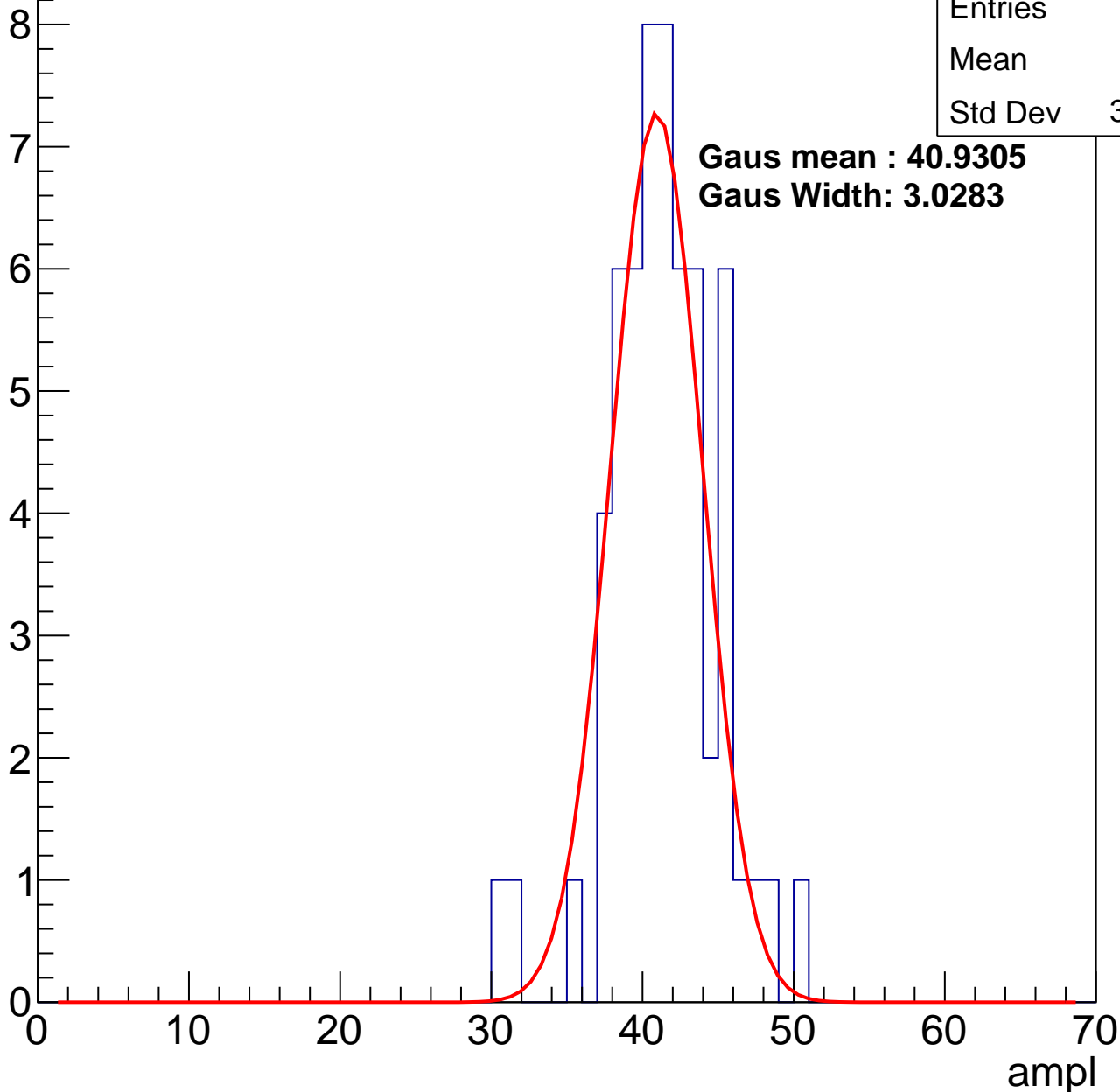
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	40.9
Std Dev	3.545

Gaus mean : 40.9305

Gaus Width: 3.0283

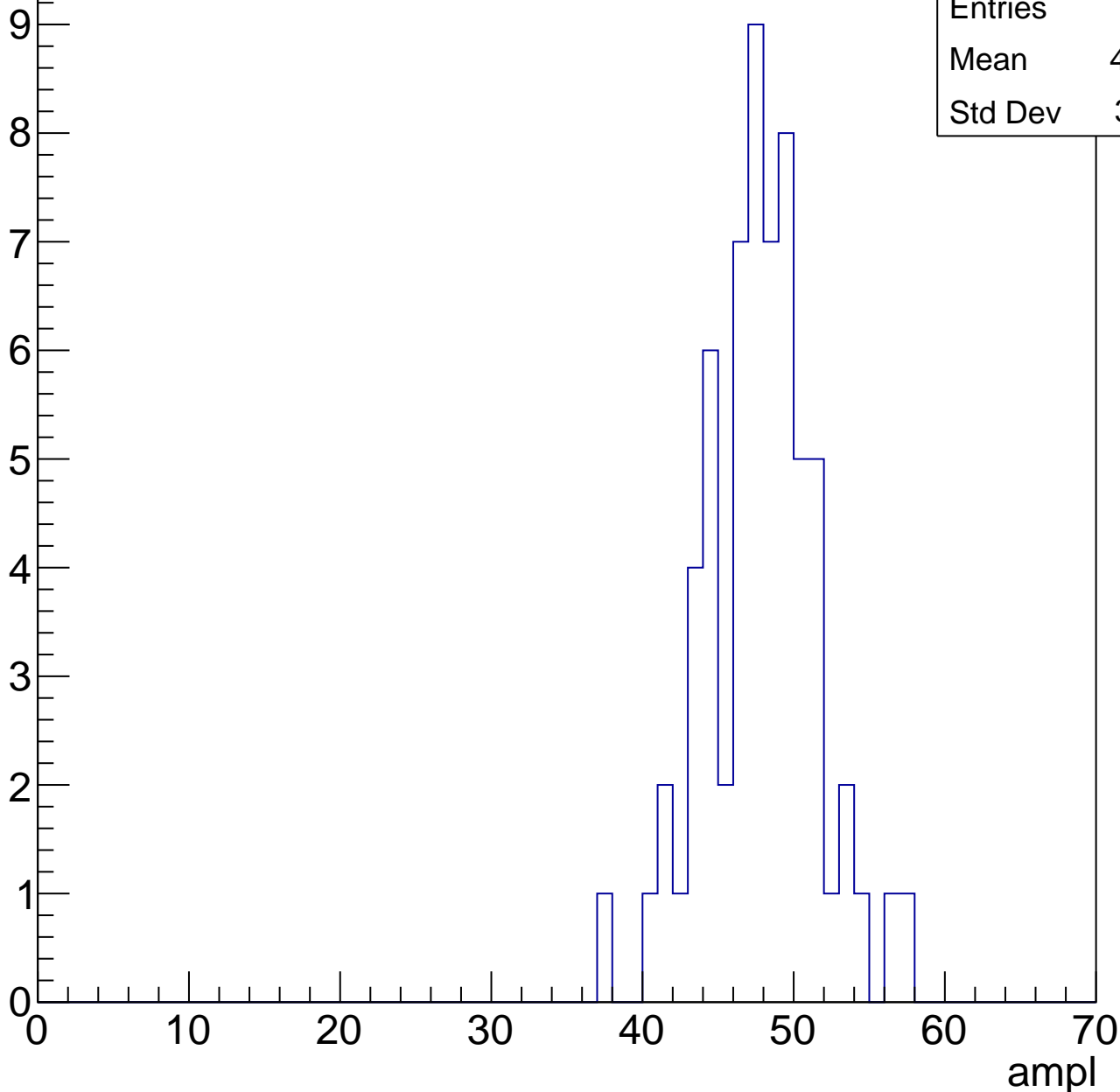


B1L101S, U6-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

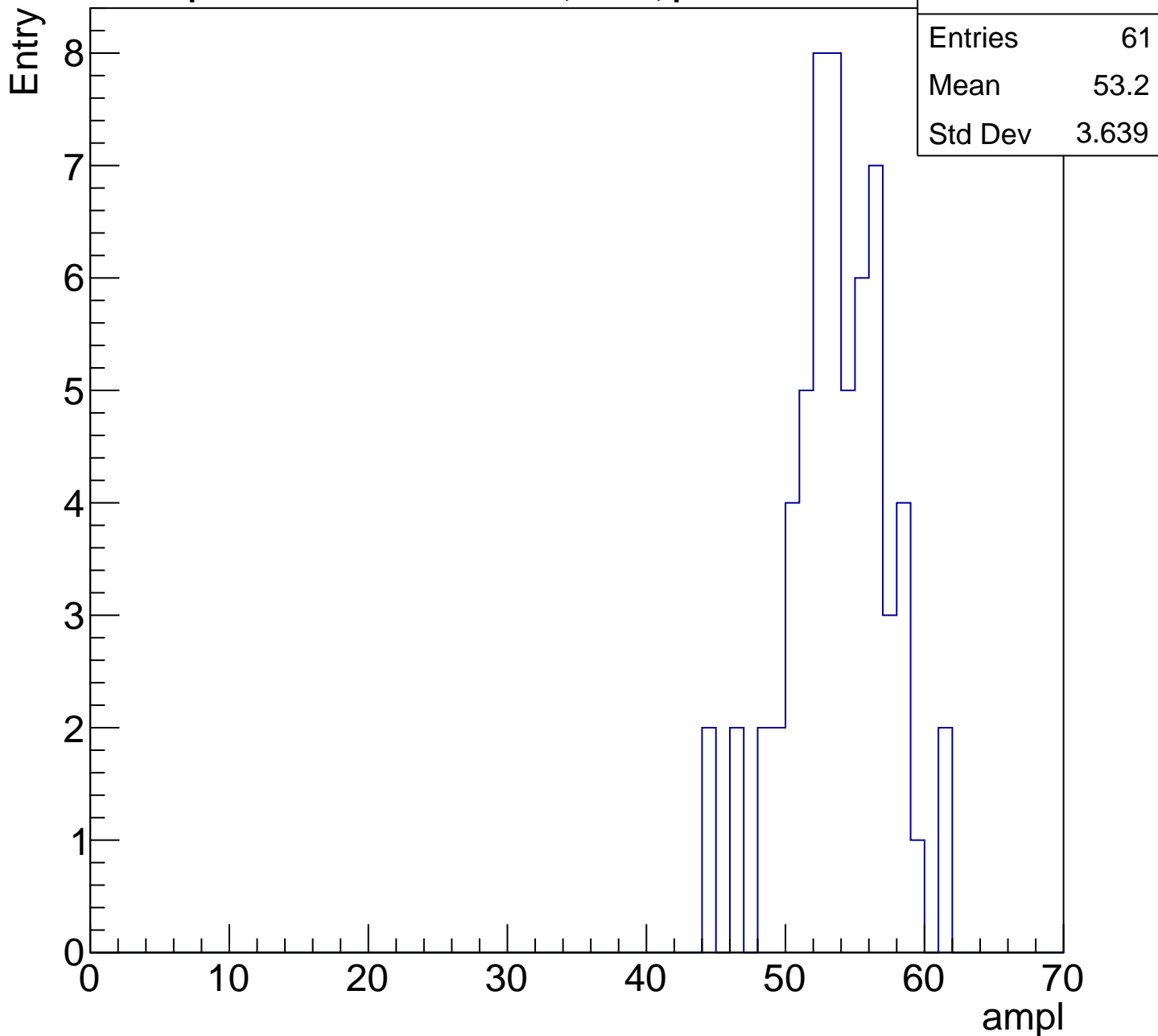
Entry

Entries	64
Mean	47.34
Std Dev	3.671



B1L101S, U6-ch88, adc4

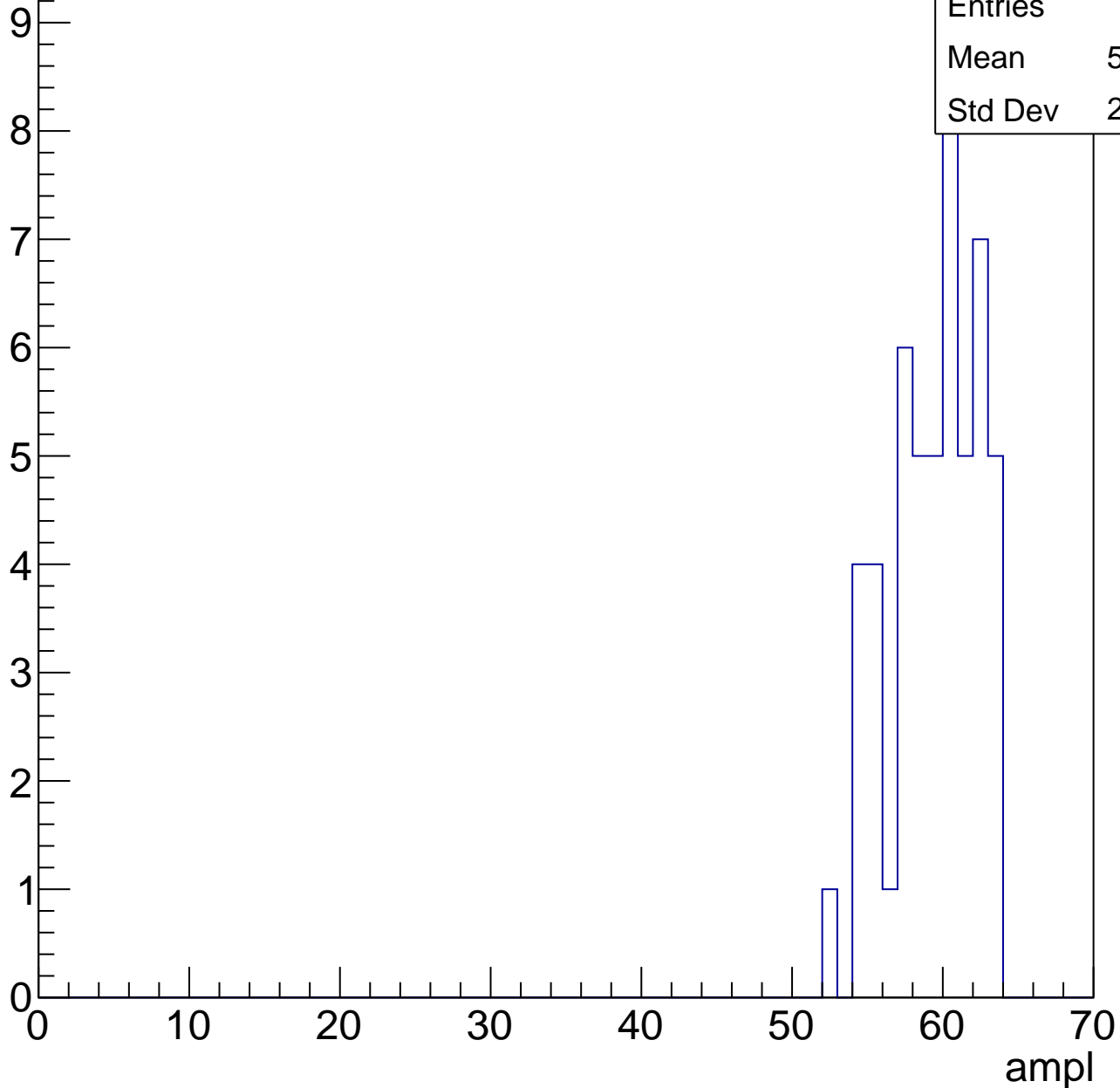
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

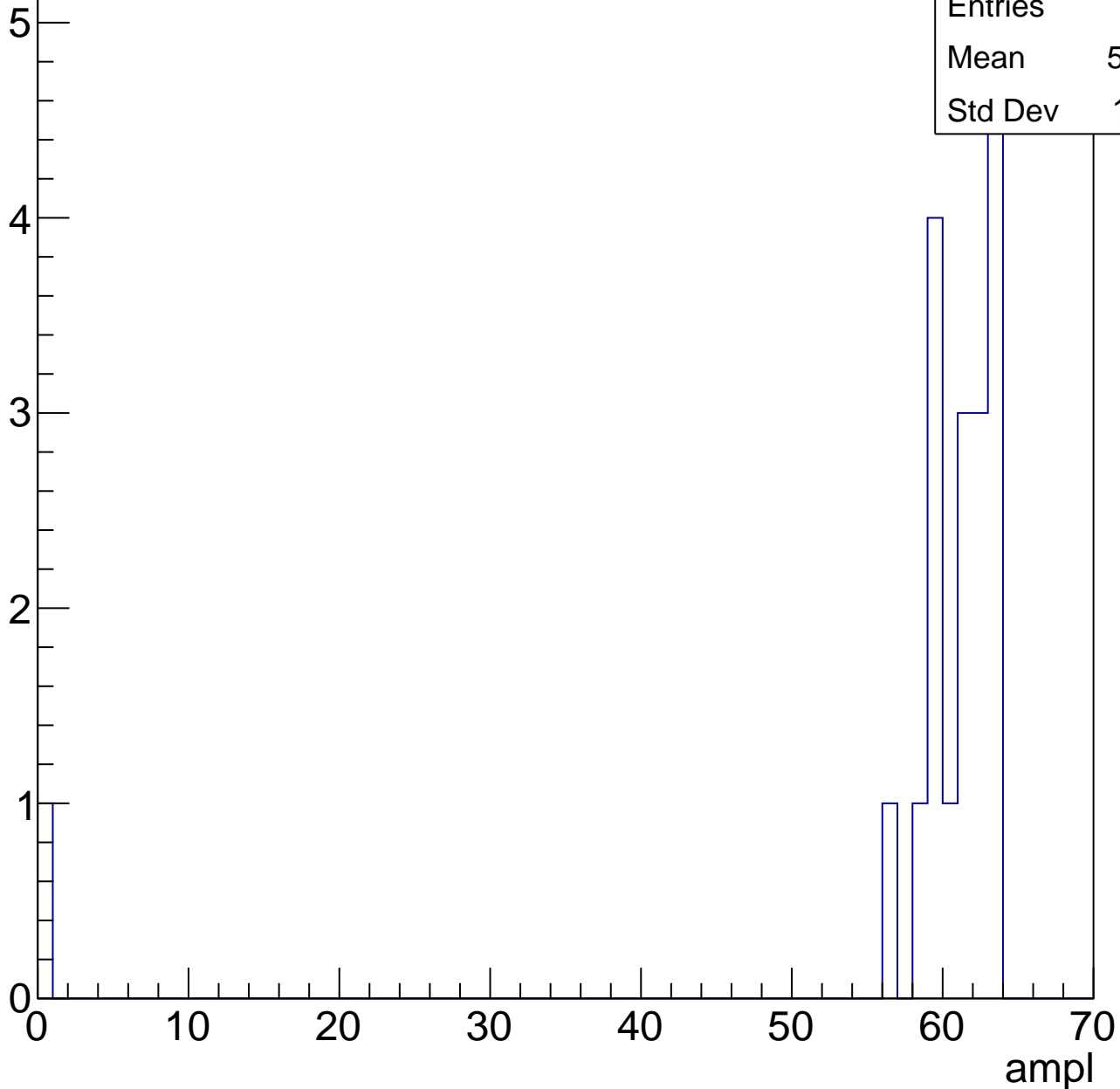


B1L101S, U6-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	57.58
Std Dev	13.71



B1L101S, U6-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch89, adc0

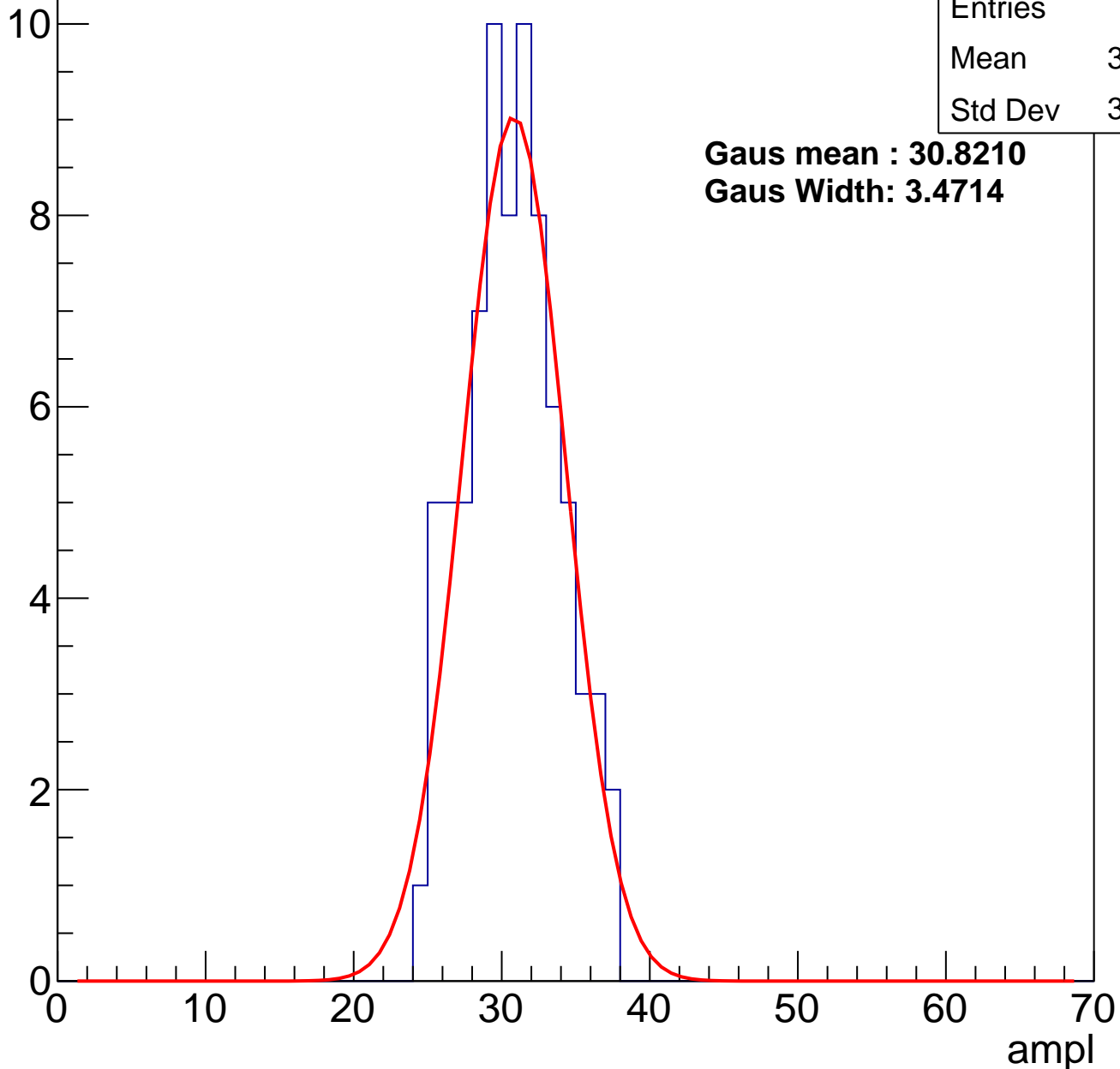
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.27
Std Dev	3.153

Gaus mean : 30.8210

Gaus Width: 3.4714

Entry



B1L101S, U6-ch89, adc1

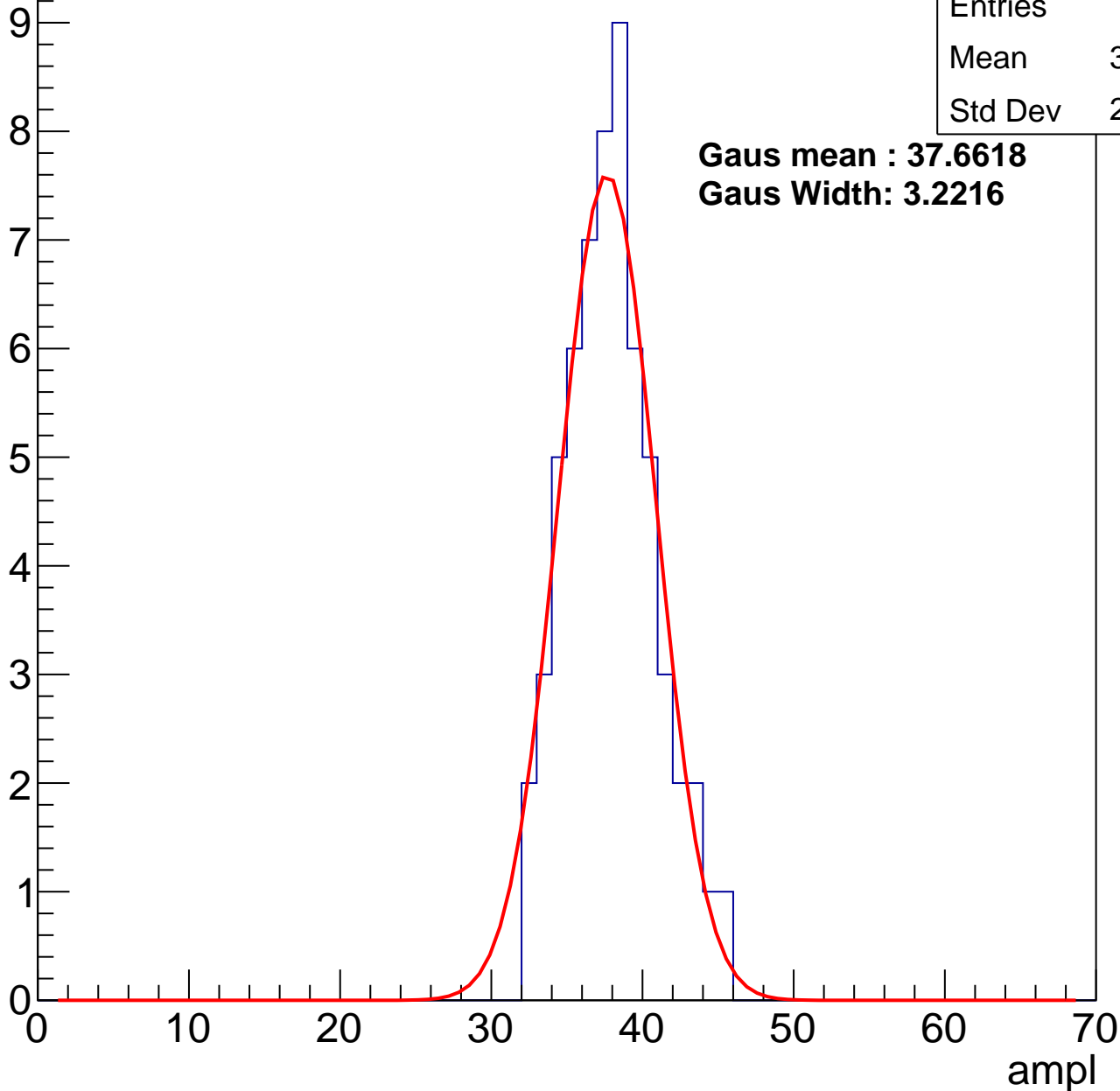
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.48
Std Dev	2.947

Gaus mean : 37.6618

Gaus Width: 3.2216

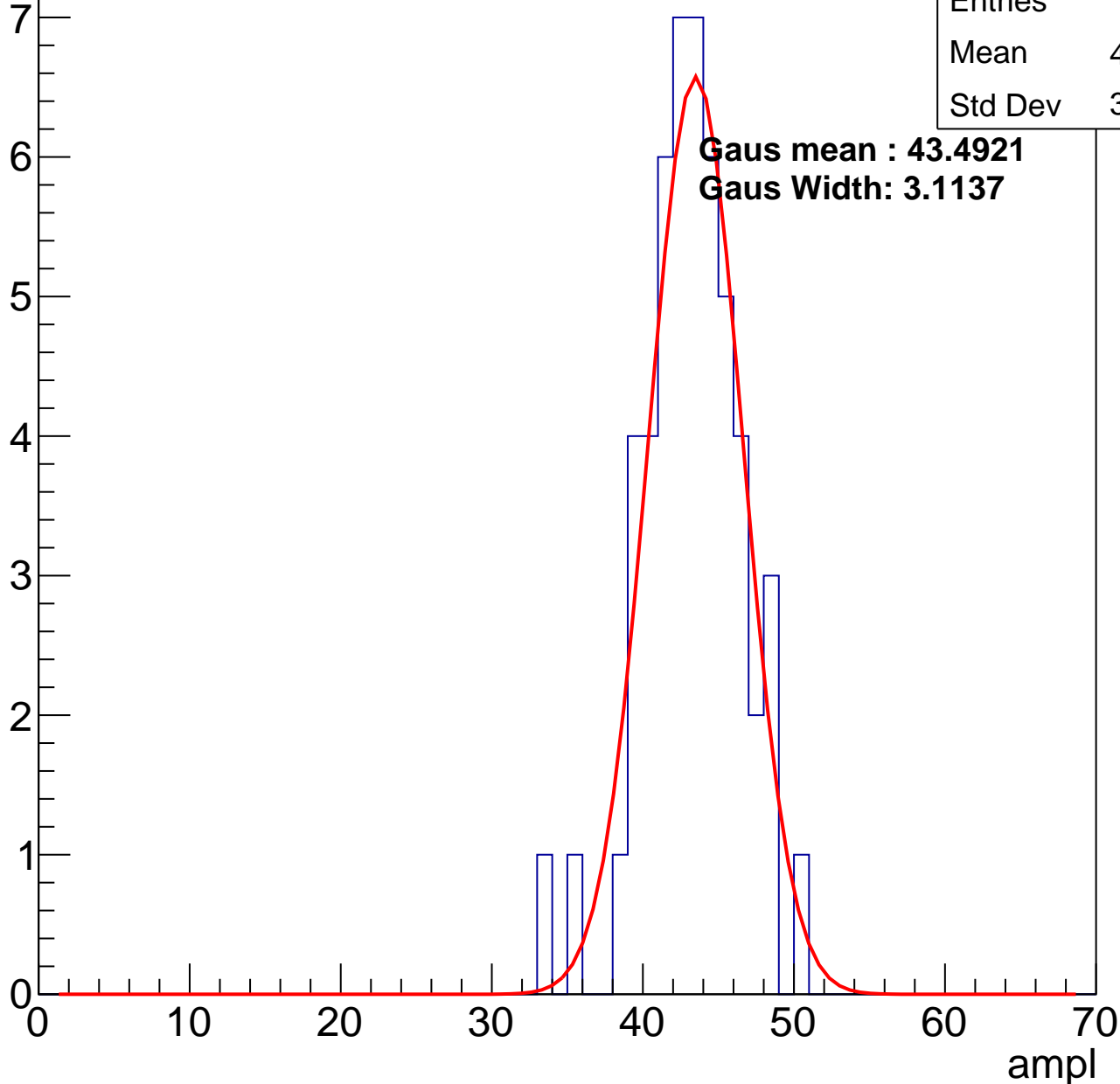


B1L101S, U6-ch89, adc2

calib_packv5_042523_0143.root, FC#0, port D2

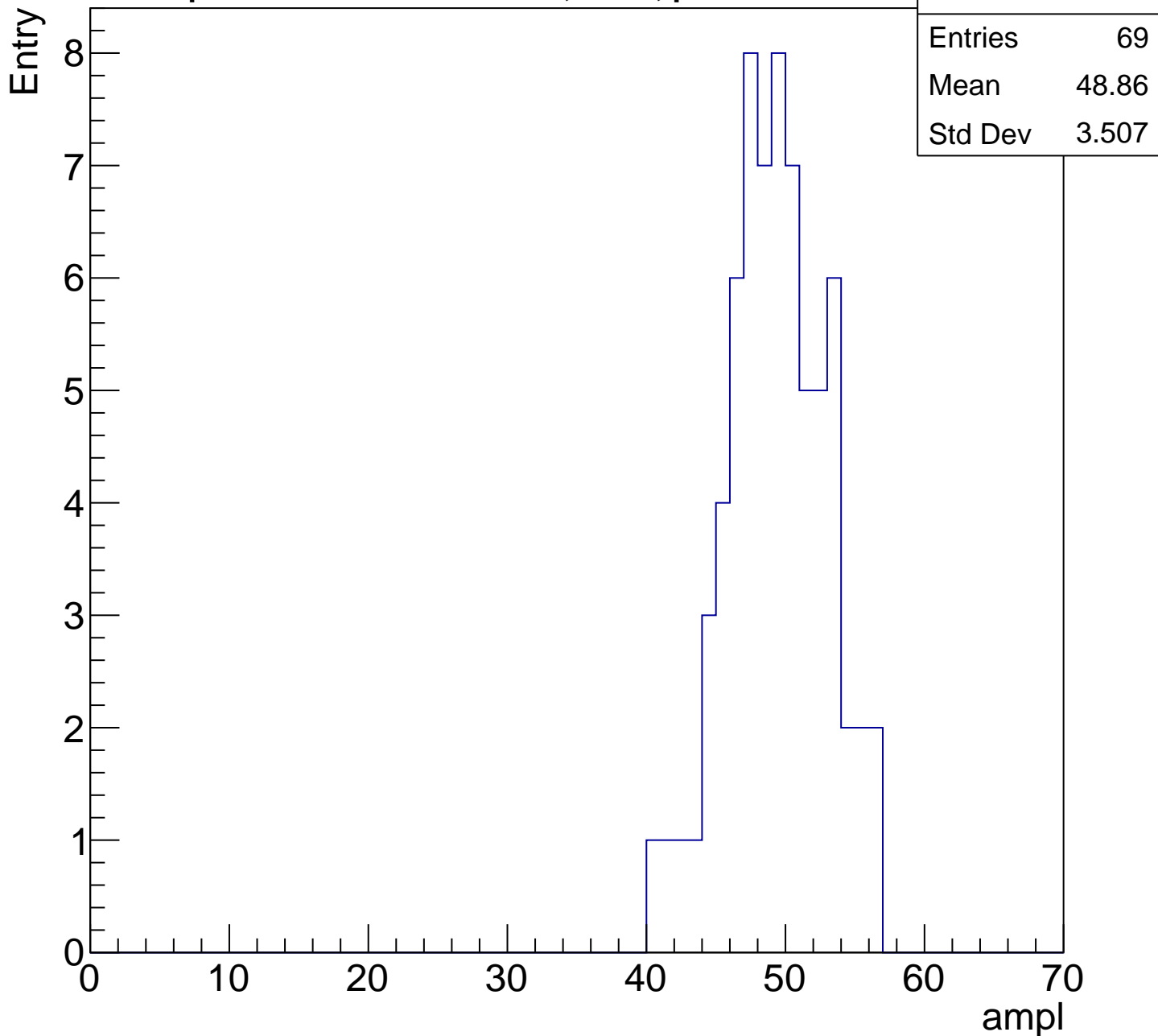
Entry

Entries	52
Mean	42.77
Std Dev	3.208



B1L101S, U6-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	55.63
Std Dev	3.287

Entry

10

8

6

4

2

0

0

10

20

30

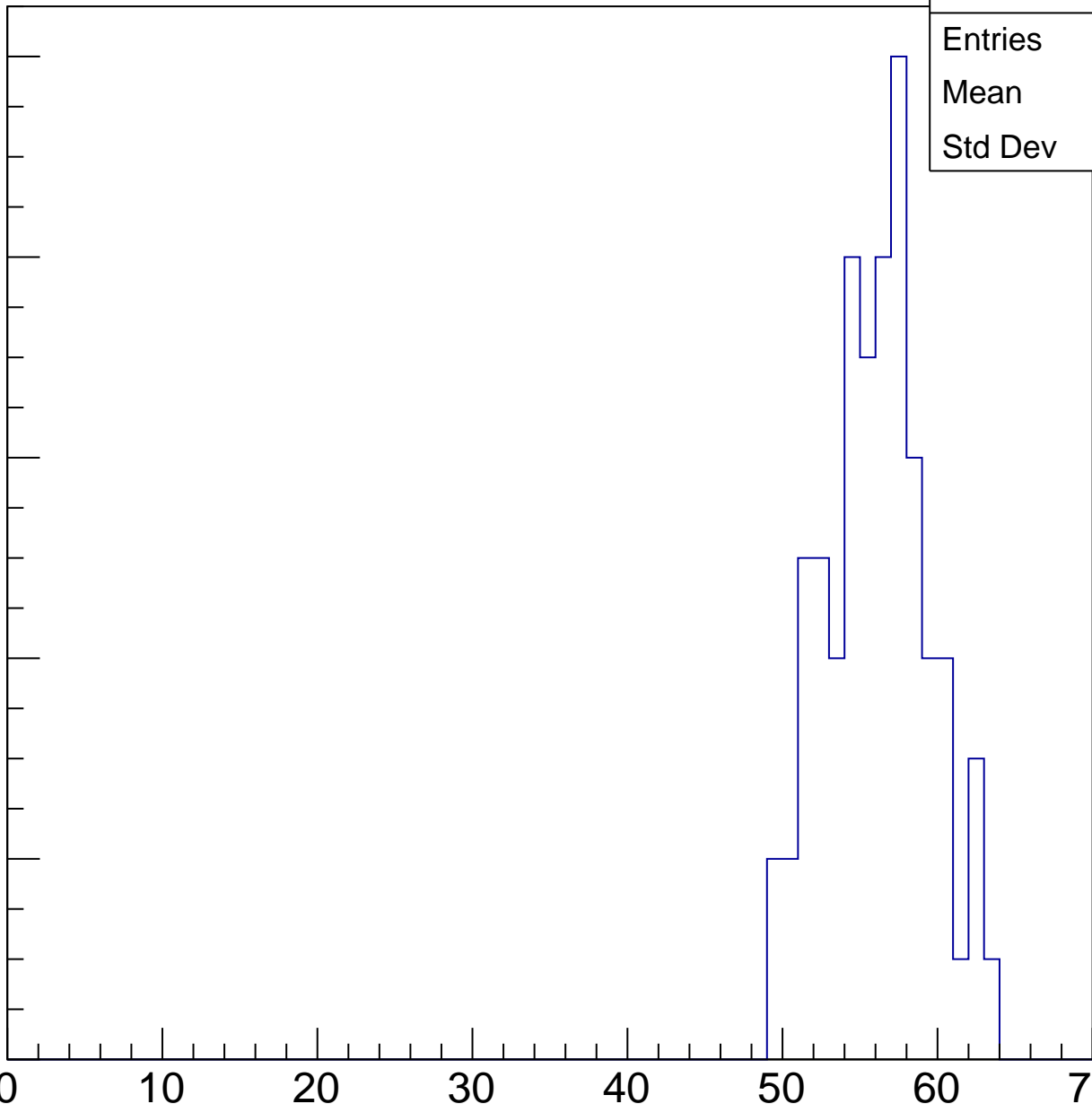
40

50

60

70

ampl

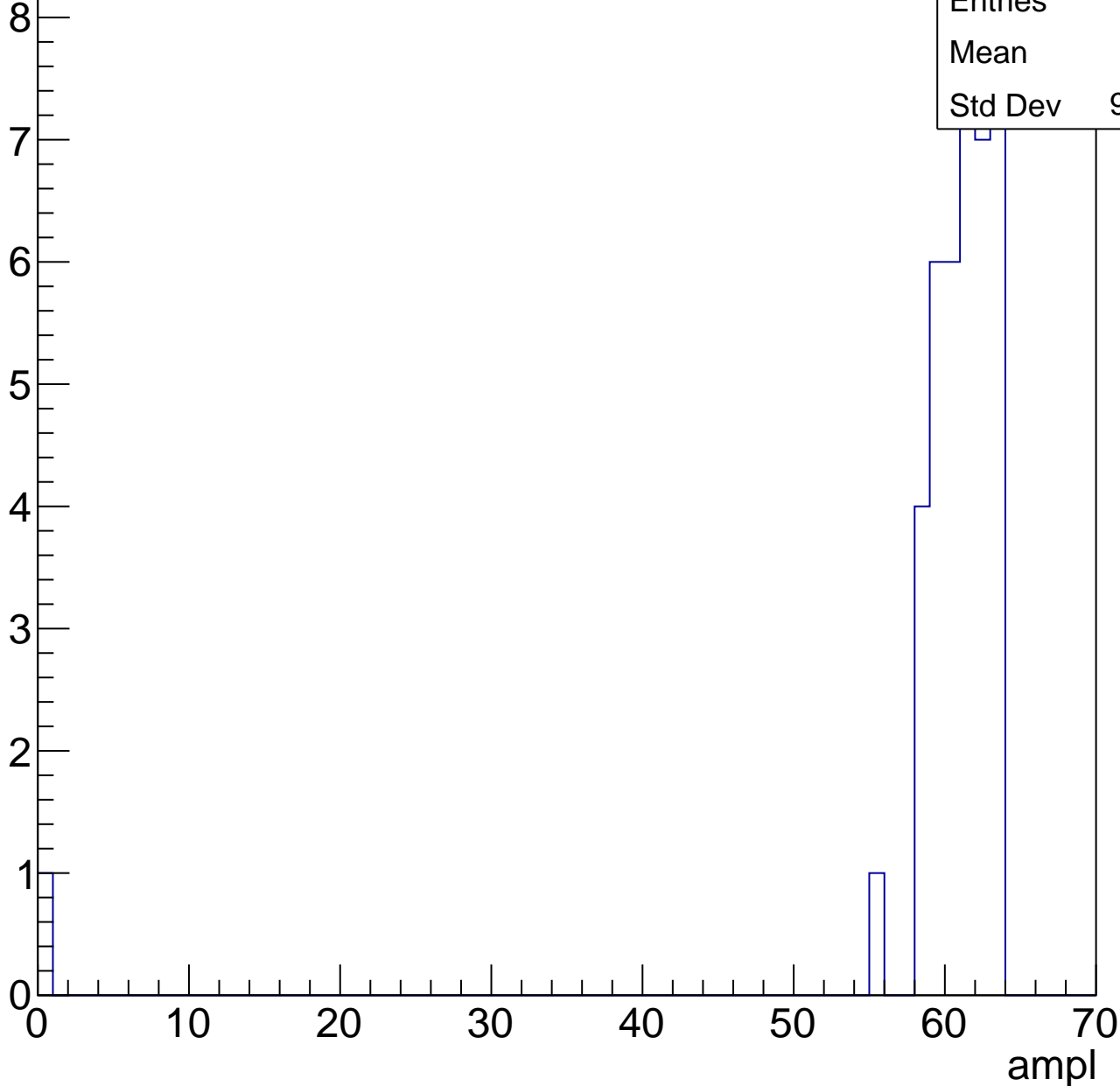


B1L101S, U6-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.2
Std Dev	9.536



B1L101S, U6-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch90, adc0

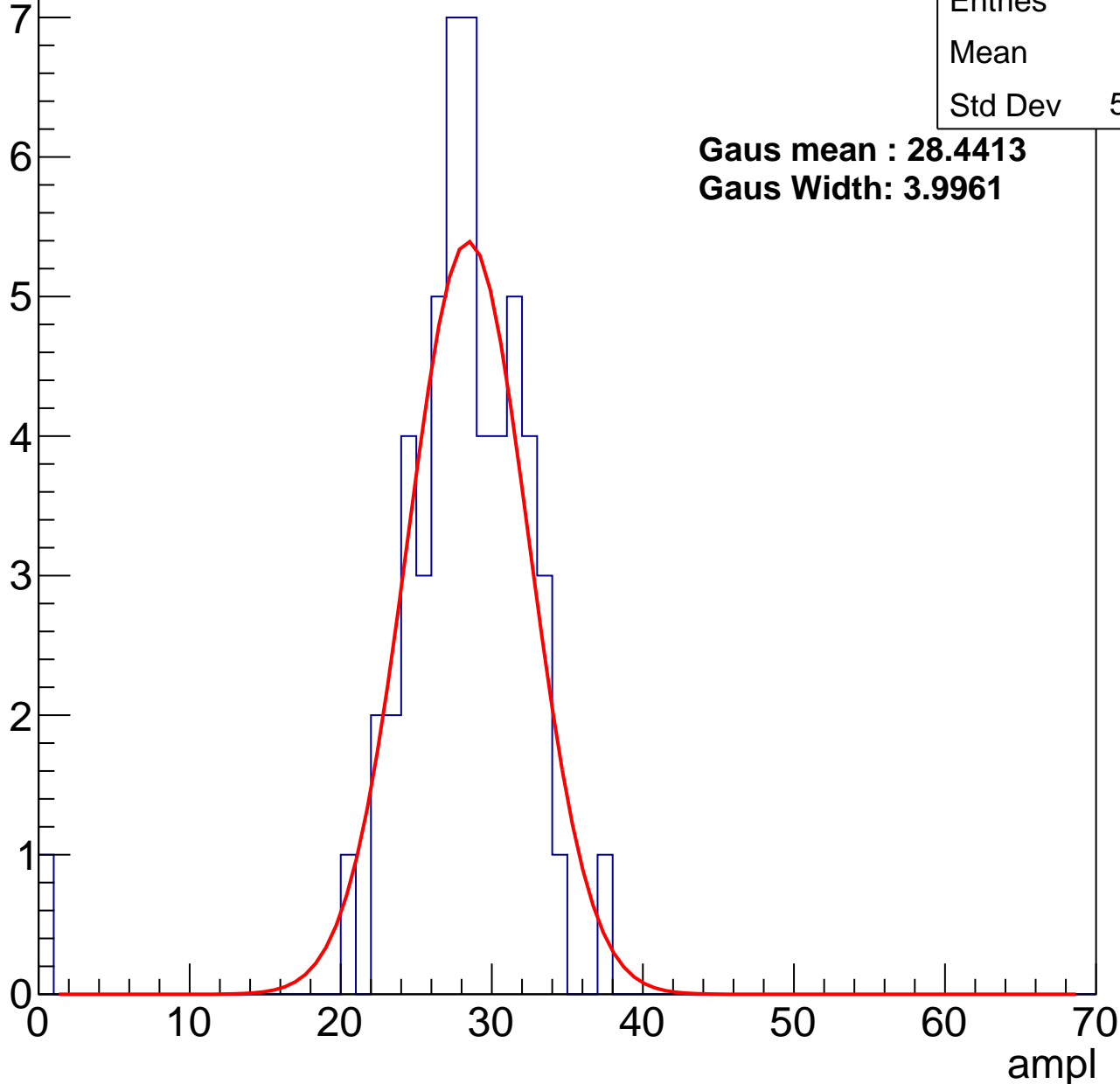
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	27.5
Std Dev	5.084

Gaus mean : 28.4413

Gaus Width: 3.9961



B1L101S, U6-ch90, adc1

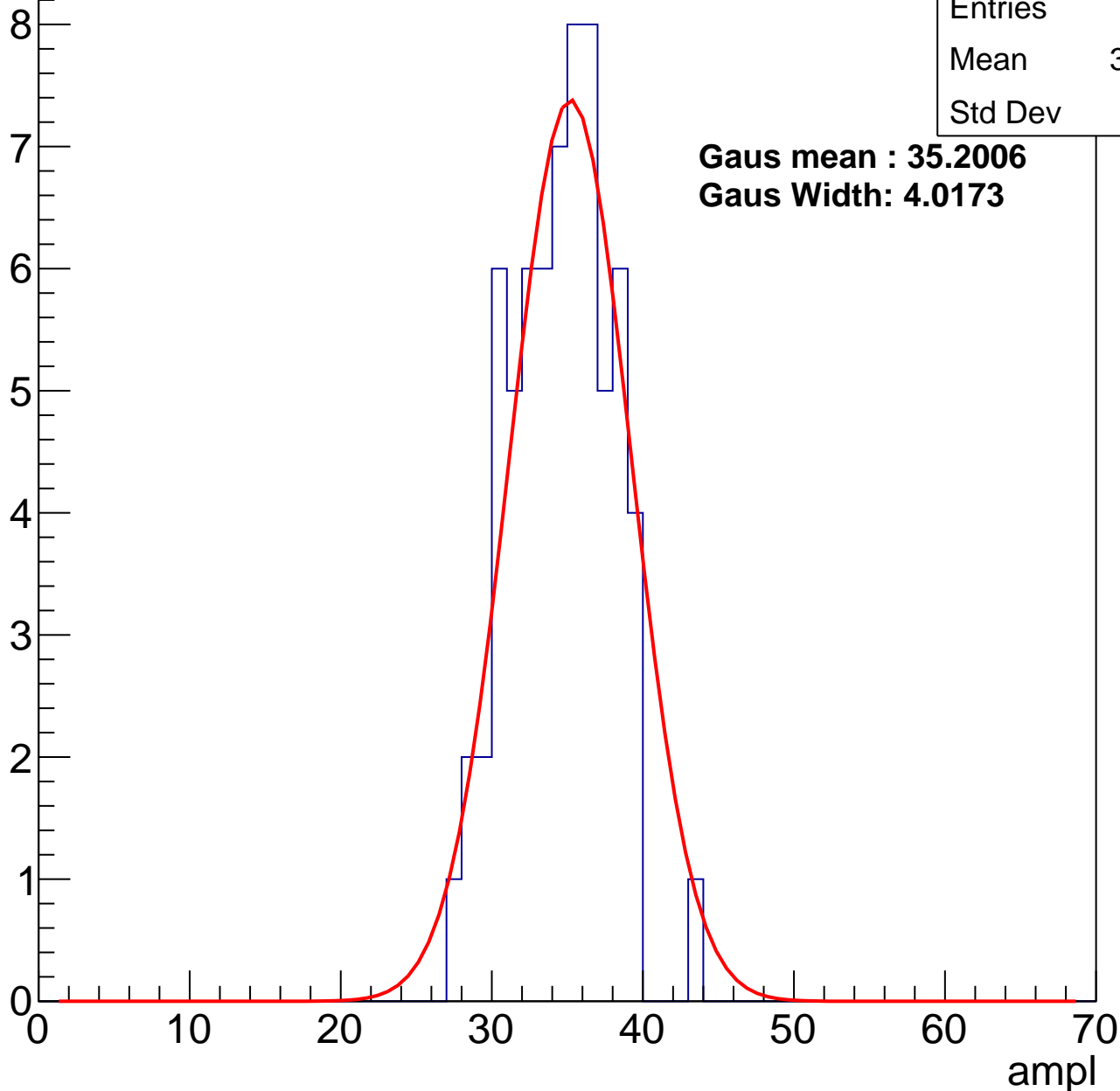
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.09
Std Dev	3.24

Gaus mean : 35.2006

Gaus Width: 4.0173



B1L101S, U6-ch90, adc2

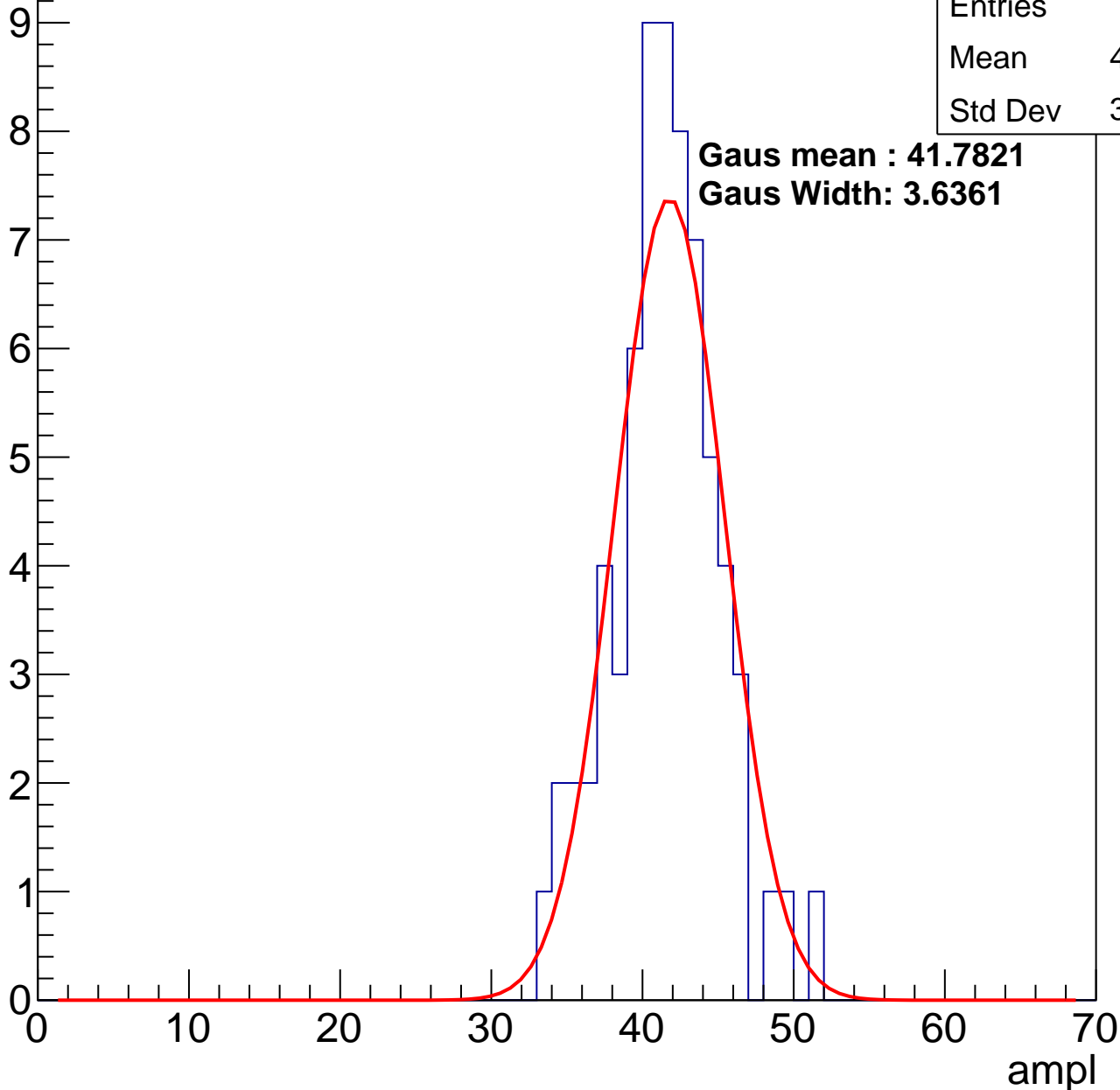
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.04
Std Dev	3.516

Gaus mean : 41.7821

Gaus Width: 3.6361

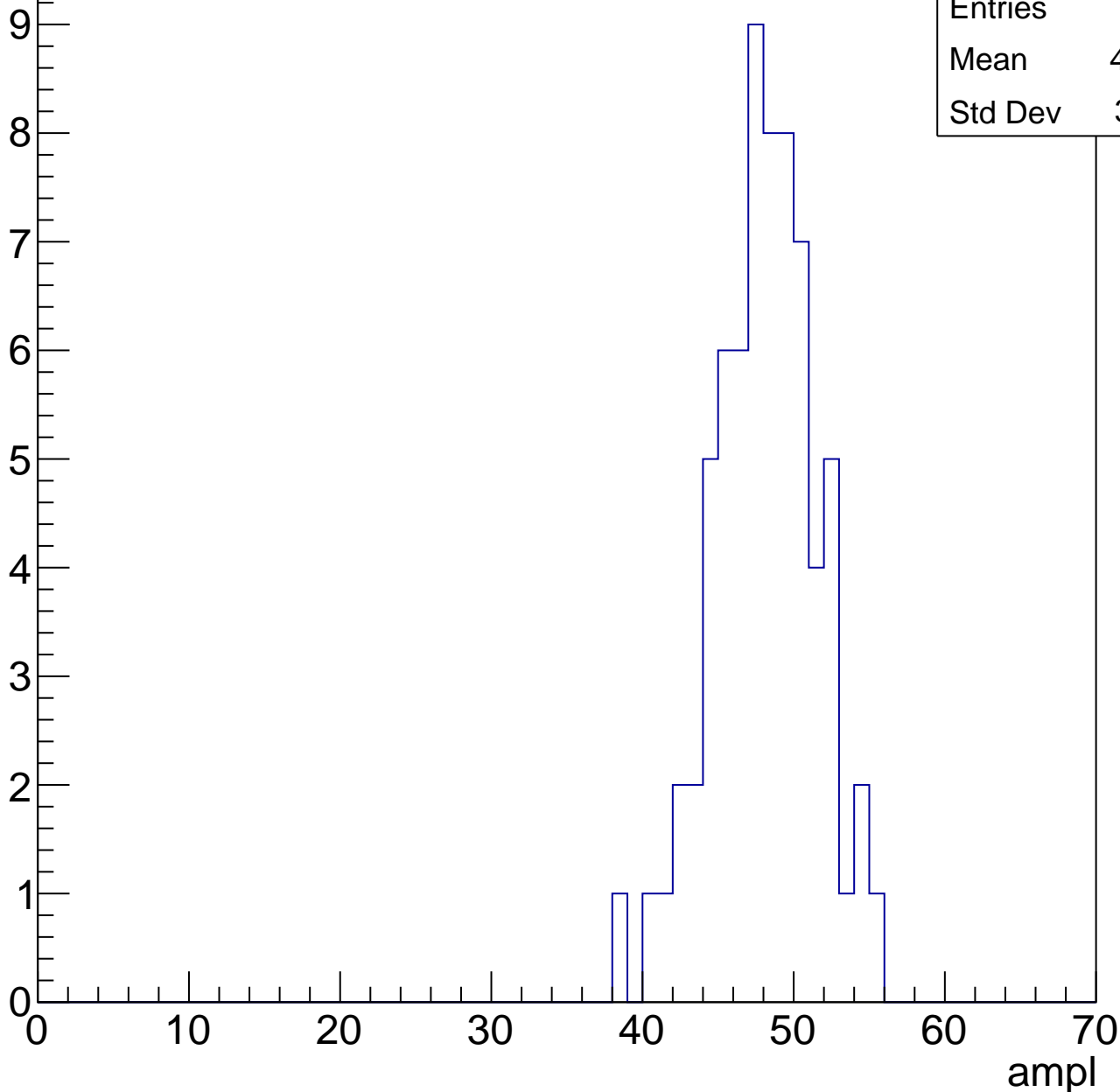


B1L101S, U6-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	47.59
Std Dev	3.381

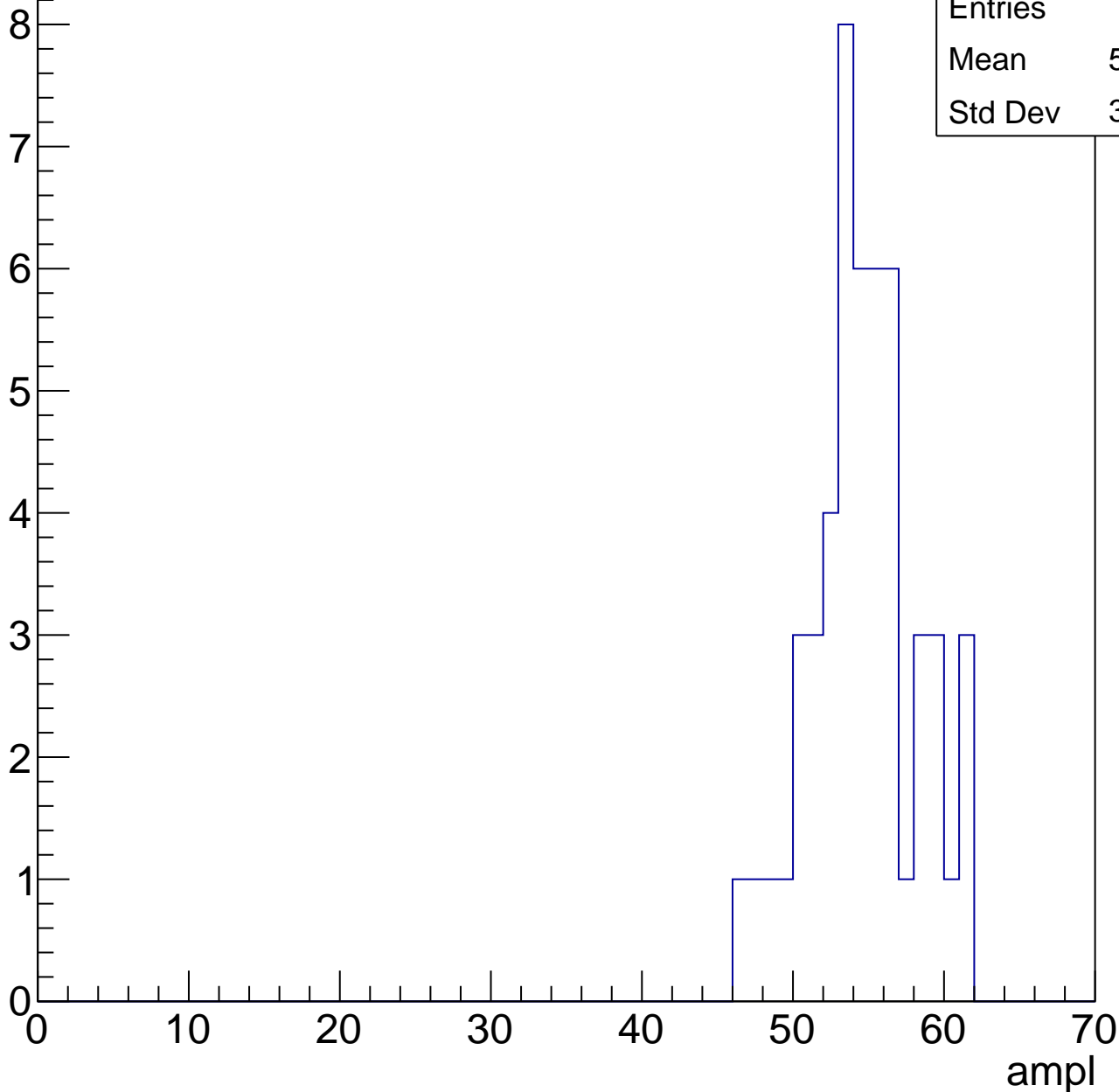


B1L101S, U6-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

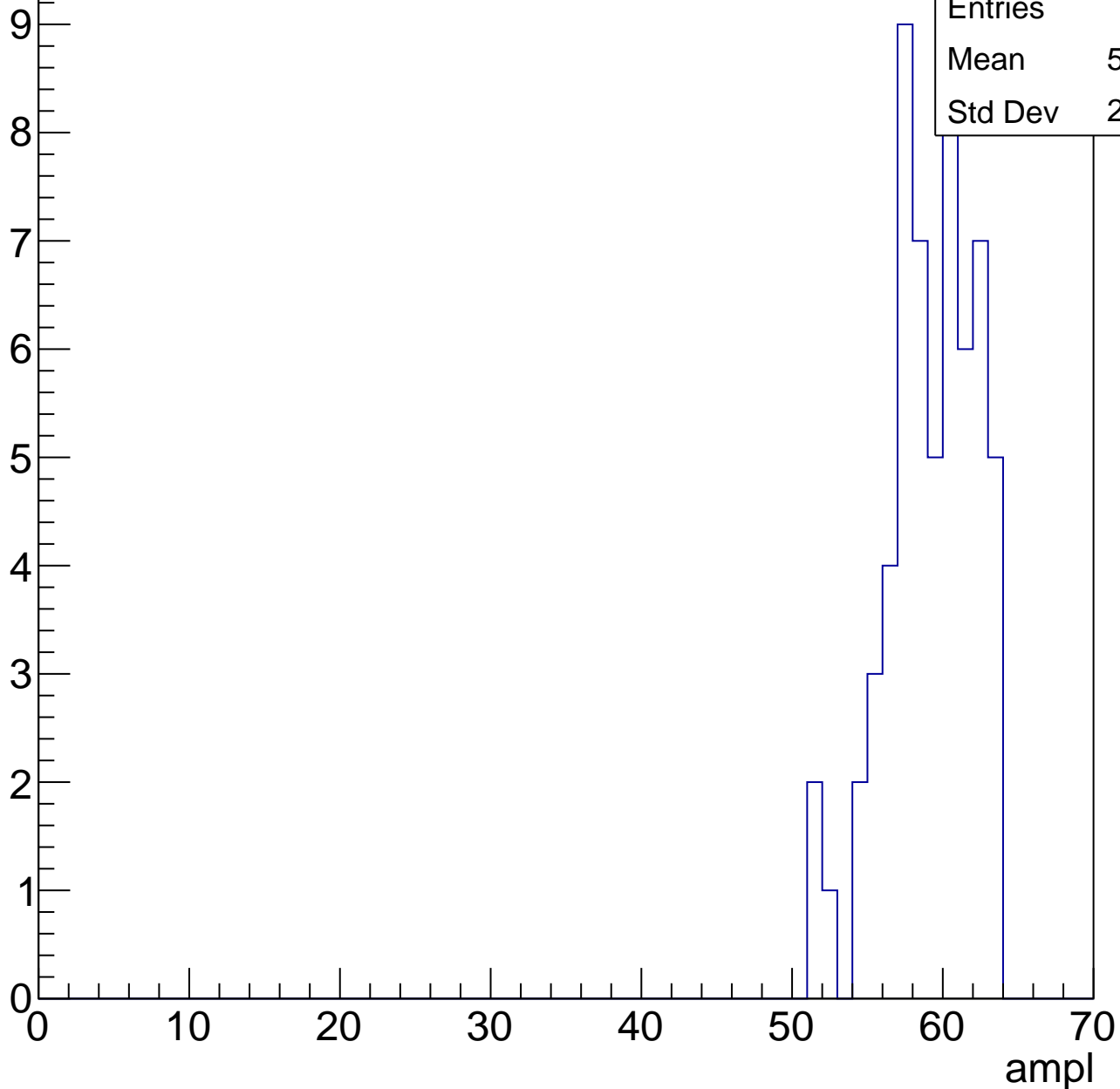
Entries	51
Mean	54.24
Std Dev	3.473



B1L101S, U6-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

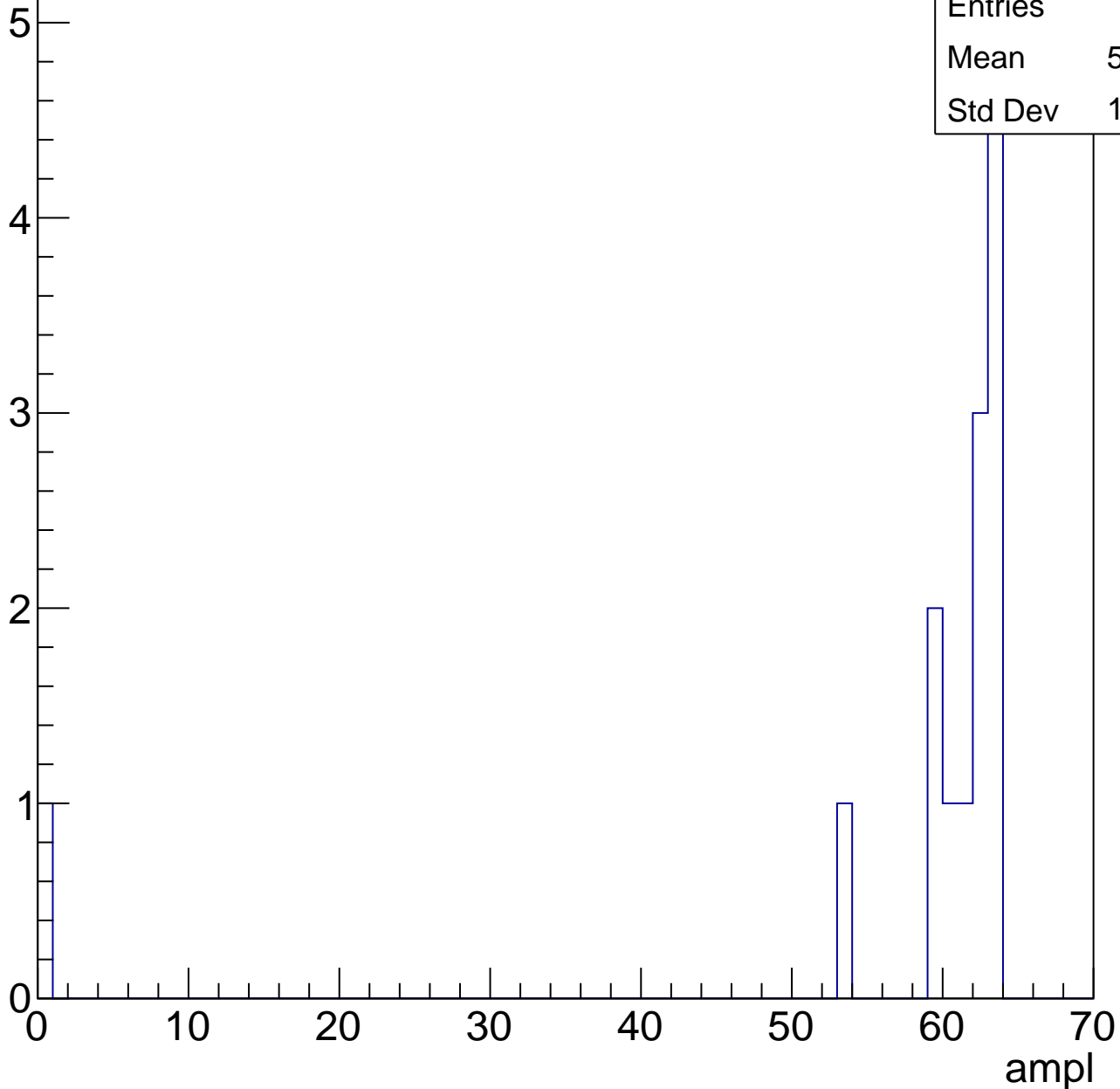


B1L101S, U6-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	56.64
Std Dev	15.93



B1L101S, U6-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch91, adc0

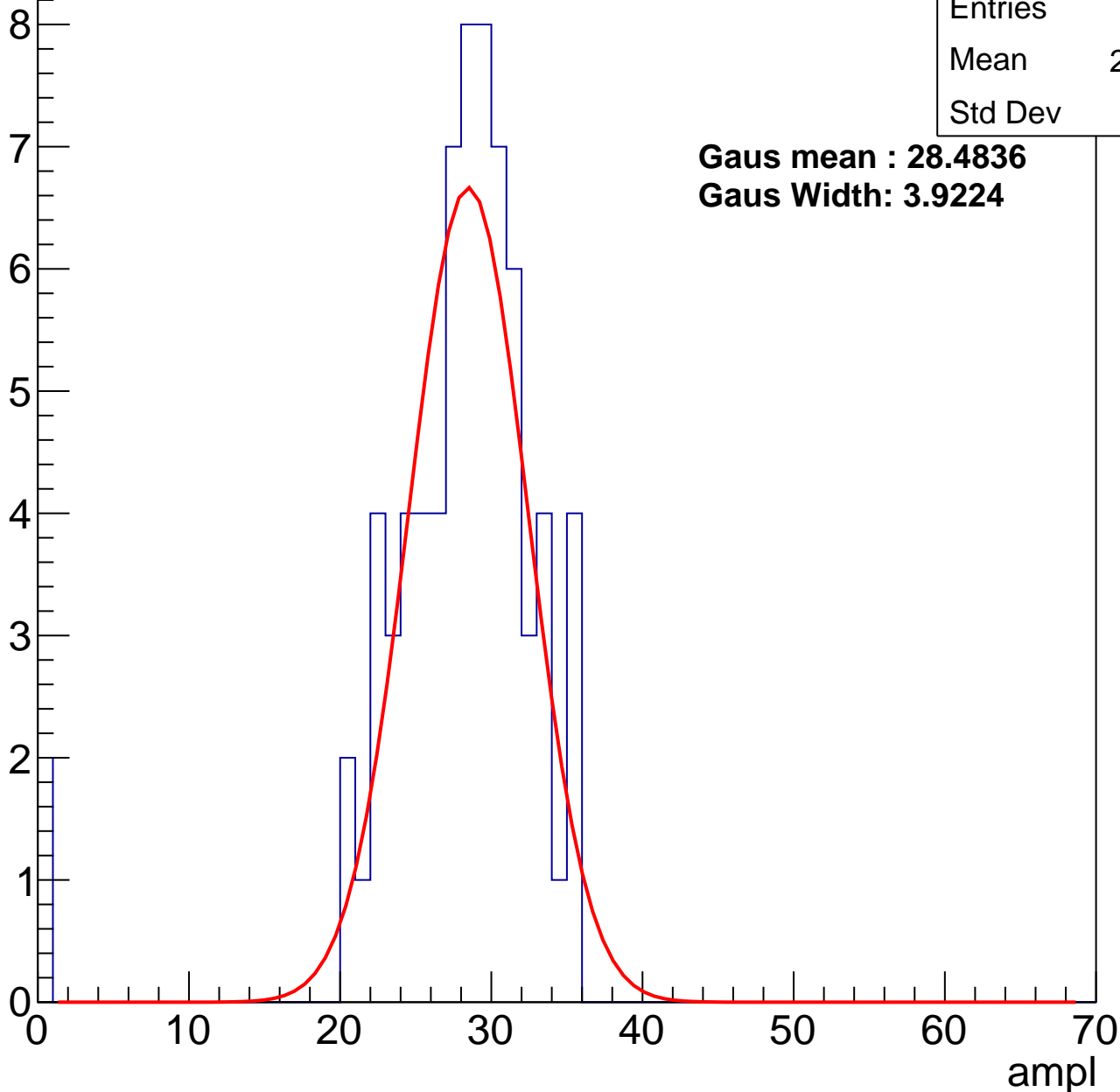
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.24
Std Dev	5.91

Gaus mean : 28.4836

Gaus Width: 3.9224



B1L101S, U6-ch91, adc1

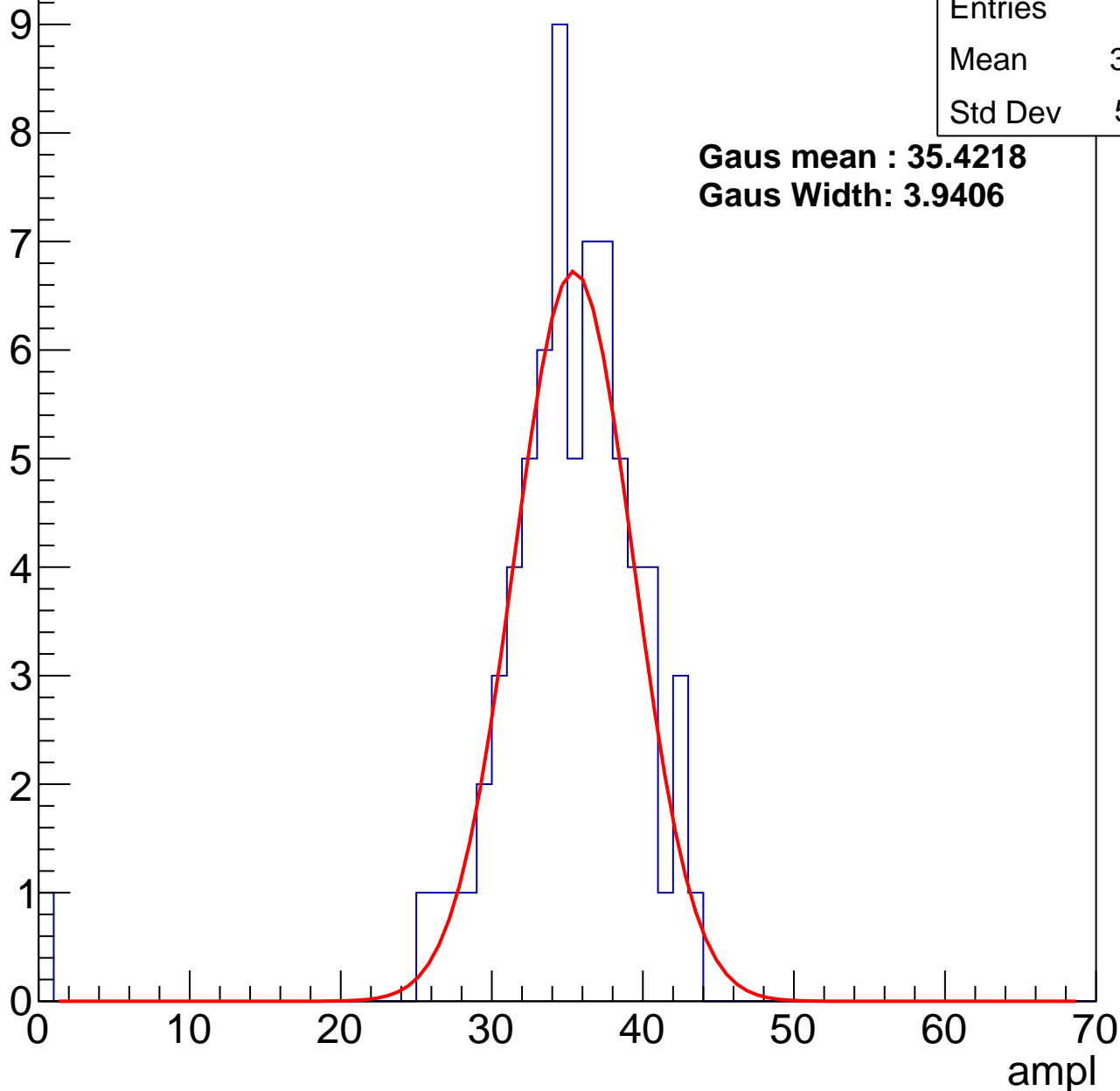
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	34.42
Std Dev	5.661

Gaus mean : 35.4218

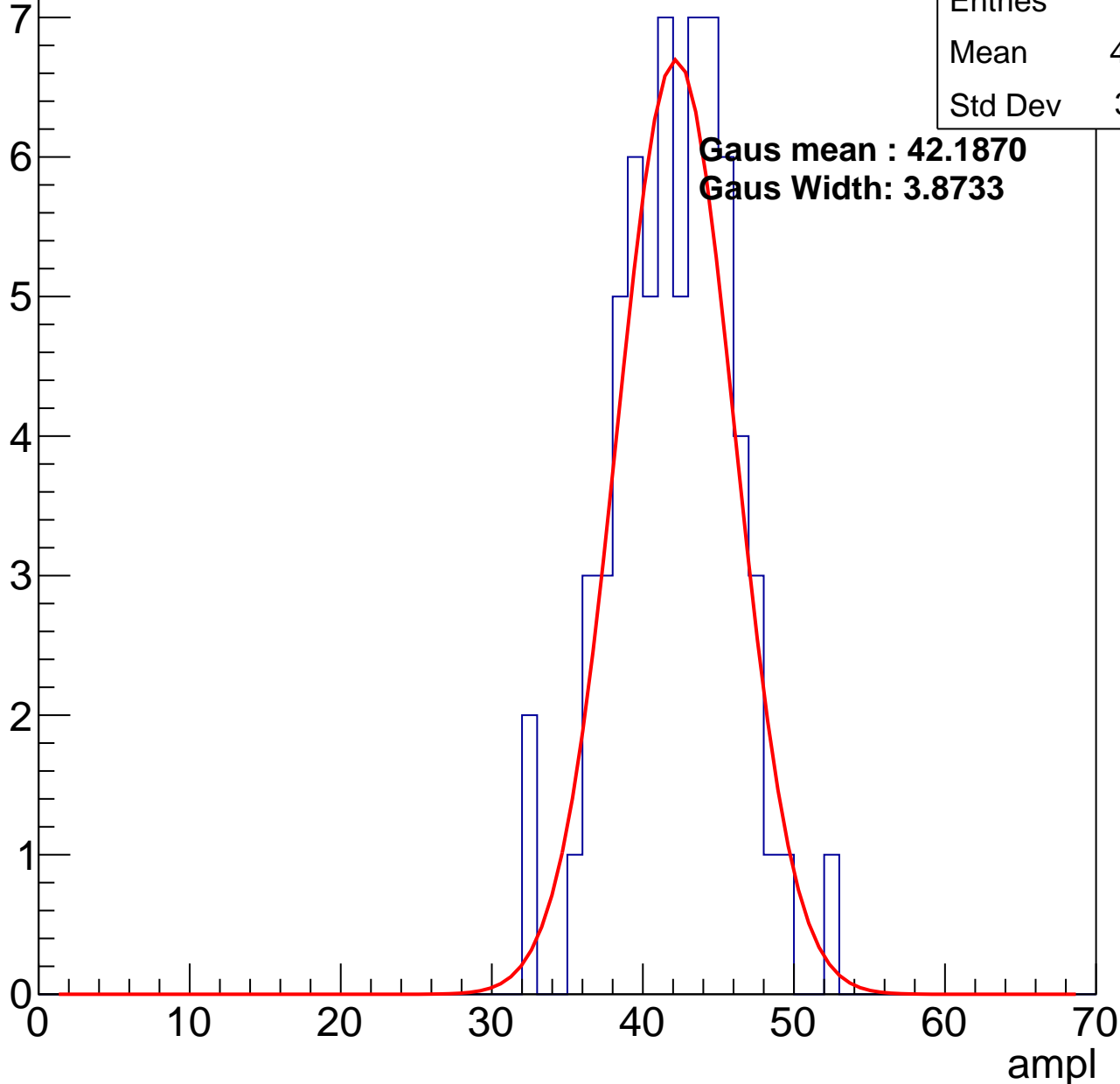
Gaus Width: 3.9406



B1L101S, U6-ch91, adc2

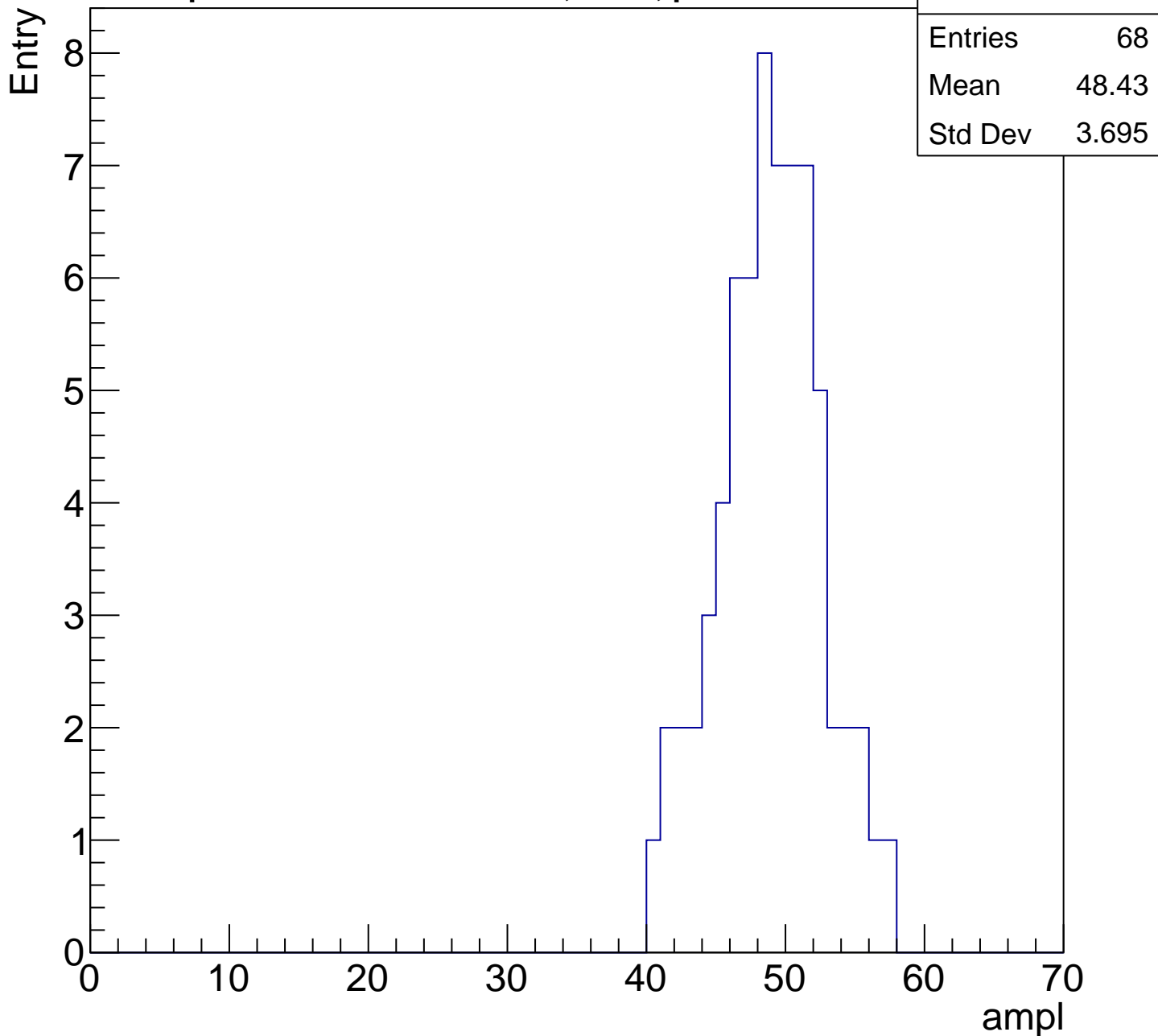
calib_packv5_042523_0143.root, FC#0, port D2

Entry



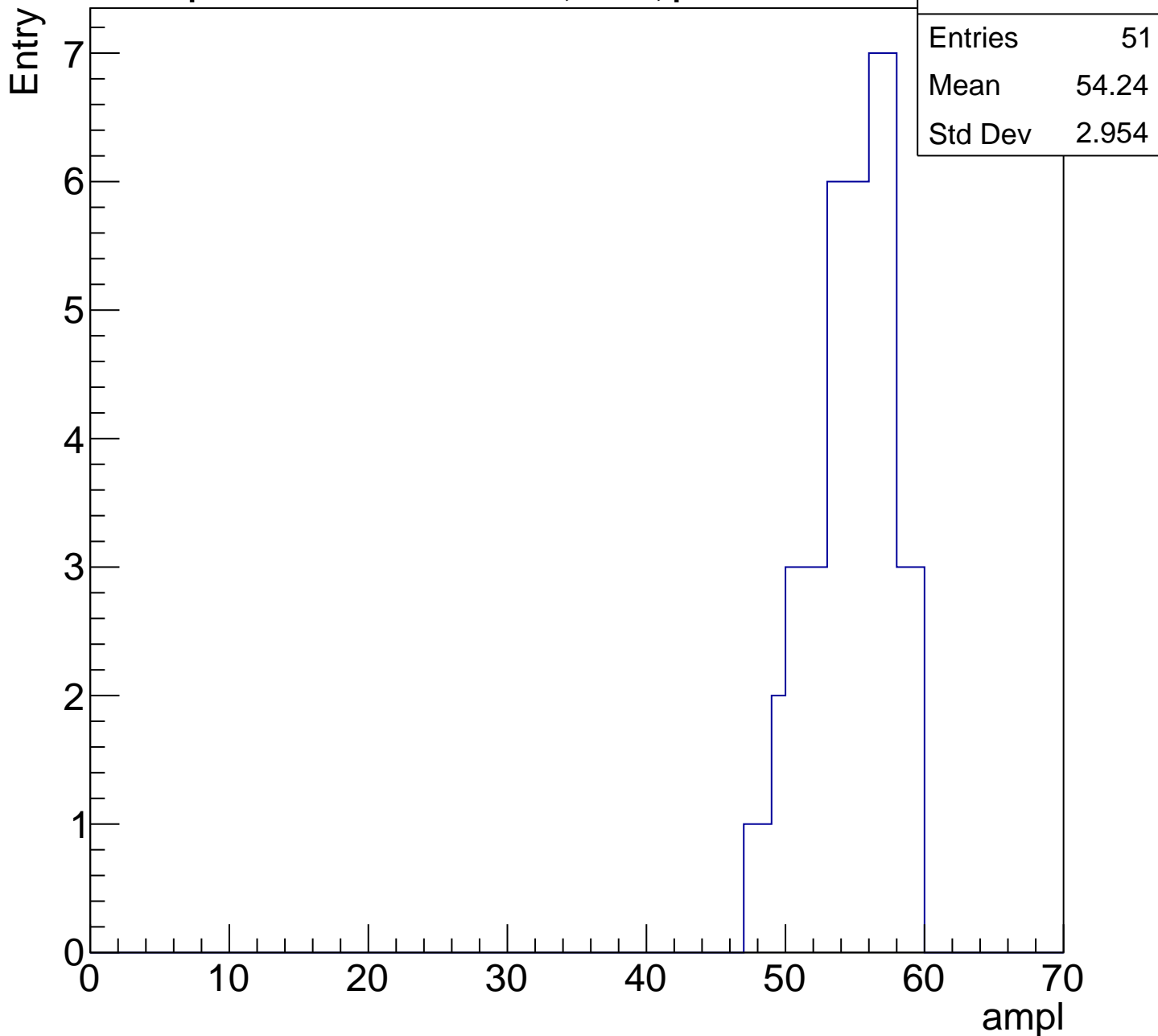
B1L101S, U6-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2



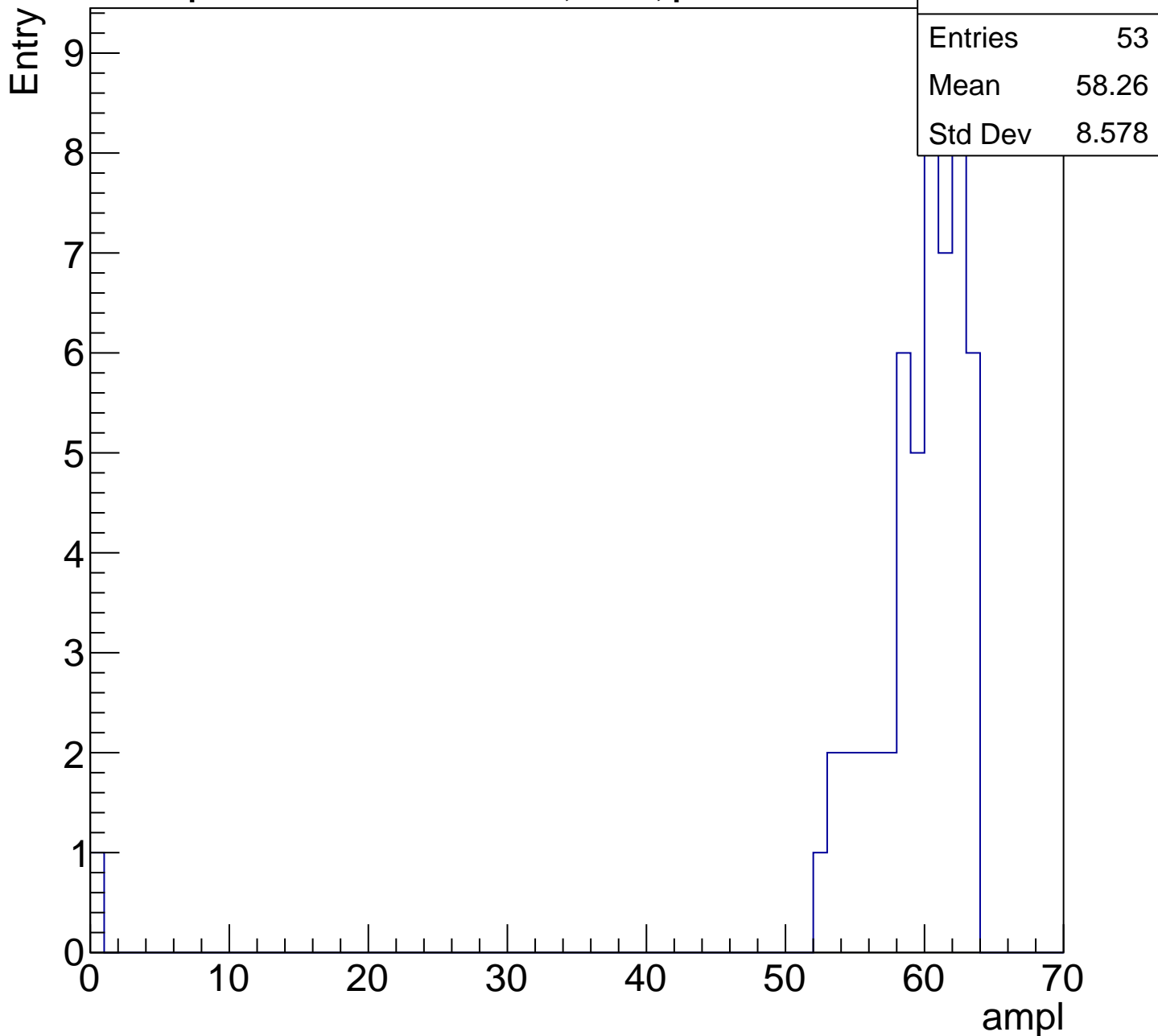
B1L101S, U6-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch91, adc5

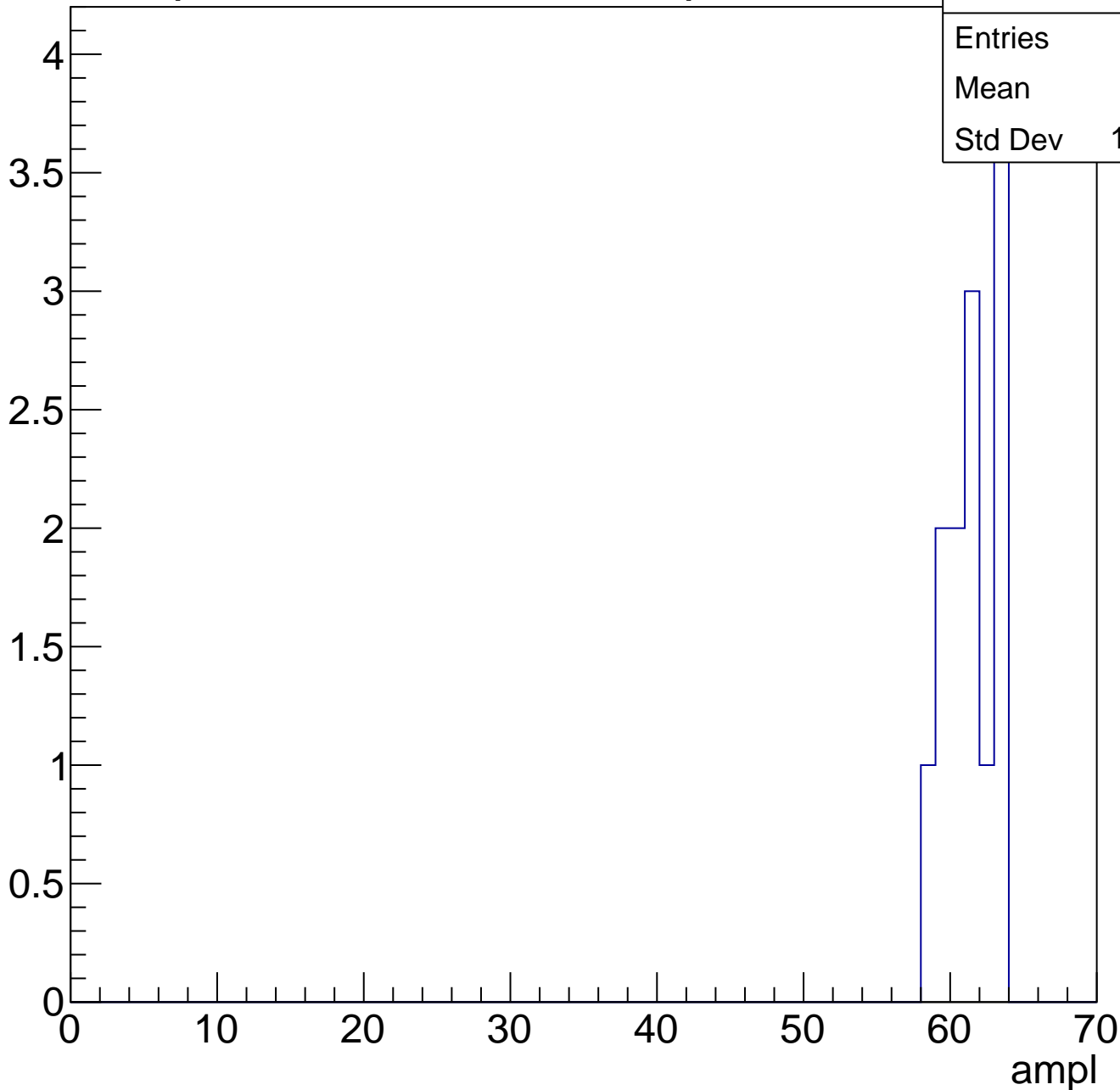
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch92, adc0

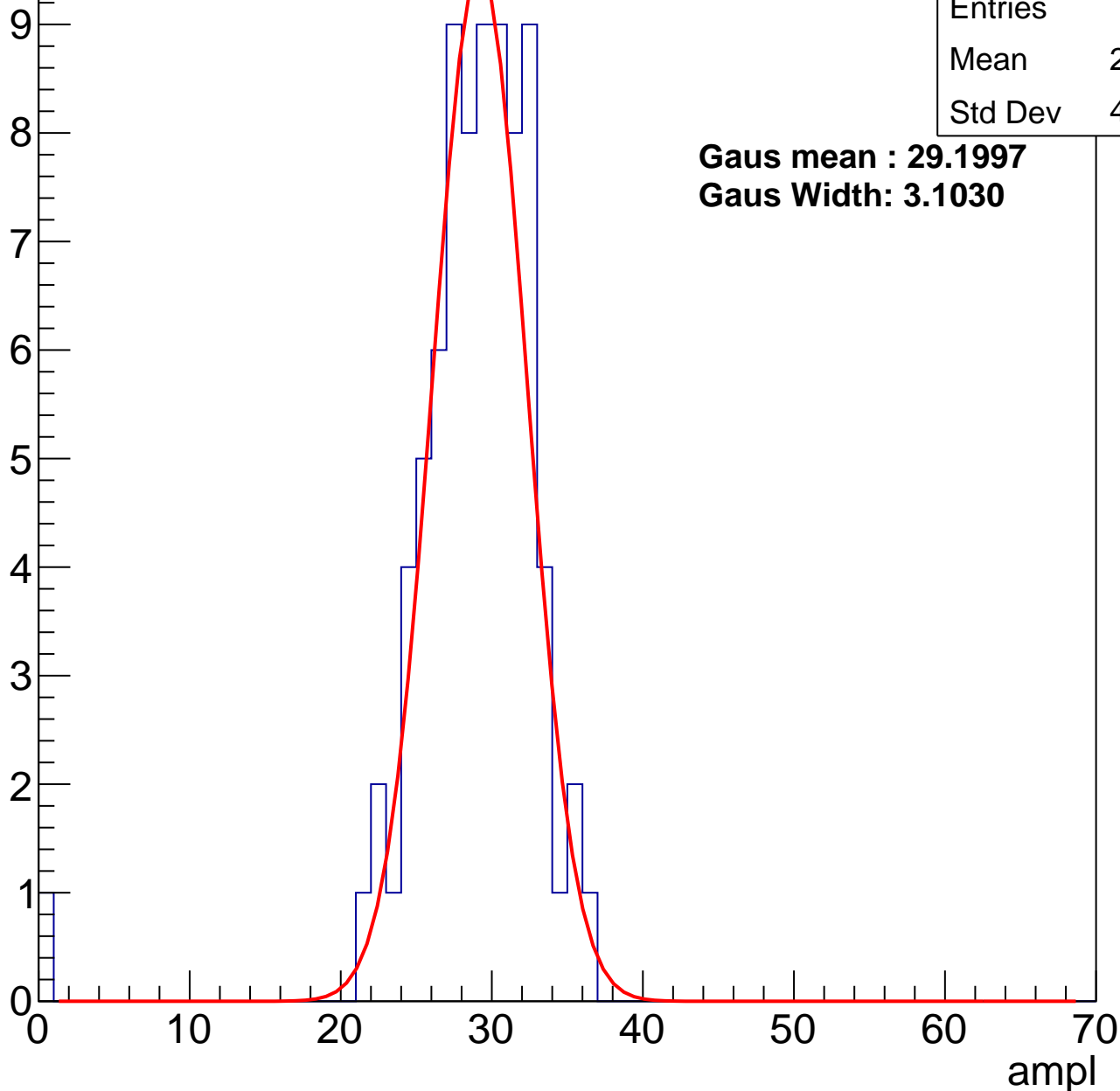
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.39
Std Dev	4.499

Gaus mean : 29.1997

Gaus Width: 3.1030



B1L101S, U6-ch92, adc1

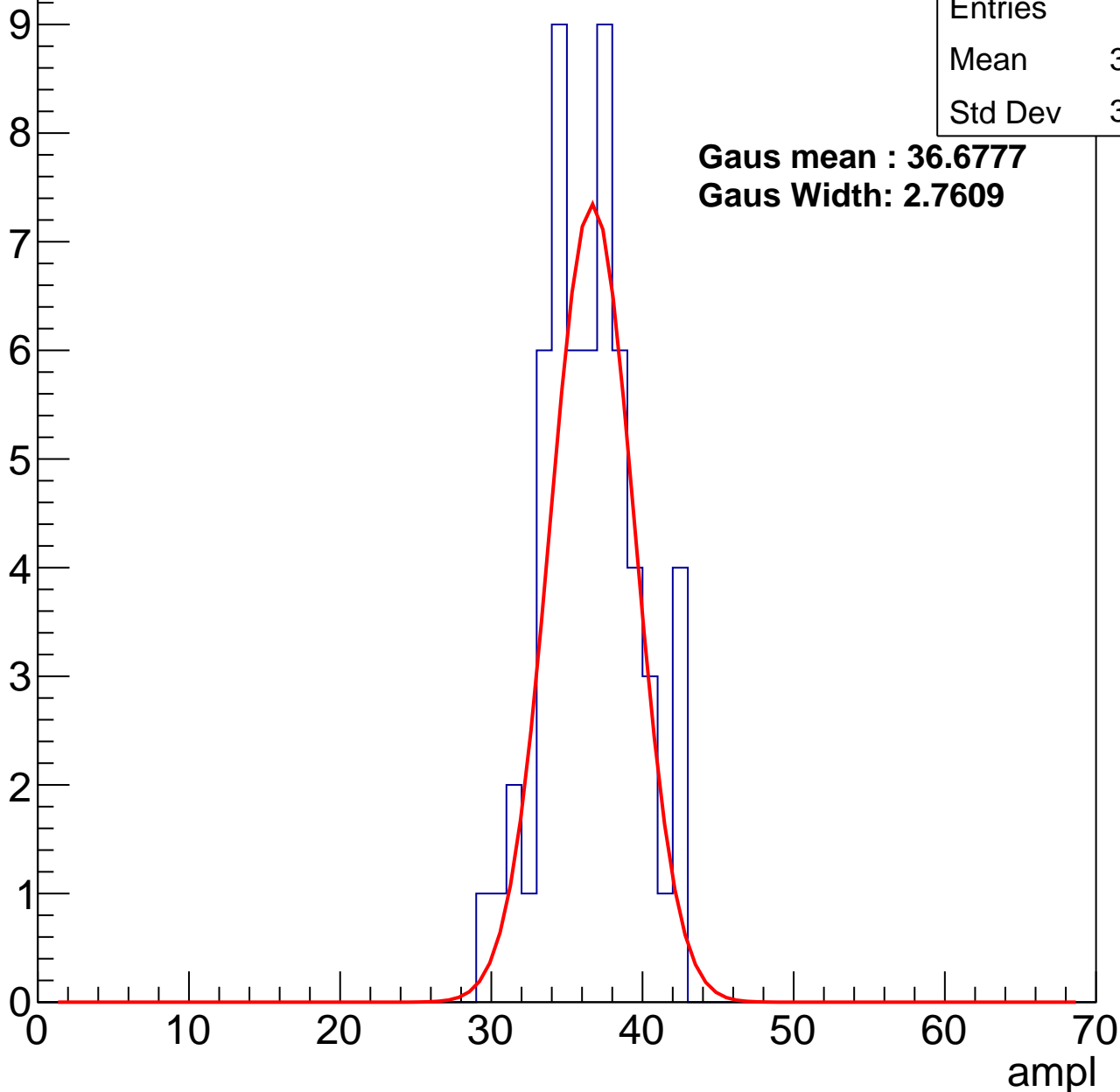
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.08
Std Dev	3.004

Gaus mean : 36.6777

Gaus Width: 2.7609



B1L101S, U6-ch92, adc2

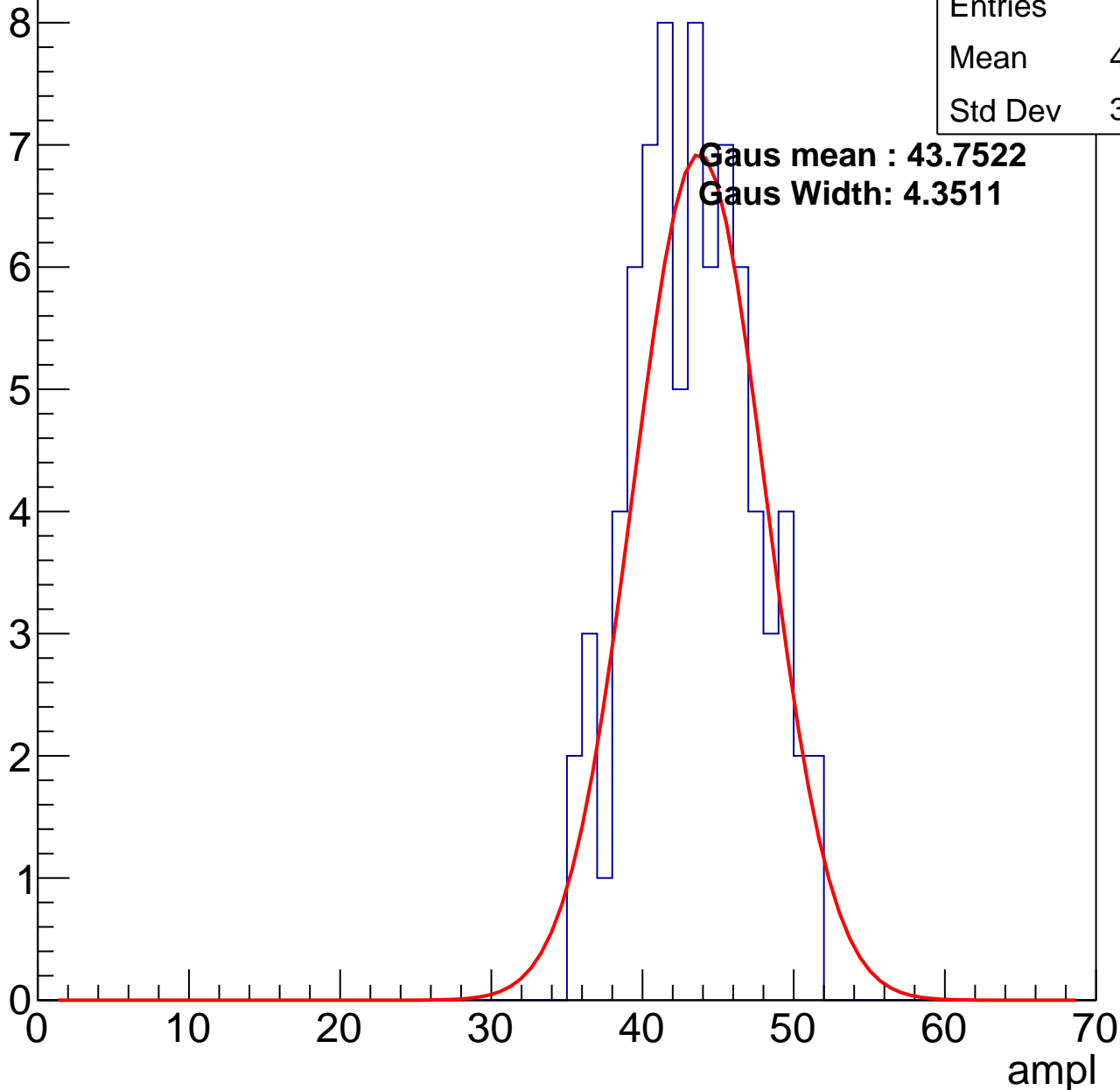
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.92
Std Dev	3.928

Gaus mean : 43.7522

Gaus Width: 4.3511

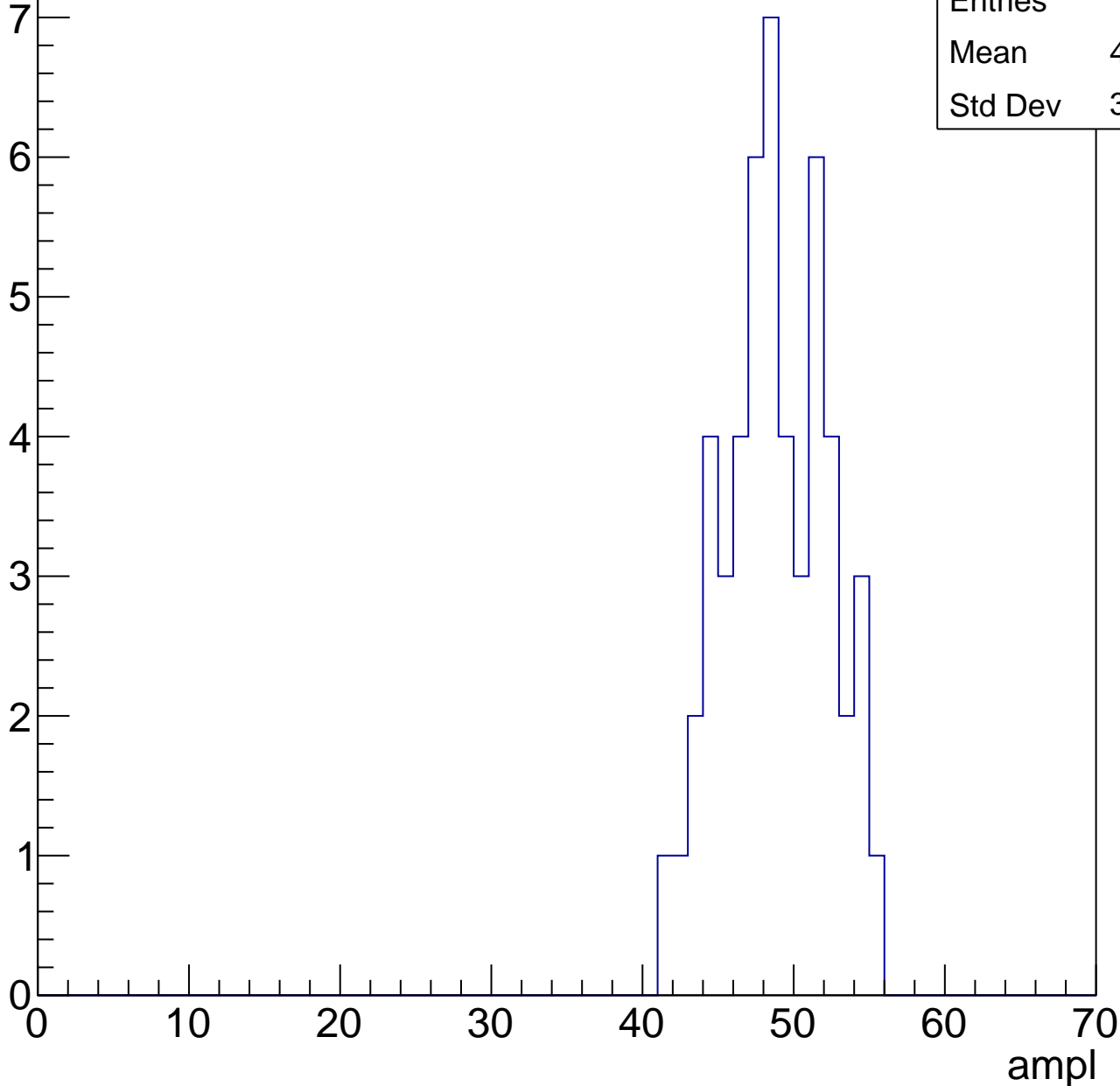


B1L101S, U6-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

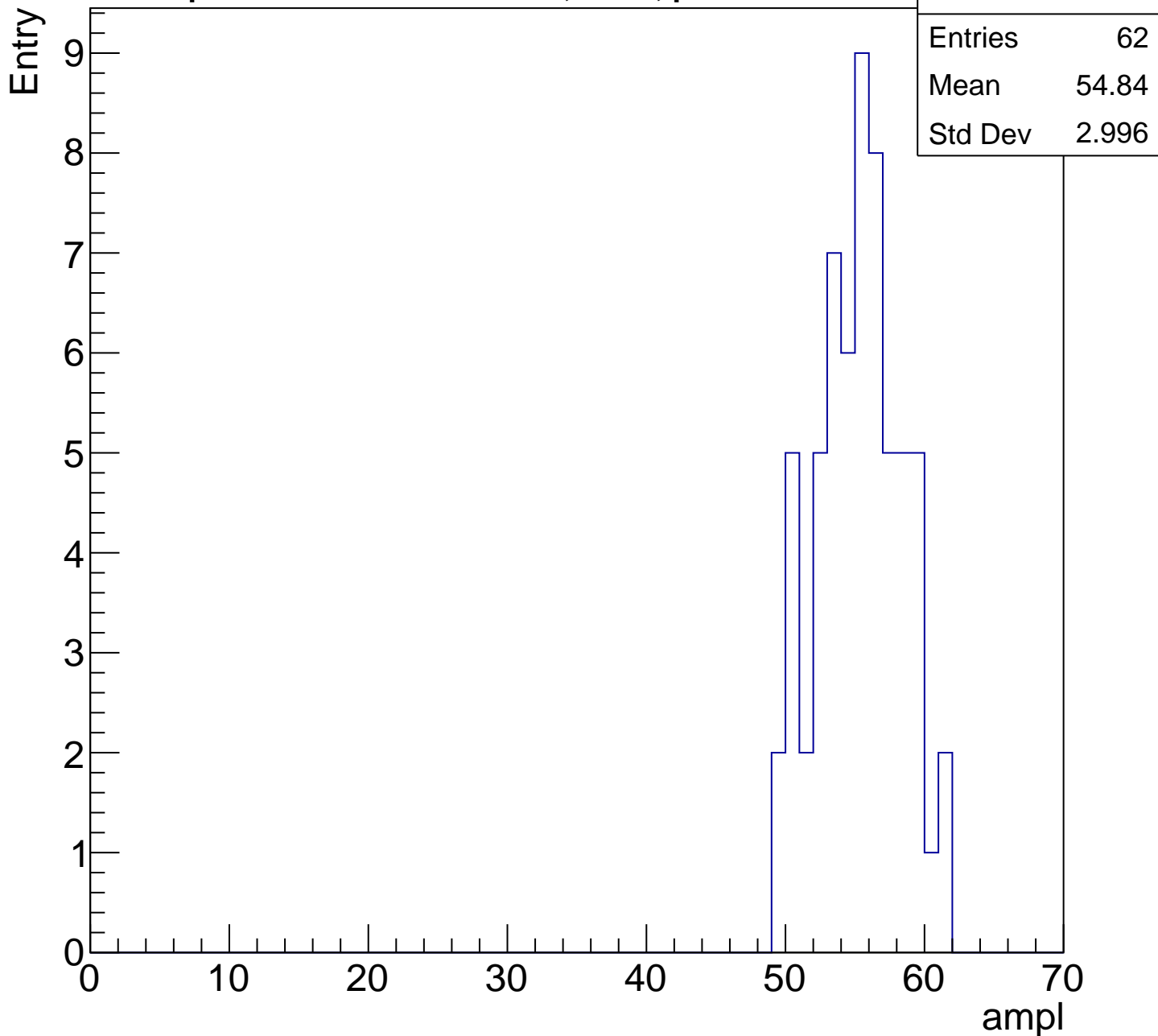
Entry

Entries	51
Mean	48.33
Std Dev	3.382



B1L101S, U6-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

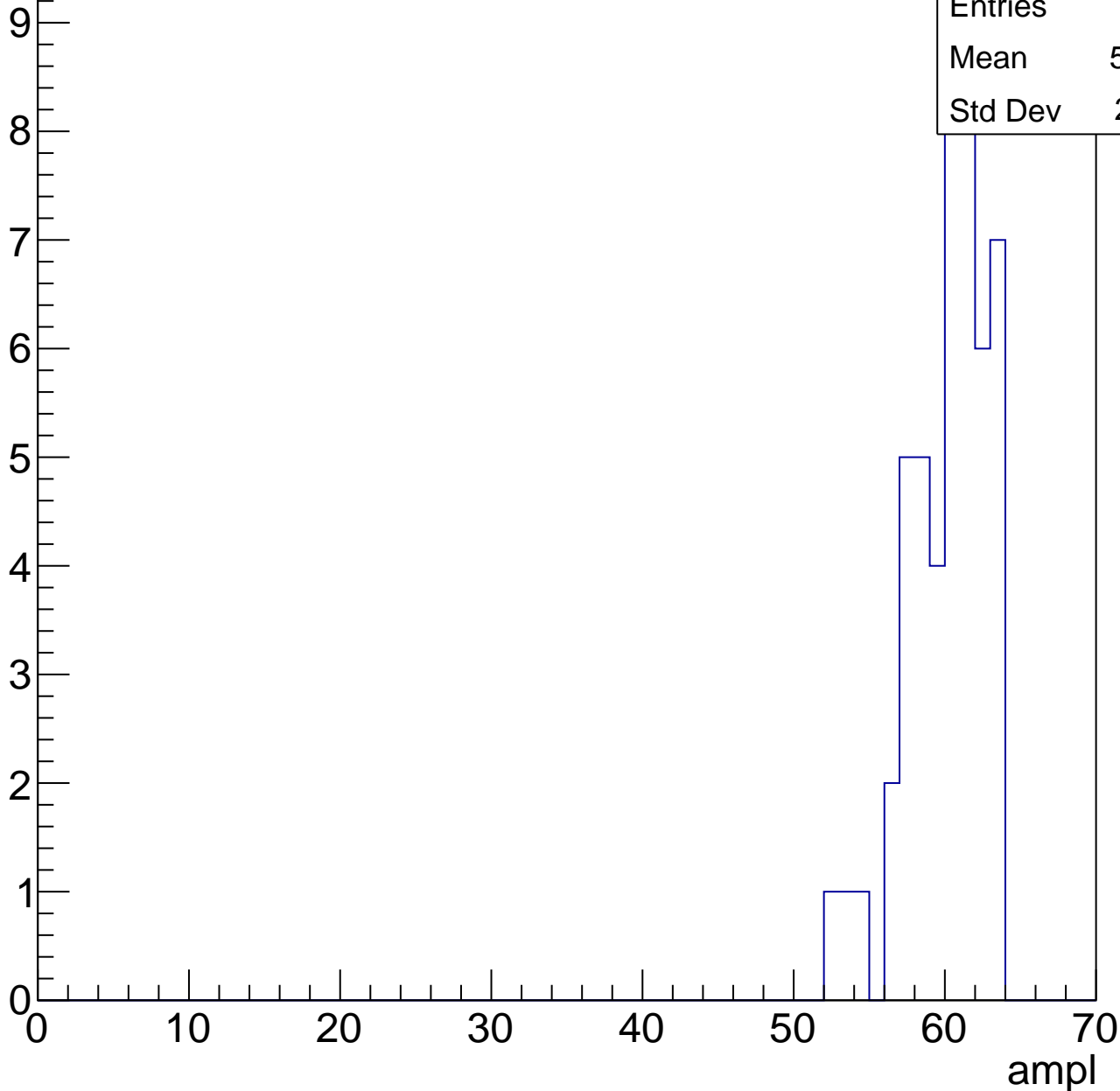


B1L101S, U6-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

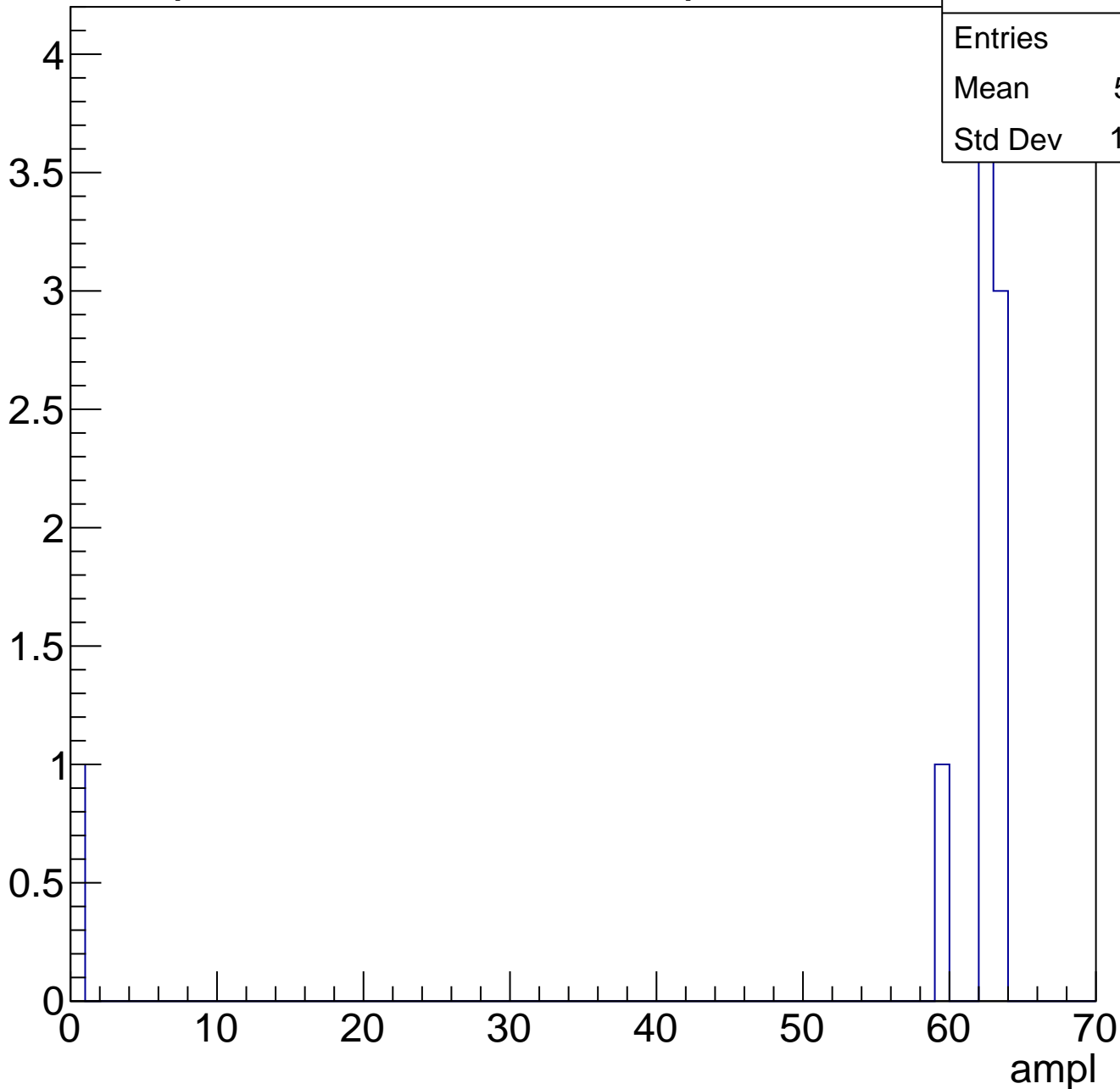
Entries	49
Mean	59.65
Std Dev	2.631



B1L101S, U6-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch93, adc0

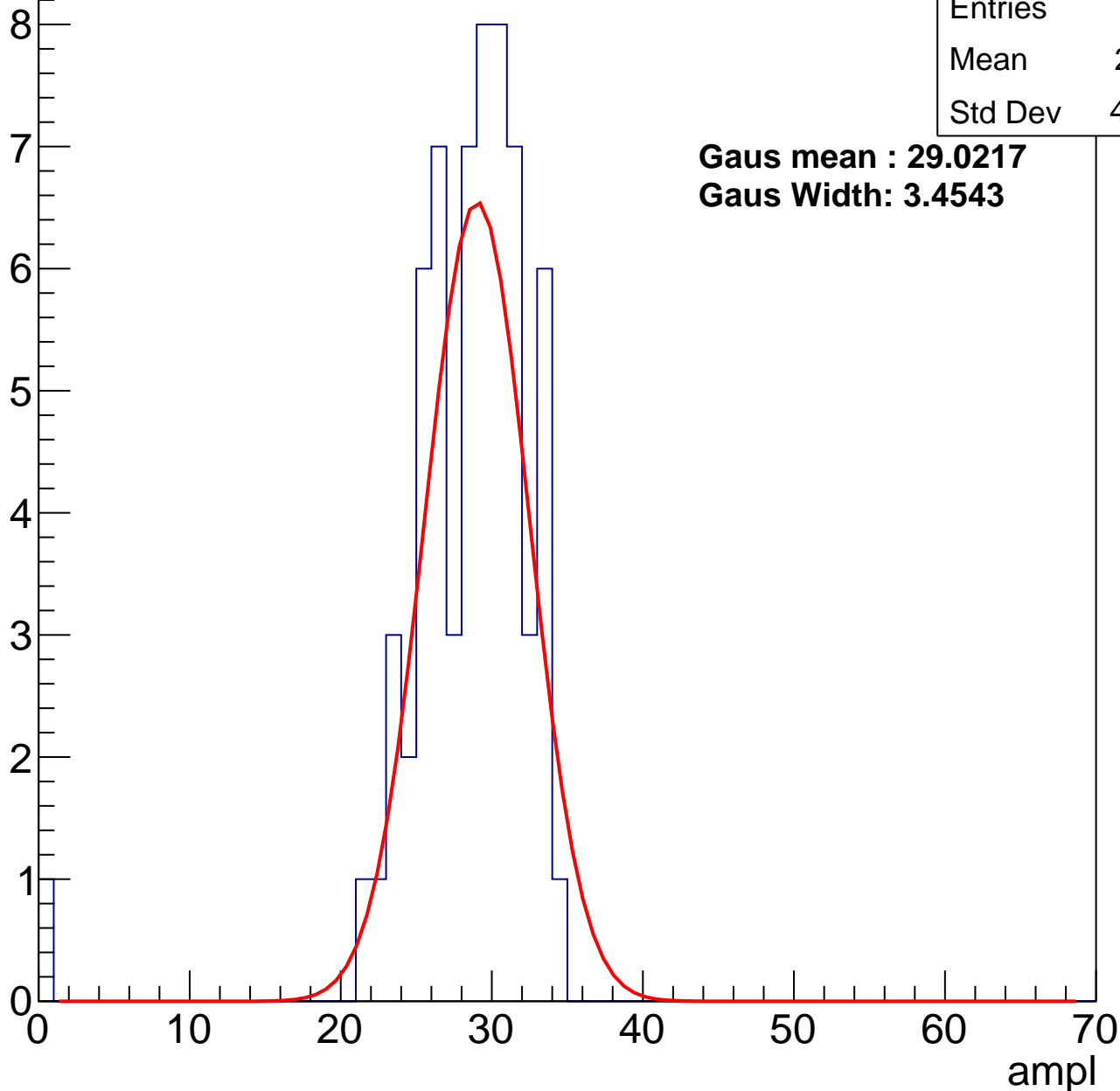
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.91
Std Dev	4.673

Gaus mean : 29.0217

Gaus Width: 3.4543



B1L101S, U6-ch93, adc1

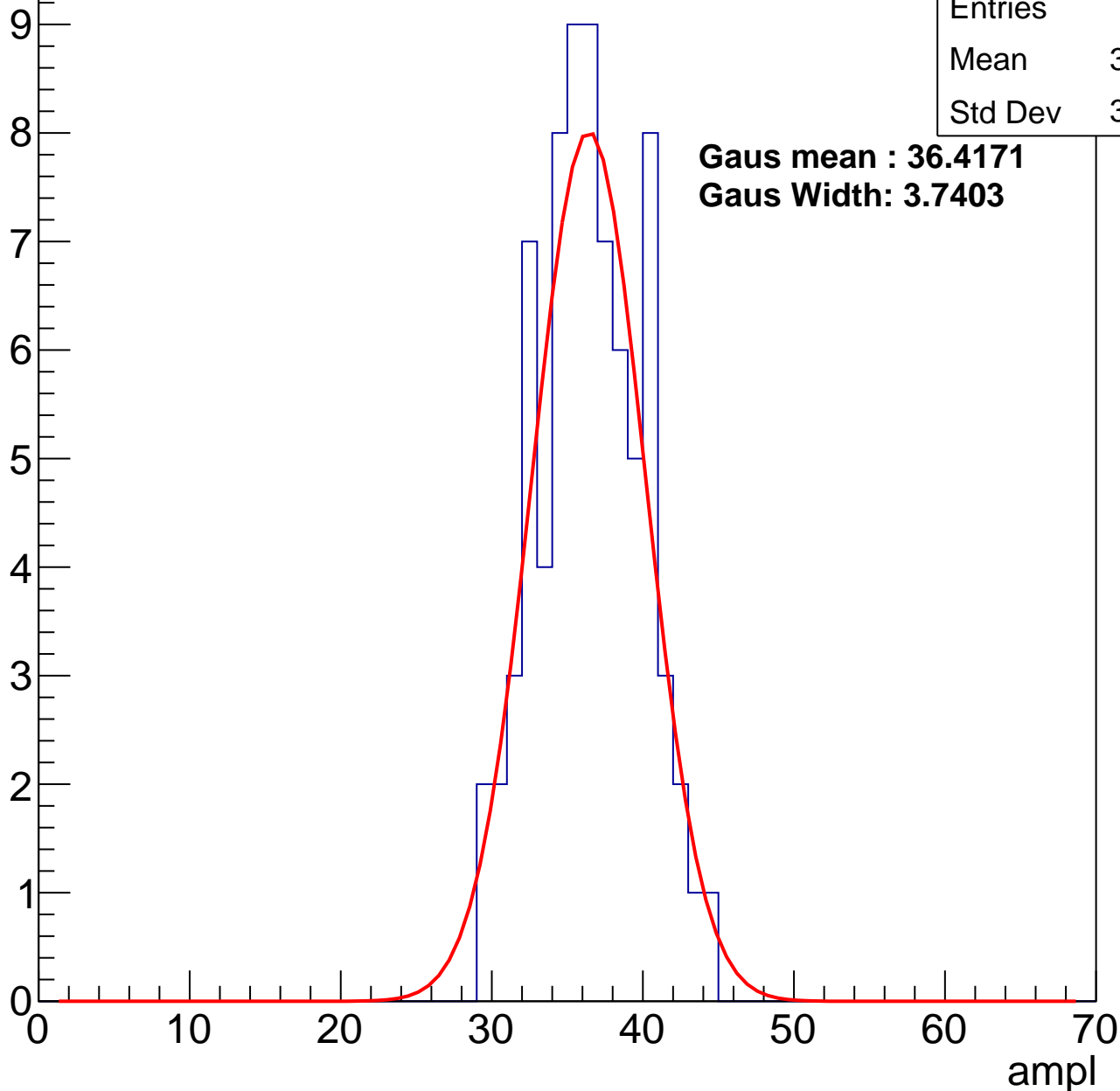
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.03
Std Dev	3.415

Gaus mean : 36.4171

Gaus Width: 3.7403



B1L101S, U6-ch93, adc2

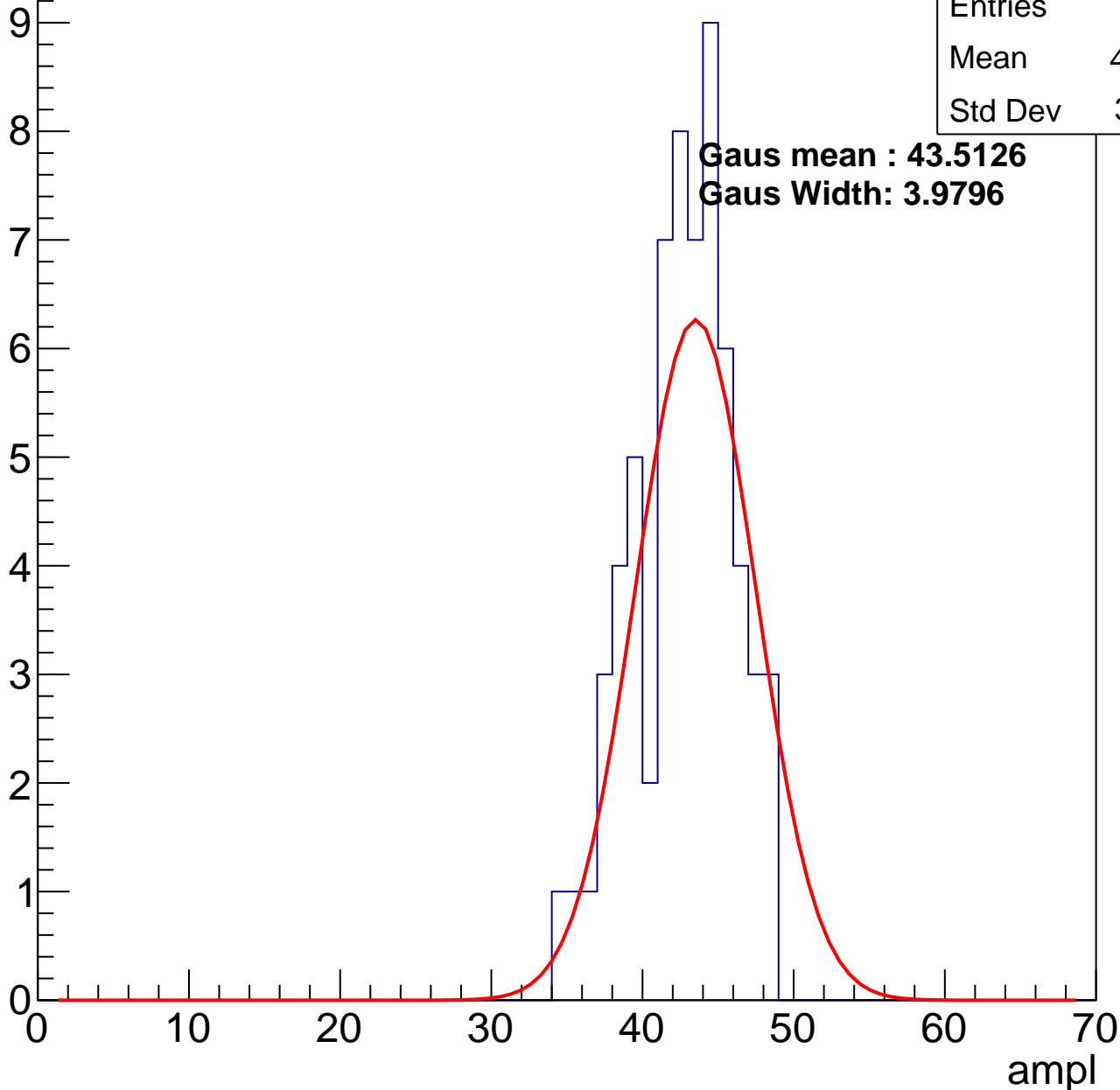
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.22
Std Dev	3.281

Gaus mean : 43.5126

Gaus Width: 3.9796

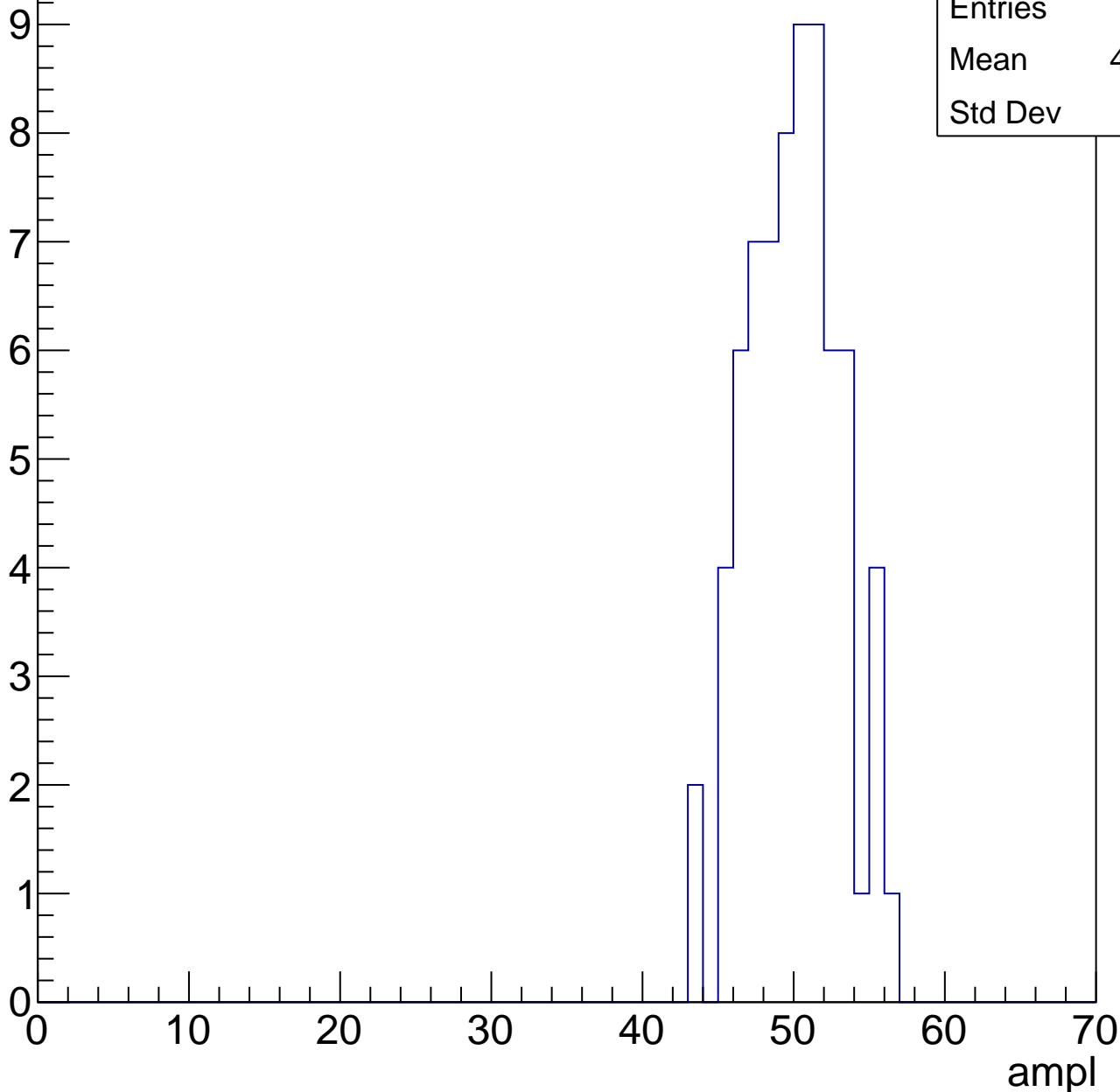


B1L101S, U6-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

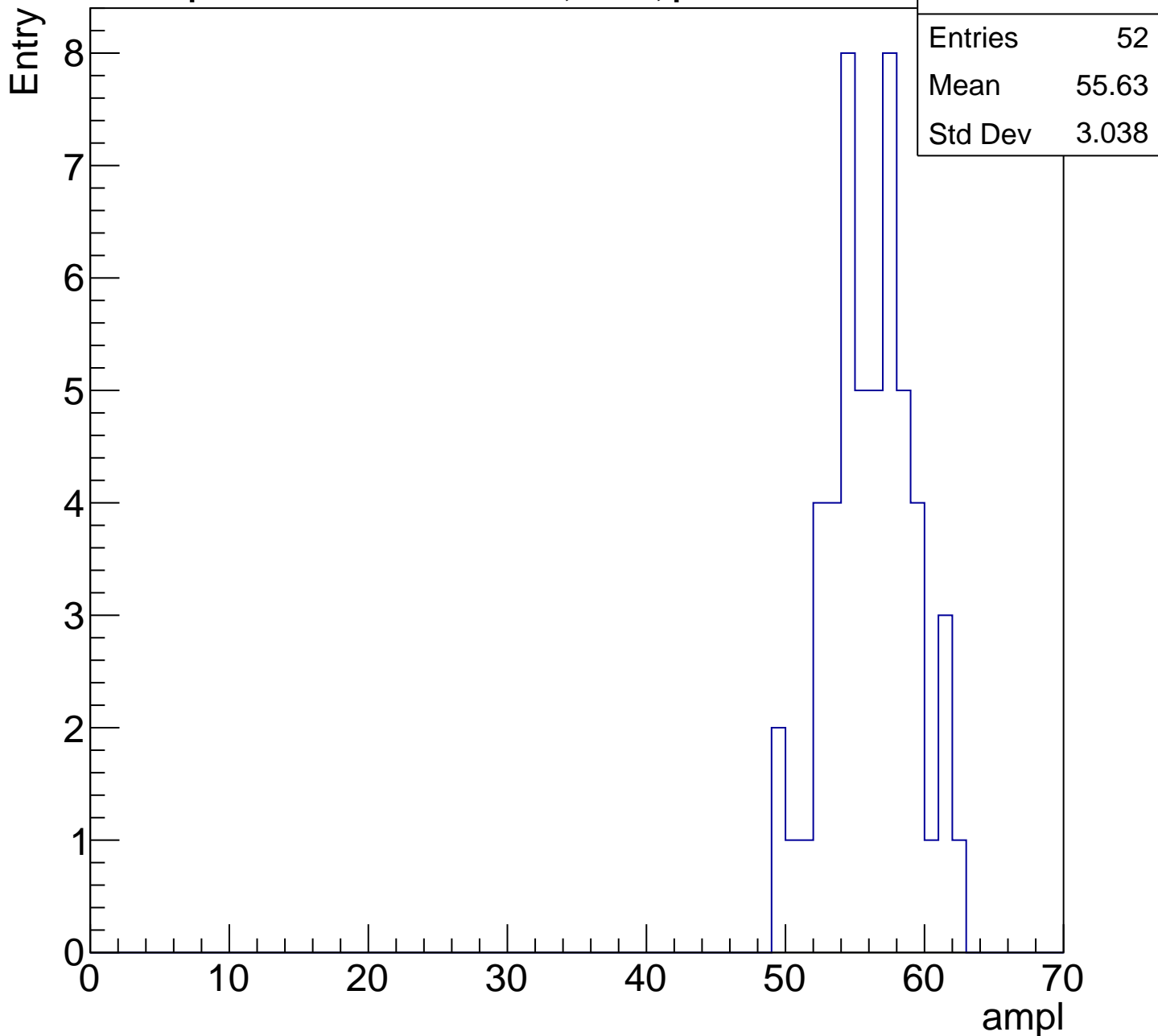
Entry

Entries	70
Mean	49.54
Std Dev	2.97



B1L101S, U6-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

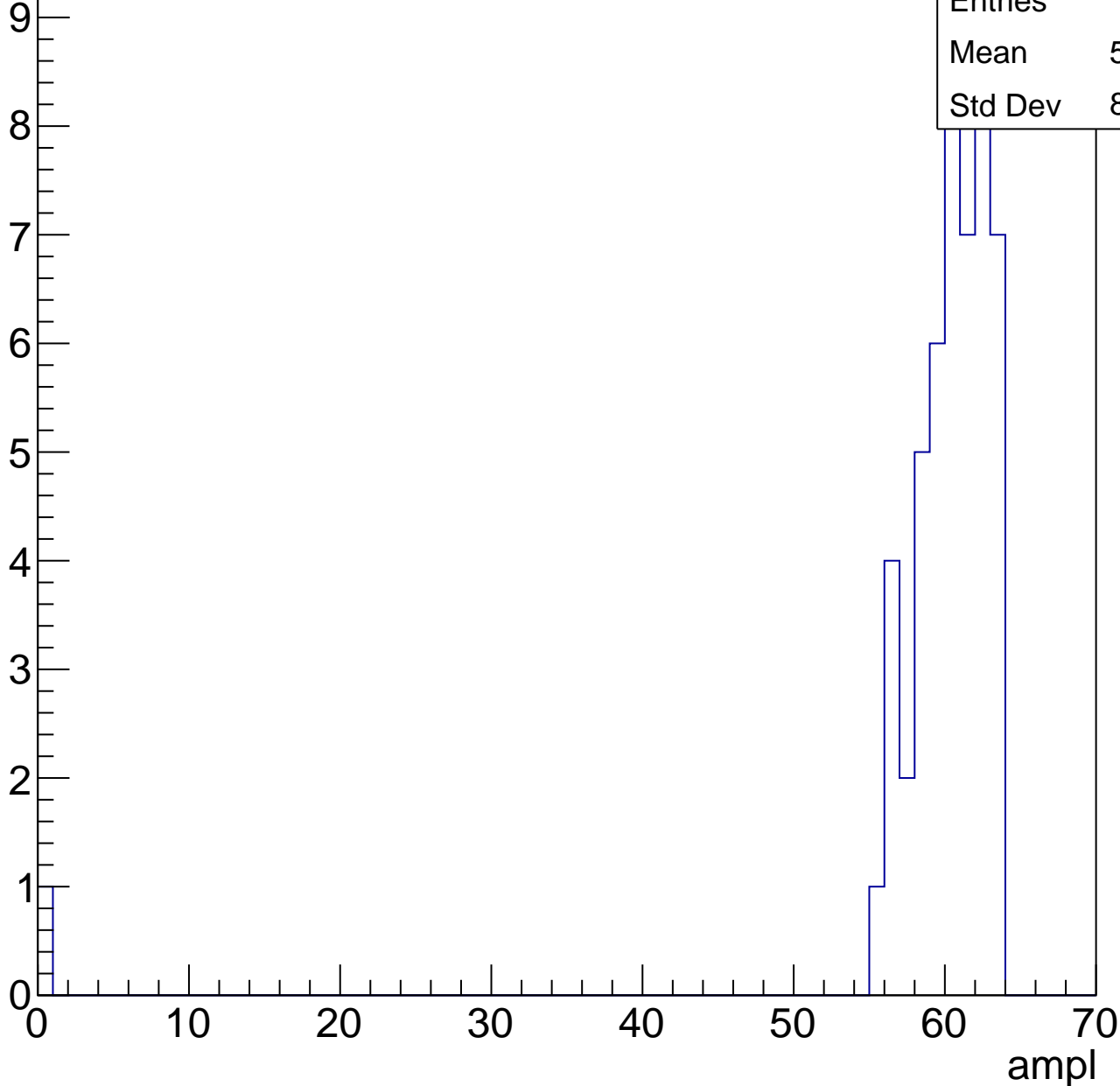


B1L101S, U6-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

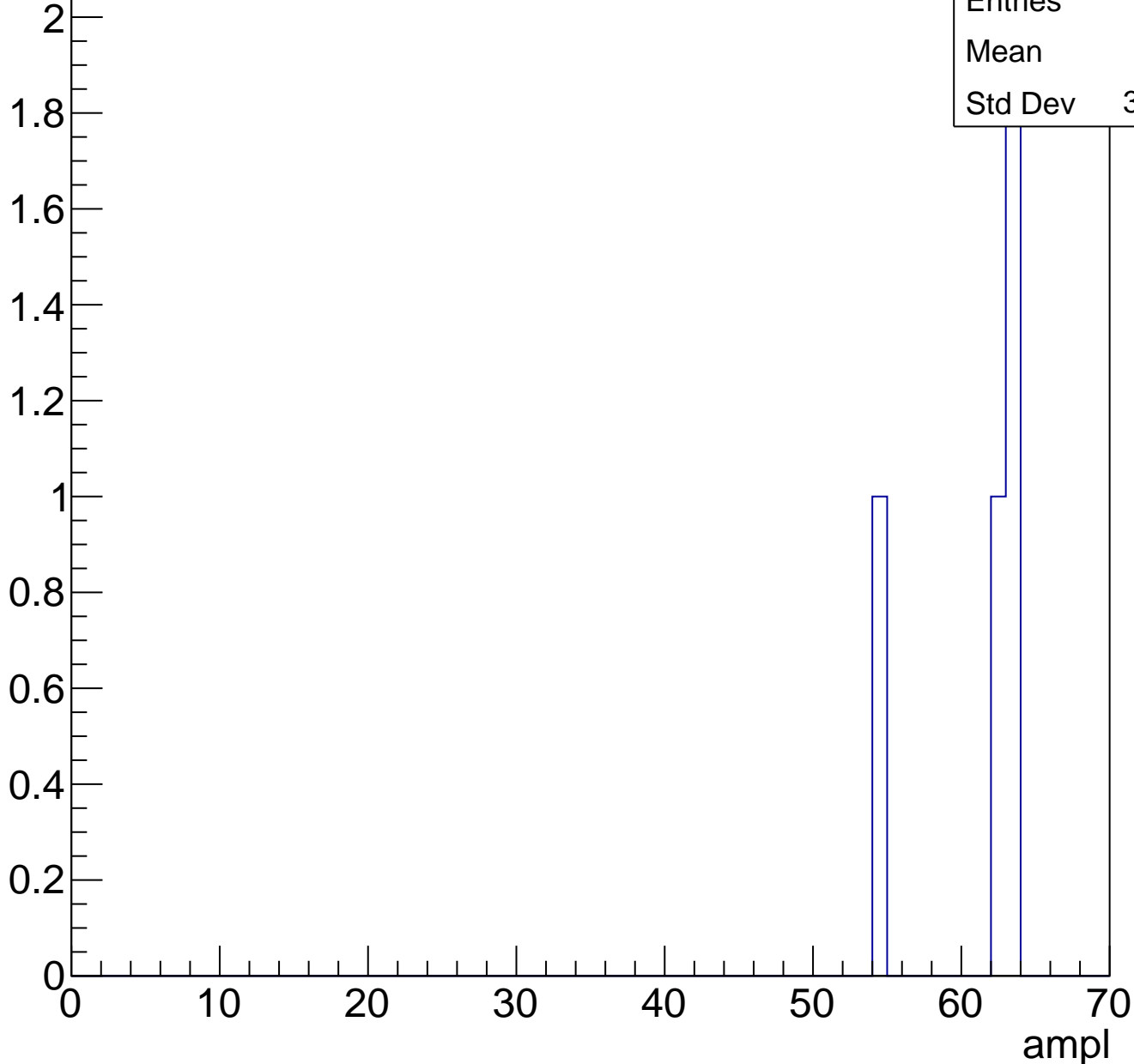
Entries	50
Mean	58.82
Std Dev	8.678



B1L101S, U6-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

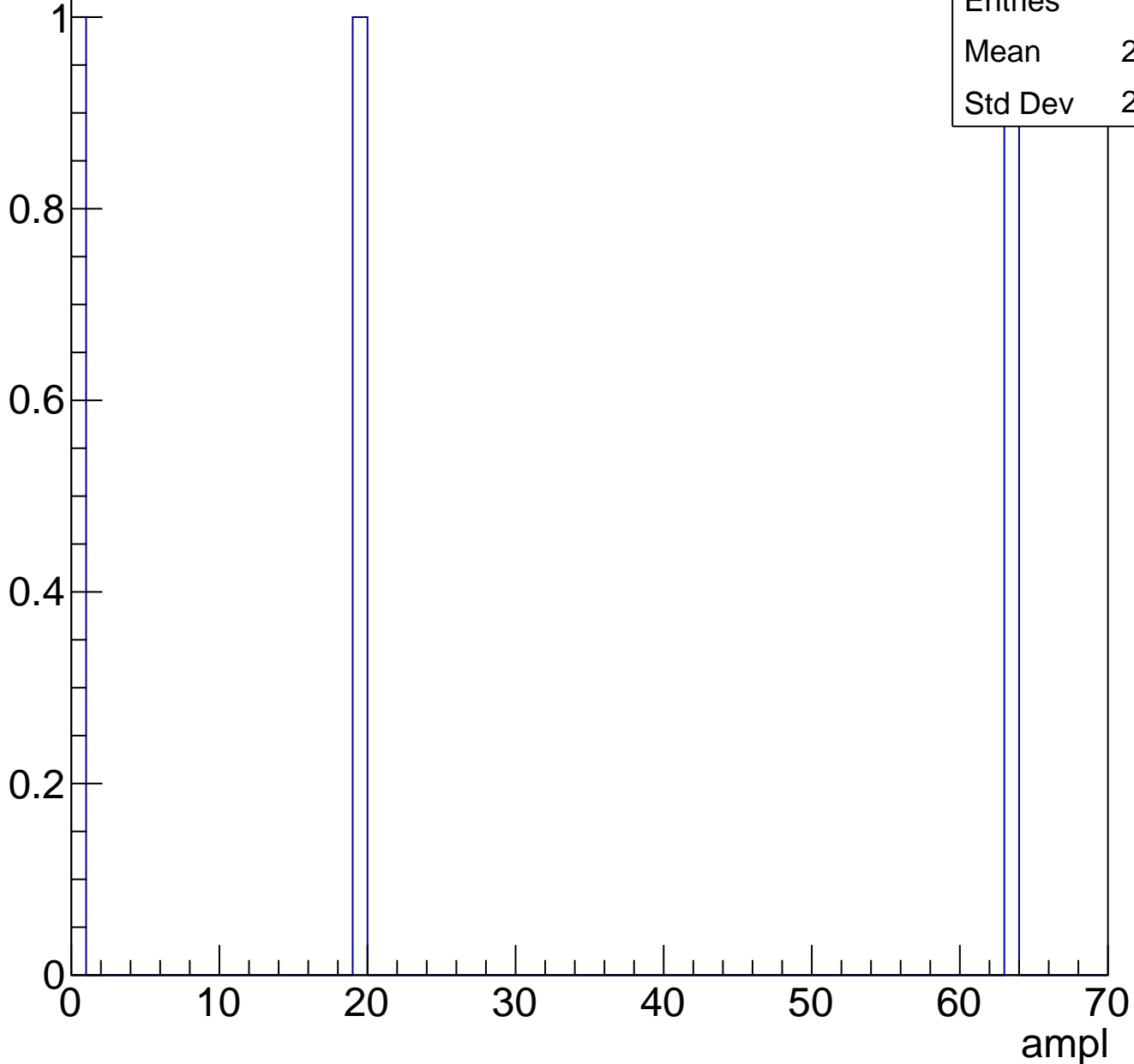
Entry



B1L101S, U6-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L101S, U6-ch94, adc0

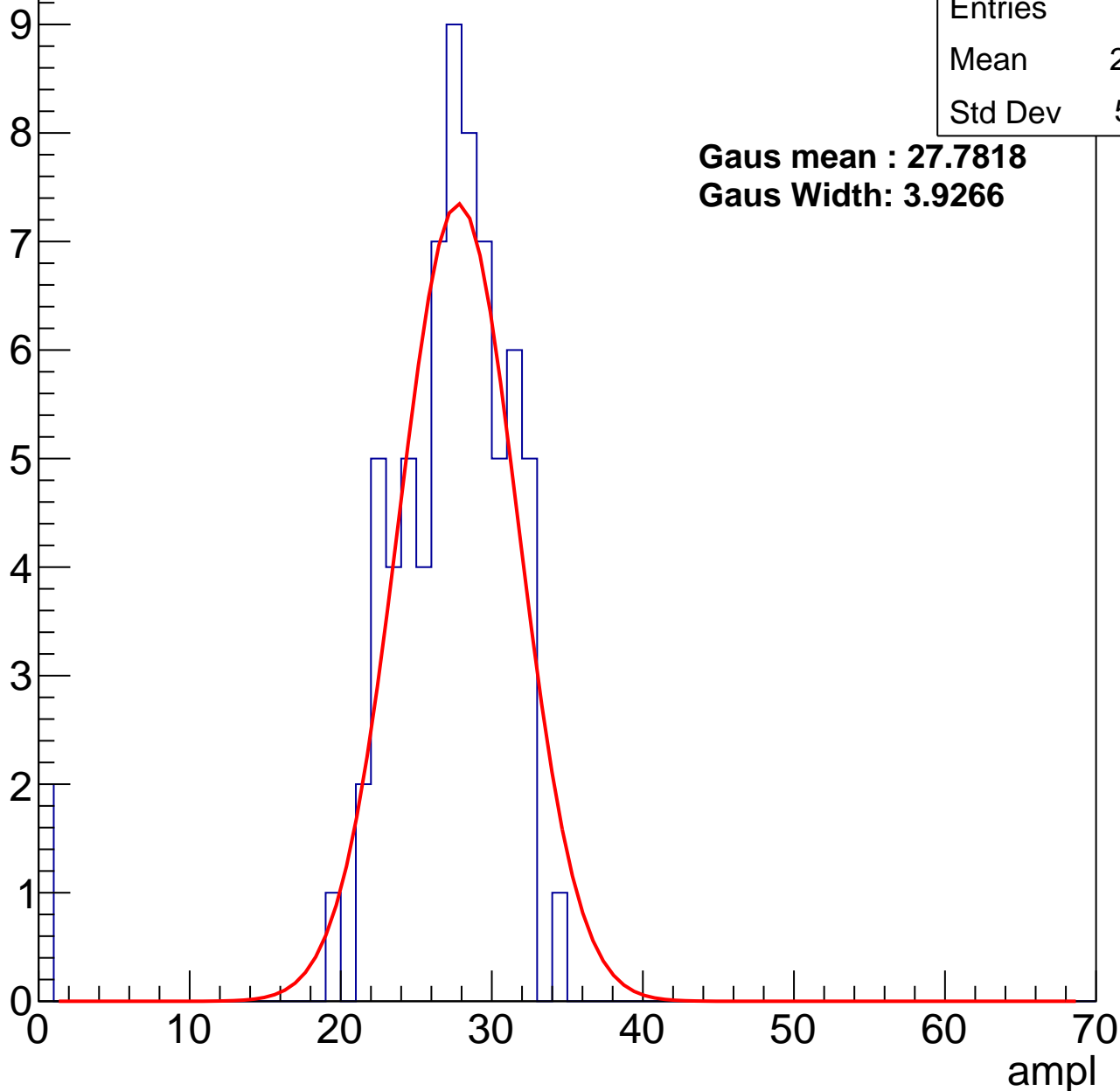
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	26.27
Std Dev	5.521

Gaus mean : 27.7818

Gaus Width: 3.9266



B1L101S, U6-ch94, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	34.3
Std Dev	3.457

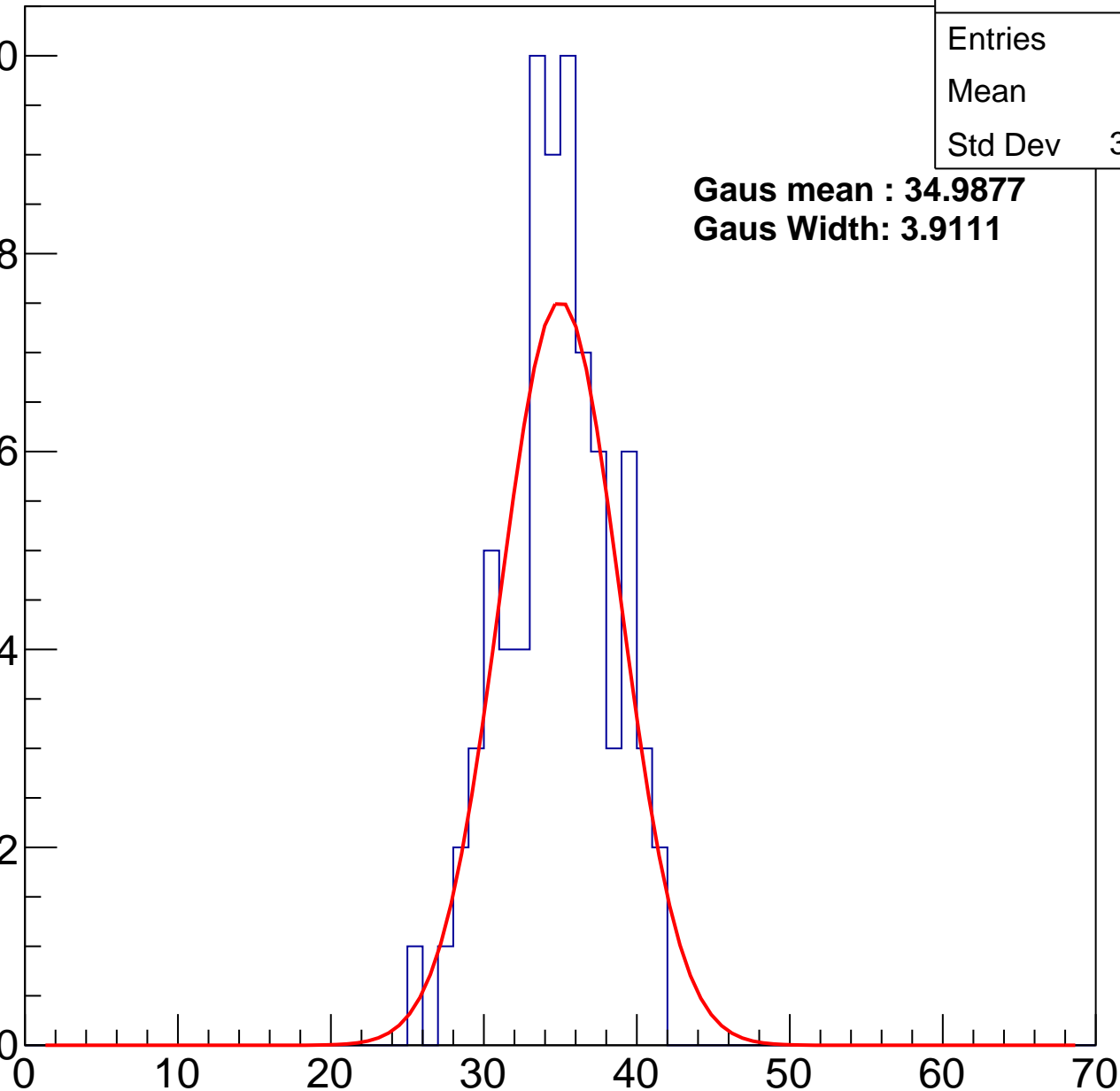
Gaus mean : 34.9877

Gaus Width: 3.9111

Entry

10
8
6
4
2
0

ampl



B1L101S, U6-ch94, adc2

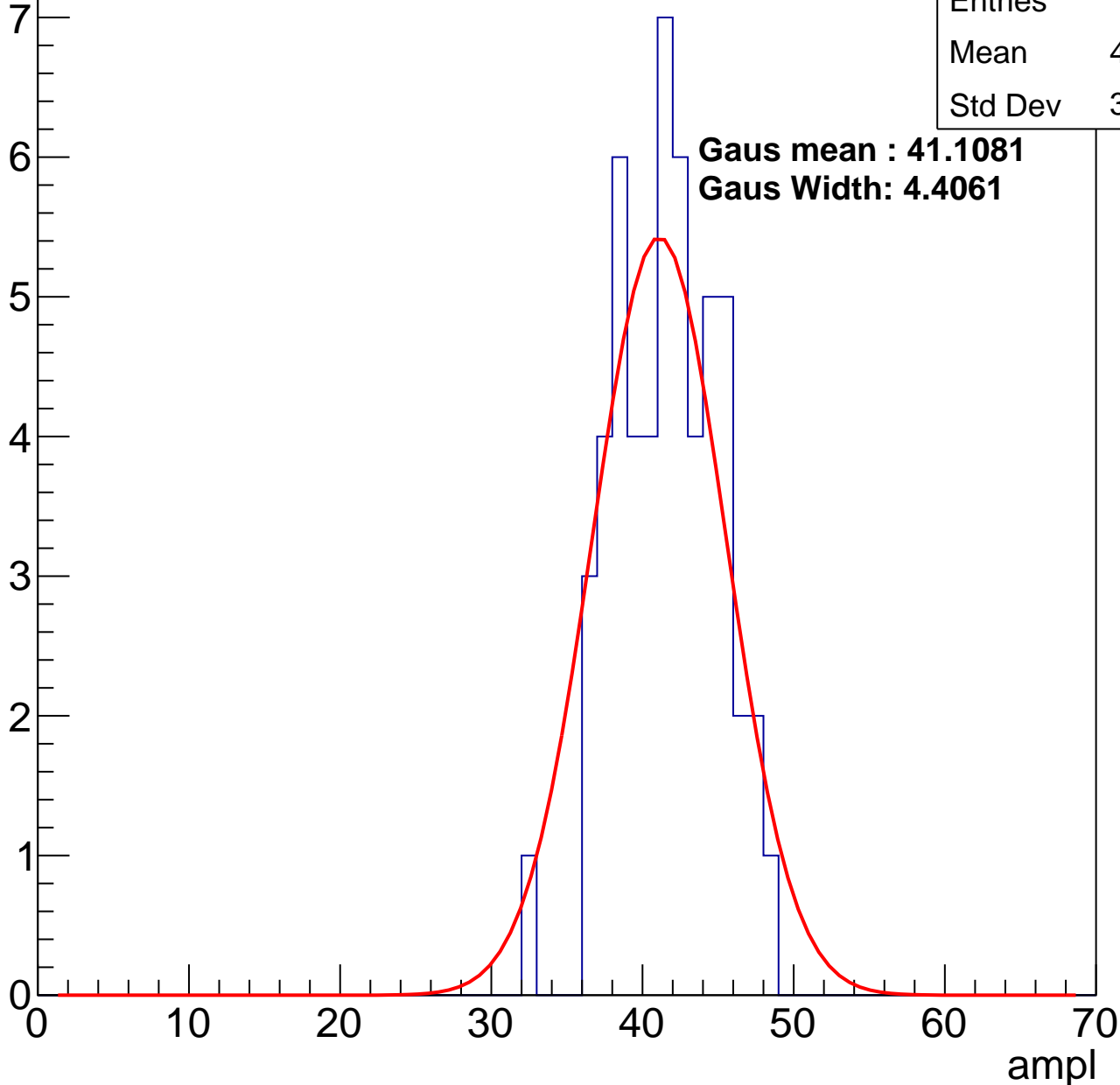
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.15
Std Dev	3.363

Gaus mean : 41.1081

Gaus Width: 4.4061

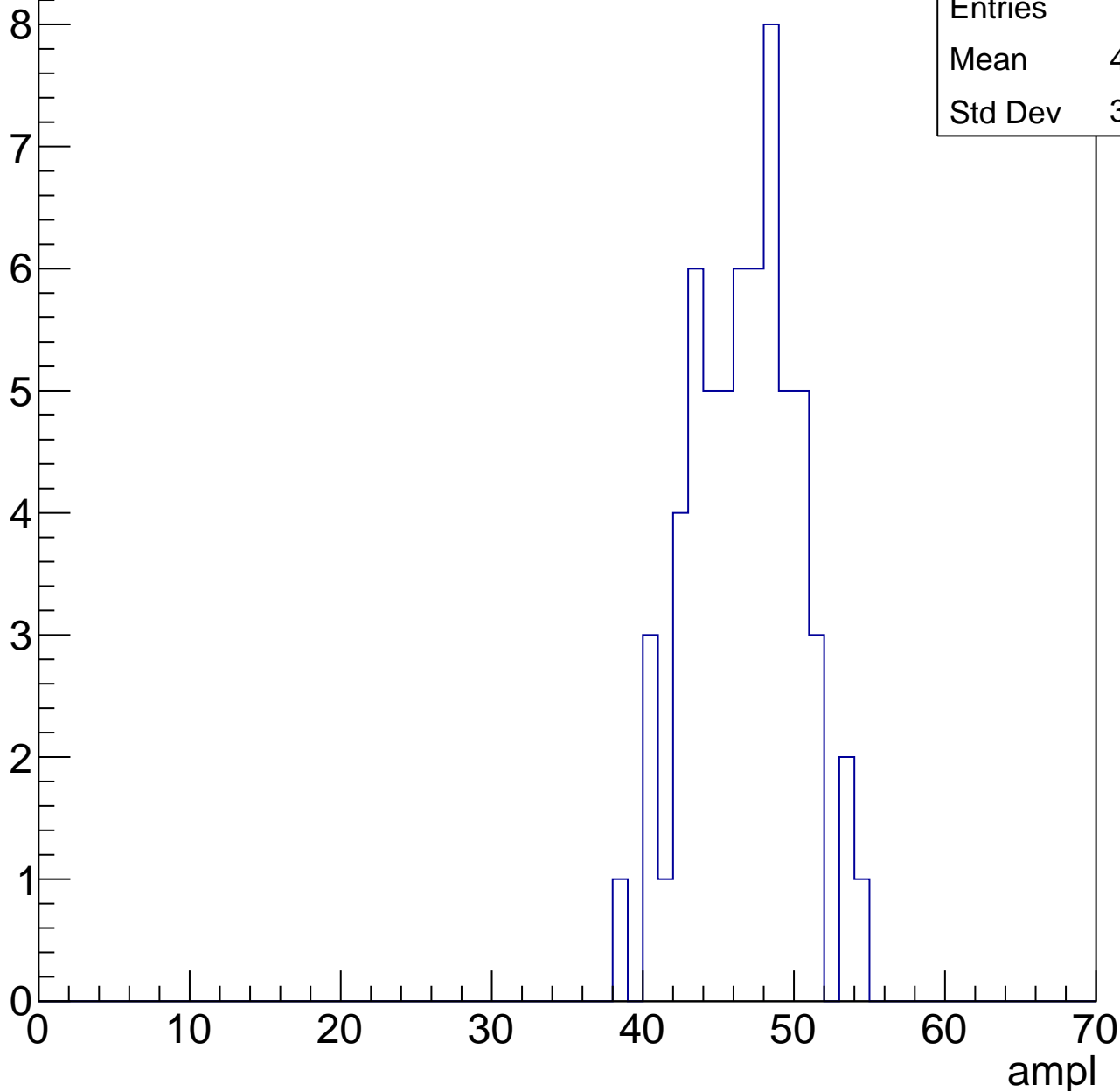


B1L101S, U6-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

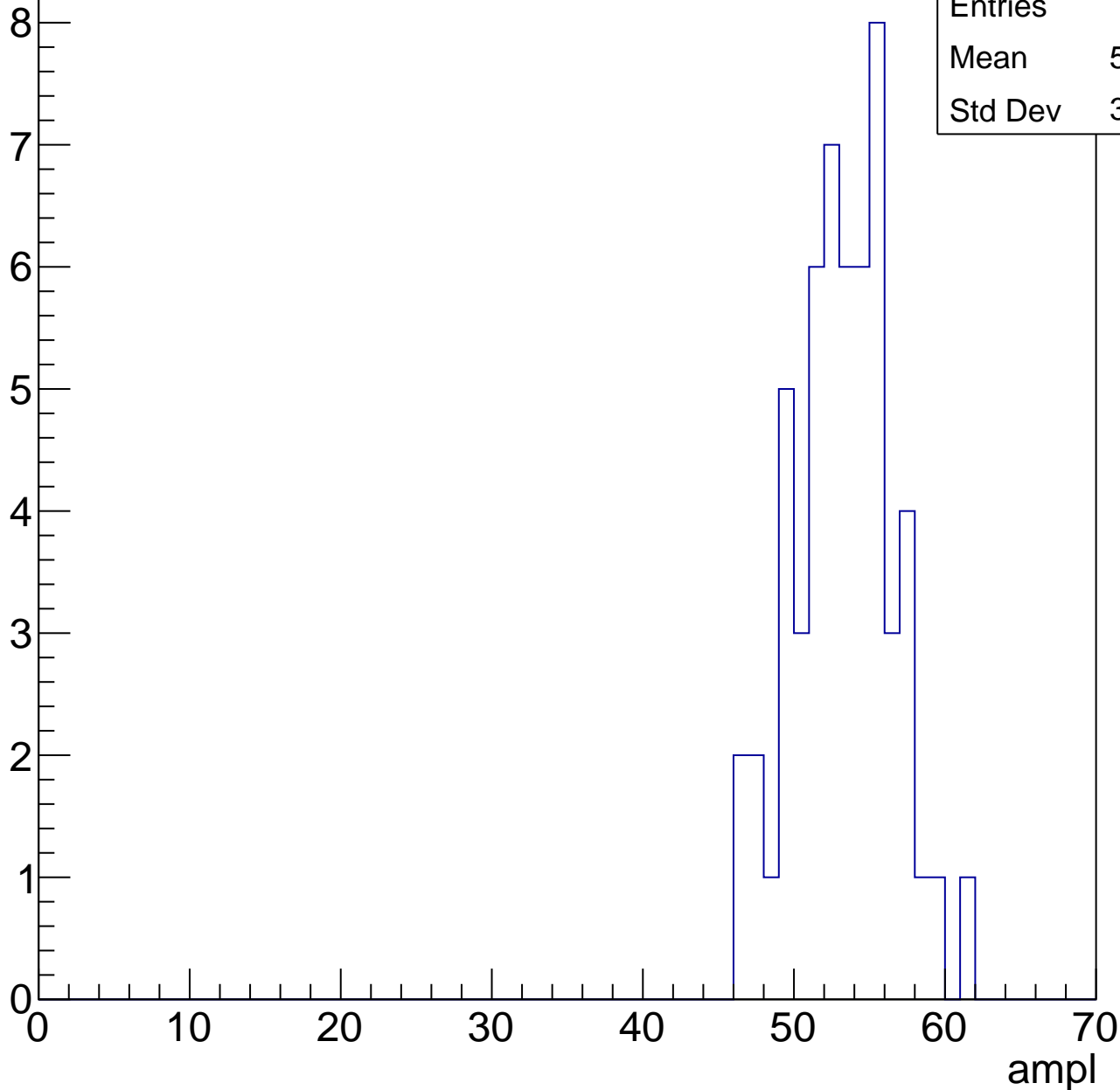
Entries	61
Mean	46.23
Std Dev	3.475



B1L101S, U6-ch94, adc4

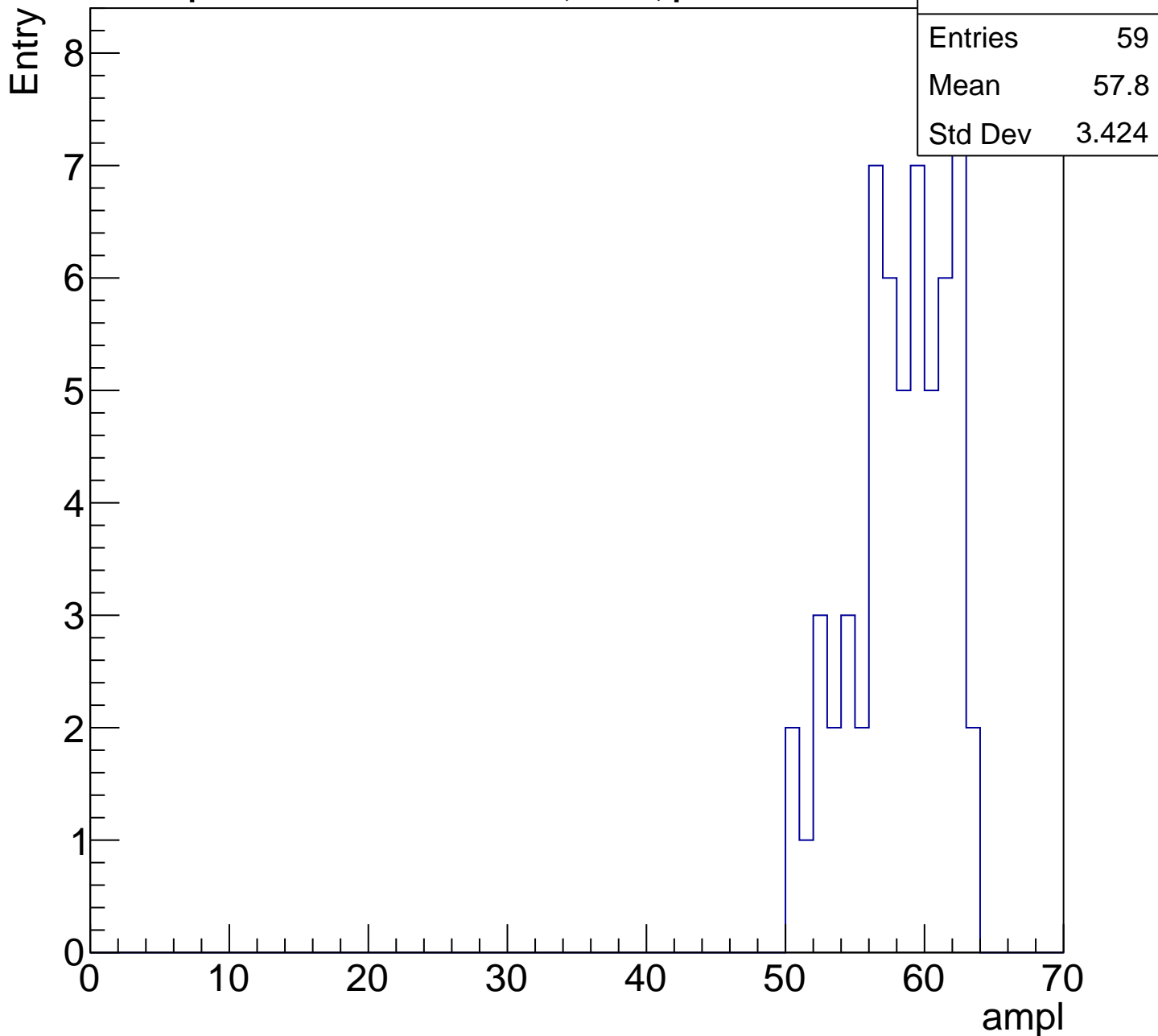
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

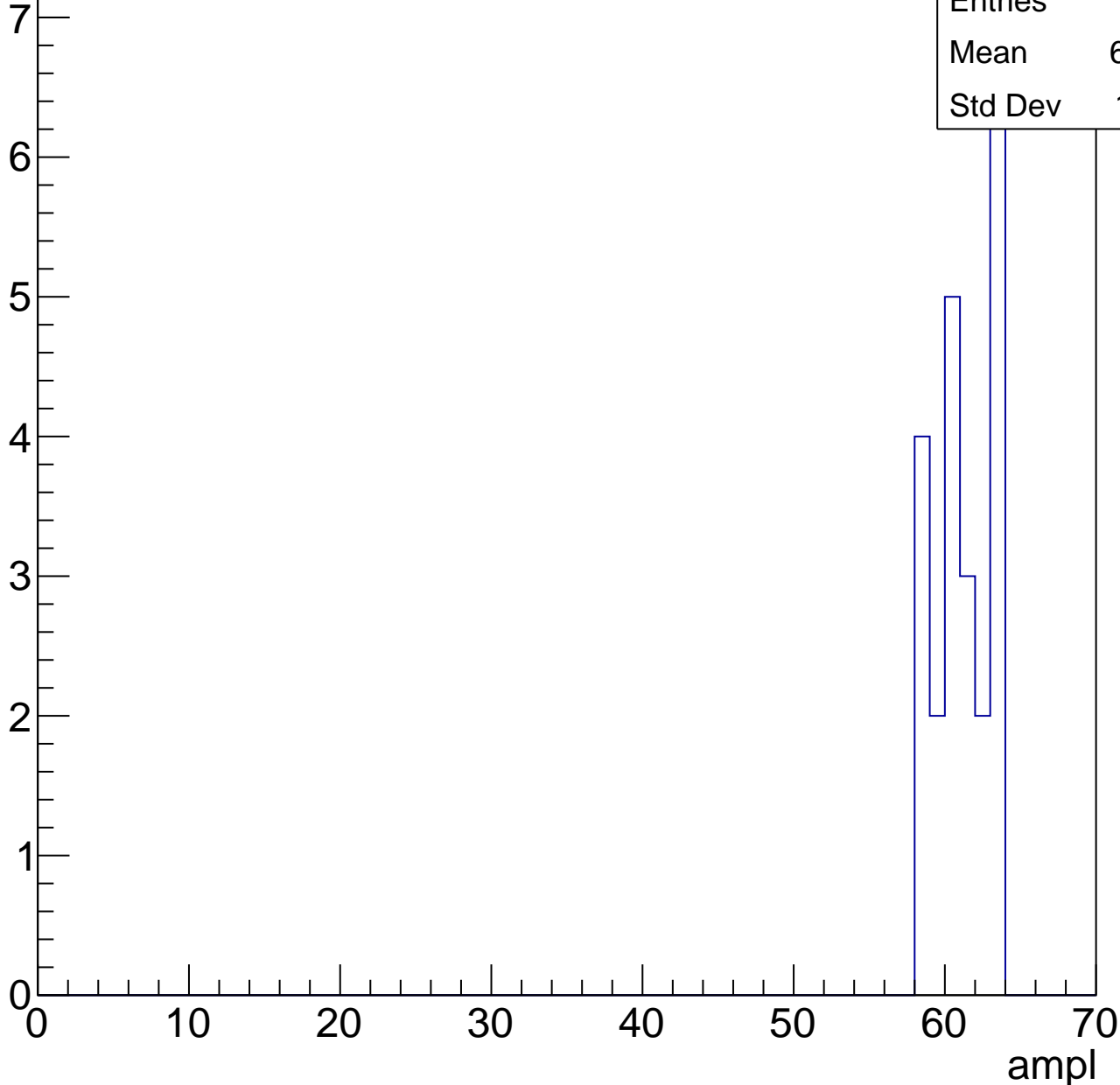


B1L101S, U6-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

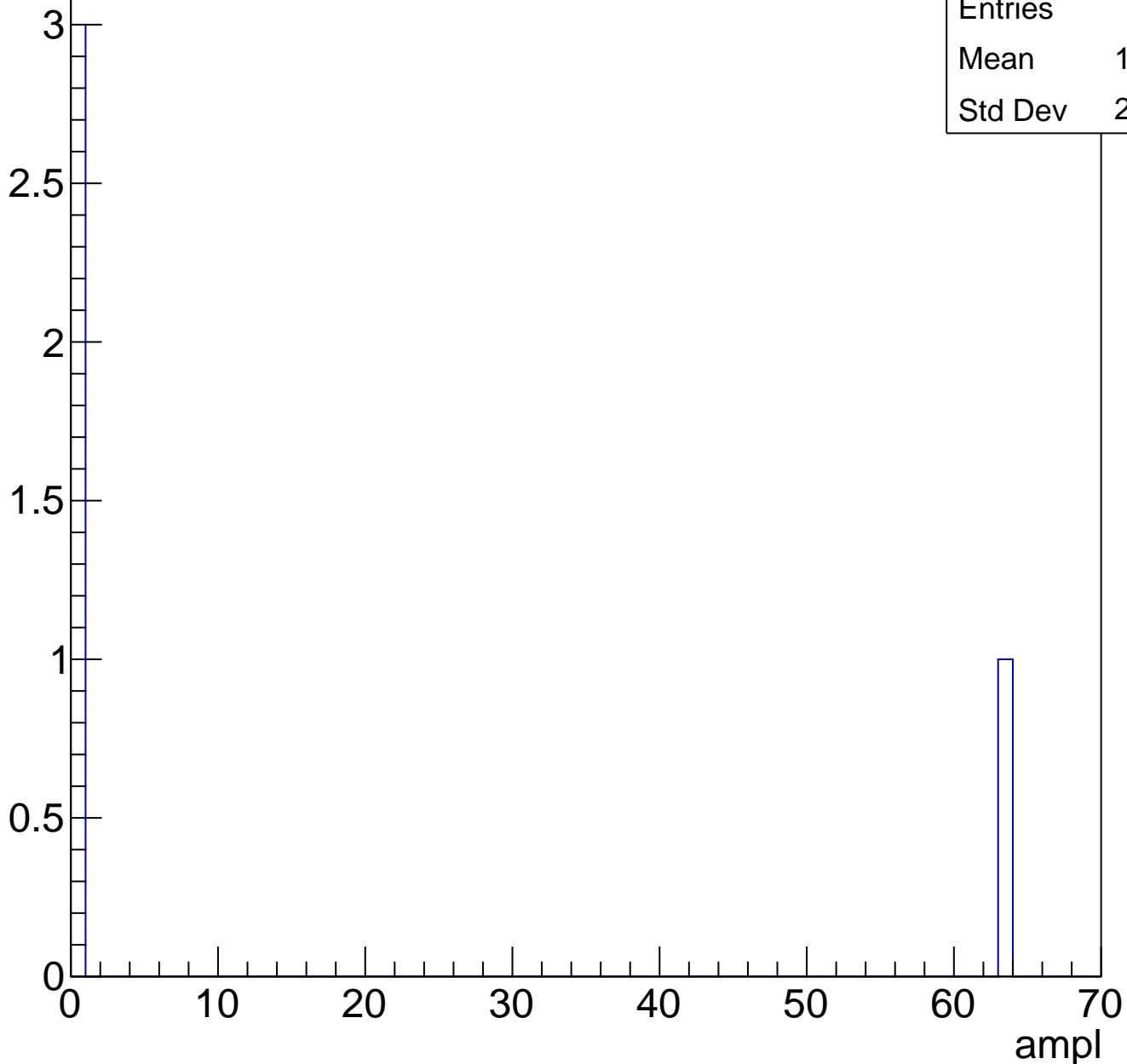
Entries	23
Mean	60.78
Std Dev	1.841



B1L101S, U6-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L101S, U6-ch95, adc0

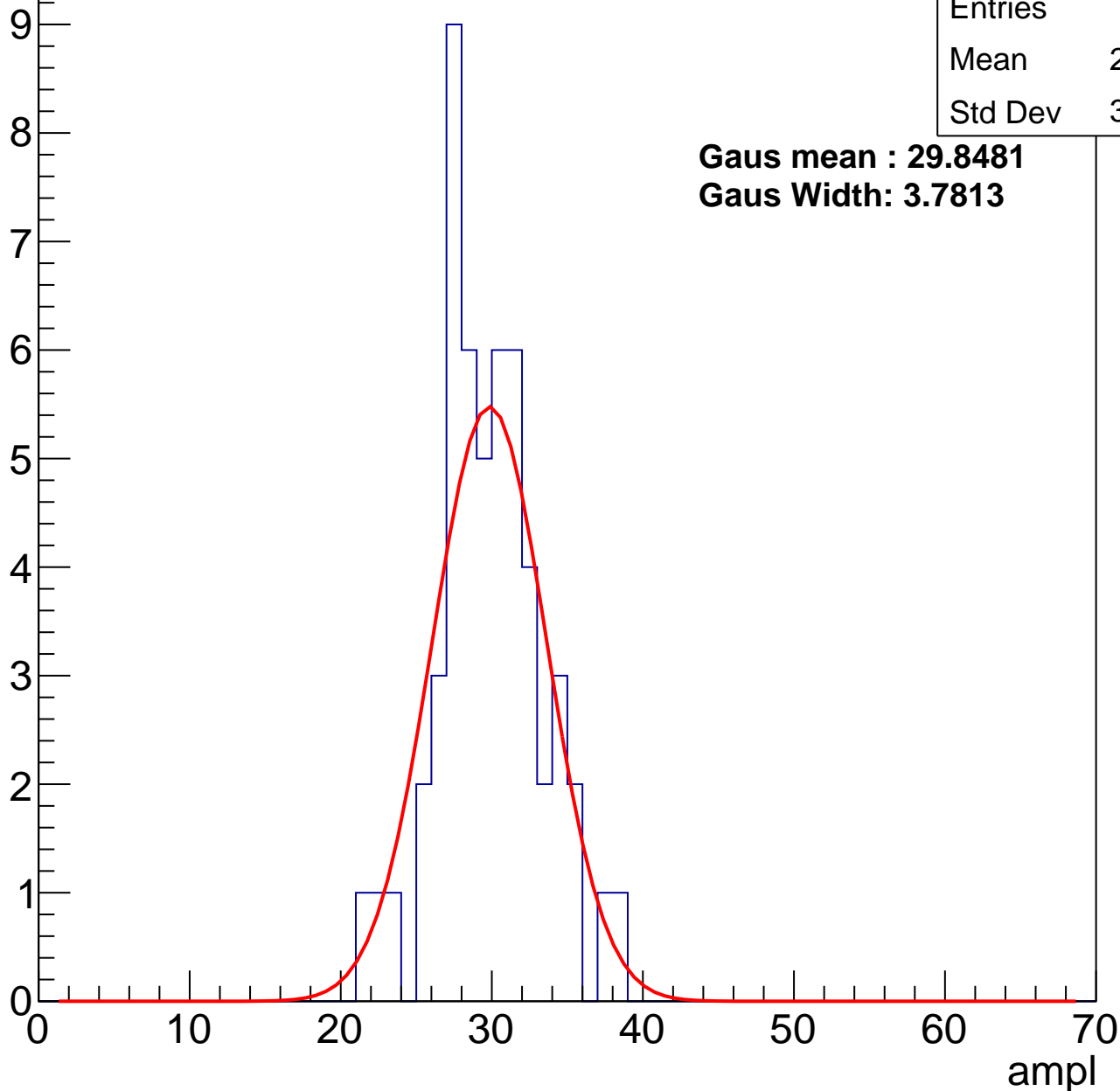
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.38
Std Dev	3.454

Gaus mean : 29.8481

Gaus Width: 3.7813



B1L101S, U6-ch95, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.08
Std Dev	3.737

Gaus mean : 36.3075

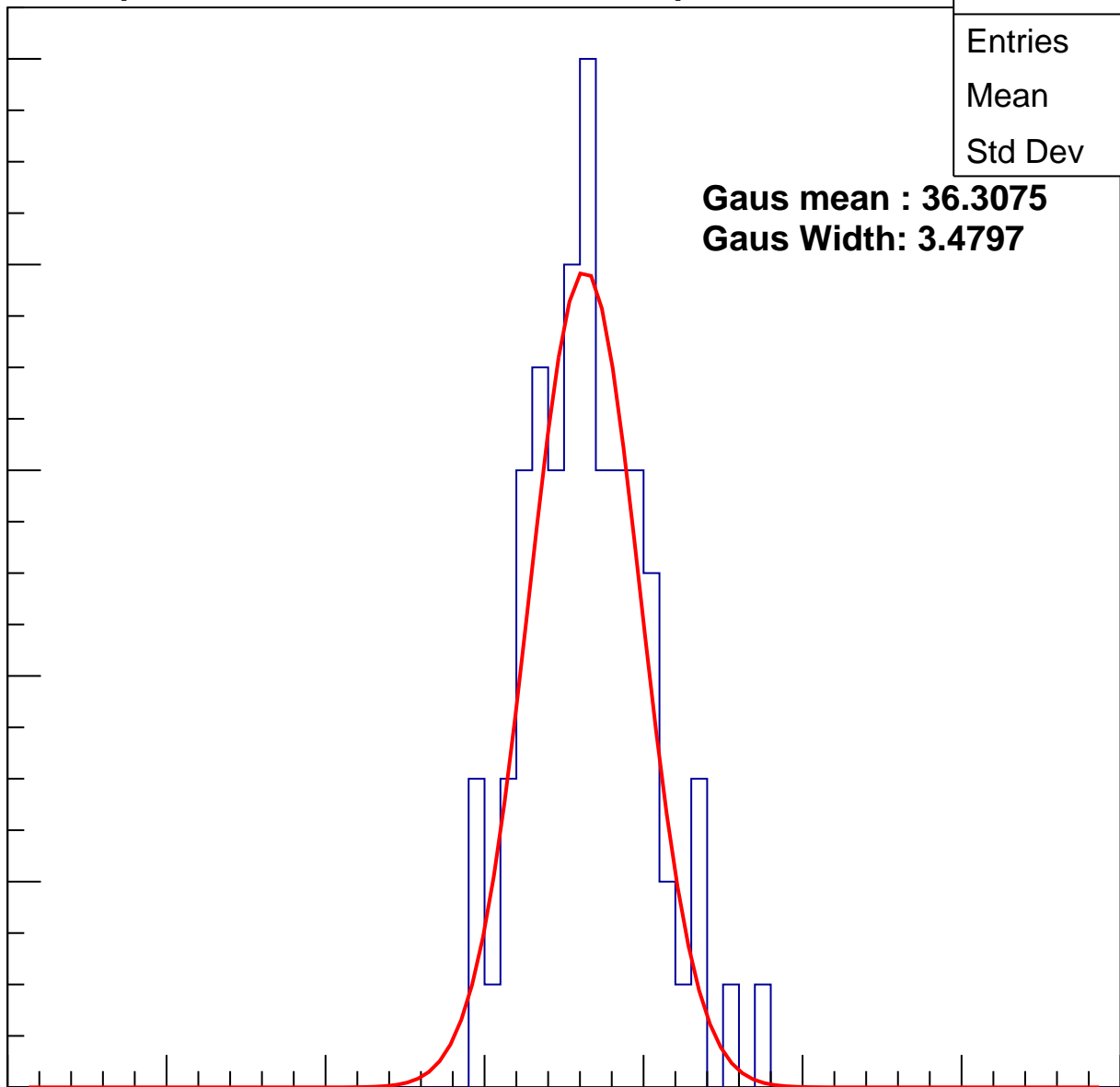
Gaus Width: 3.4797

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch95, adc2

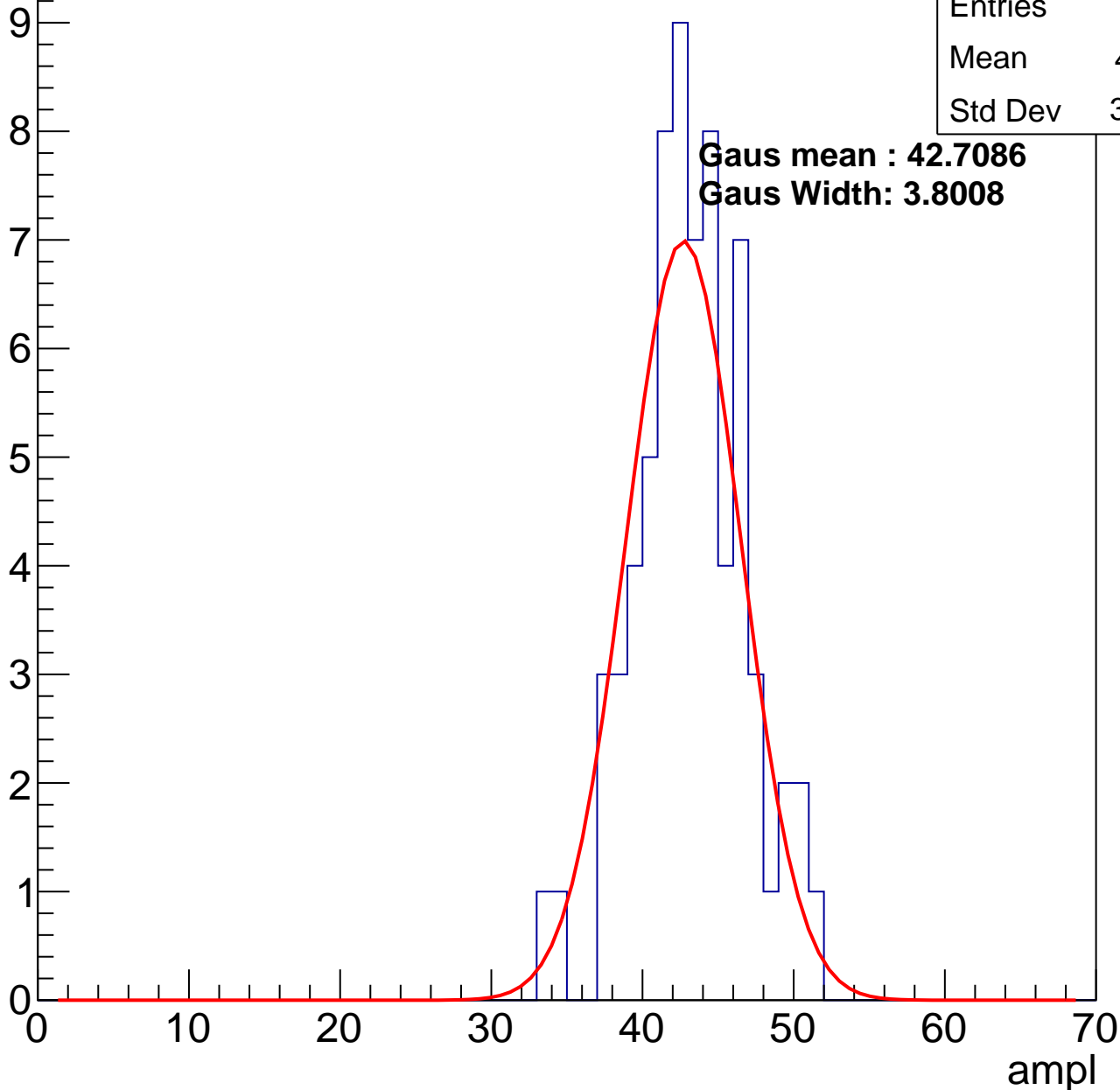
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.71
Std Dev	3.616

Gaus mean : 42.7086

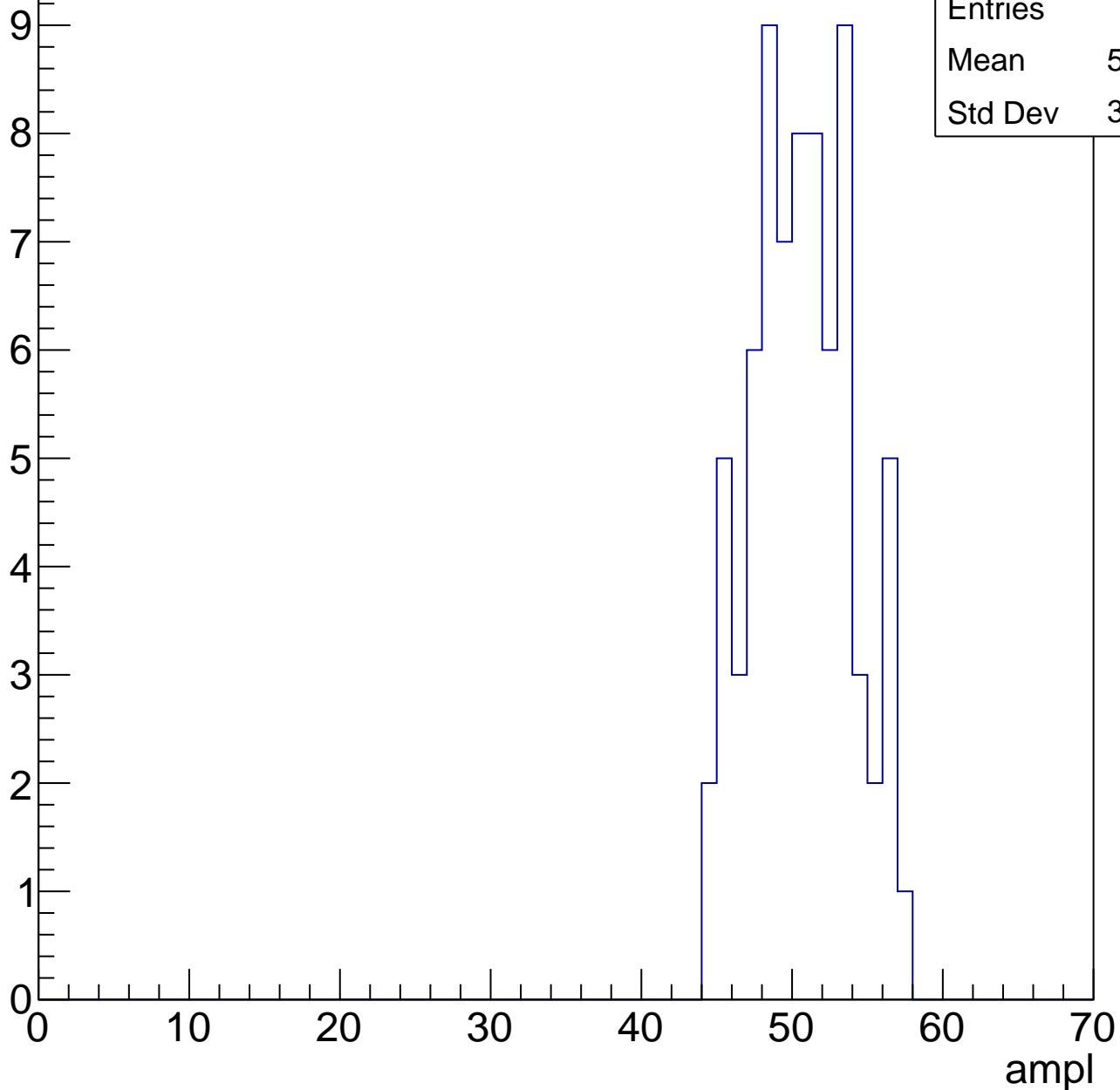
Gaus Width: 3.8008



B1L101S, U6-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

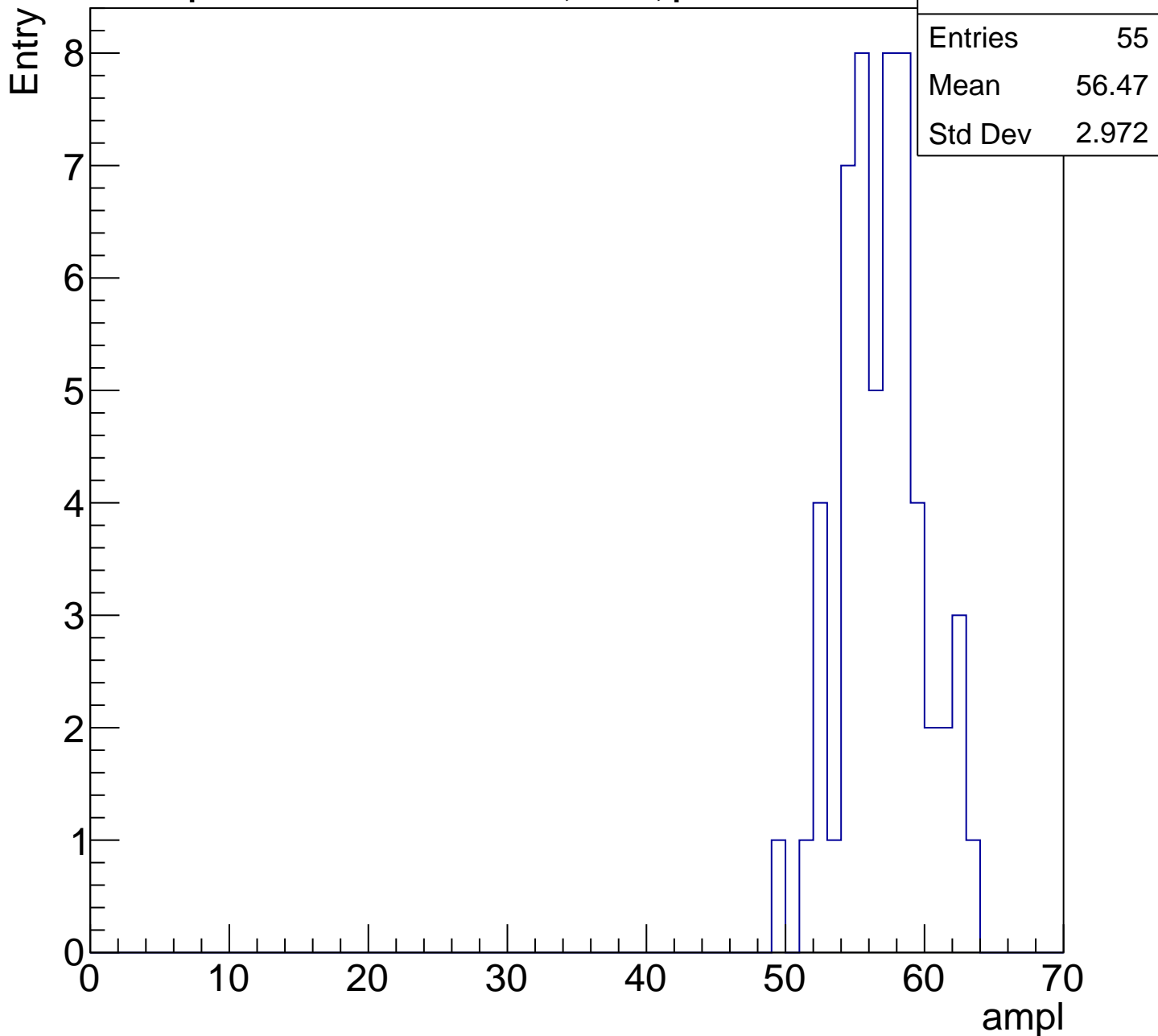
Entry



Entries	74
Mean	50.19
Std Dev	3.245

B1L101S, U6-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.23
Std Dev	9.744

ampl

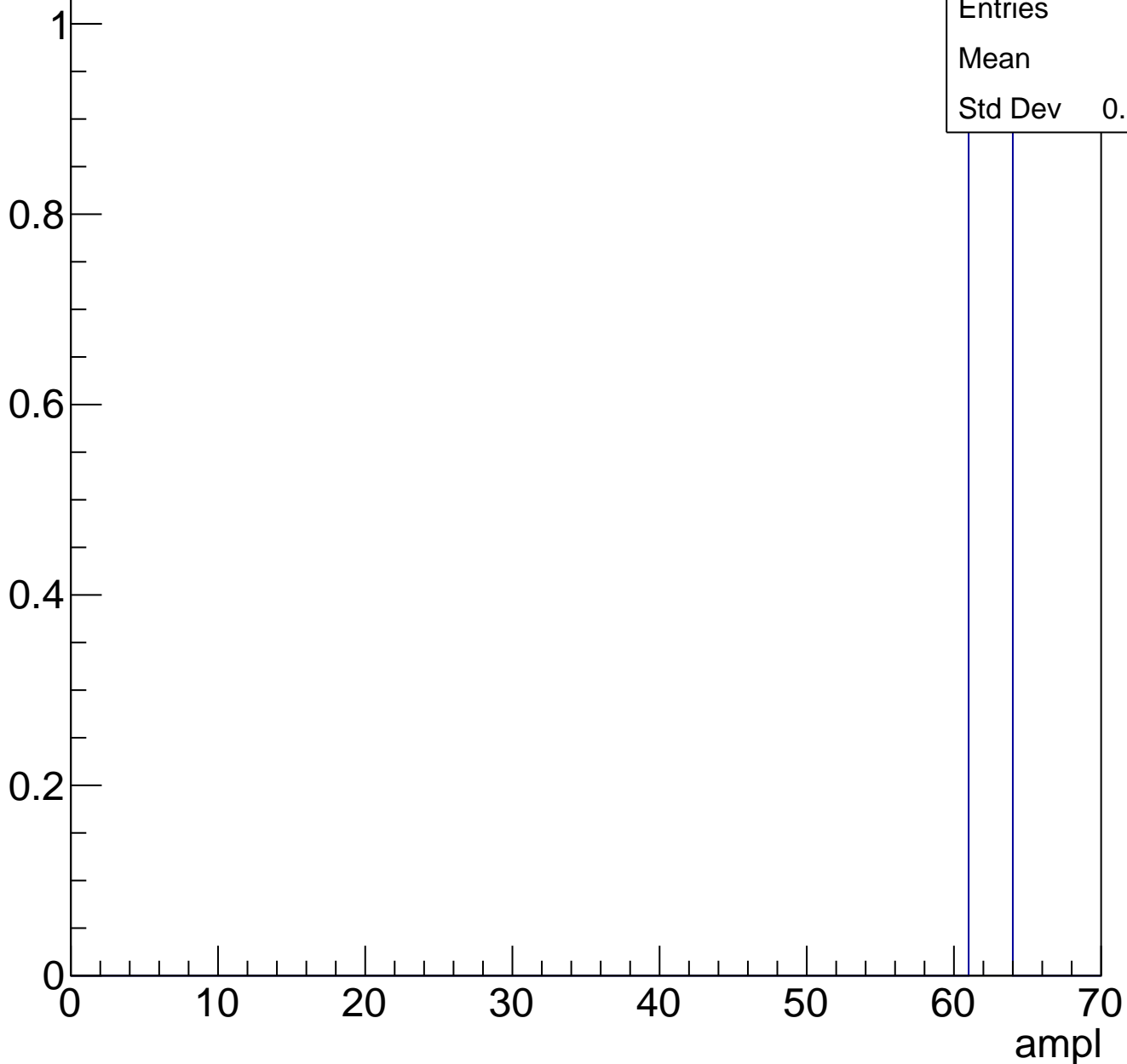
0 10 20 30 40 50 60 70

0

B1L101S, U6-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch96, adc0

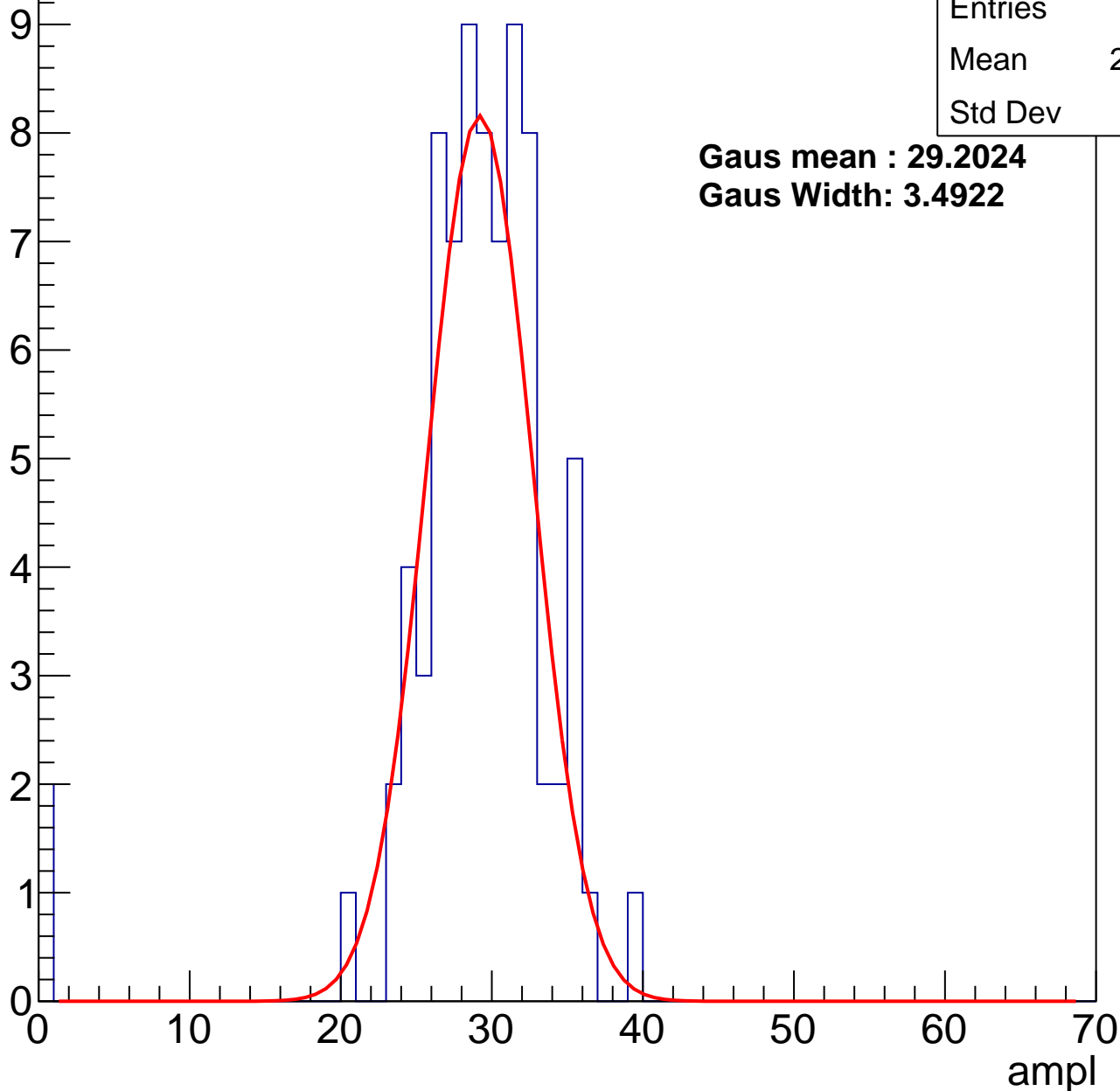
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.44
Std Dev	5.73

Gaus mean : 29.2024

Gaus Width: 3.4922



B1L101S, U6-ch96, adc1

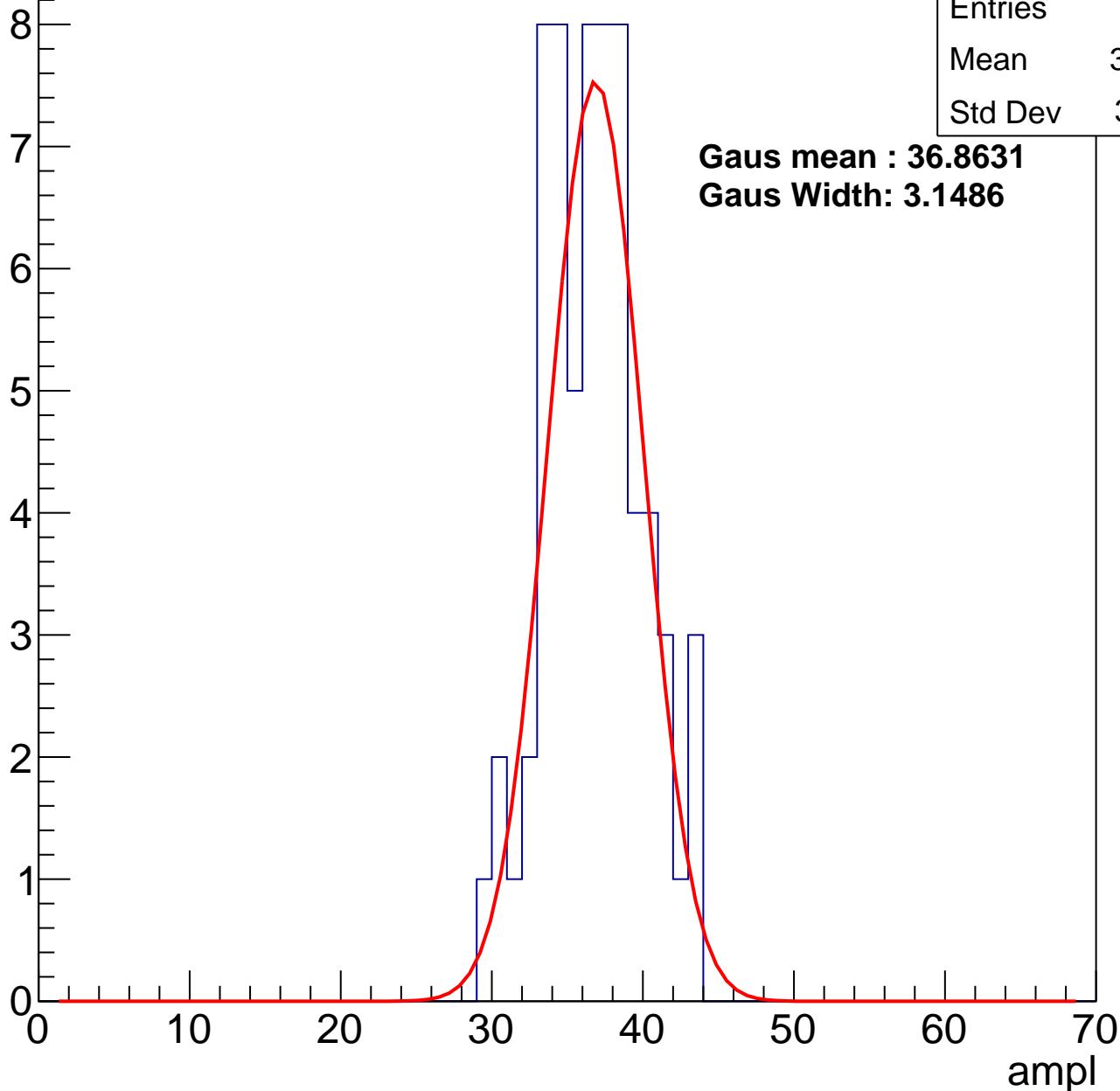
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.26
Std Dev	3.211

Gaus mean : 36.8631

Gaus Width: 3.1486



B1L101S, U6-ch96, adc2

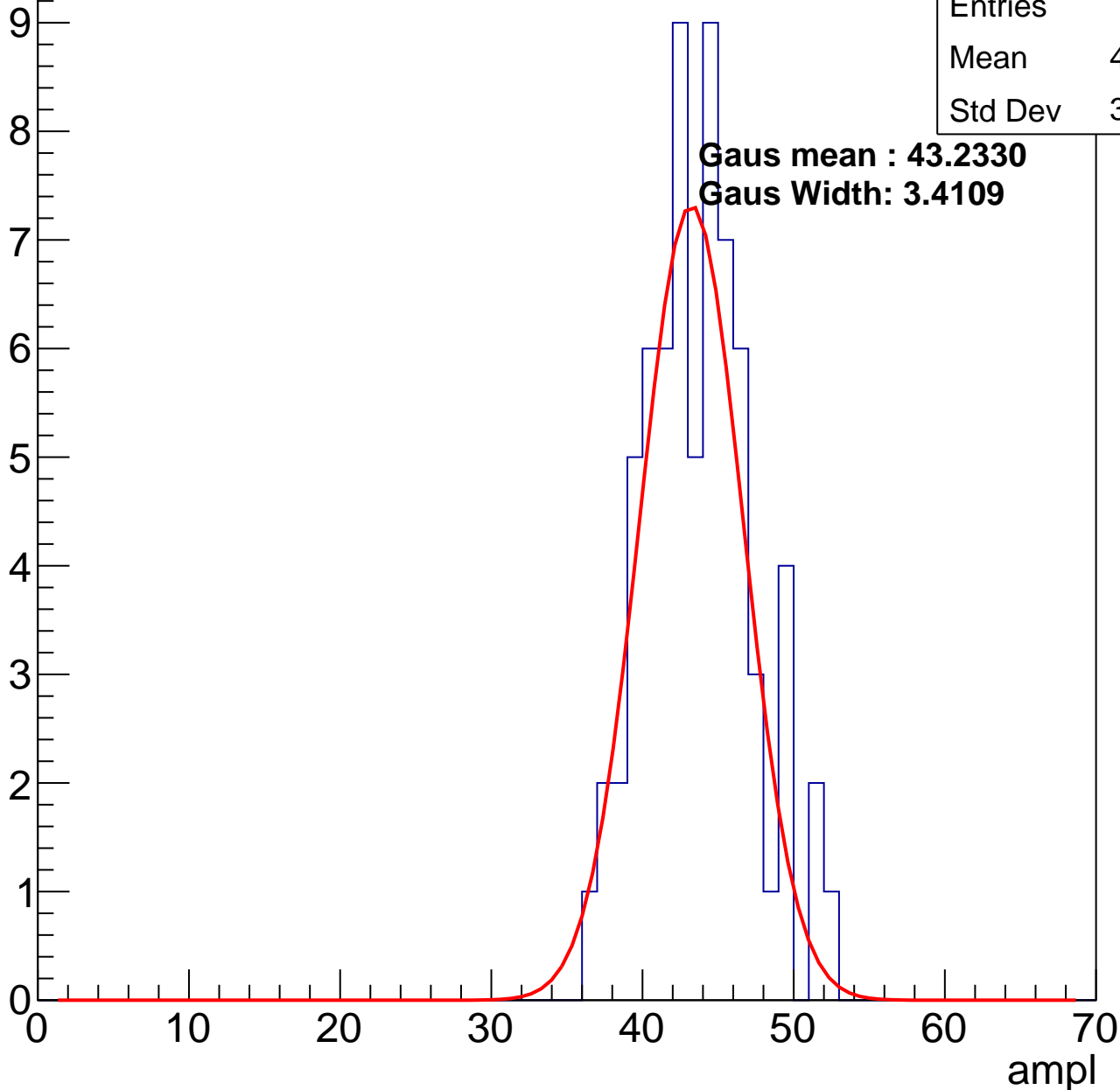
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.28
Std Dev	3.509

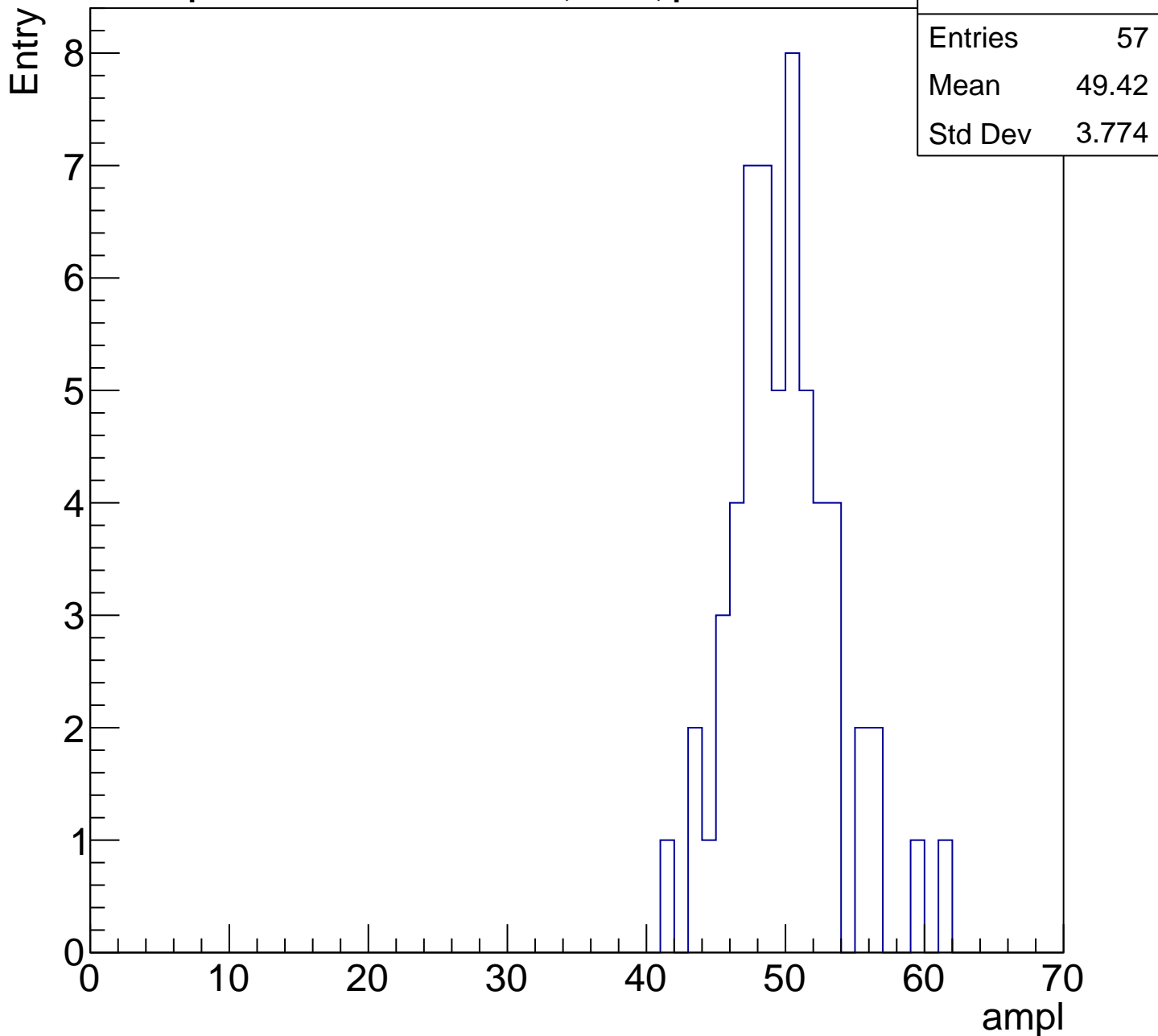
Gaus mean : 43.2330

Gaus Width: 3.4109



B1L101S, U6-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch96, adc4

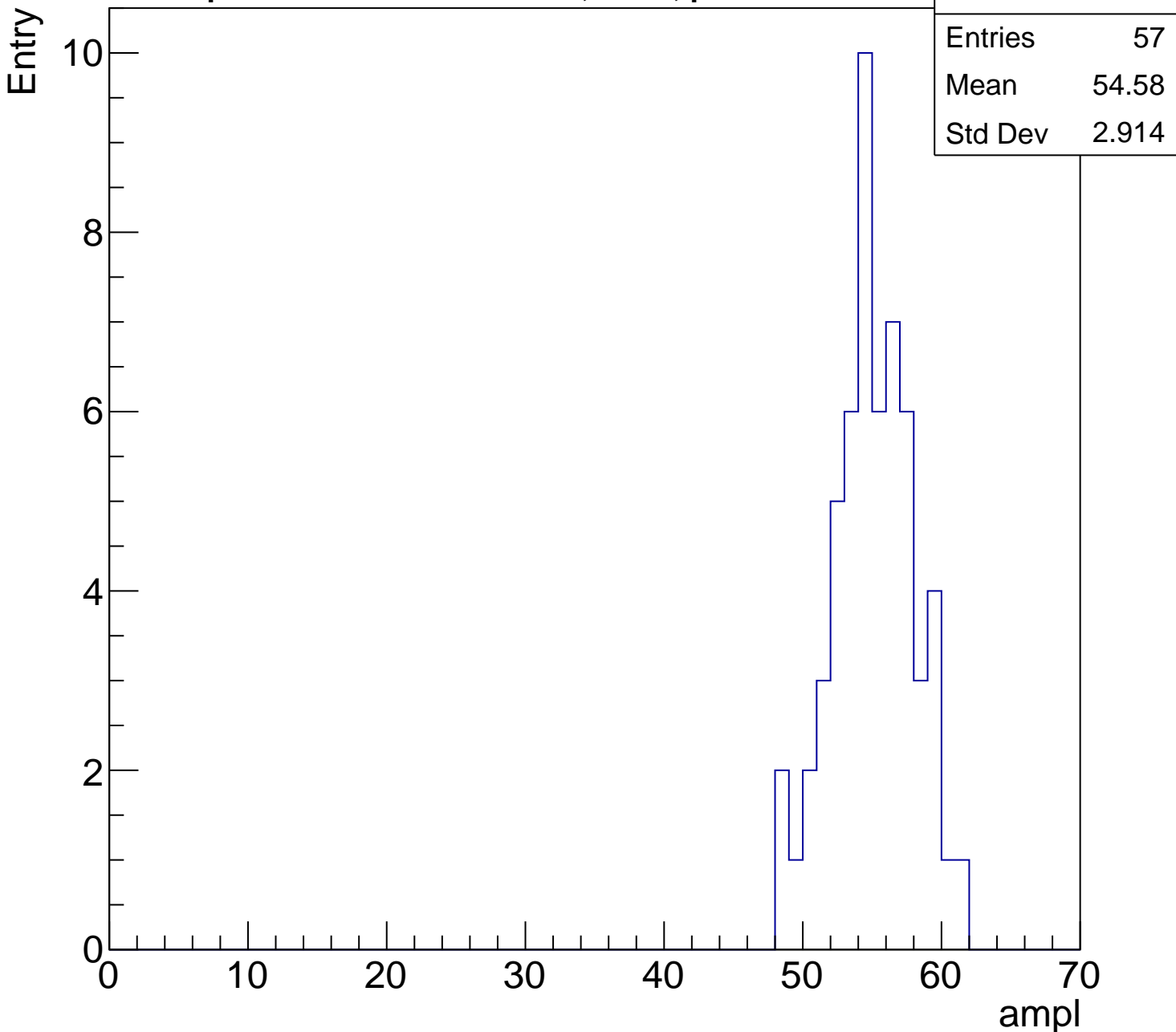
calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	54.58
Std Dev	2.914

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

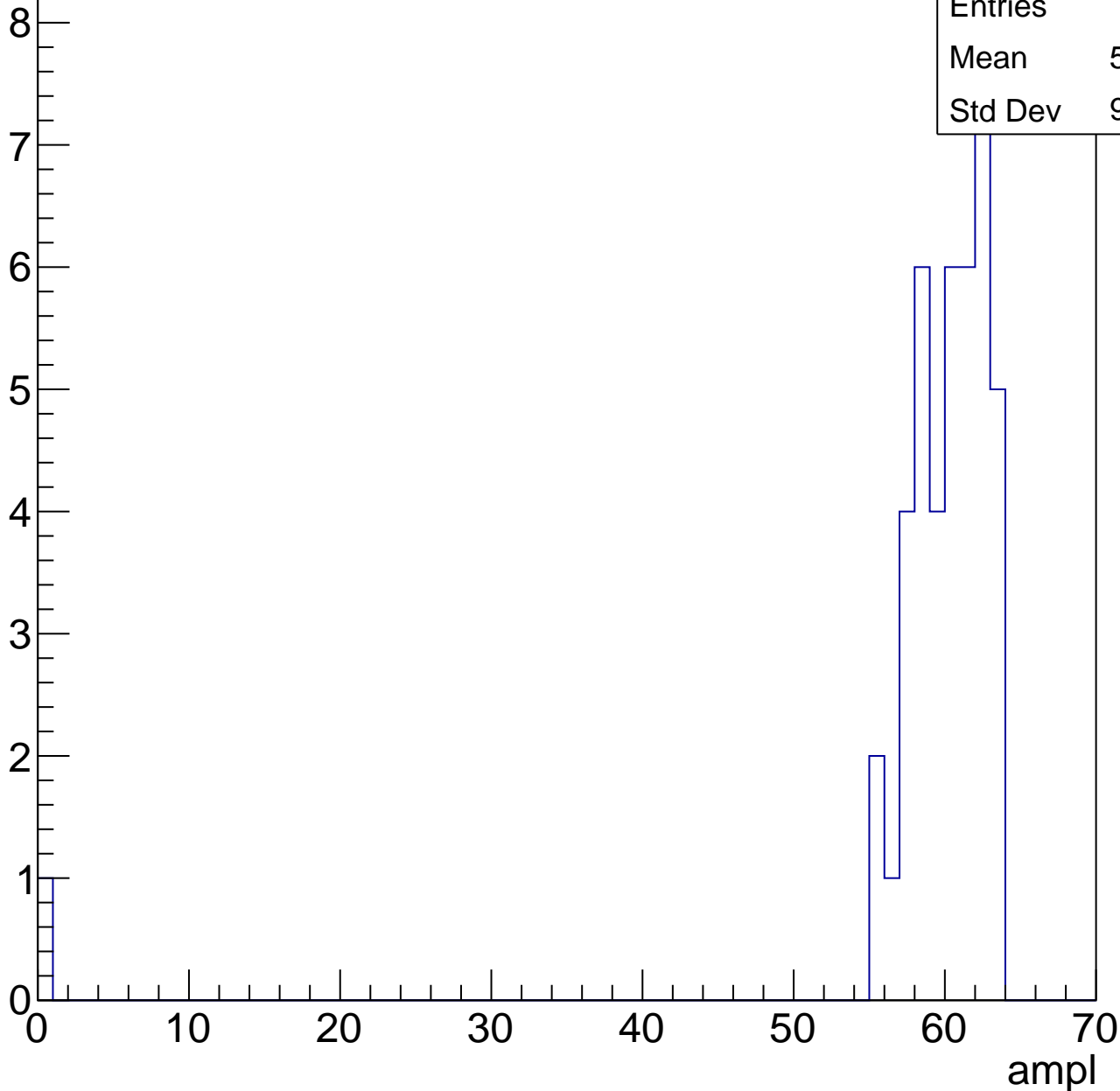


B1L101S, U6-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.49
Std Dev	9.295

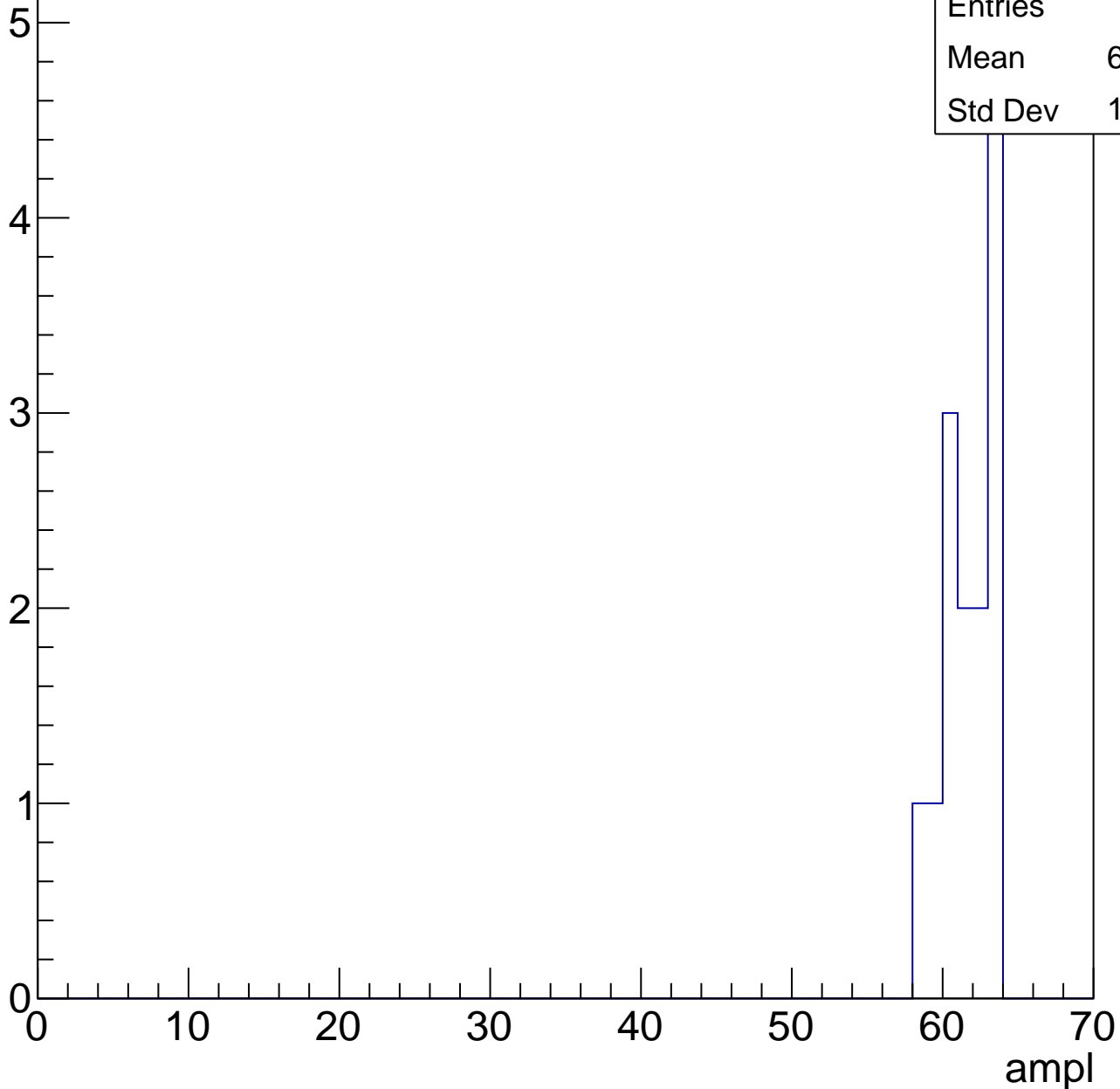


B1L101S, U6-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.29
Std Dev	1.623



B1L101S, U6-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch97, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	29.94
Std Dev	5.088

Gaus mean : 30.3715

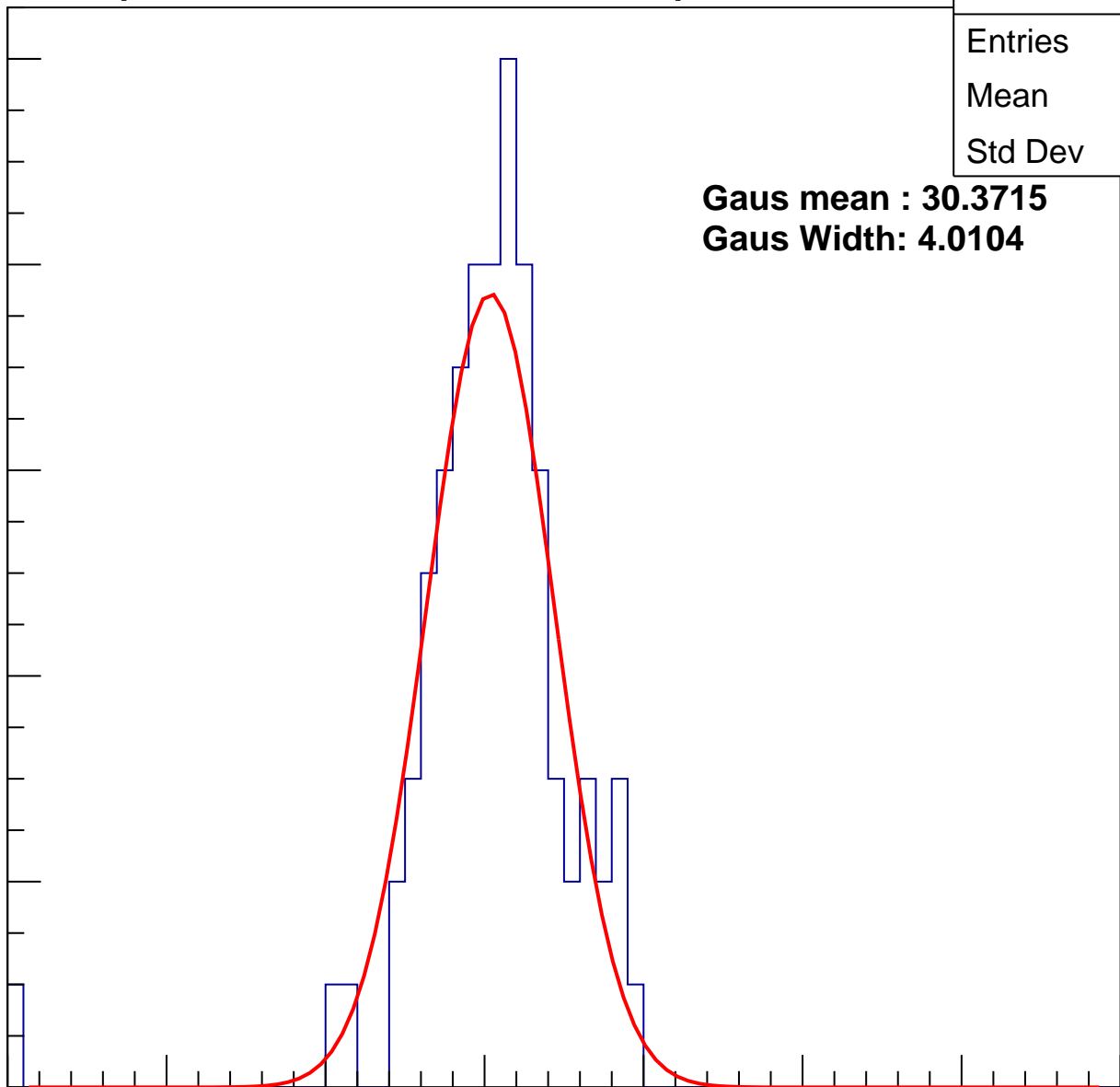
Gaus Width: 4.0104

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch97, adc1

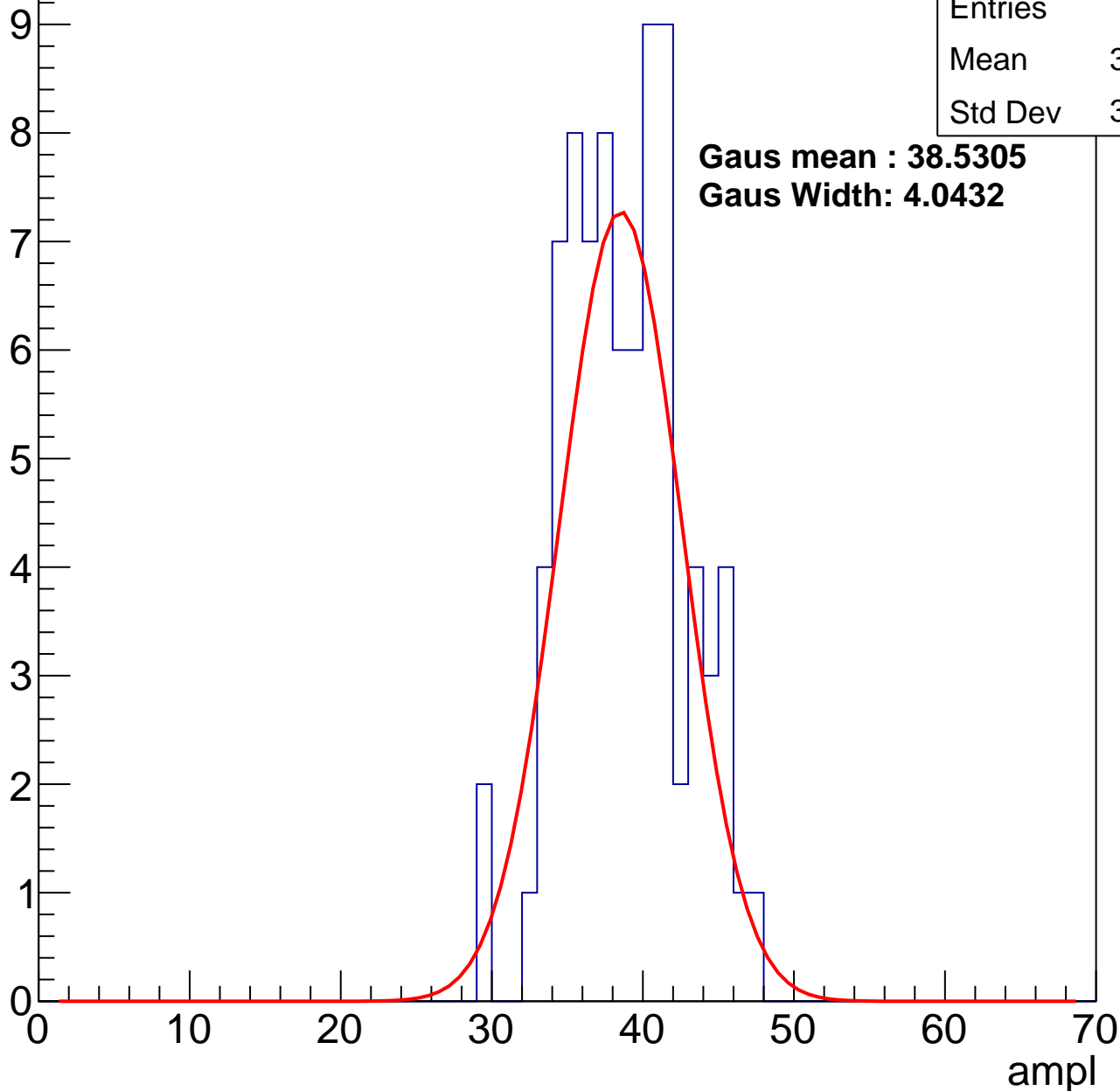
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	38.29
Std Dev	3.849

Gaus mean : 38.5305

Gaus Width: 4.0432



B1L101S, U6-ch97, adc2

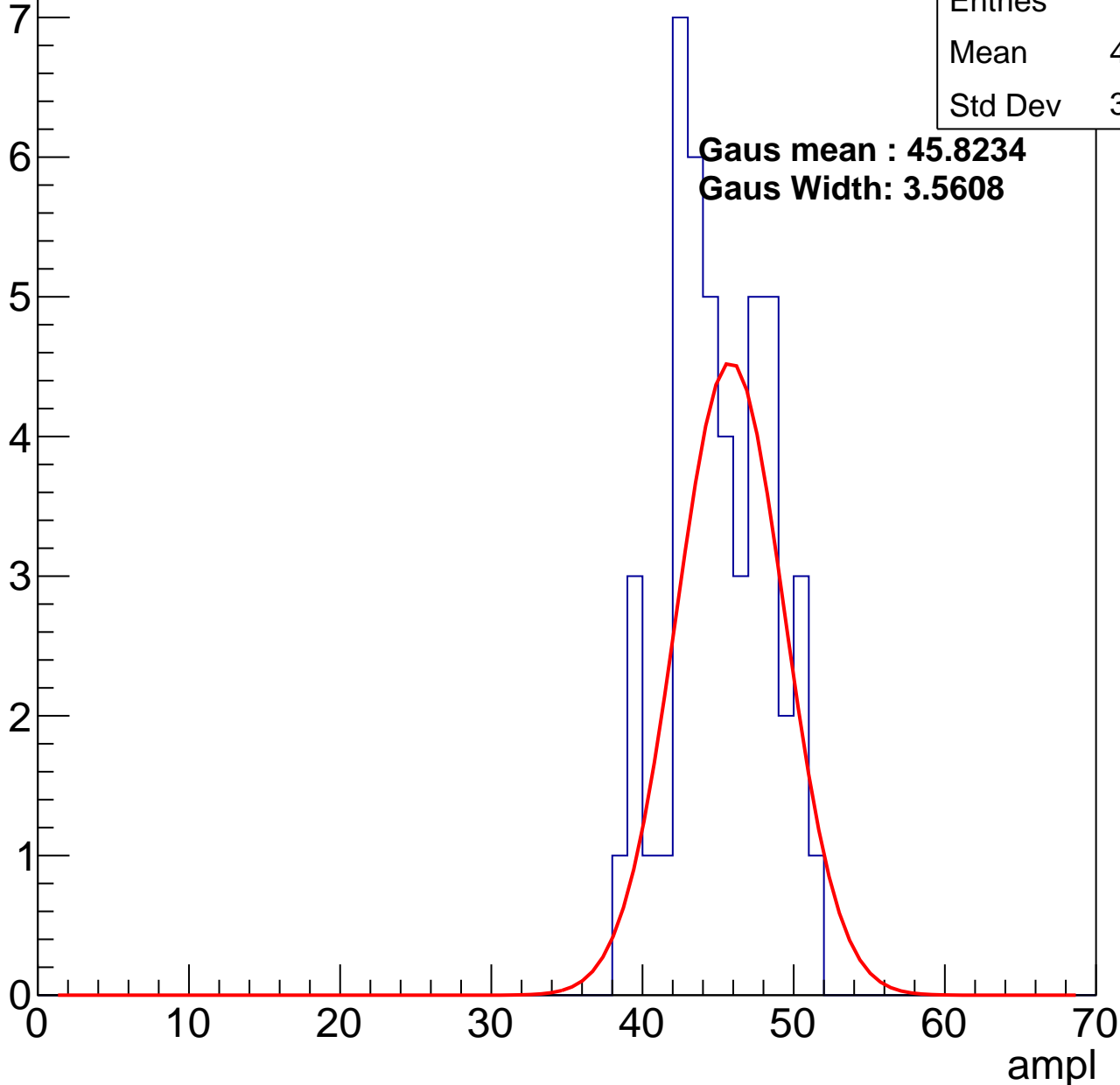
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	44.68
Std Dev	3.249

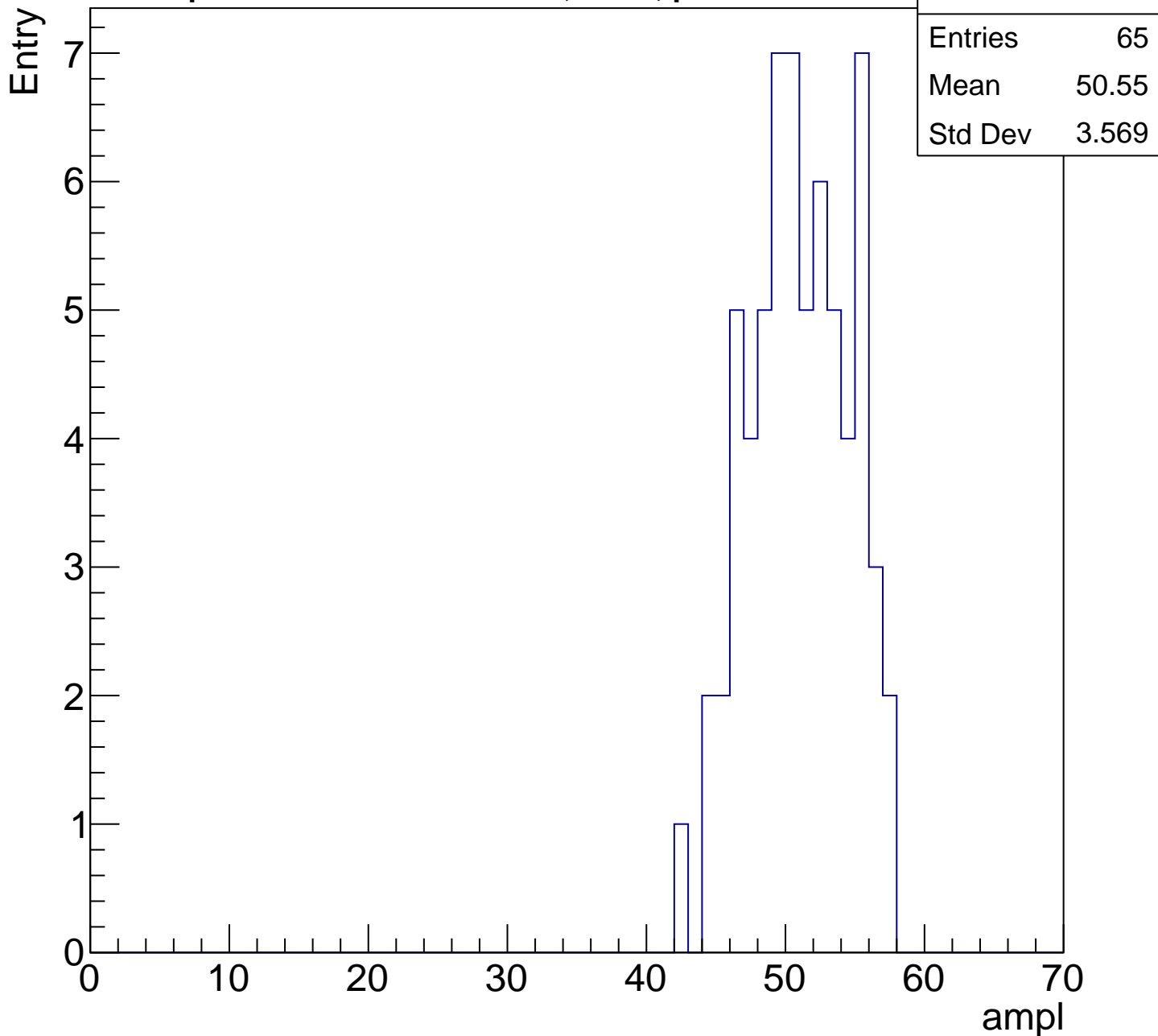
Gaus mean : 45.8234

Gaus Width: 3.5608



B1L101S, U6-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

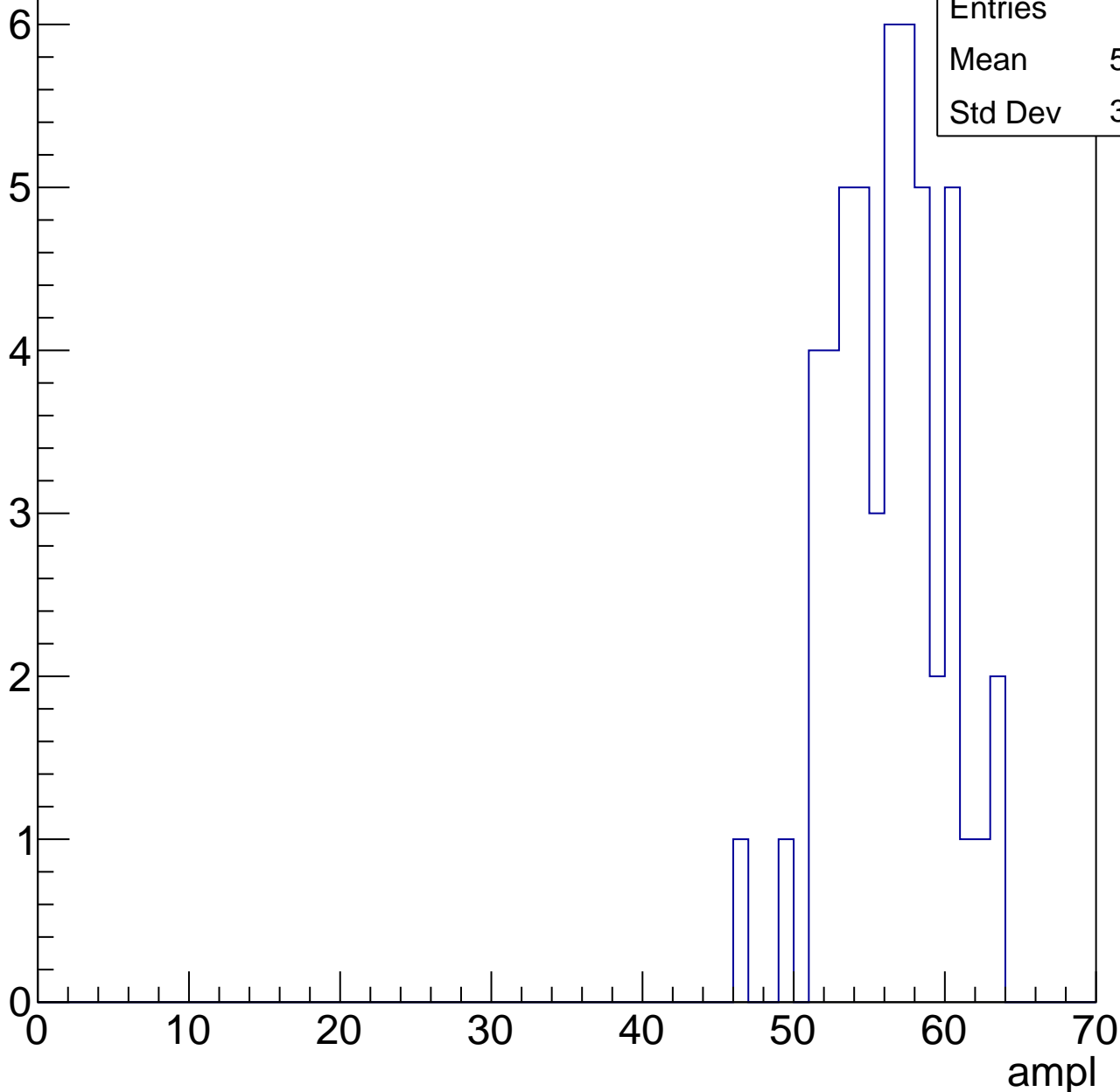


B1L101S, U6-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

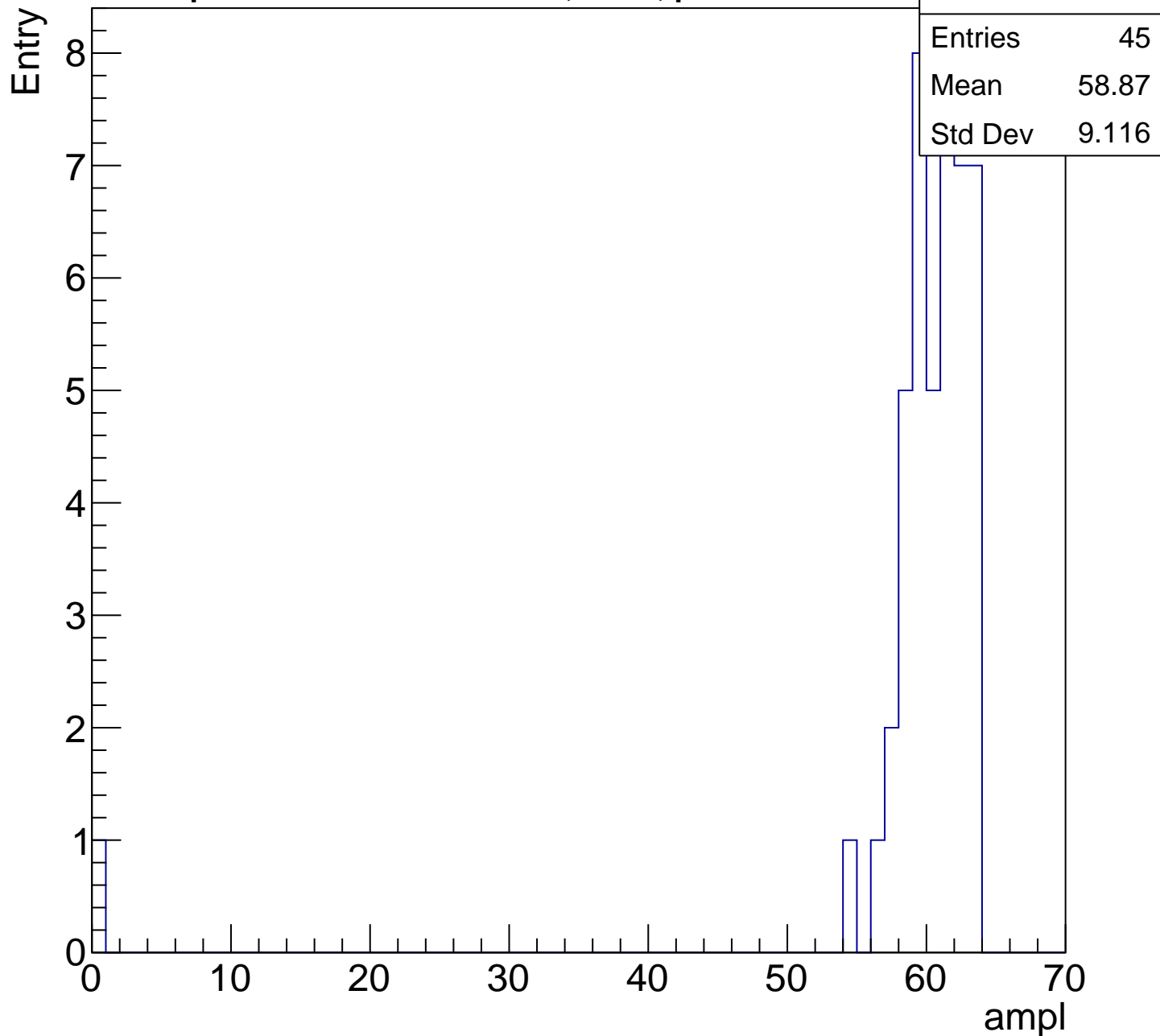
Entry

Entries	51
Mean	55.73
Std Dev	3.598



B1L101S, U6-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch98, adc0

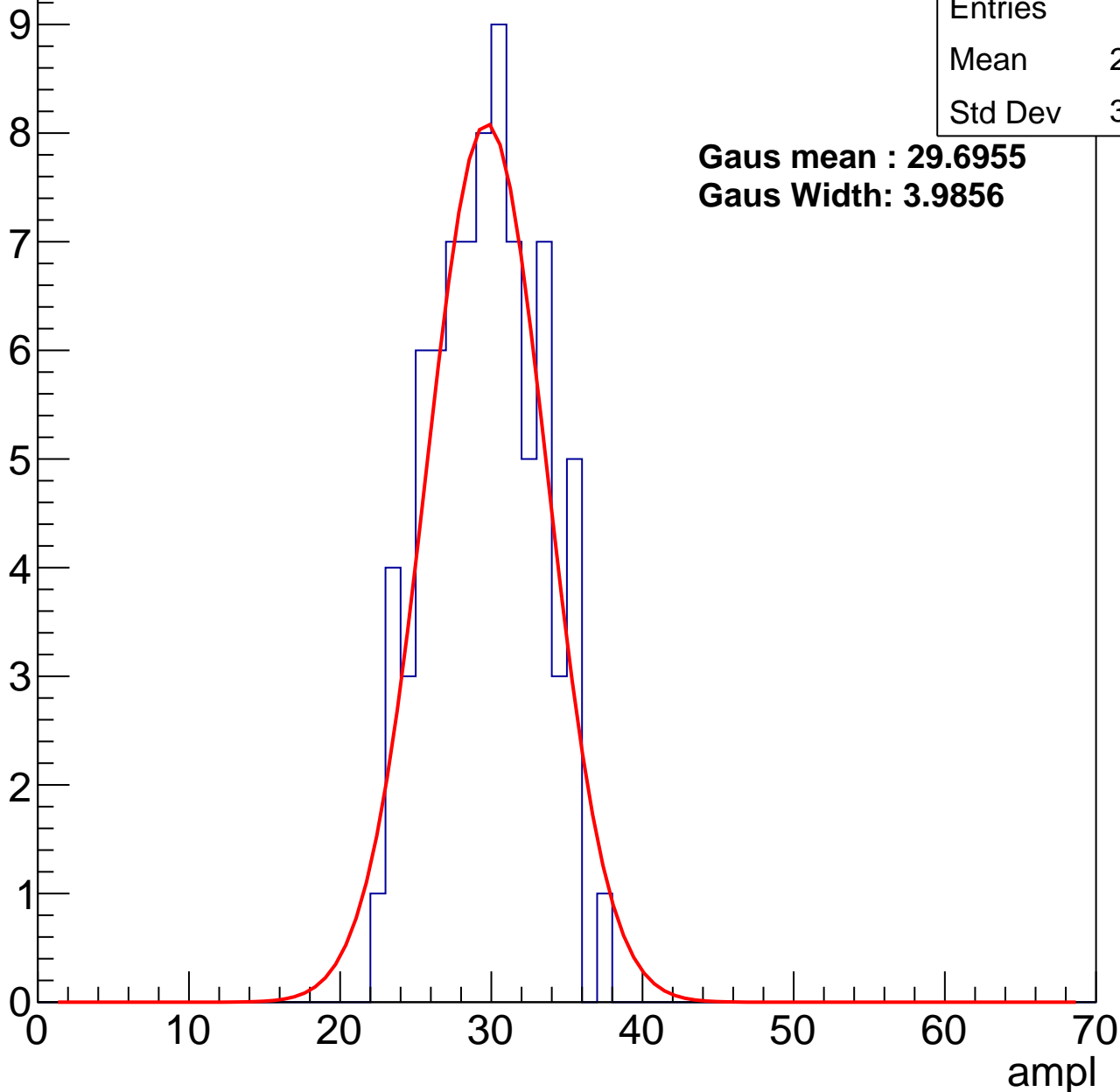
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.13
Std Dev	3.495

Gaus mean : 29.6955

Gaus Width: 3.9856



B1L101S, U6-ch98, adc1

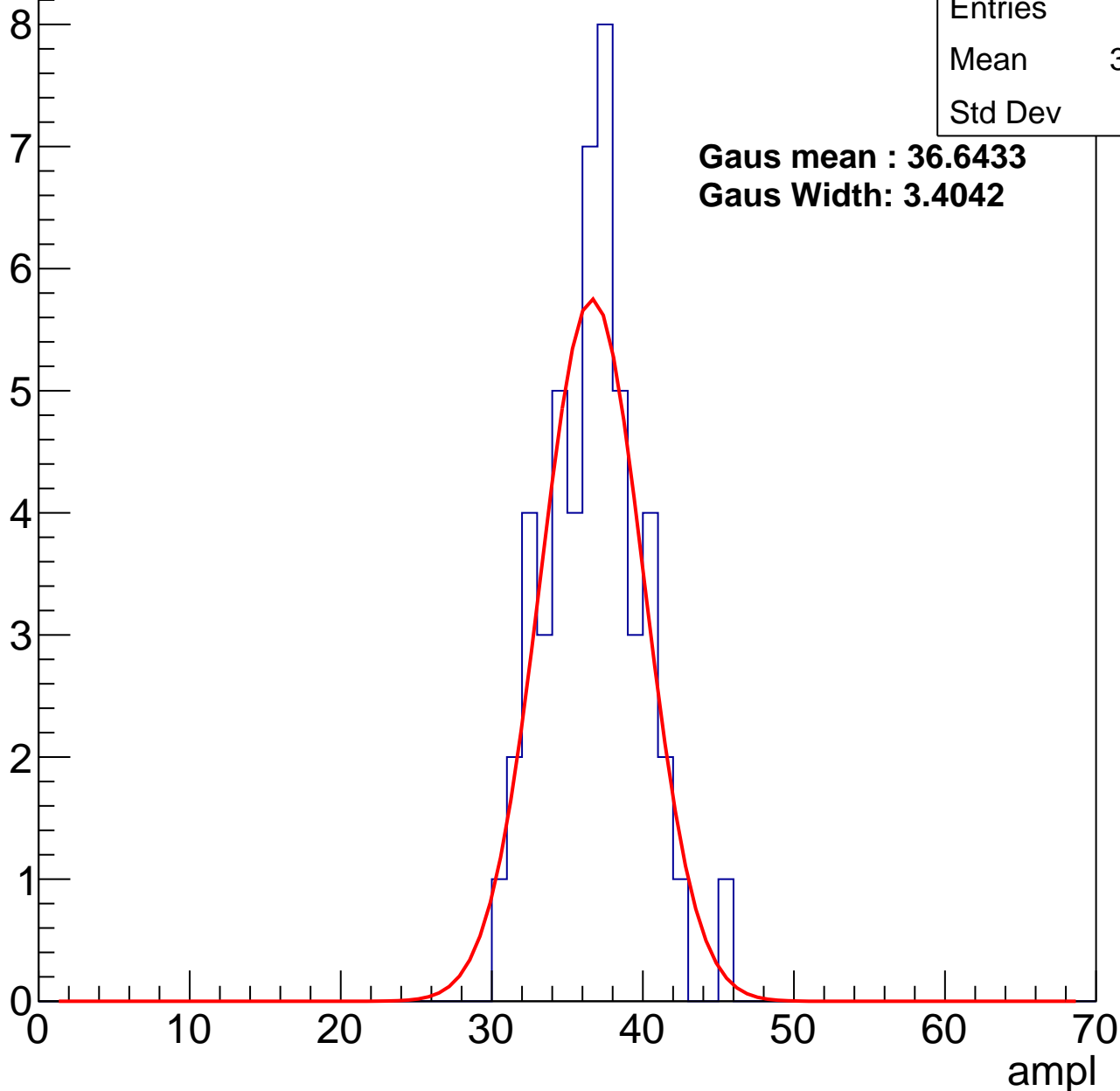
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	36.26
Std Dev	3.11

Gaus mean : 36.6433

Gaus Width: 3.4042



B1L101S, U6-ch98, adc2

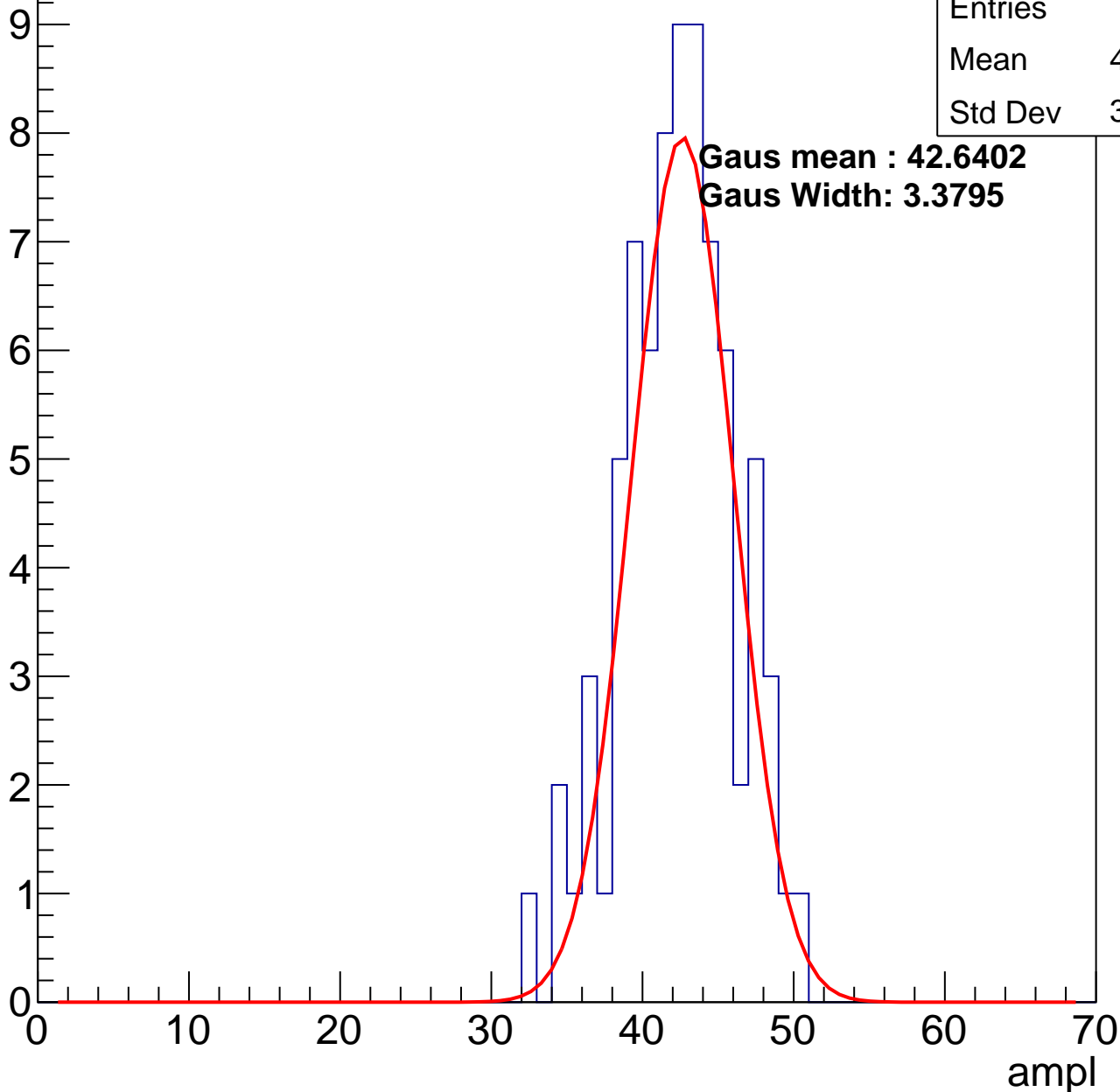
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	41.87
Std Dev	3.697

Gaus mean : 42.6402

Gaus Width: 3.3795

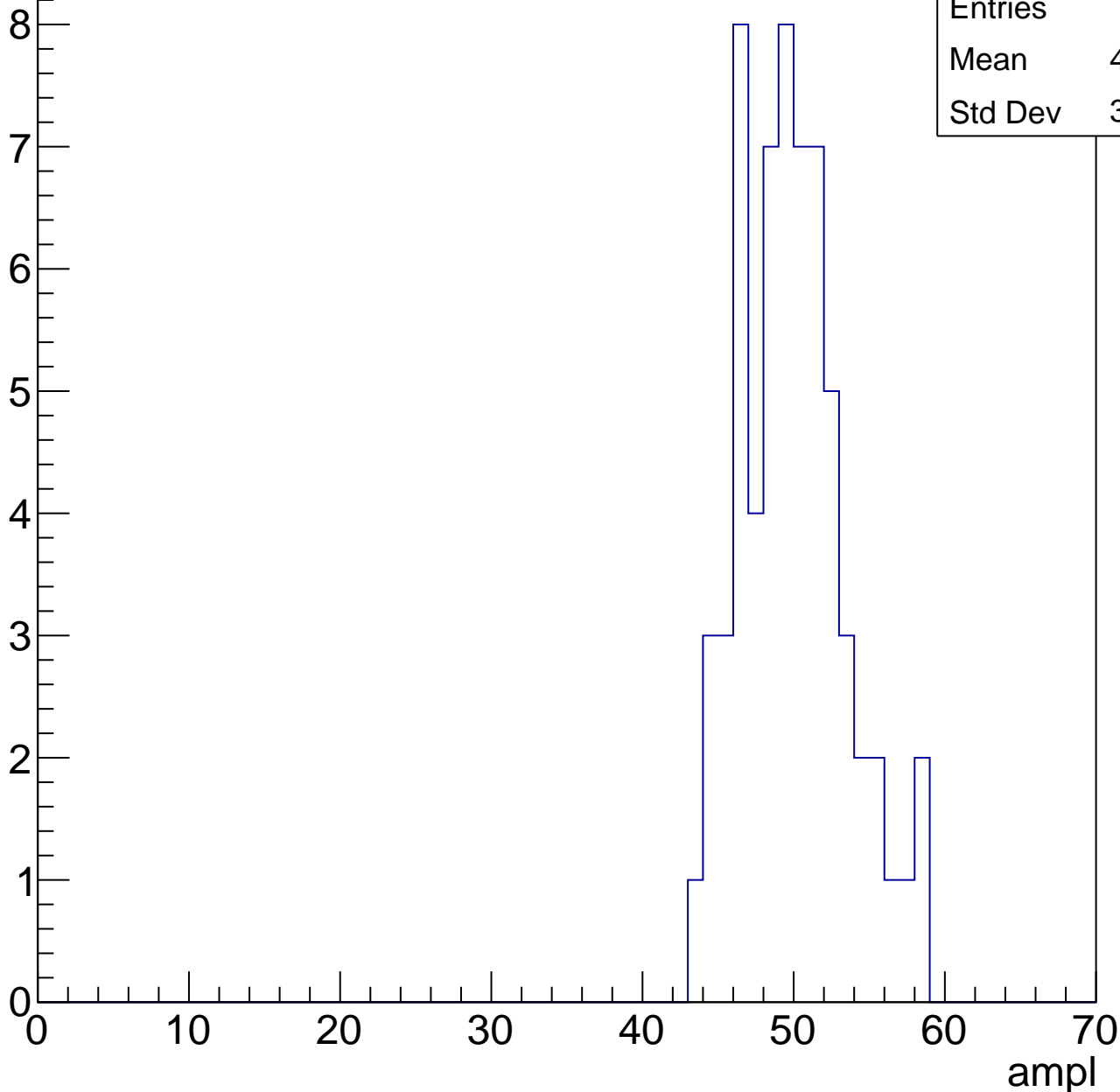


B1L101S, U6-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

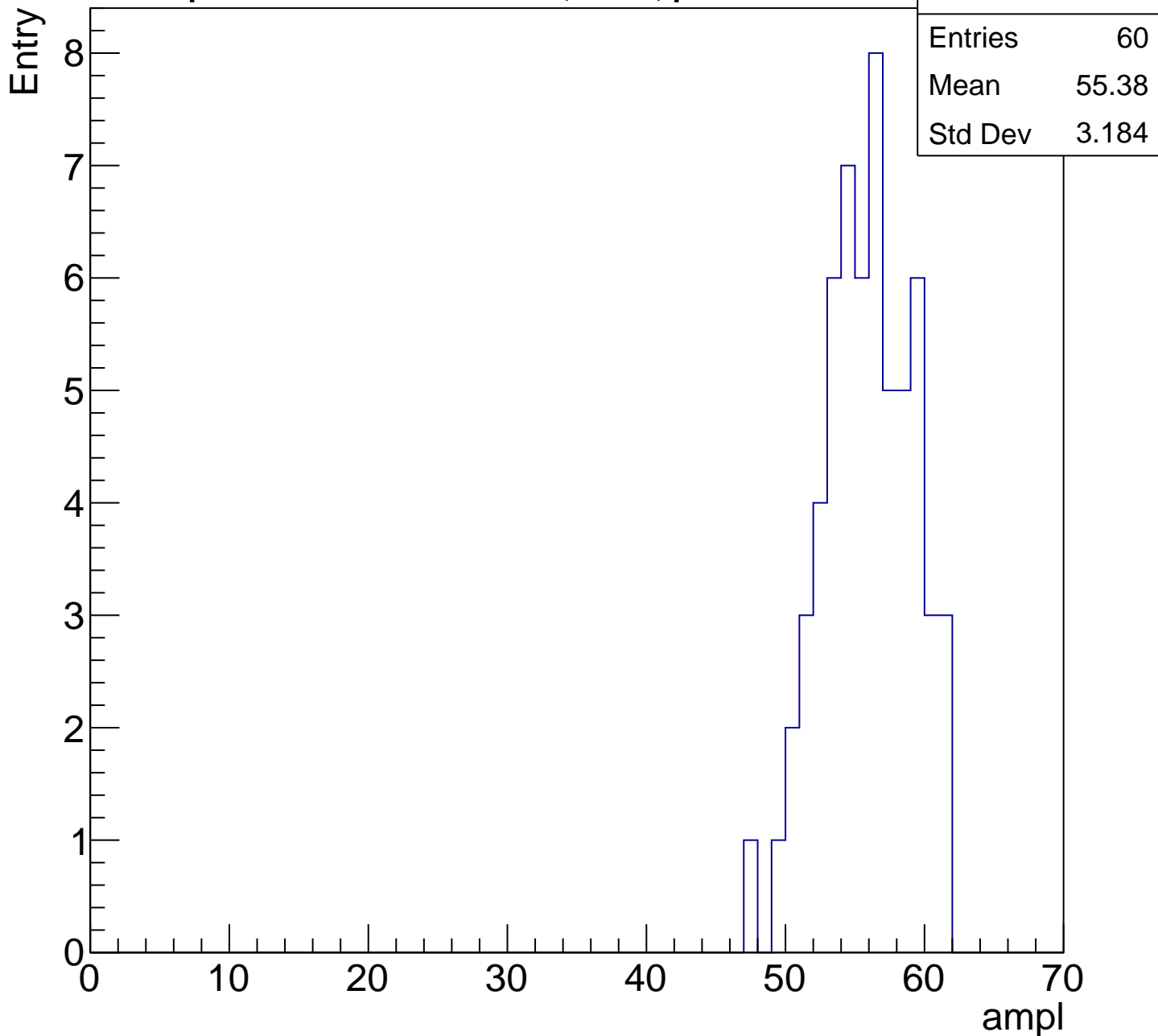
Entry

Entries	64
Mean	49.48
Std Dev	3.455



B1L101S, U6-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch98, adc5

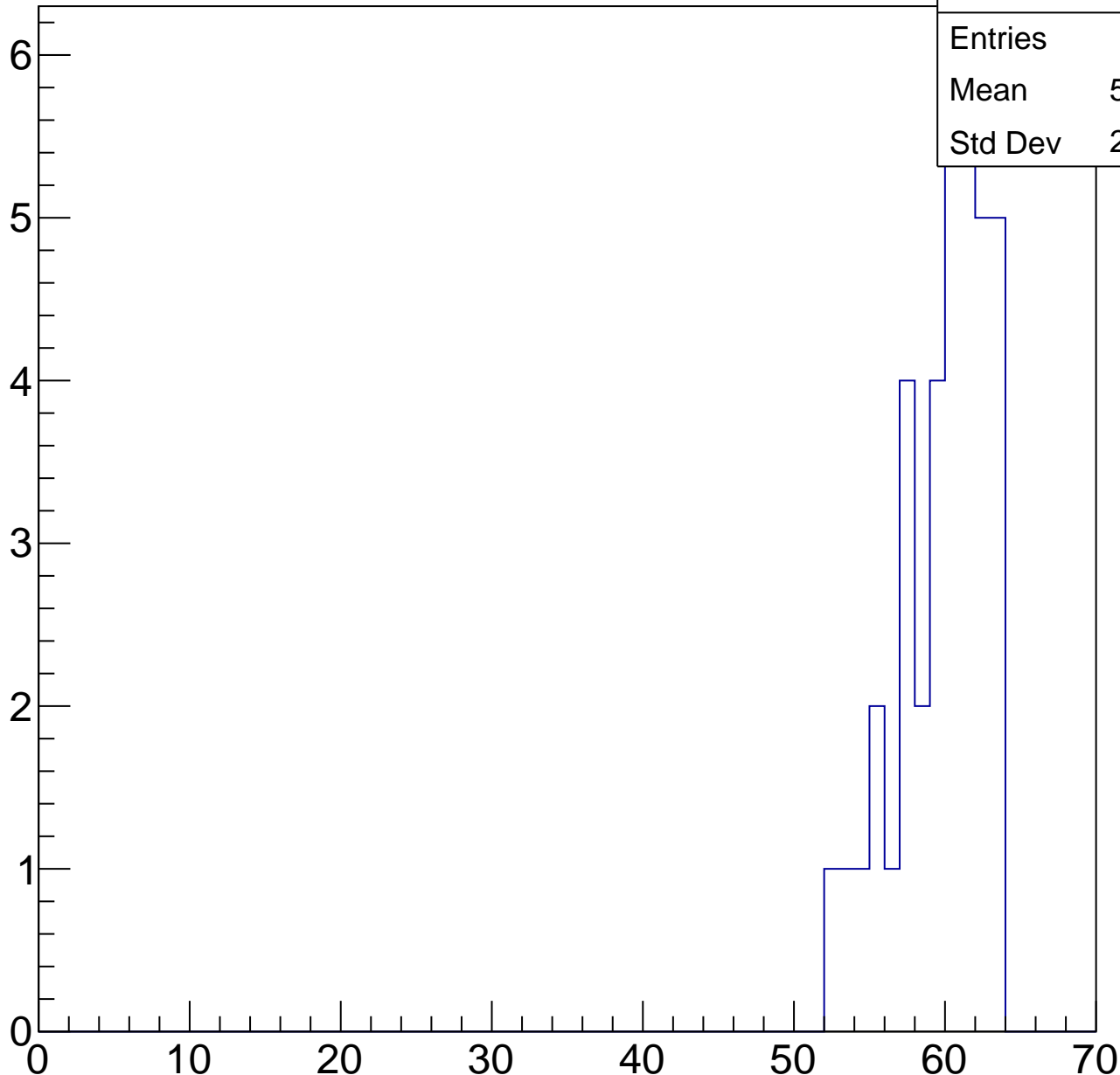
calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	38
Mean	59.37
Std Dev	2.897

ampl

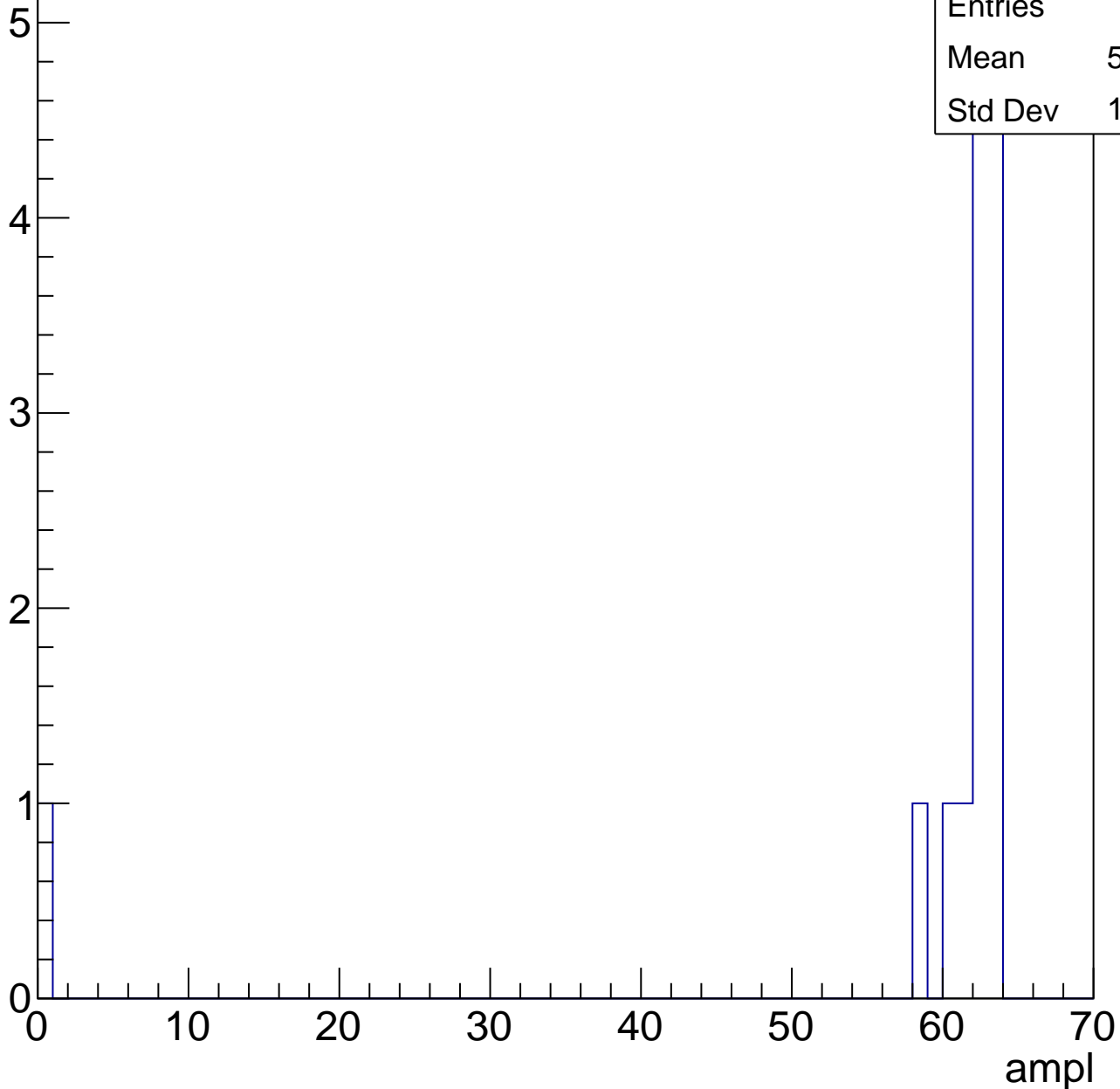


B1L101S, U6-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.43
Std Dev	15.99



B1L101S, U6-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch99, adc0

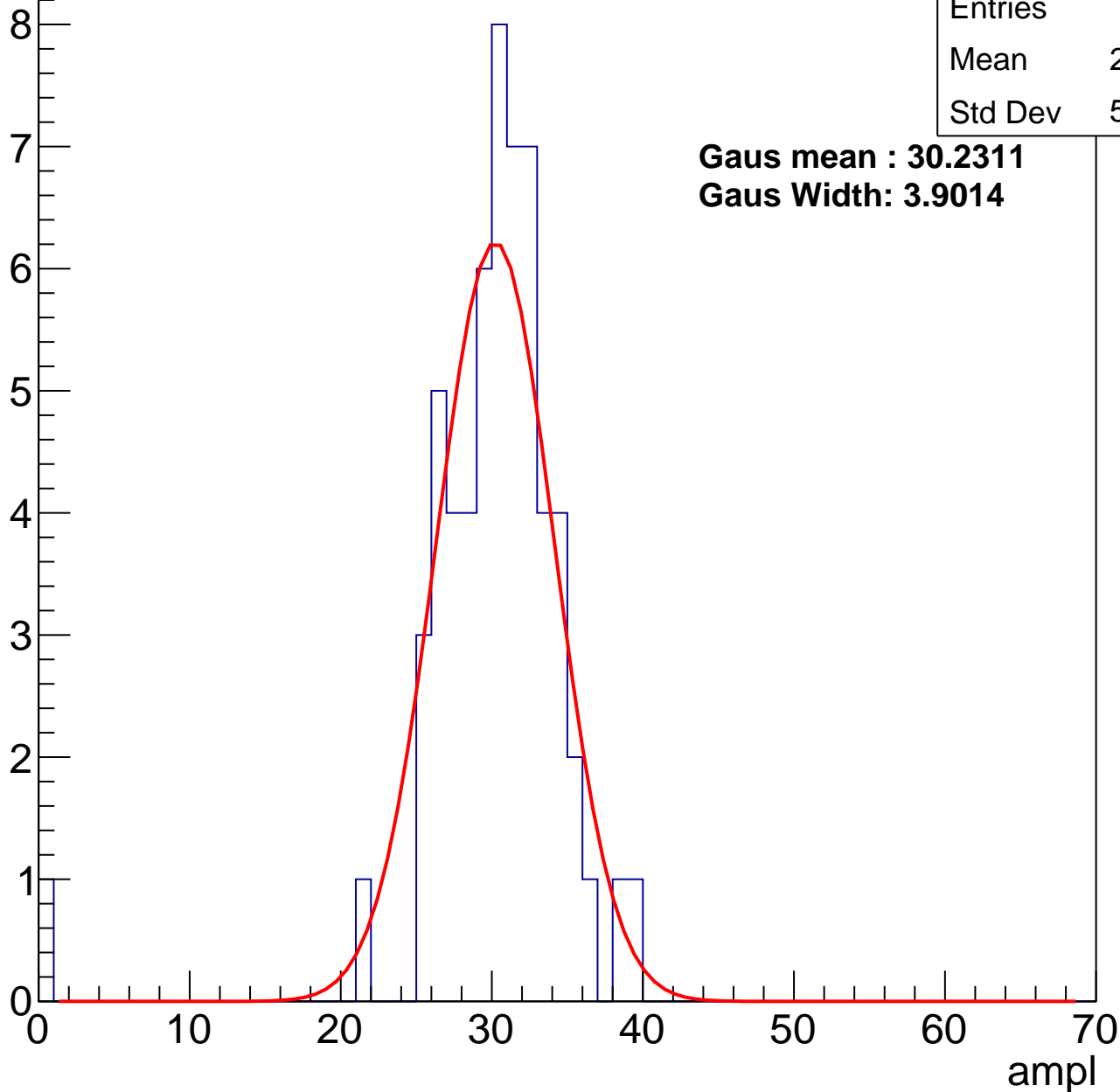
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.69
Std Dev	5.133

Gaus mean : 30.2311

Gaus Width: 3.9014



B1L101S, U6-ch99, adc1

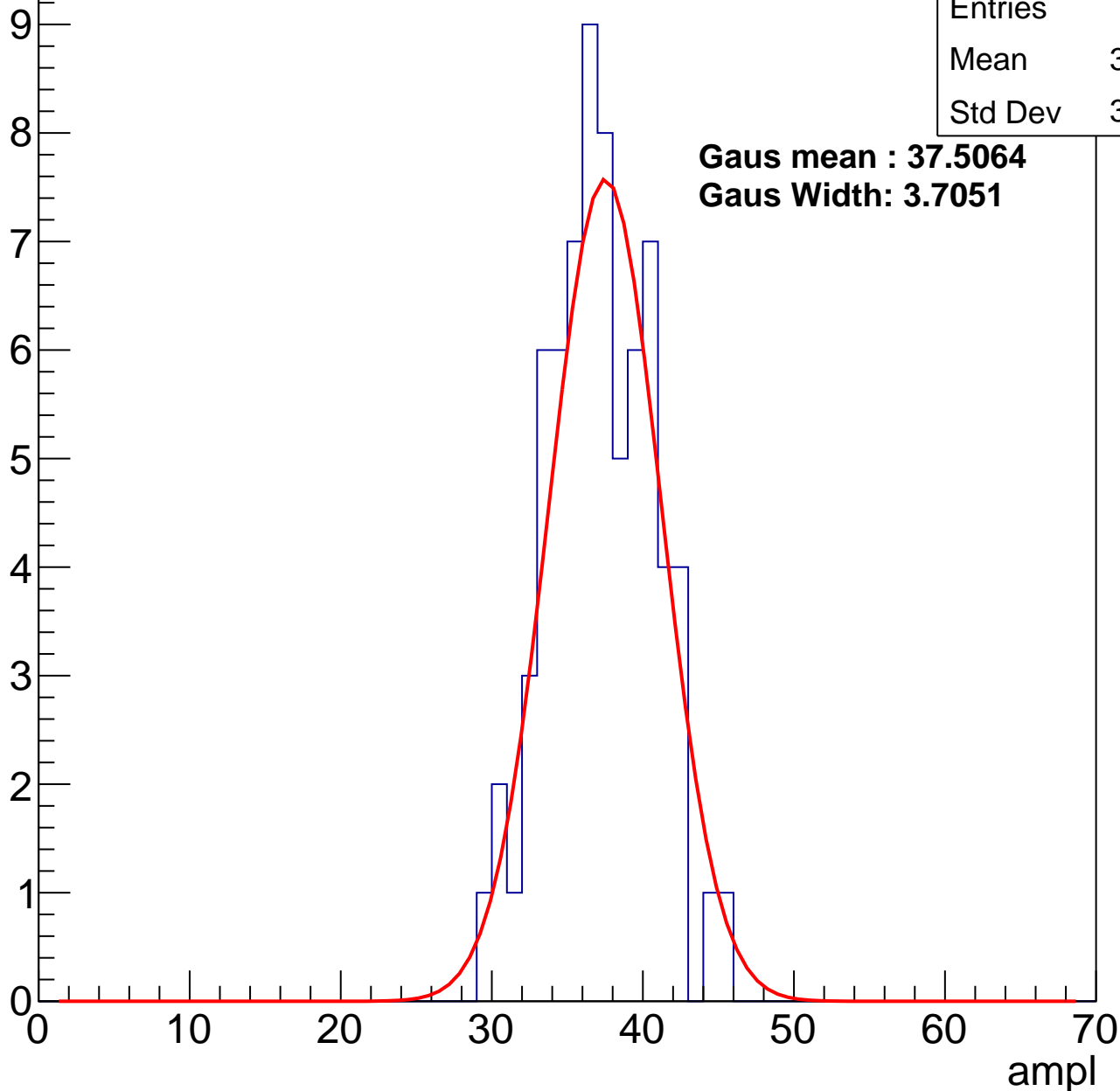
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.73
Std Dev	3.407

Gaus mean : 37.5064

Gaus Width: 3.7051



B1L101S, U6-ch99, adc2

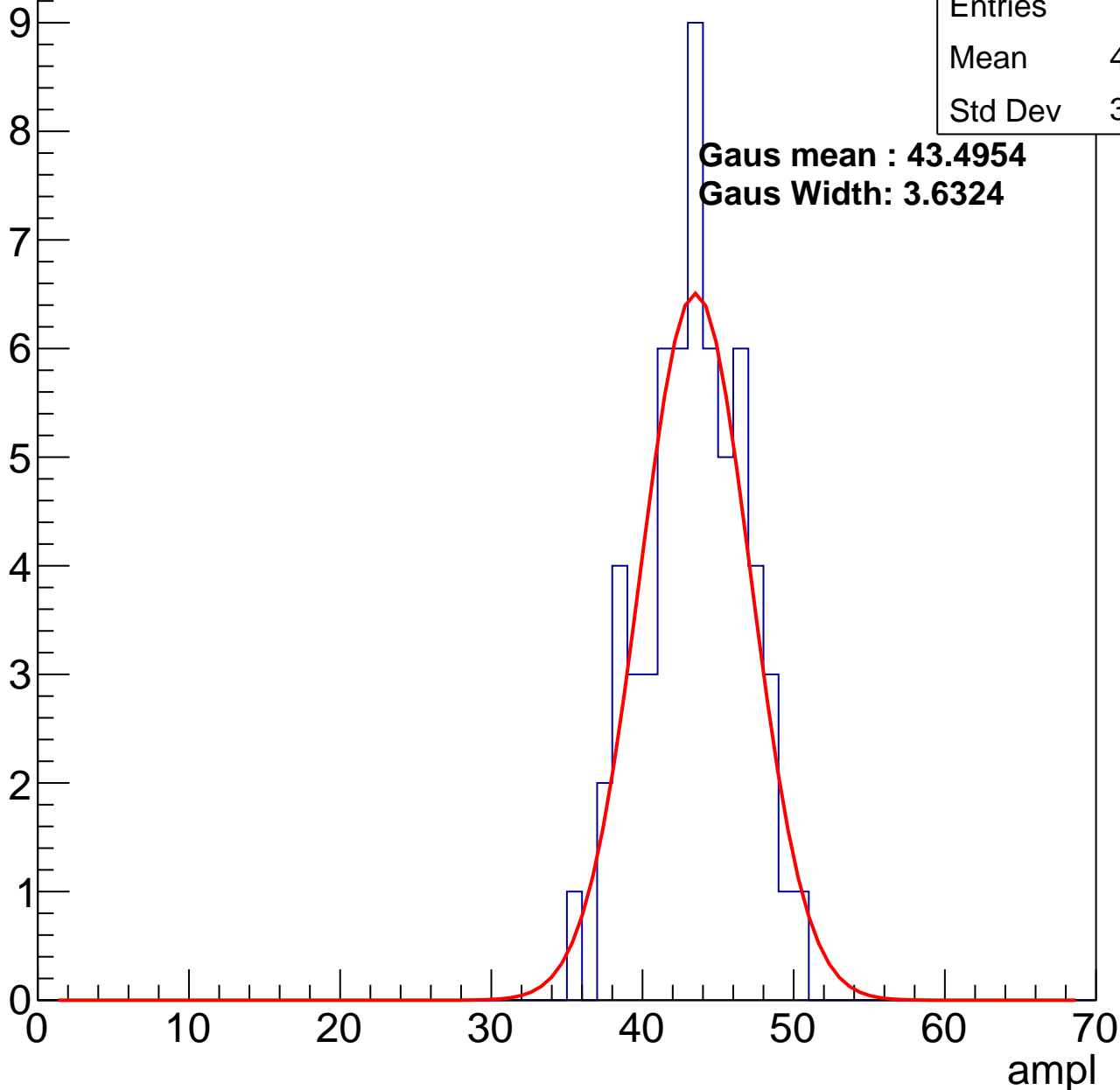
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.98
Std Dev	3.279

Gaus mean : 43.4954

Gaus Width: 3.6324

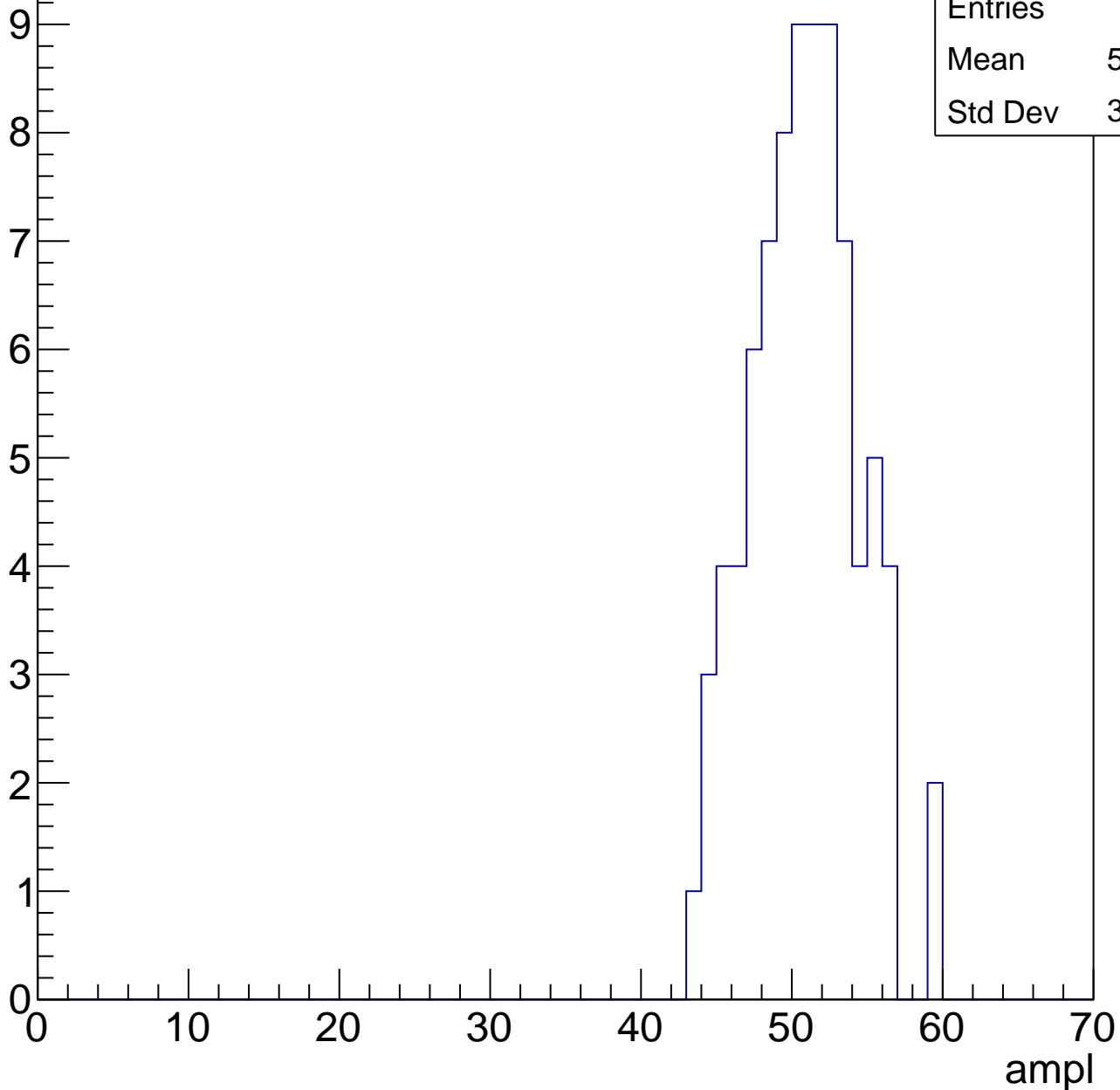


B1L101S, U6-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	50.37
Std Dev	3.497

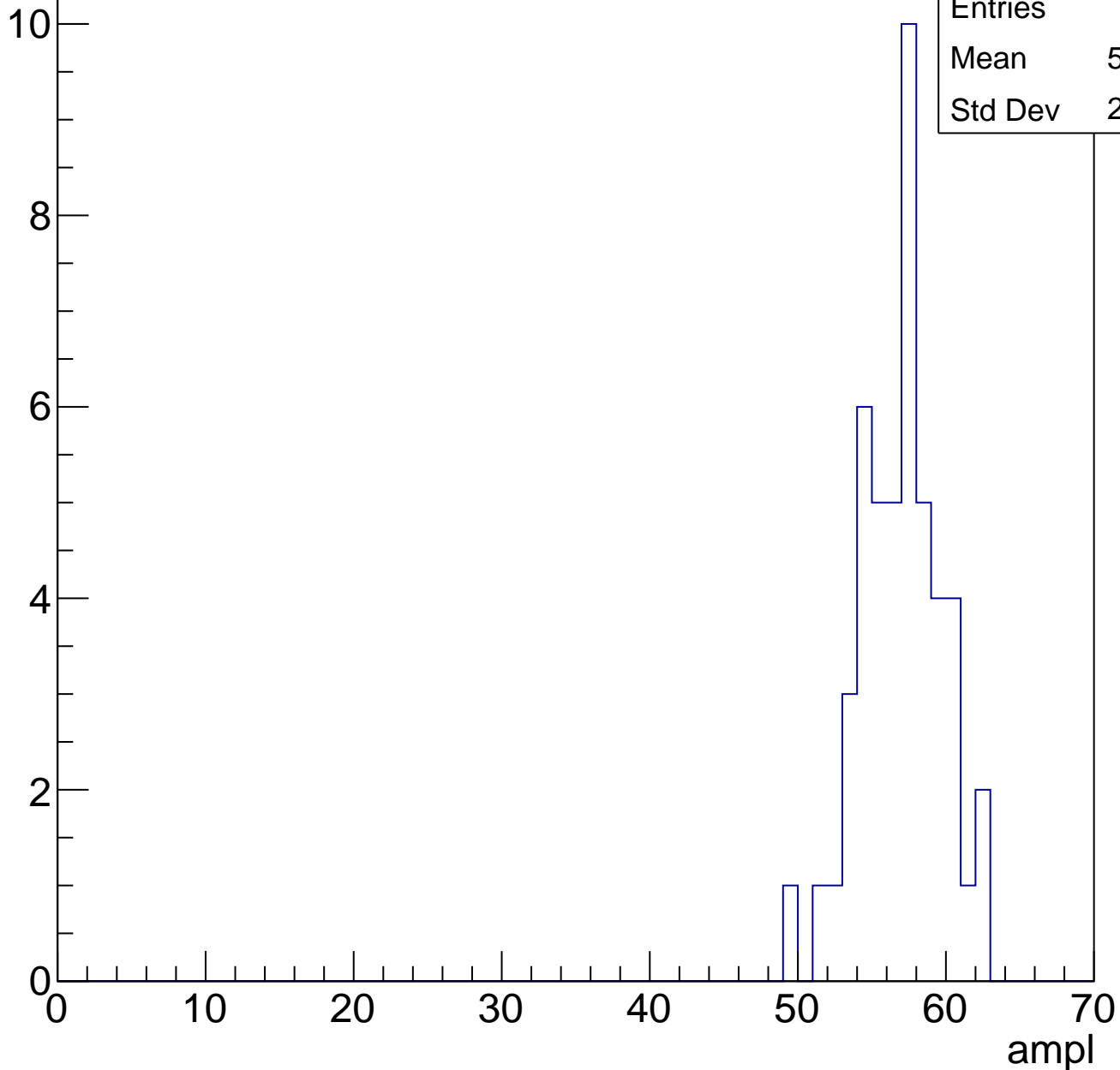


B1L101S, U6-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	48
Mean	56.48
Std Dev	2.746

Entry

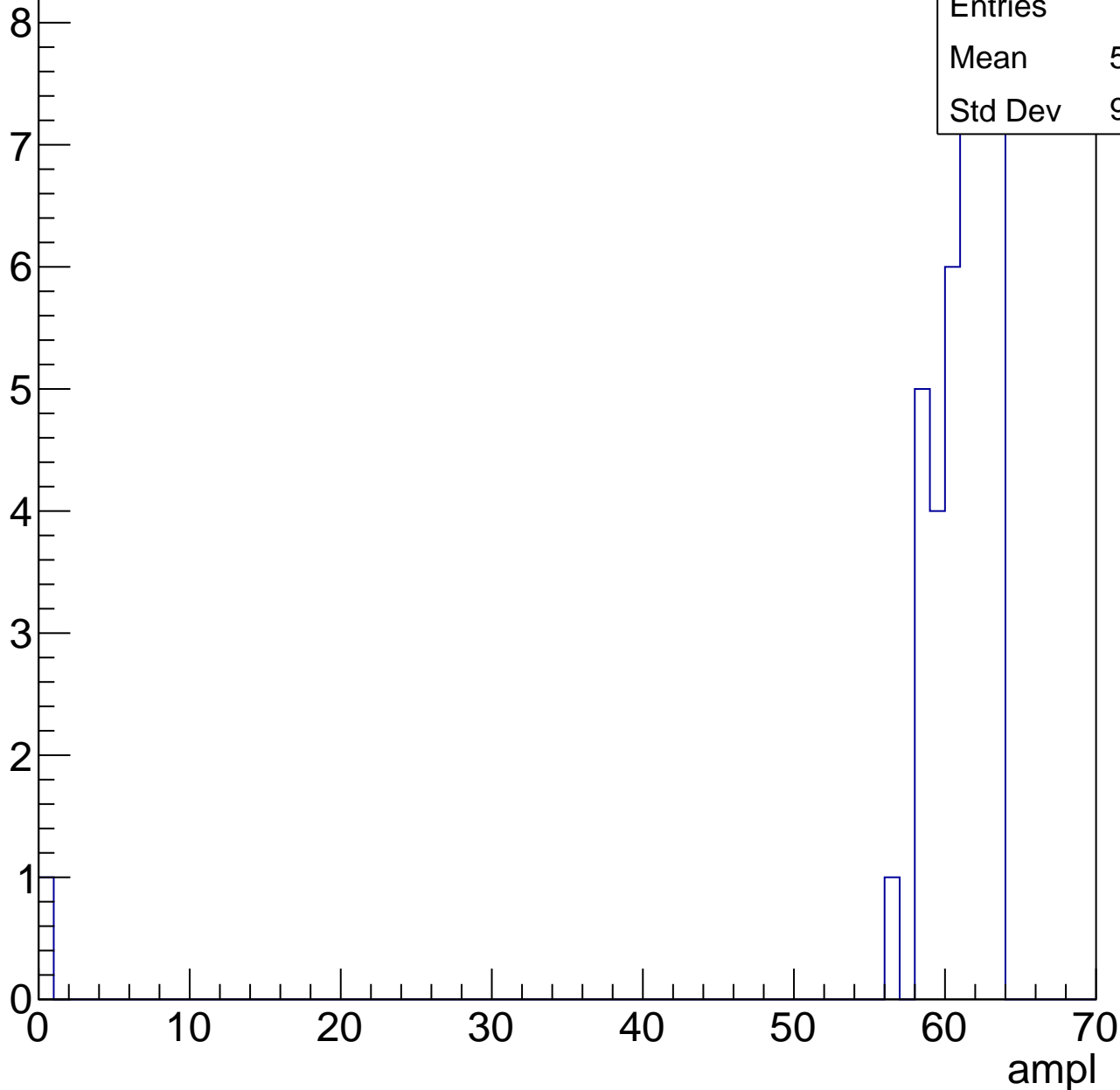


B1L101S, U6-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.27
Std Dev	9.538



B1L101S, U6-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch100, adc0

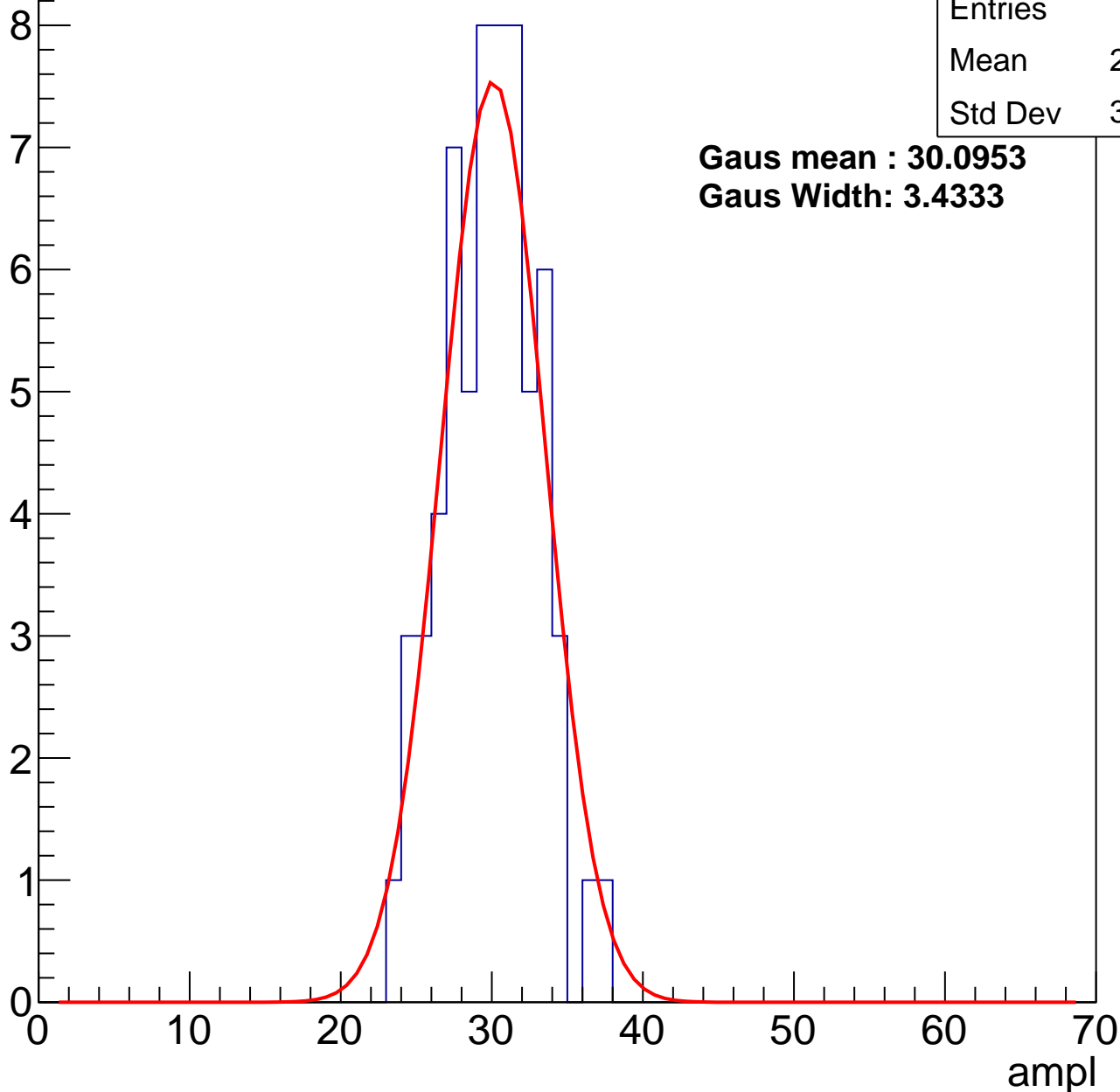
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.46
Std Dev	3.044

Gaus mean : 30.0953

Gaus Width: 3.4333



B1L101S, U6-ch100, adc1

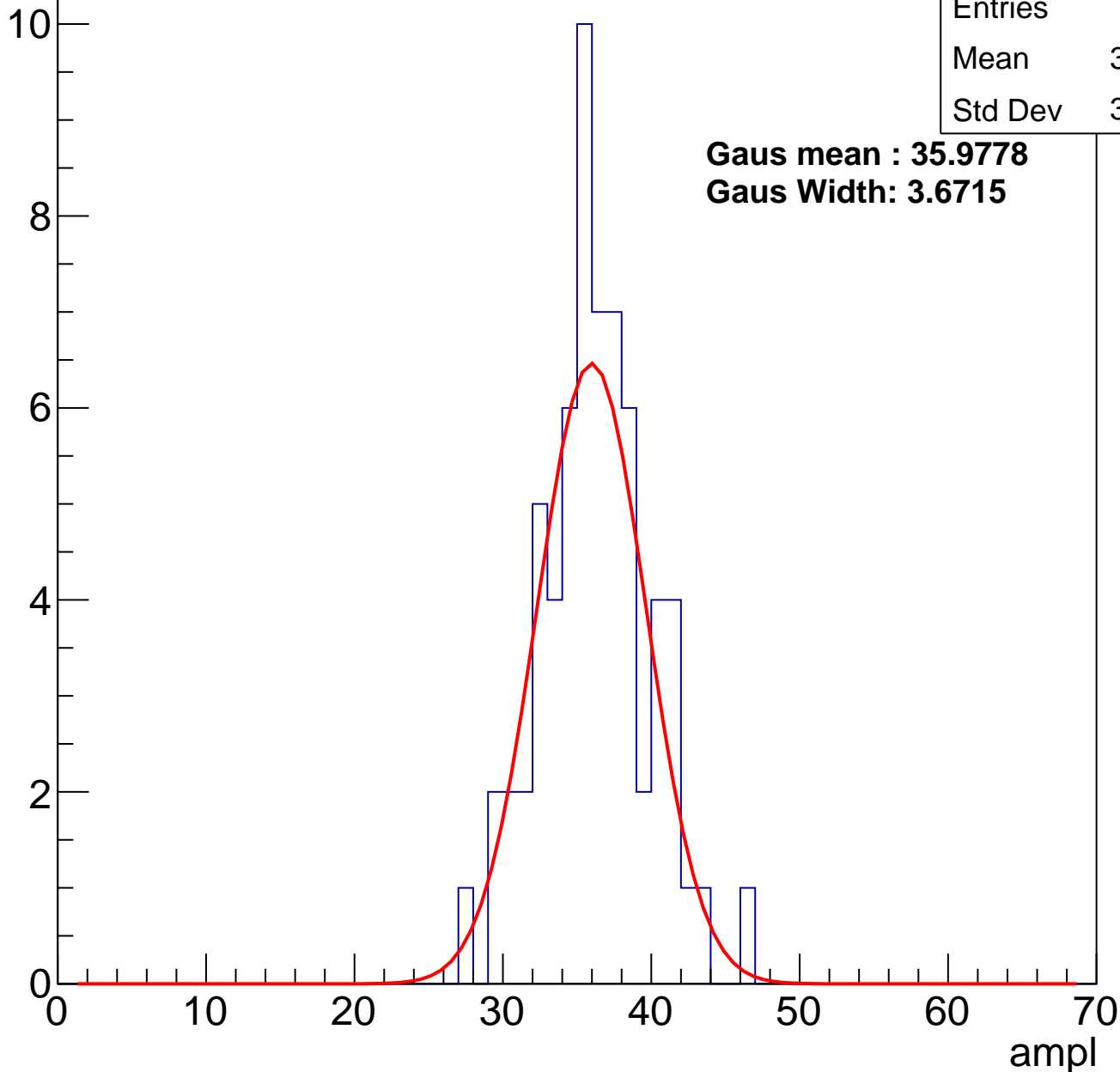
calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	35.77
Std Dev	3.607

Gaus mean : 35.9778

Gaus Width: 3.6715

Entry



B1L101S, U6-ch100, adc2

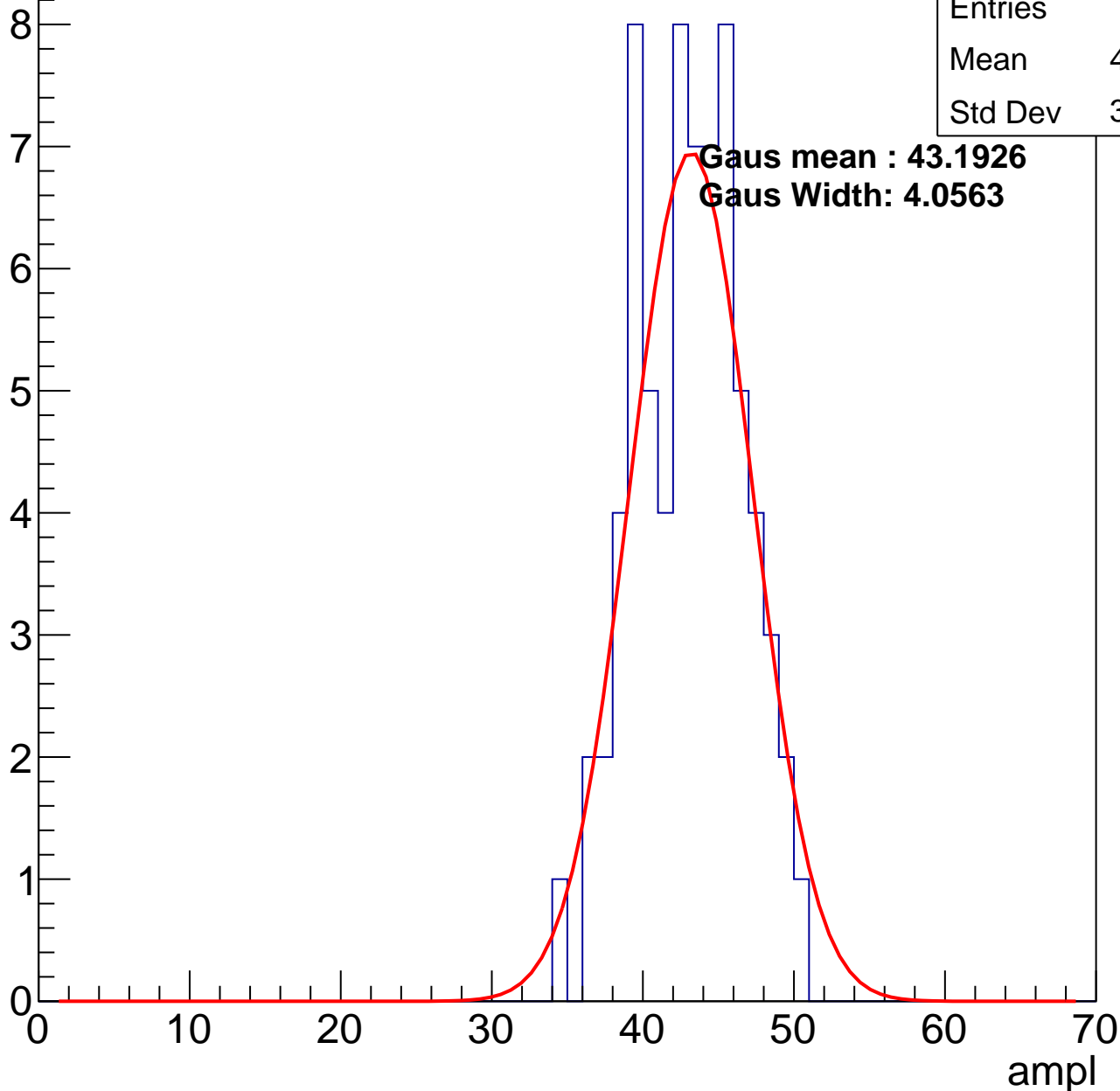
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.58
Std Dev	3.519

Gaus mean : 43.1926

Gaus Width: 4.0563

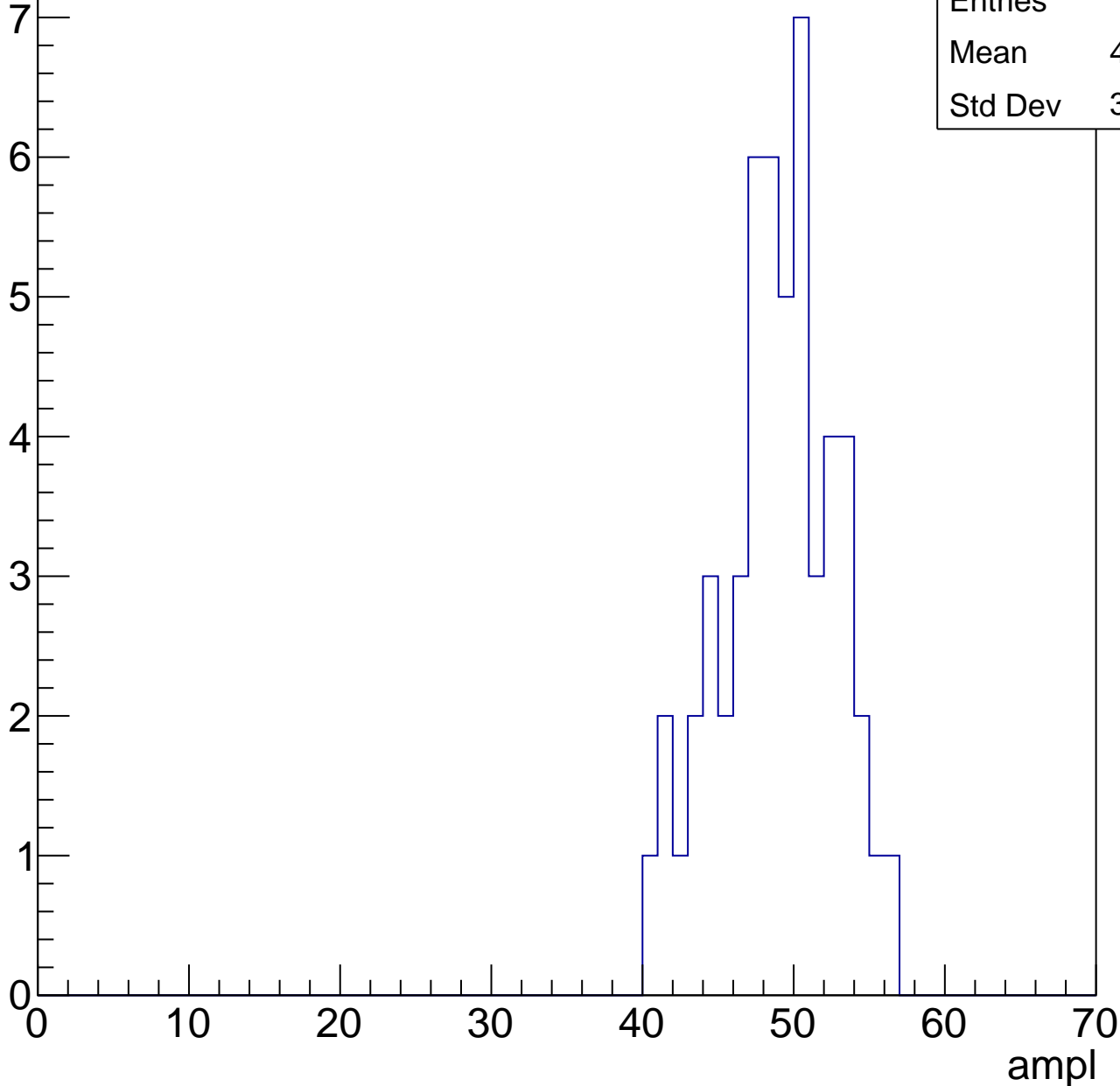


B1L101S, U6-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	48.43
Std Dev	3.714

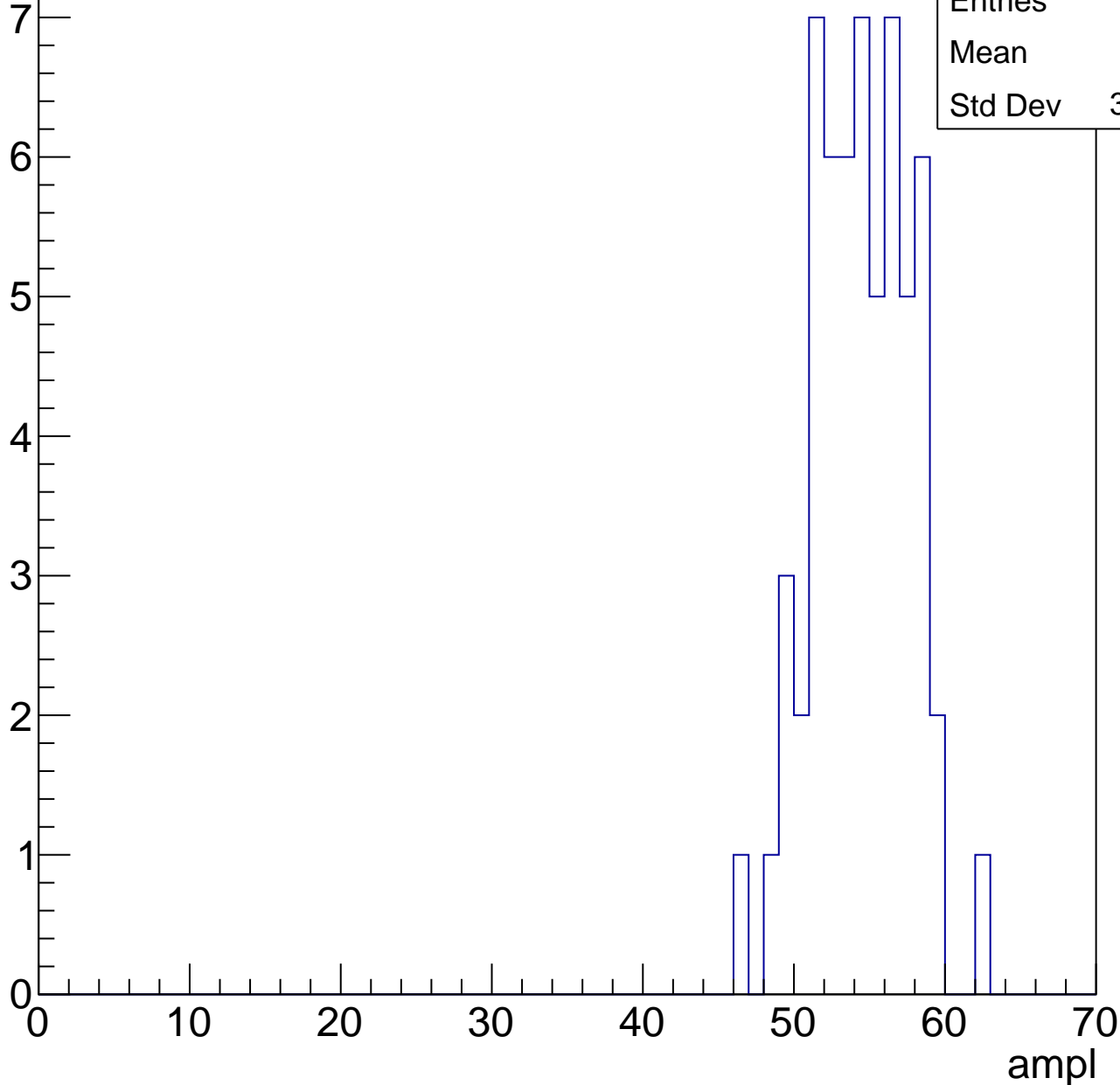


B1L101S, U6-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54
Std Dev	3.157

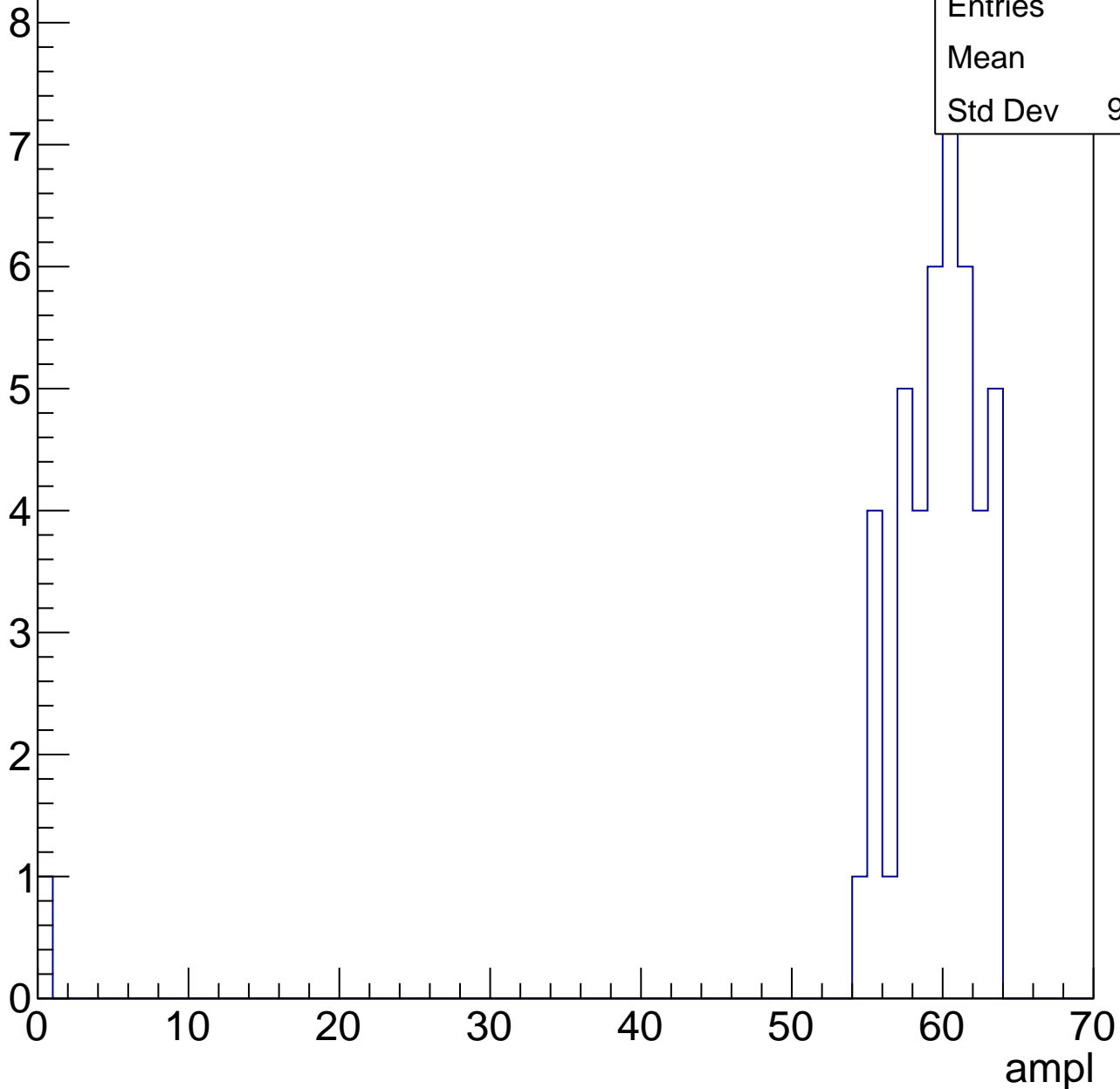


B1L101S, U6-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58
Std Dev	9.075

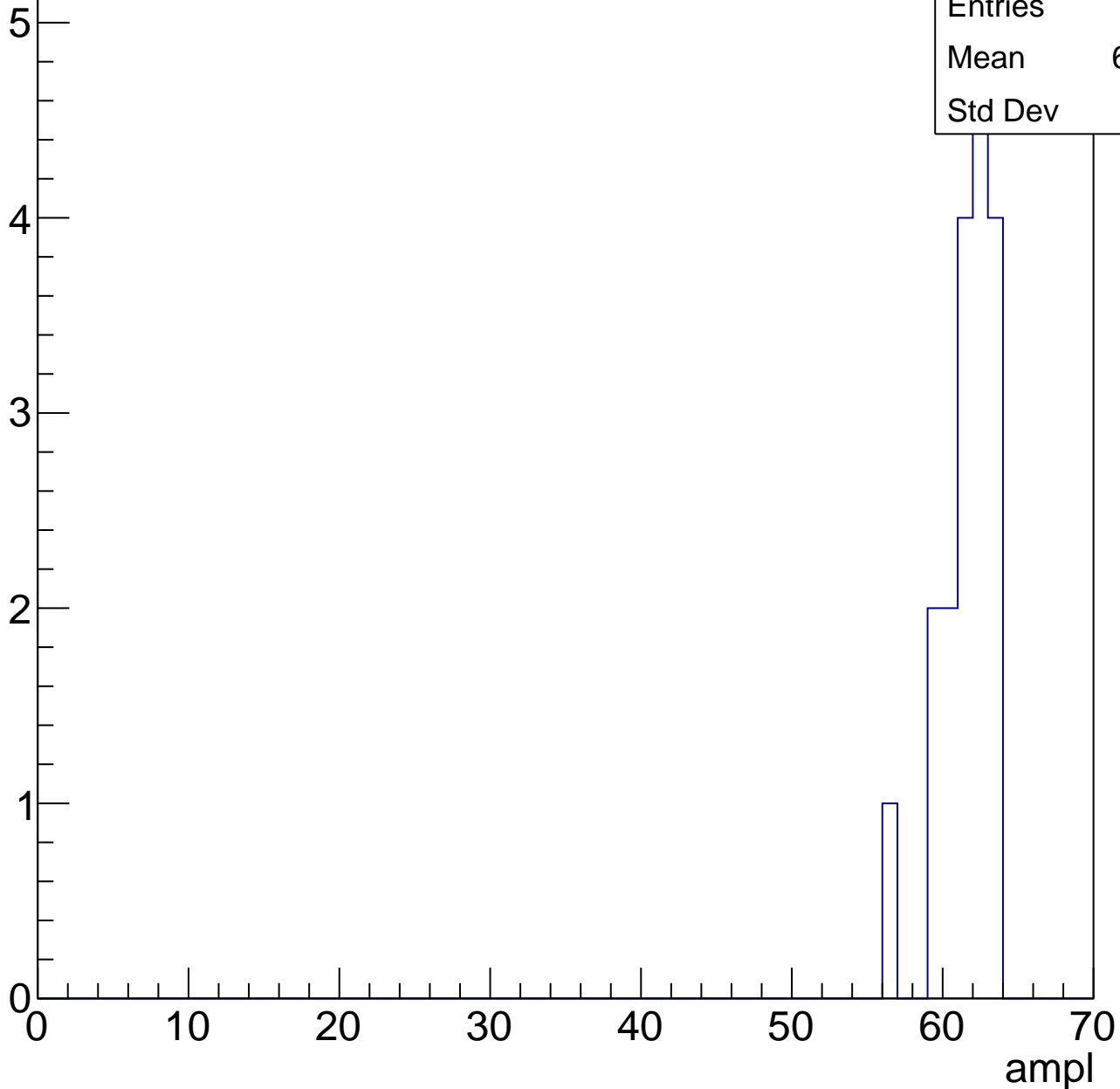


B1L101S, U6-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

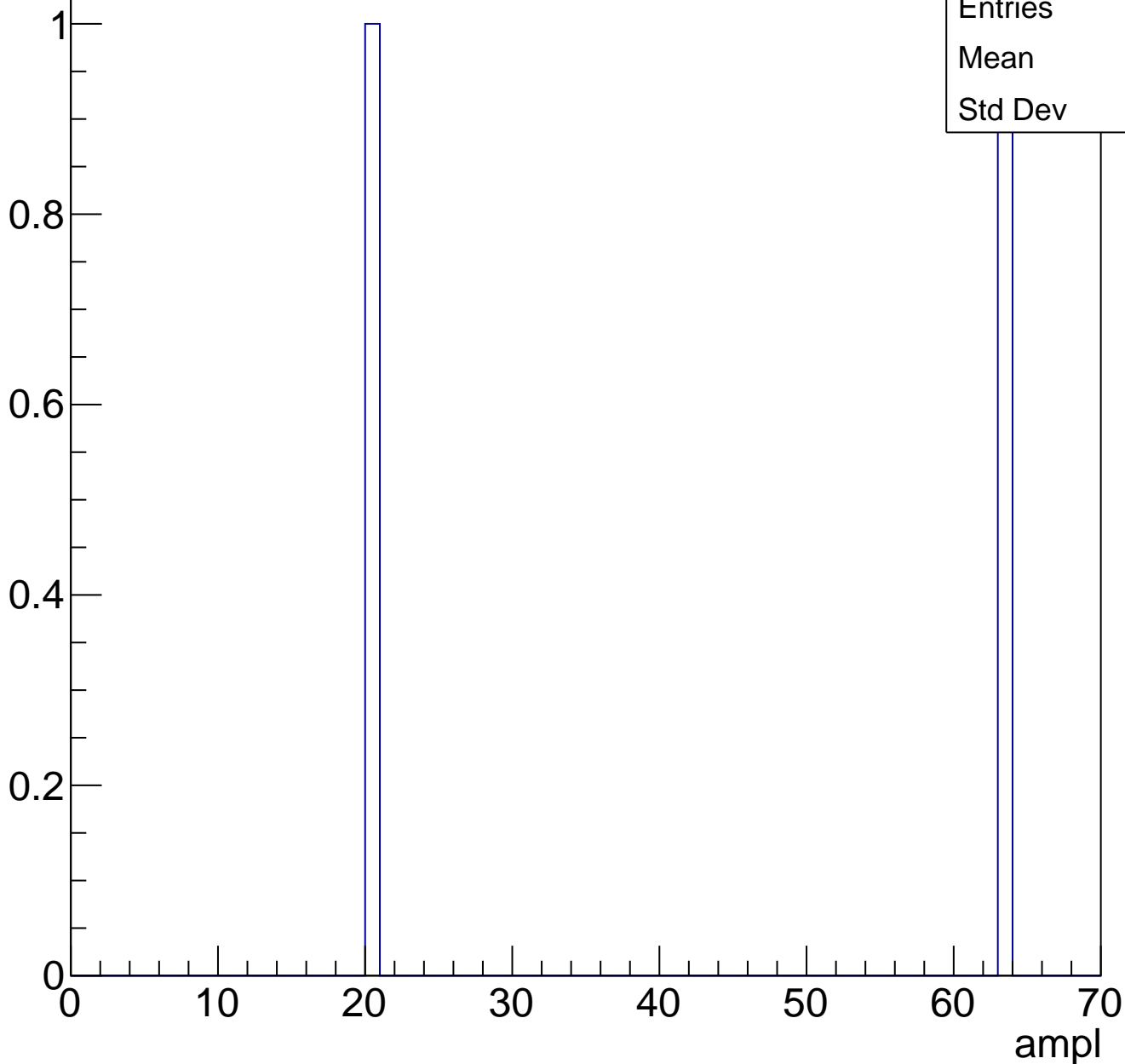
Entries	18
Mean	61.11
Std Dev	1.76



B1L101S, U6-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch101, adc0

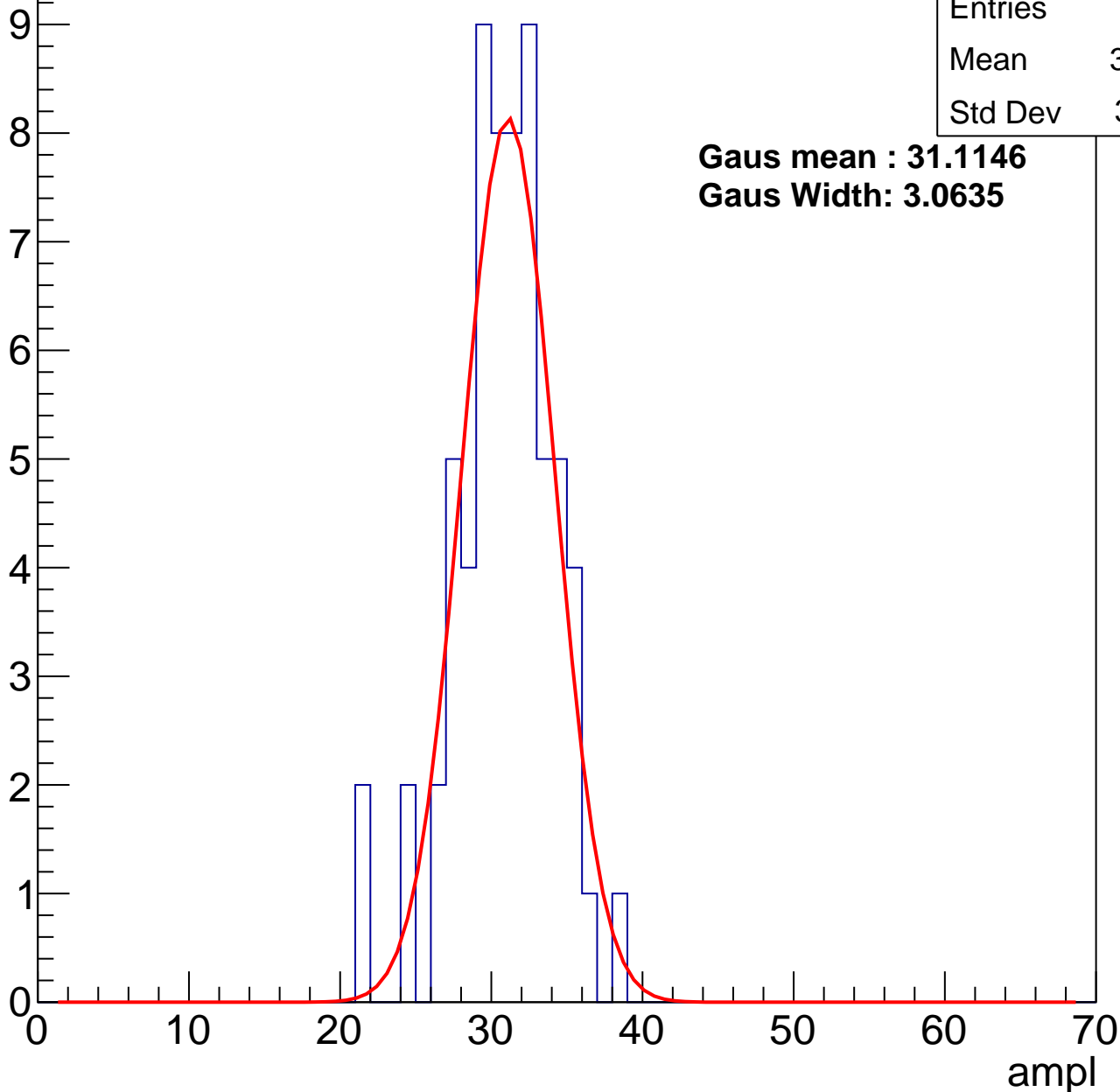
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.38
Std Dev	3.271

Gaus mean : 31.1146

Gaus Width: 3.0635

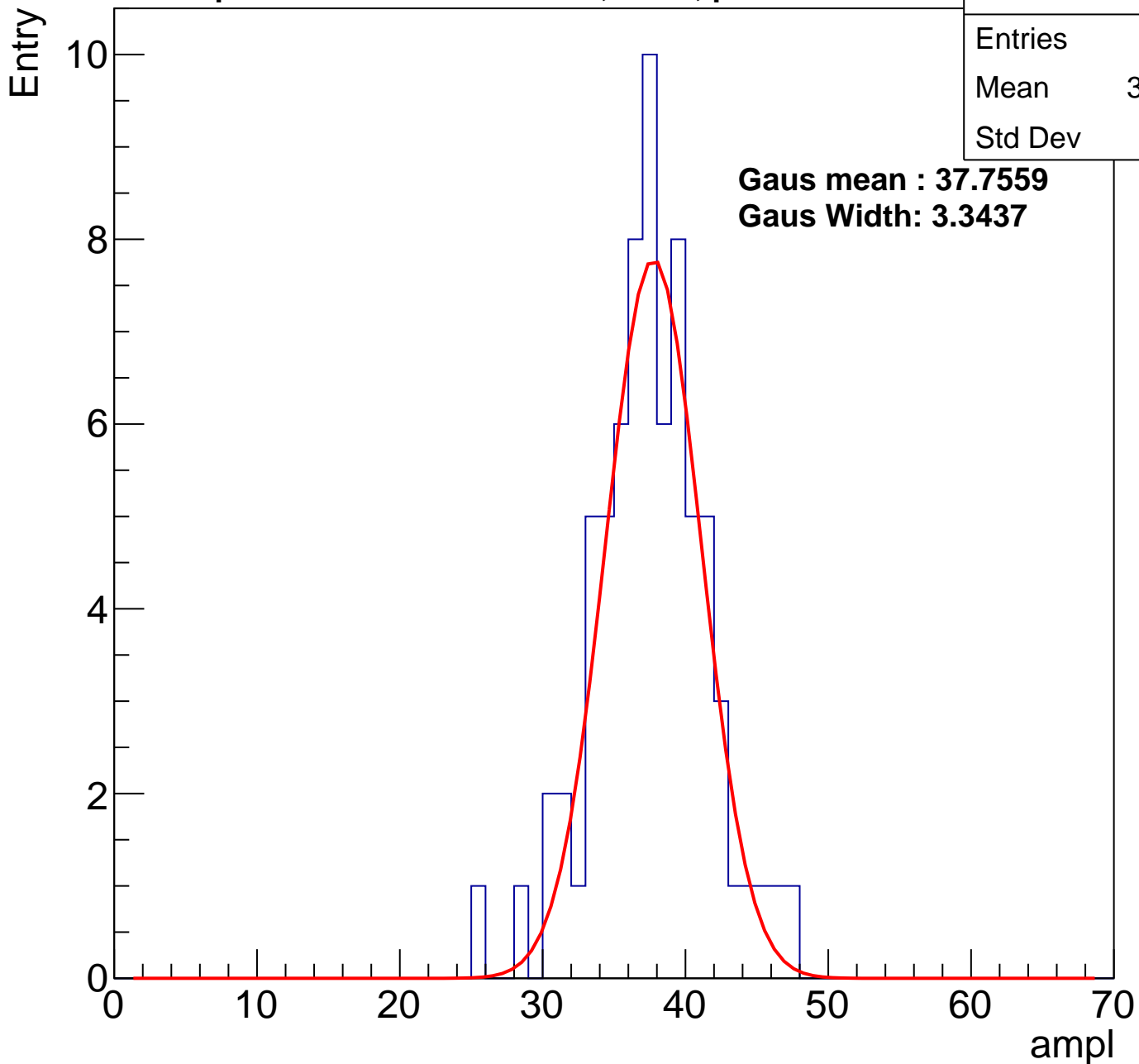


B1L101S, U6-ch101, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	37.07
Std Dev	3.96

Gaus mean : 37.7559
Gaus Width: 3.3437



B1L101S, U6-ch101, adc2

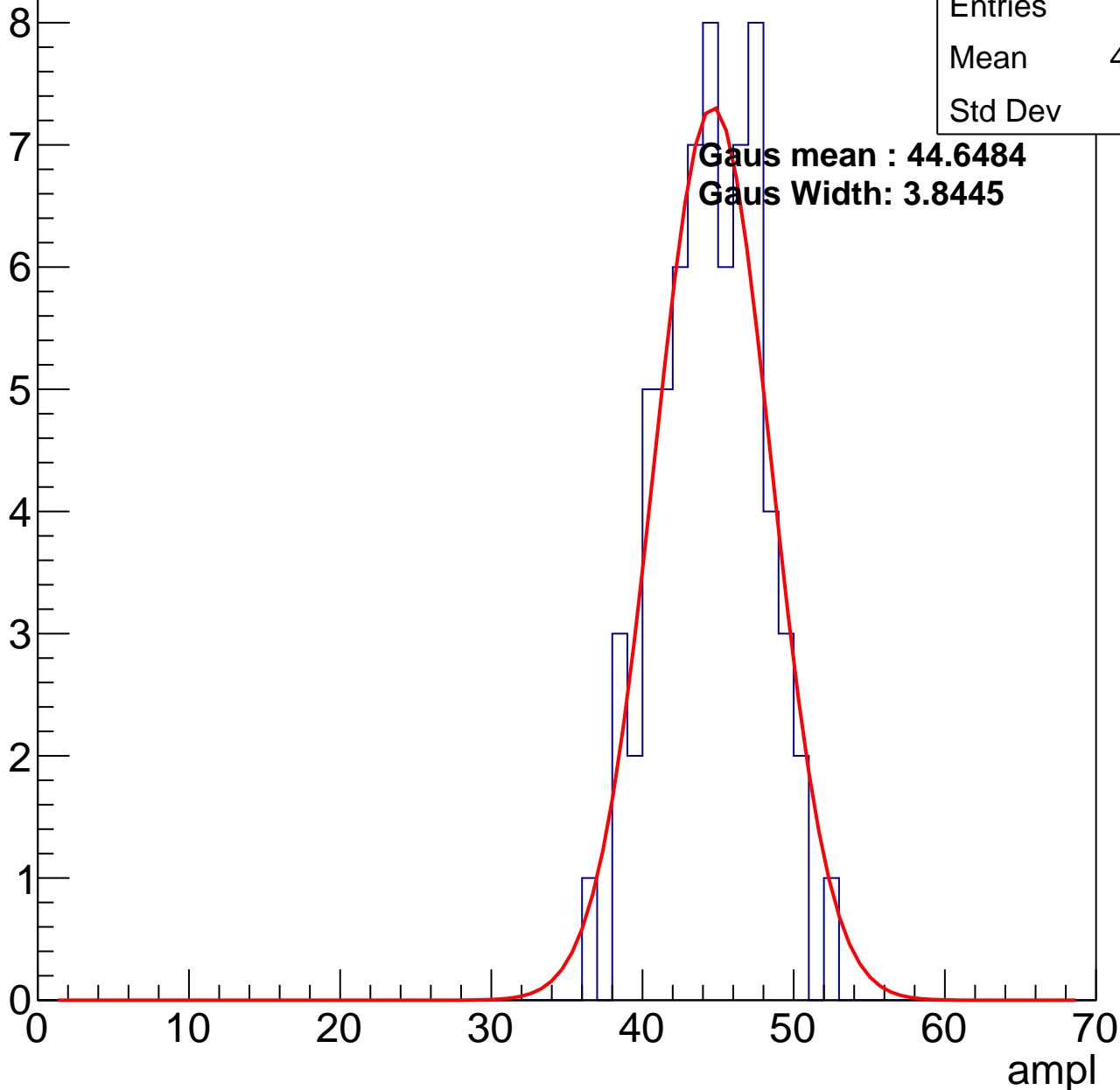
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.07
Std Dev	3.34

Gaus mean : 44.6484

Gaus Width: 3.8445

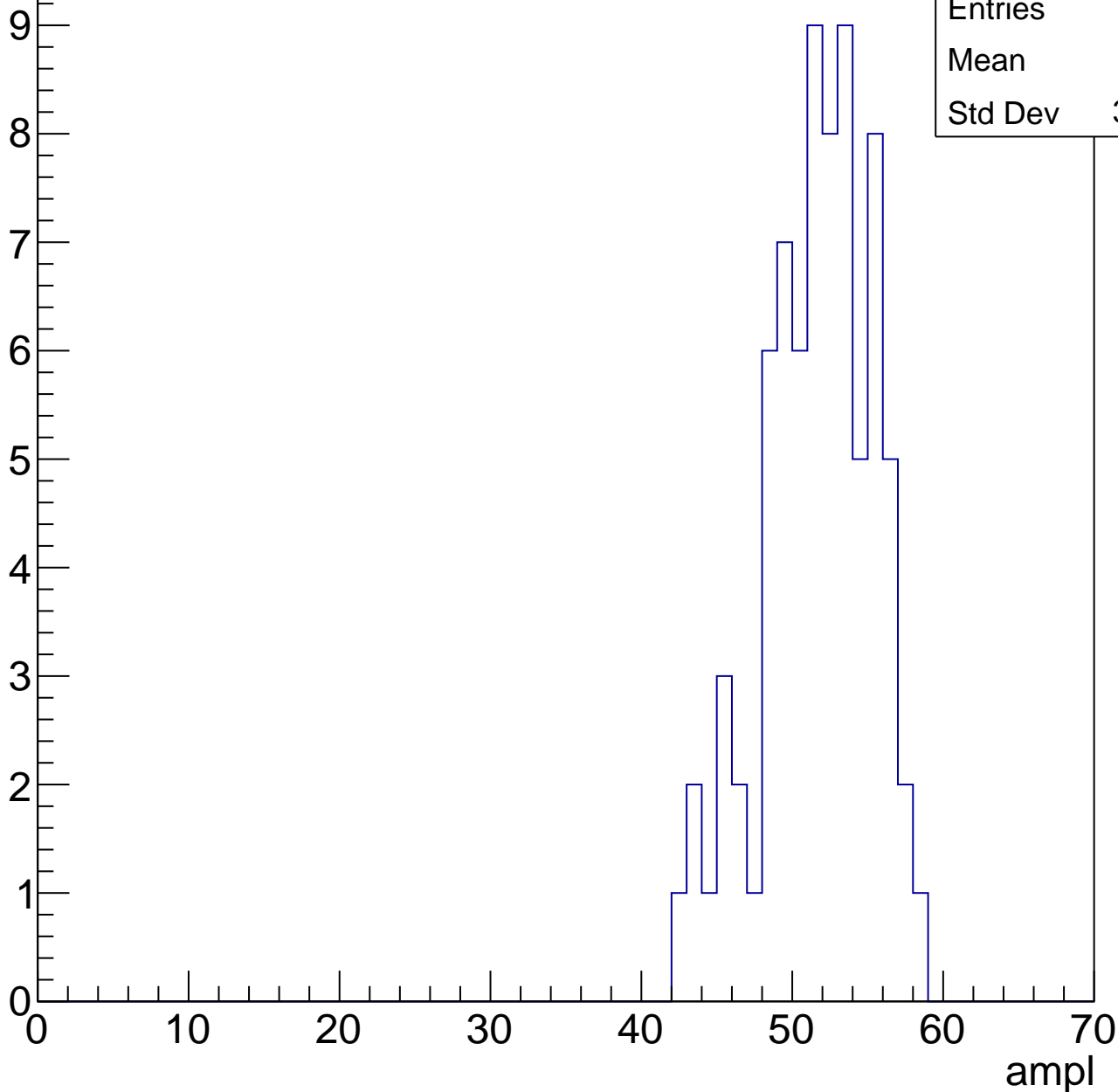


B1L101S, U6-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	51.2
Std Dev	3.591

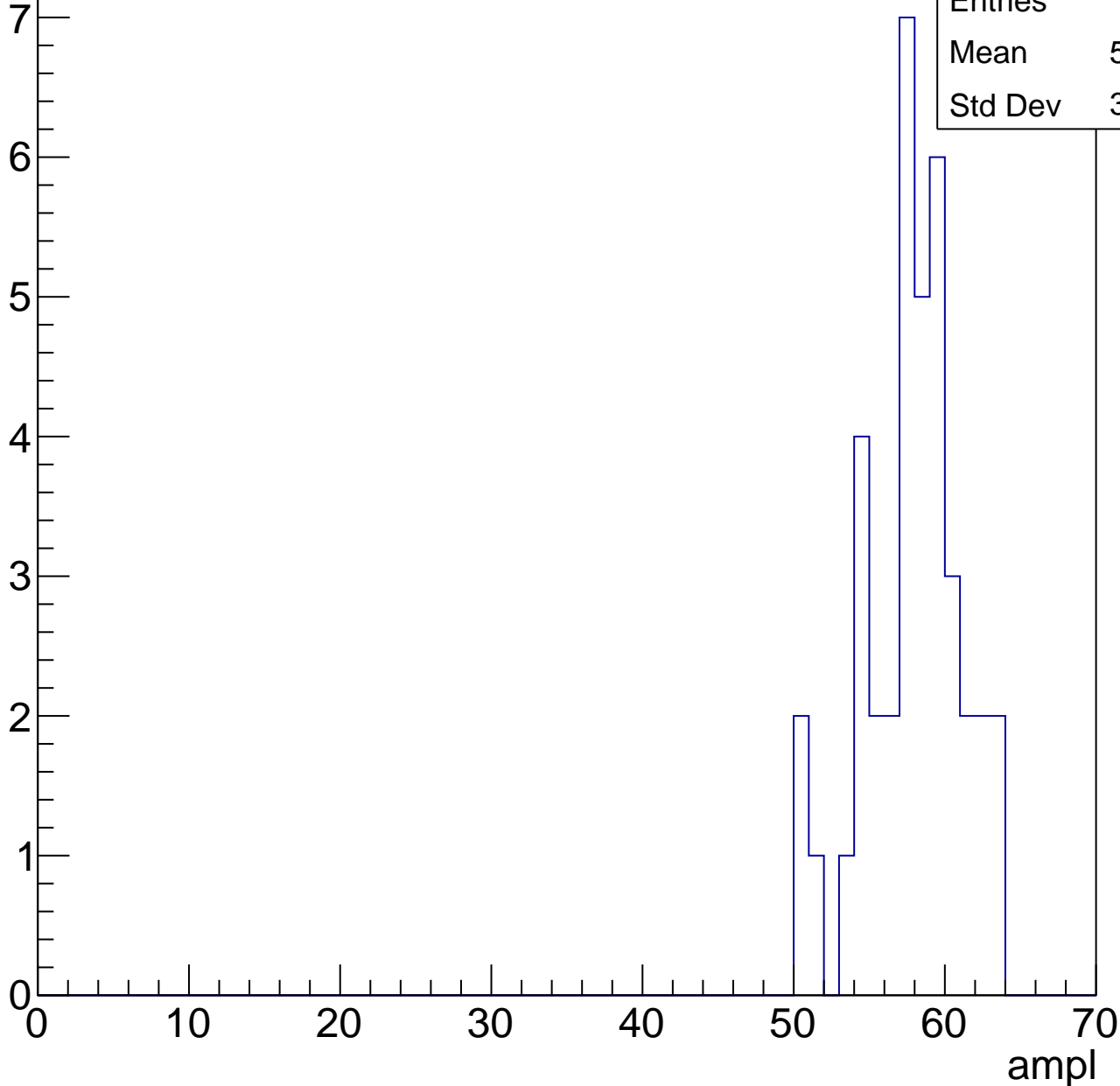


B1L101S, U6-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	57.36
Std Dev	3.206

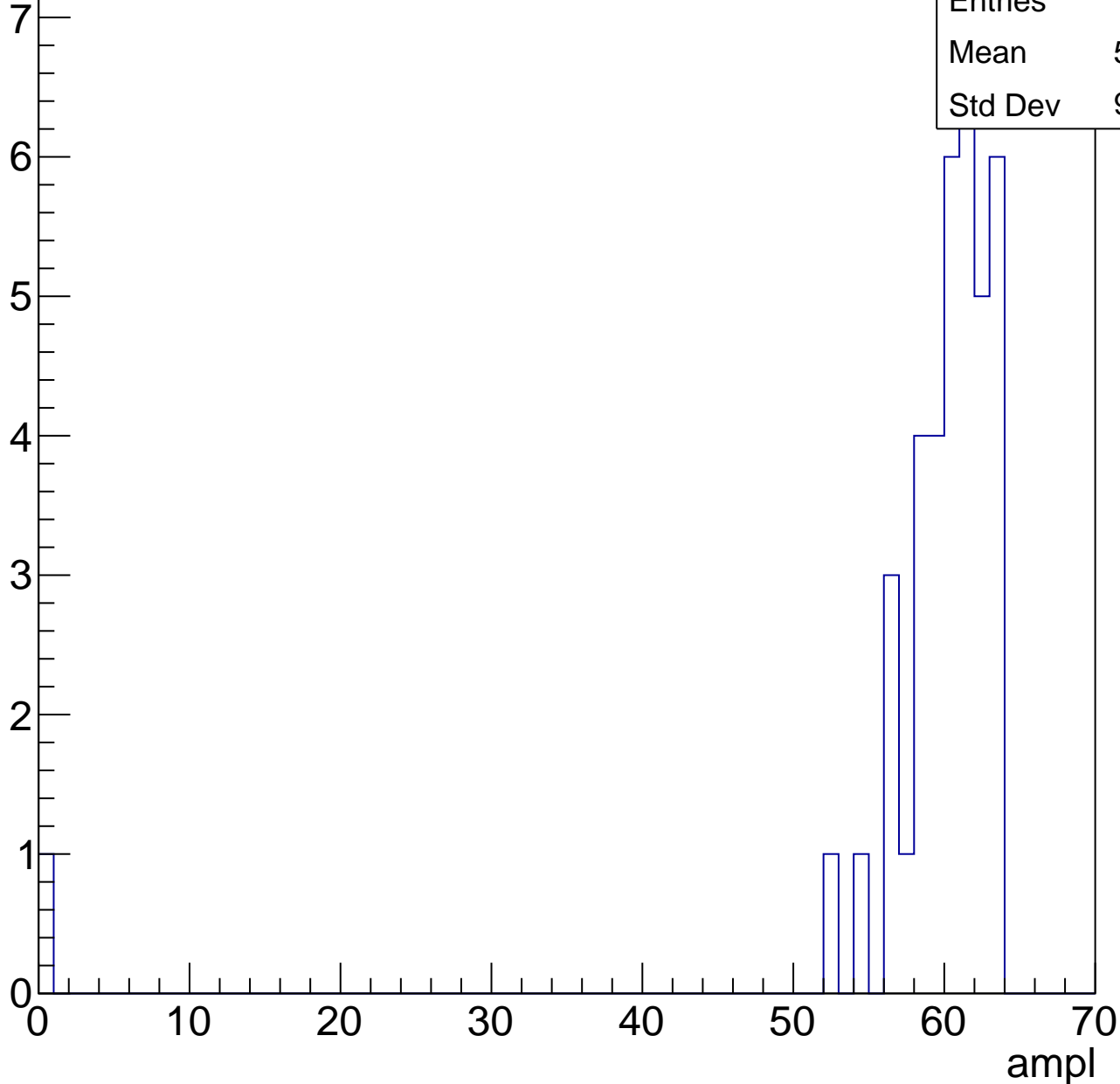


B1L101S, U6-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.31
Std Dev	9.801



B1L101S, U6-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch102, adc0

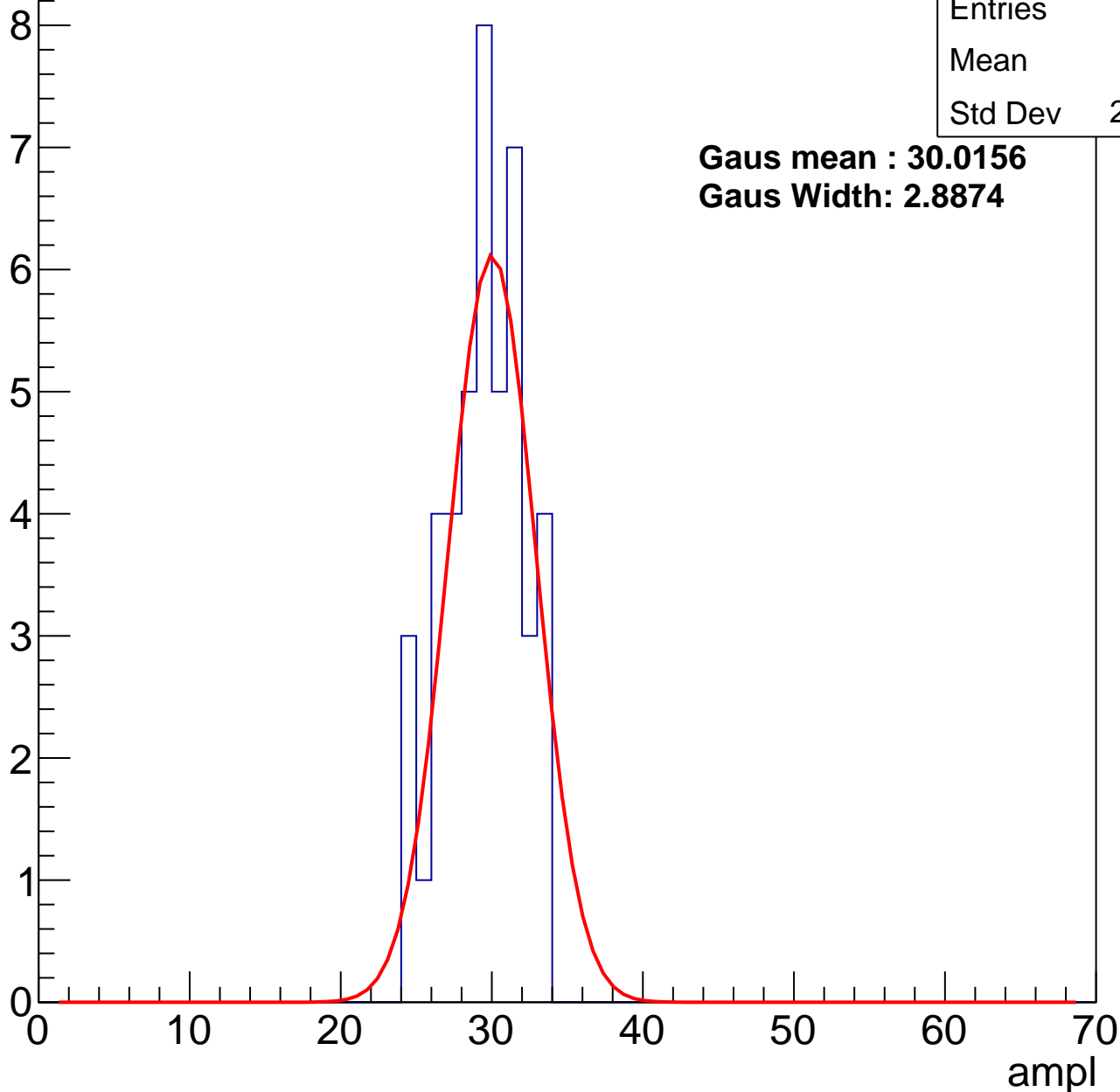
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	29
Std Dev	2.486

Gaus mean : 30.0156

Gaus Width: 2.8874



B1L101S, U6-ch102, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	35.64
Std Dev	3.489

Gaus mean : 35.6818

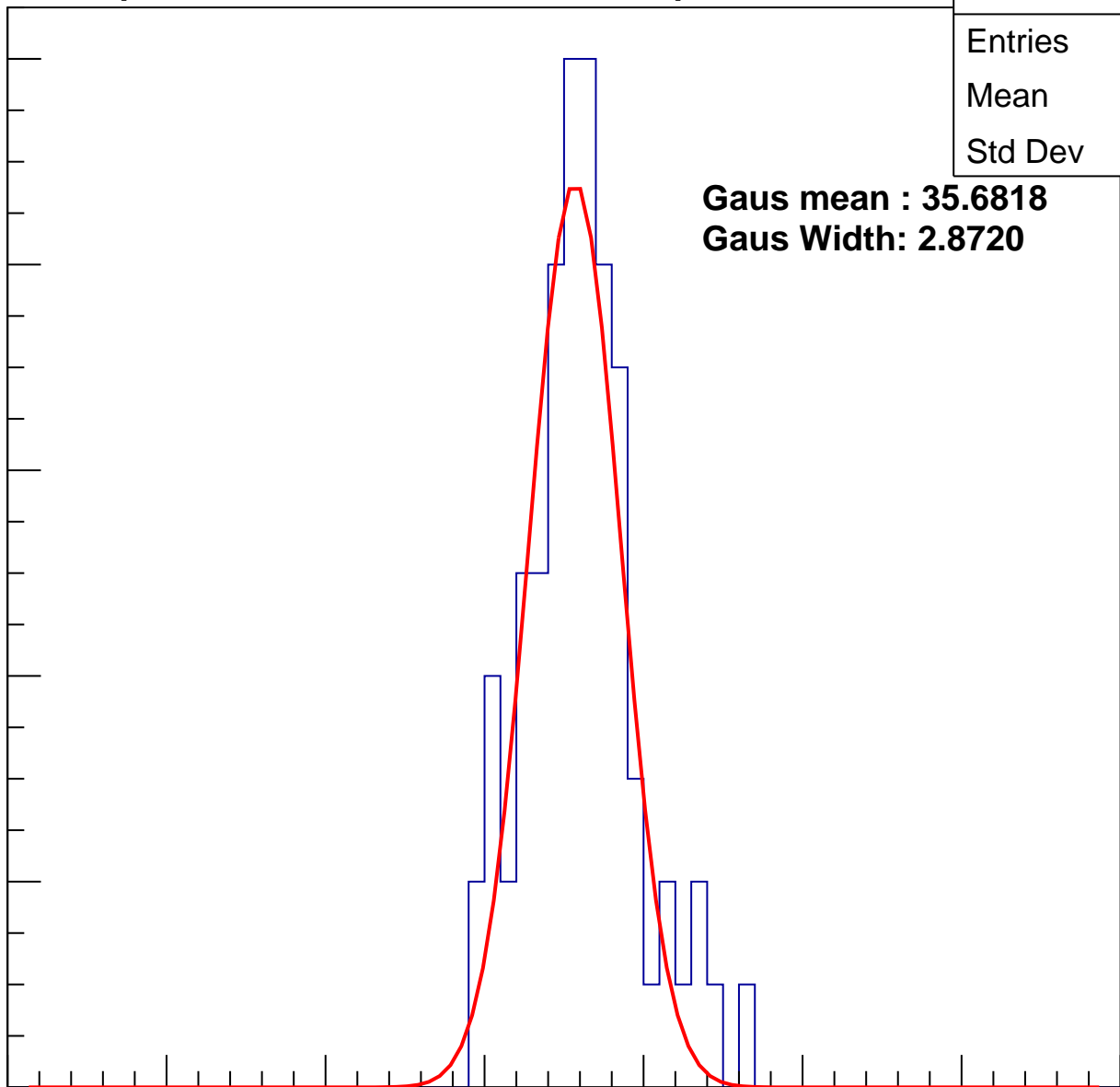
Gaus Width: 2.8720

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch102, adc2

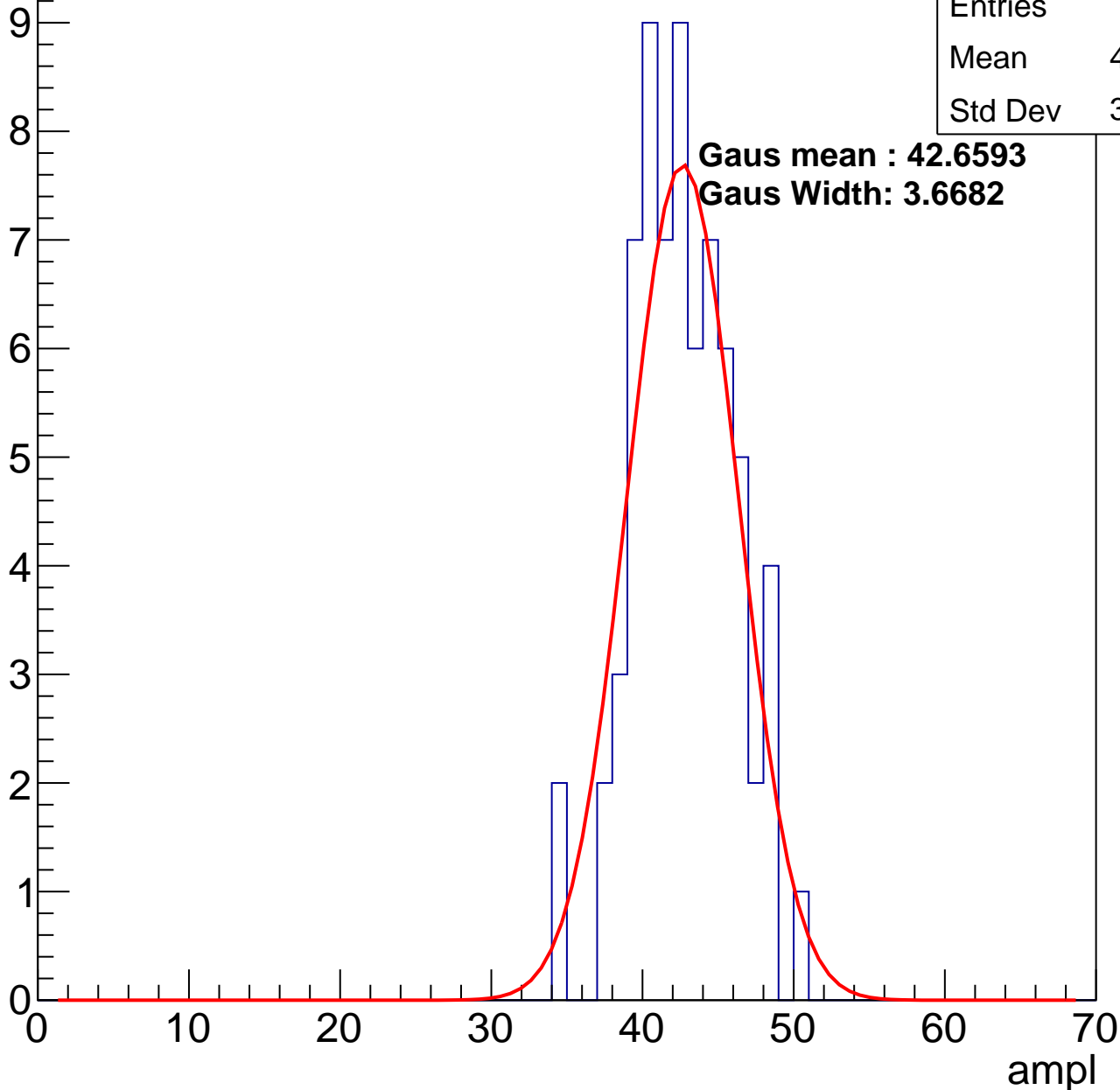
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.23
Std Dev	3.283

Gaus mean : 42.6593

Gaus Width: 3.6682



B1L101S, U6-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	49.22
Std Dev	3.199

Entry

10

8

6

4

2

0

0

10

20

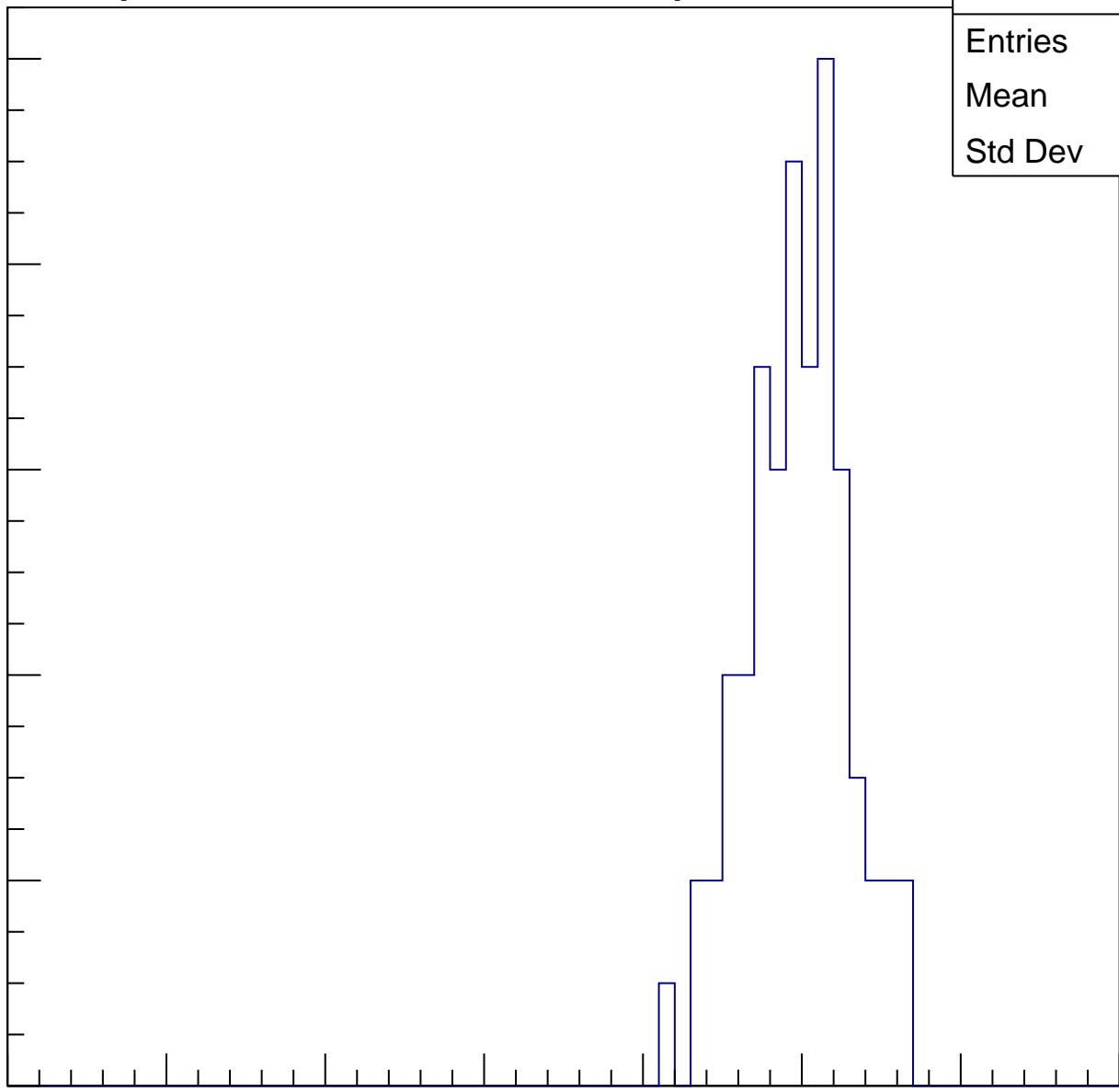
30

40

50

60

ampl

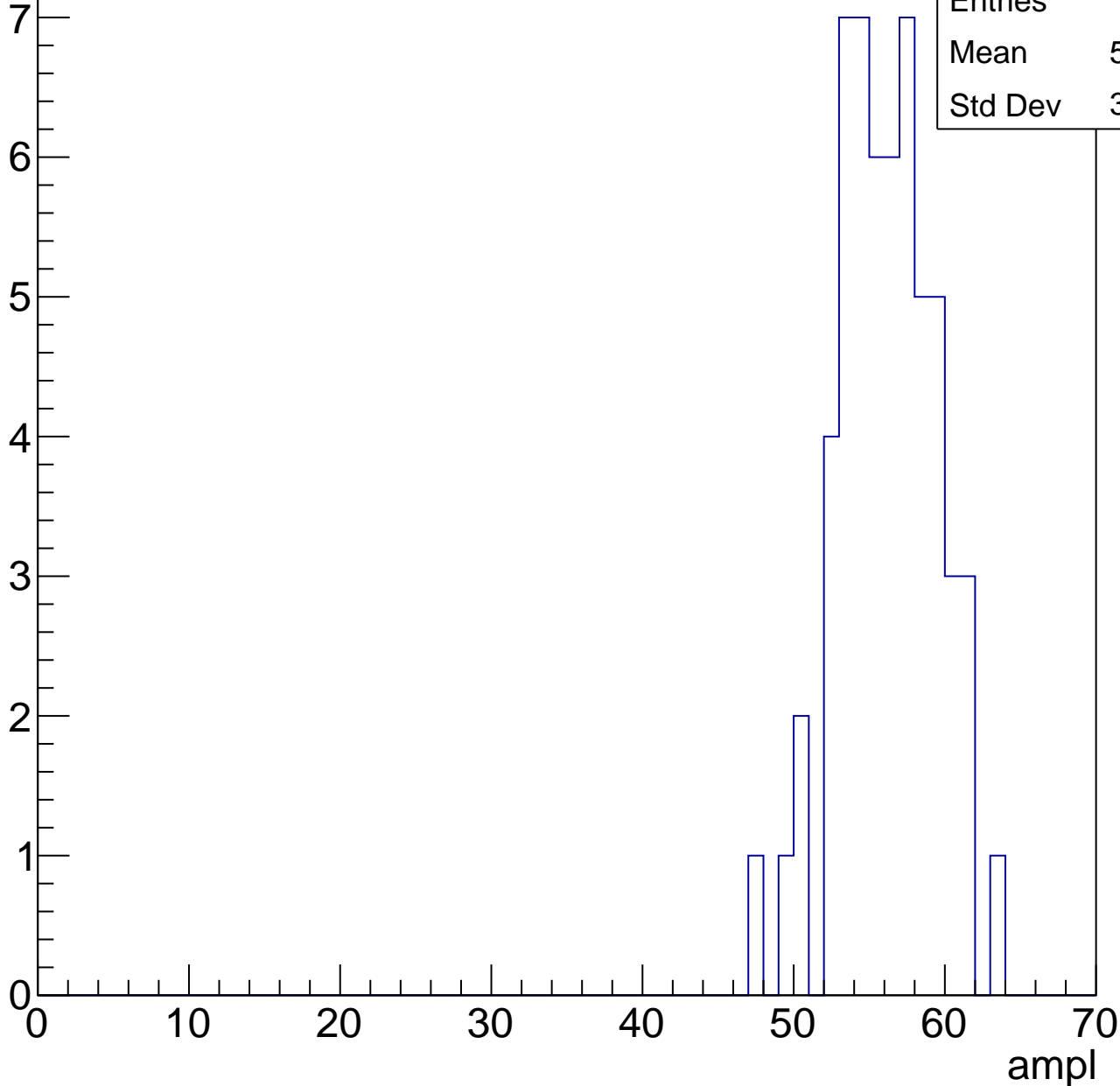


B1L101S, U6-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

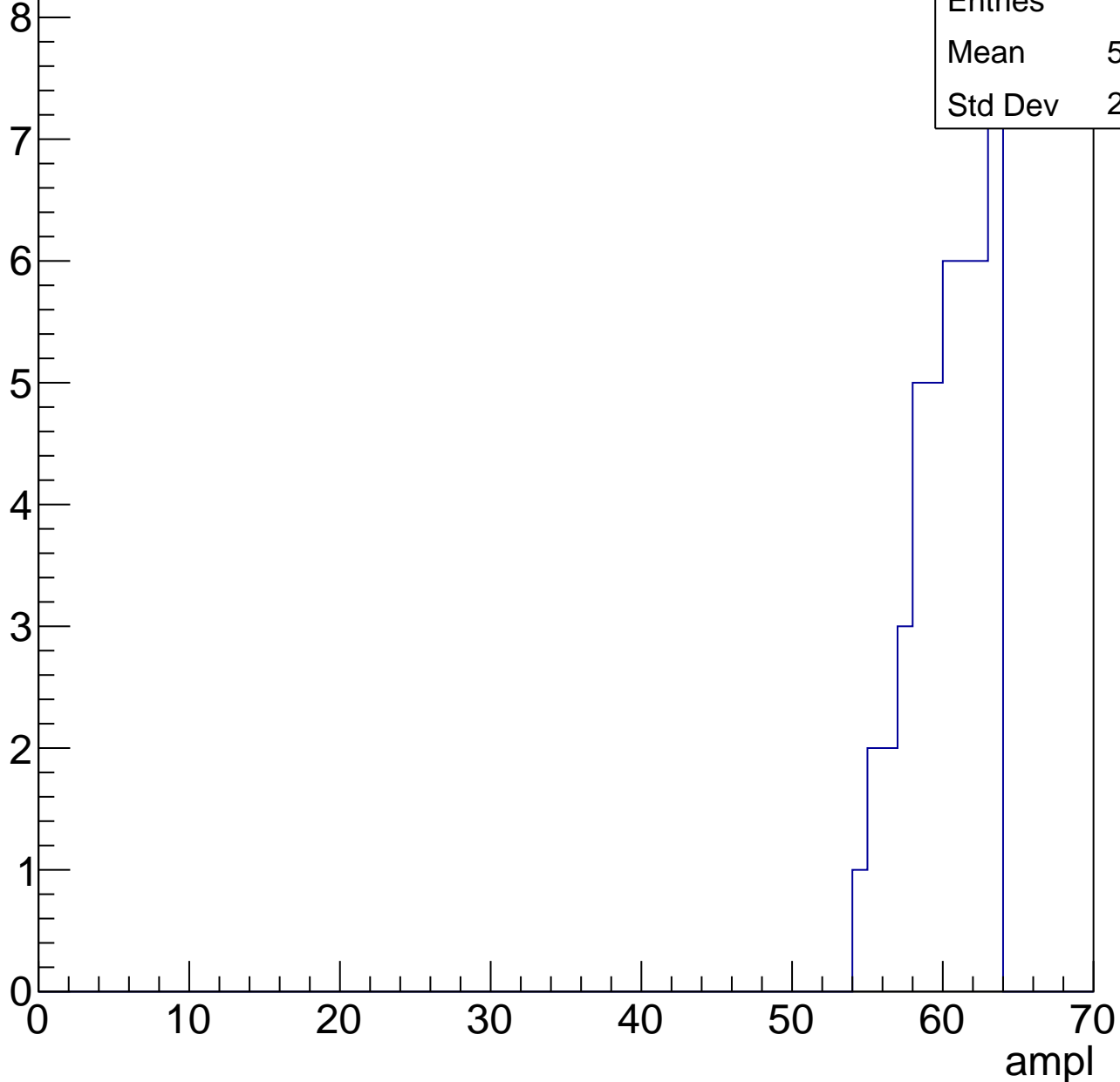
Entries	58
Mean	55.67
Std Dev	3.218



B1L101S, U6-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

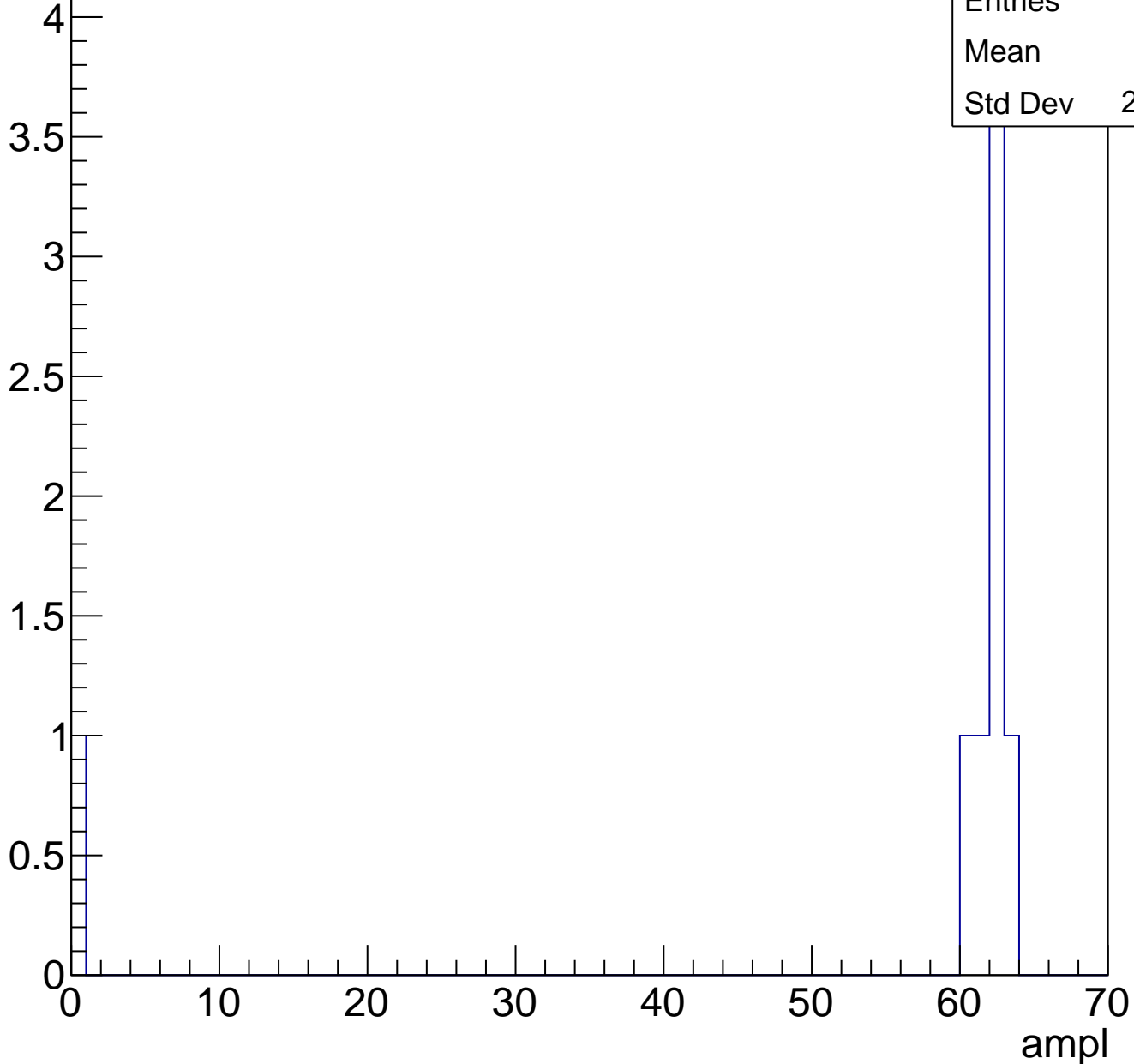
Entry



B1L101S, U6-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U6-ch103, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	29.51
Std Dev	4.757

Gaus mean : 30.5611

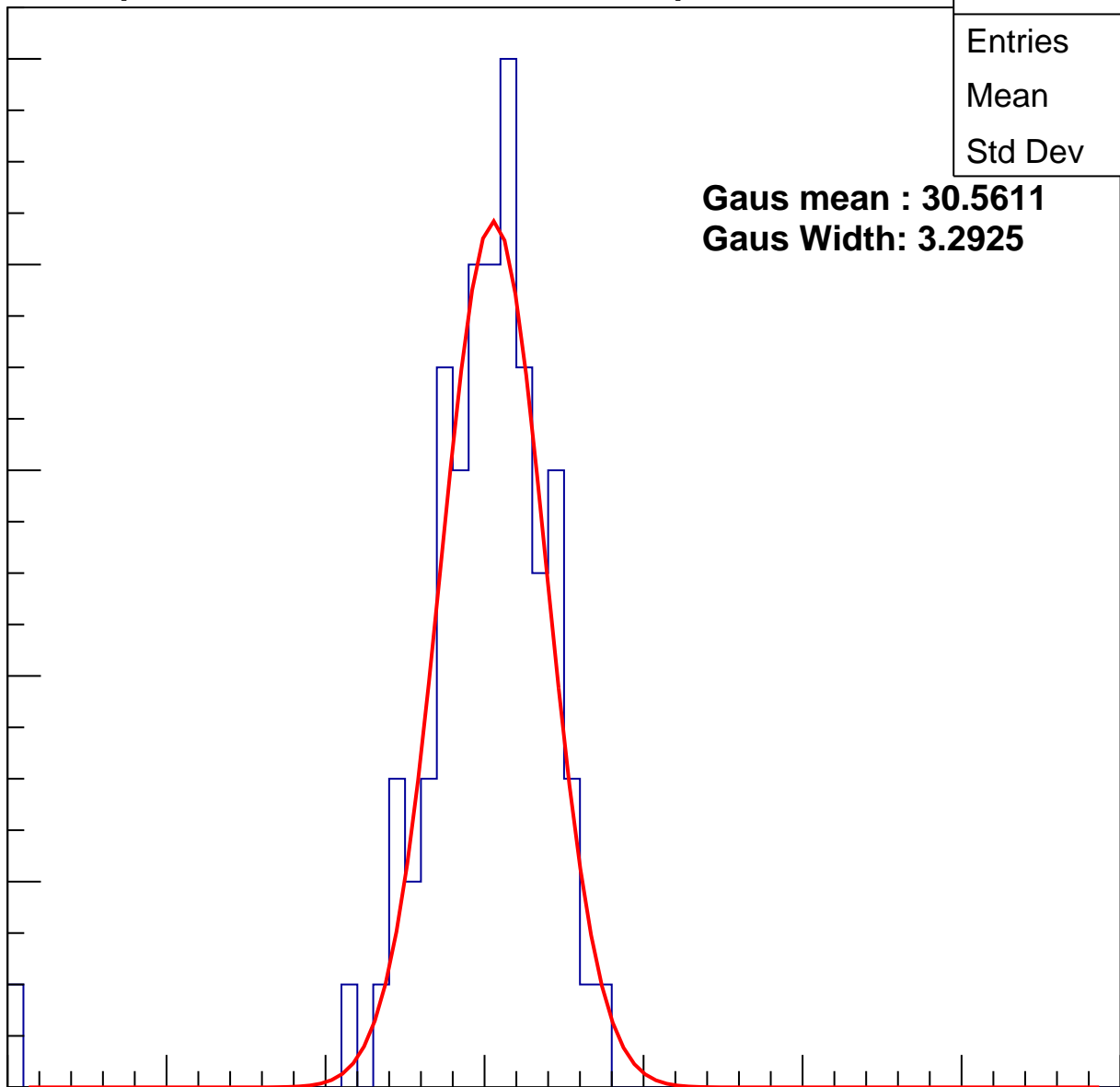
Gaus Width: 3.2925

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch103, adc1

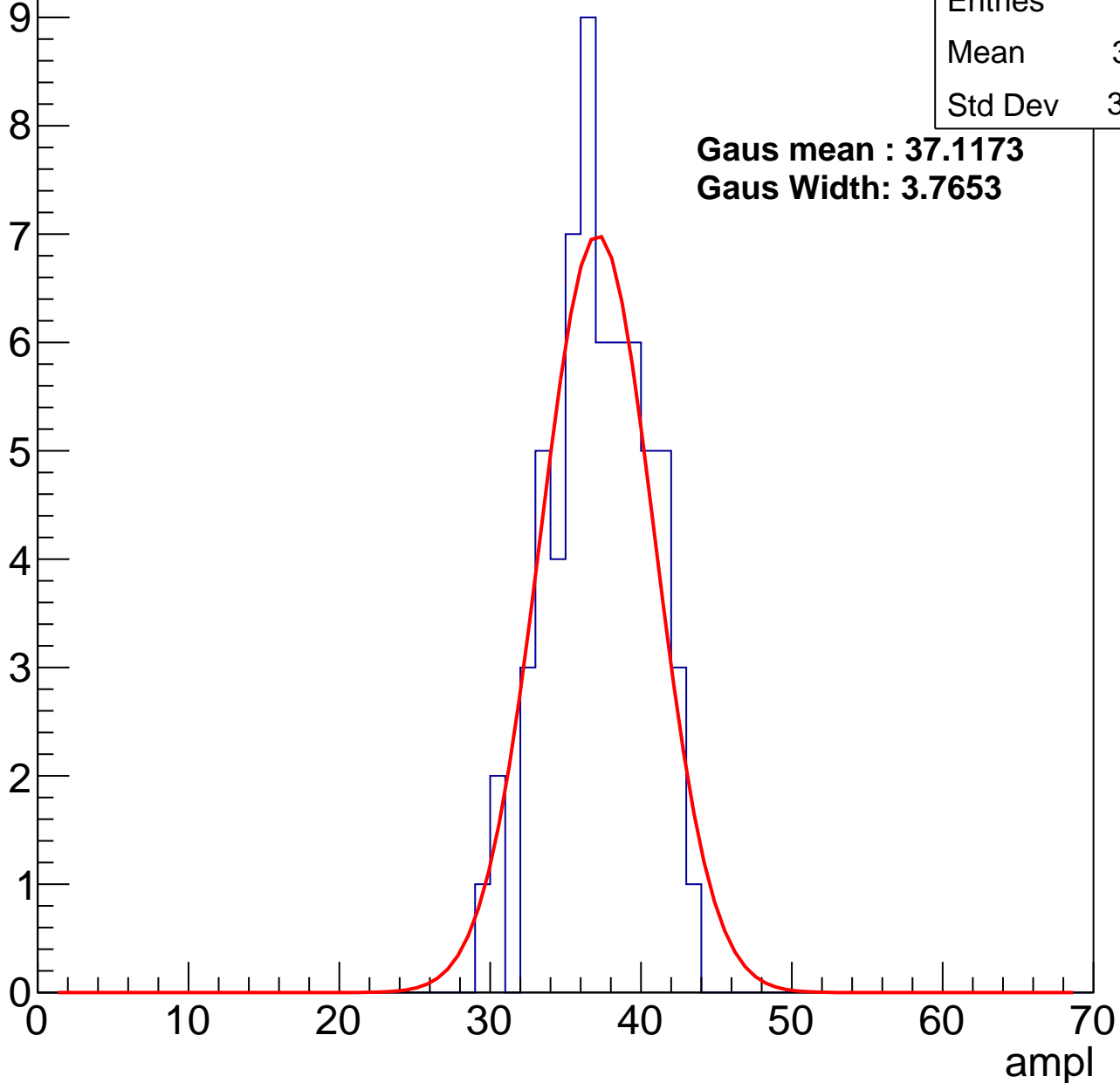
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.71
Std Dev	3.214

Gaus mean : 37.1173

Gaus Width: 3.7653



B1L101S, U6-ch103, adc2

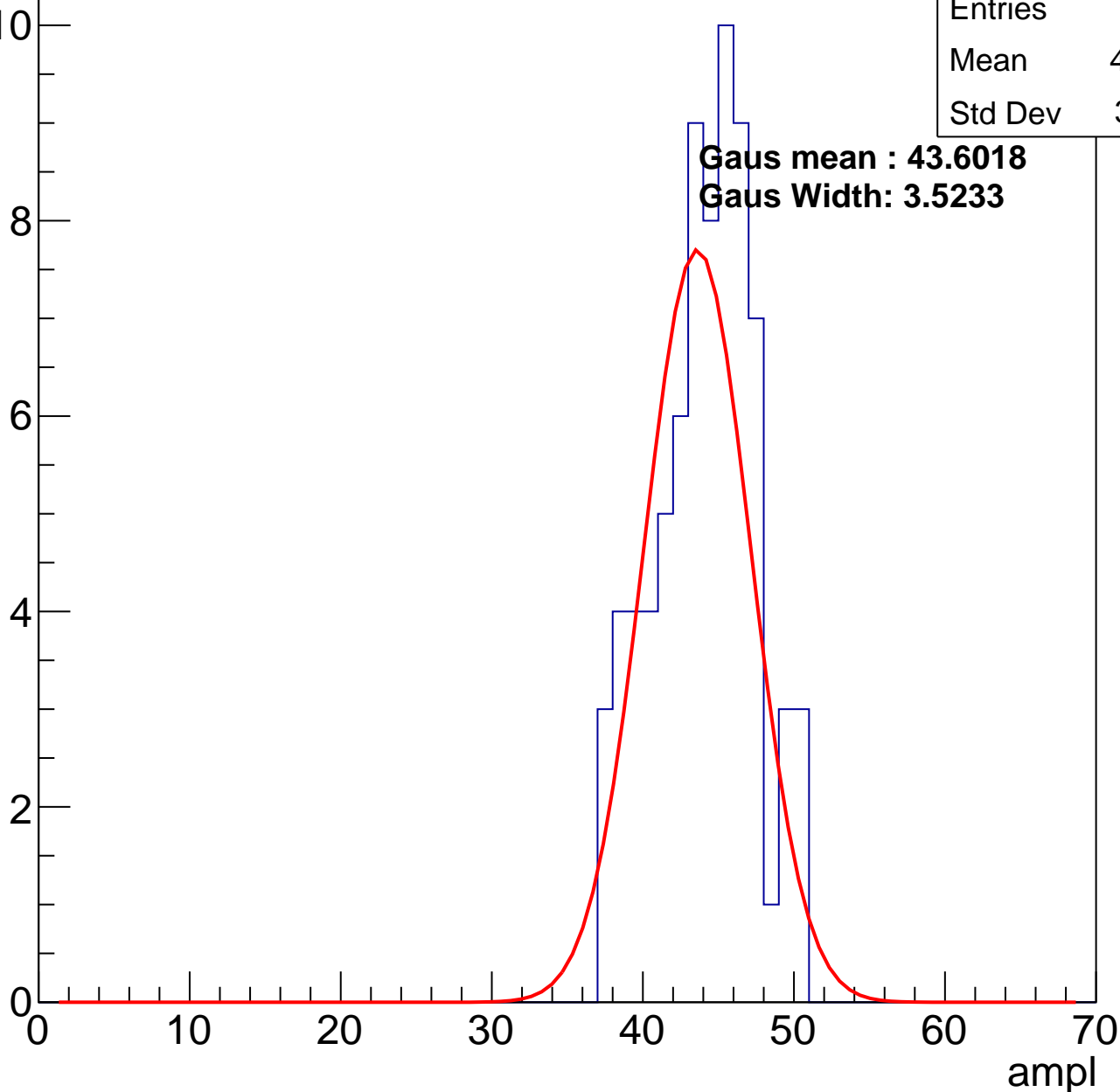
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	43.59
Std Dev	3.301

Gaus mean : 43.6018

Gaus Width: 3.5233

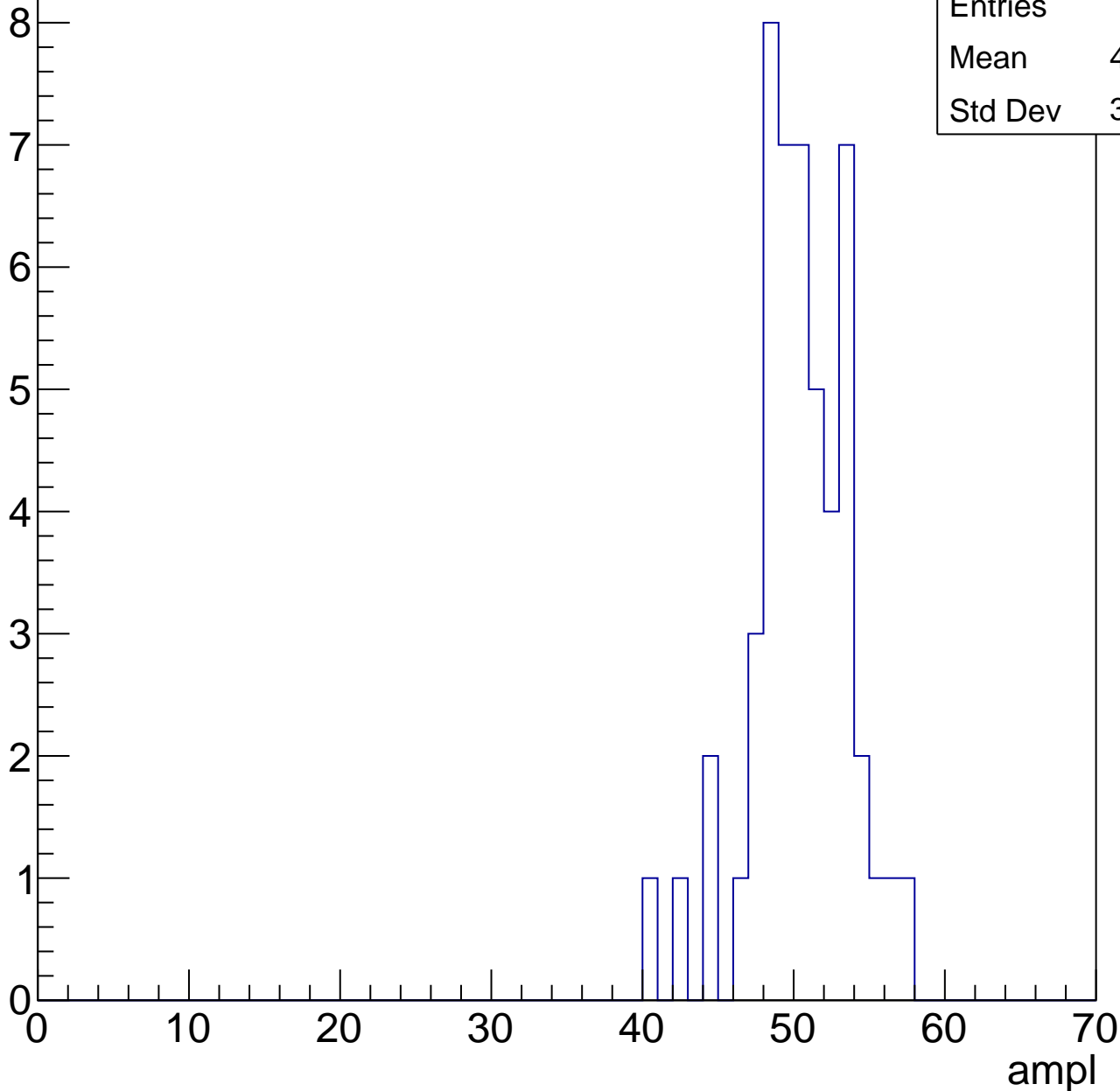


B1L101S, U6-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

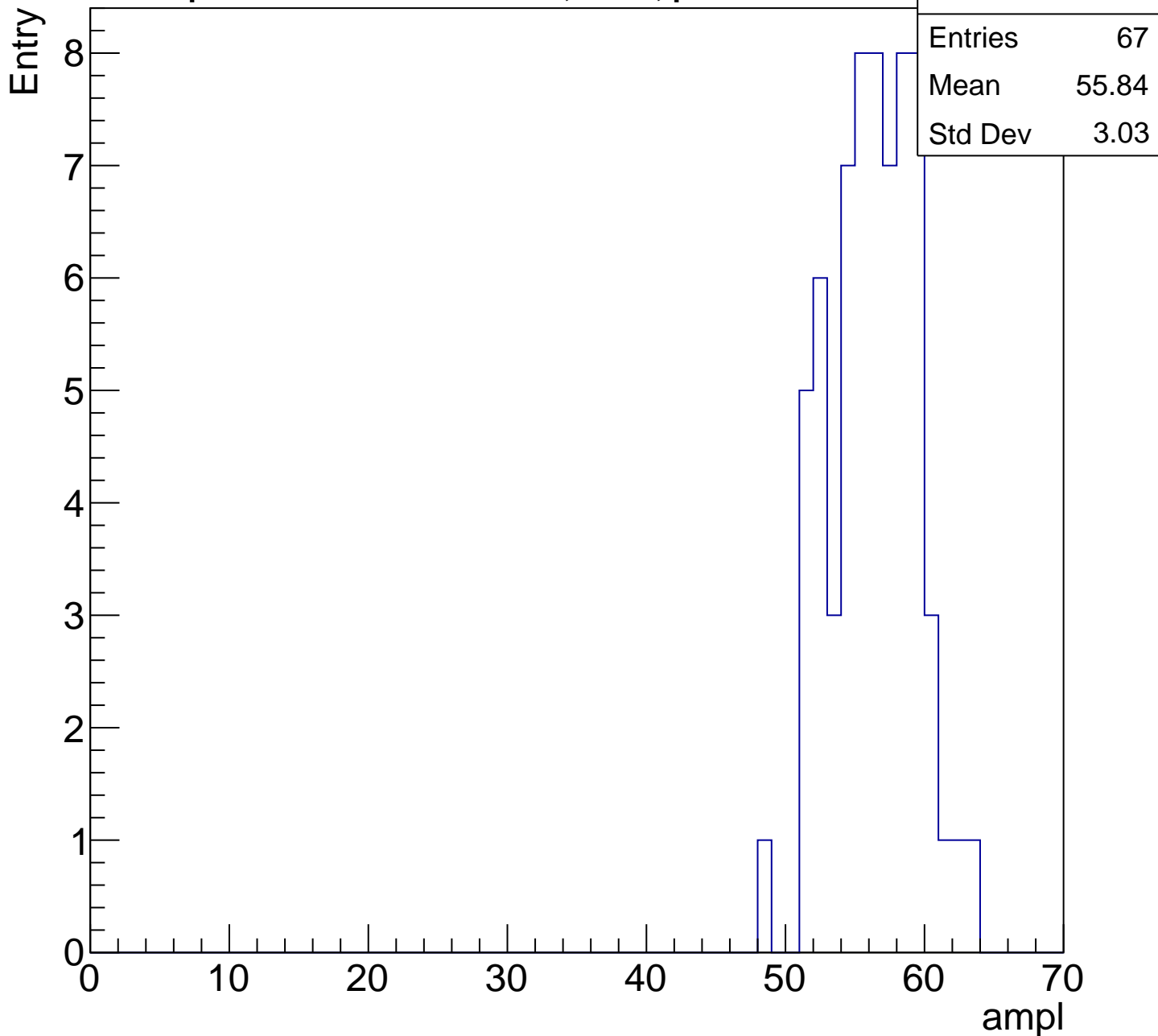
Entry

Entries	51
Mean	49.88
Std Dev	3.264



B1L101S, U6-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

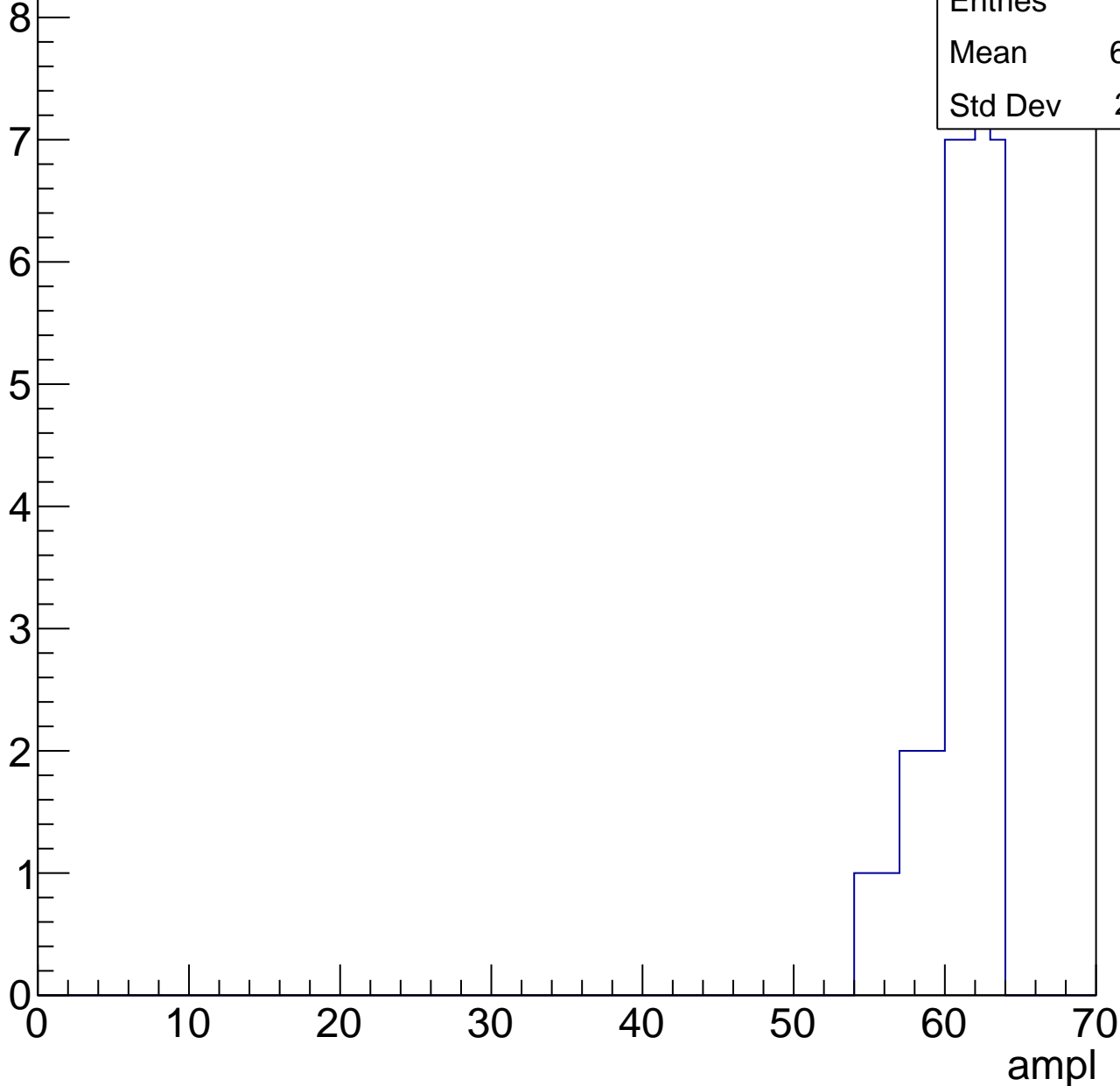


B1L101S, U6-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

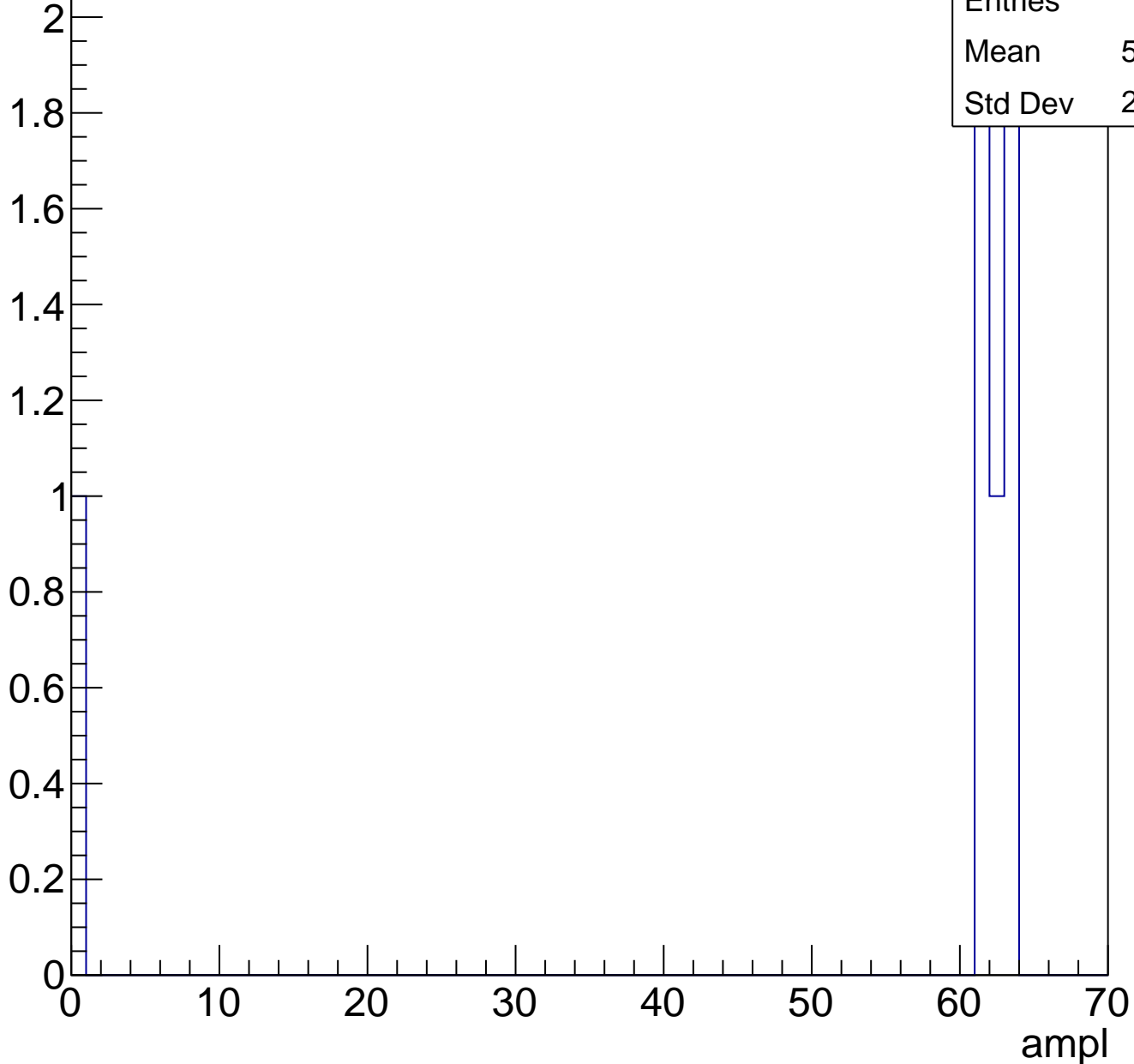
Entries	38
Mean	60.45
Std Dev	2.291



B1L101S, U6-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch104, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	54
Mean	27.83
Std Dev	4.924

Gaus mean : 28.9969

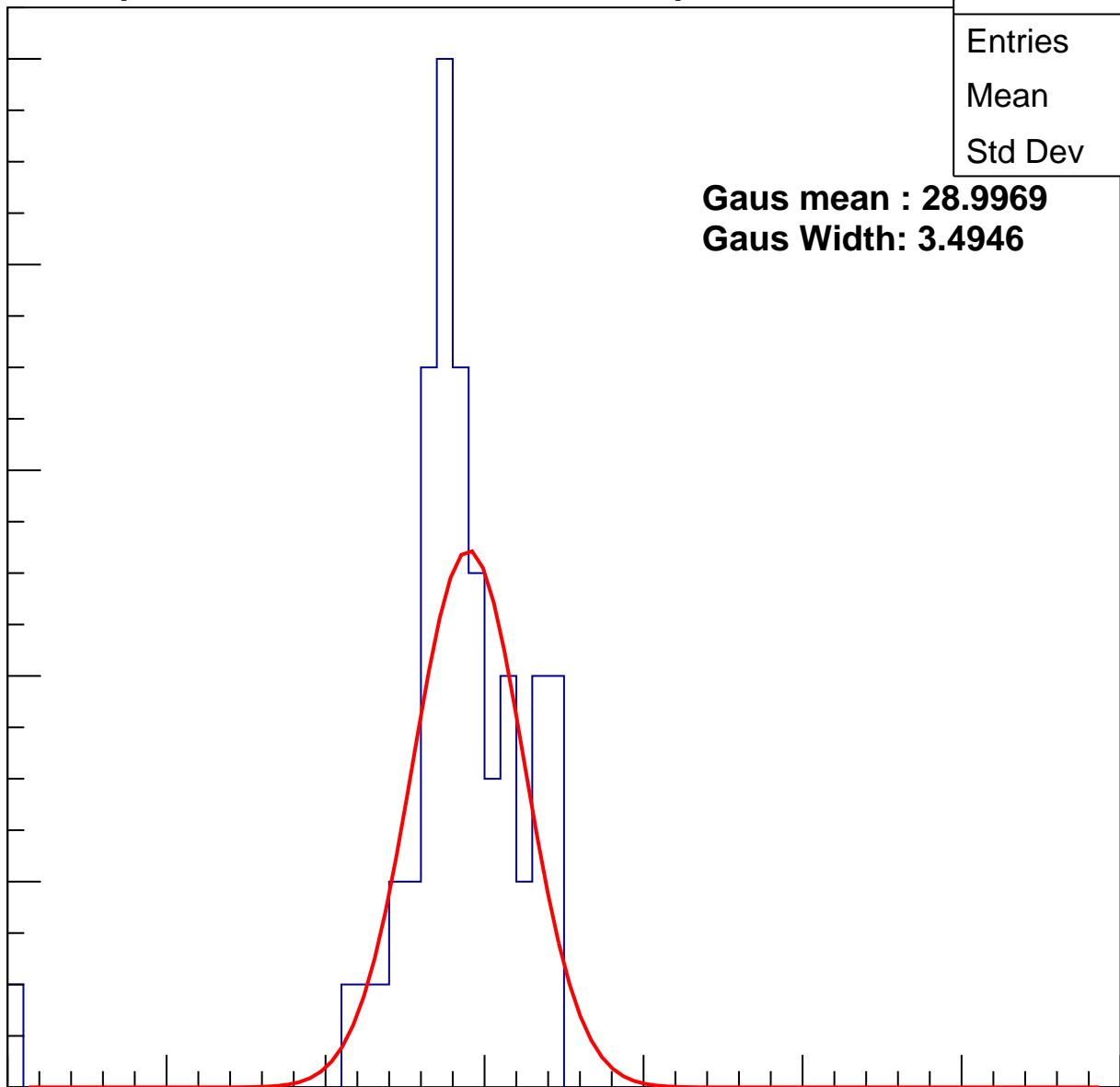
Gaus Width: 3.4946

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch104, adc1

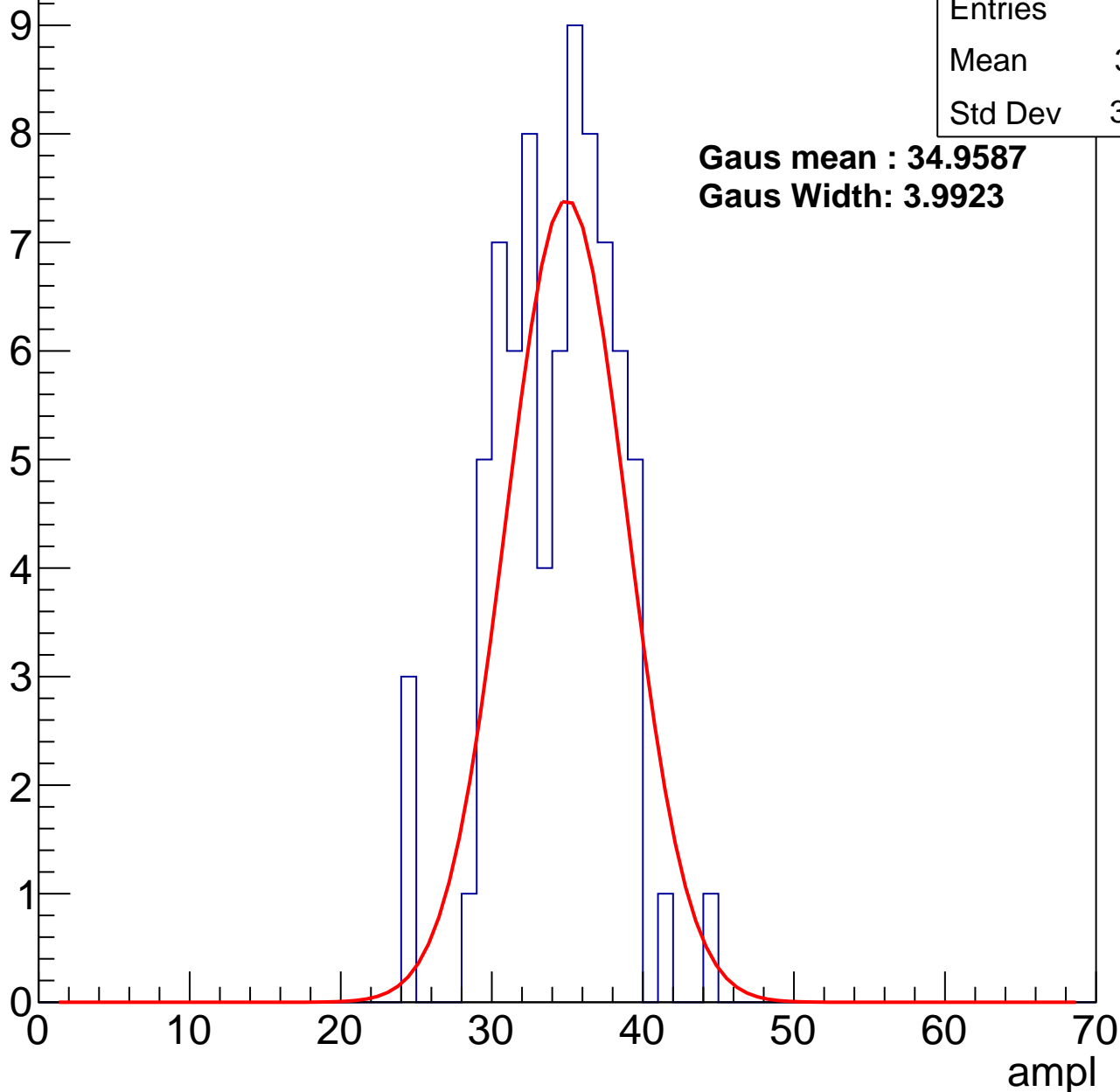
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	33.81
Std Dev	3.838

Gaus mean : 34.9587

Gaus Width: 3.9923



B1L101S, U6-ch104, adc2

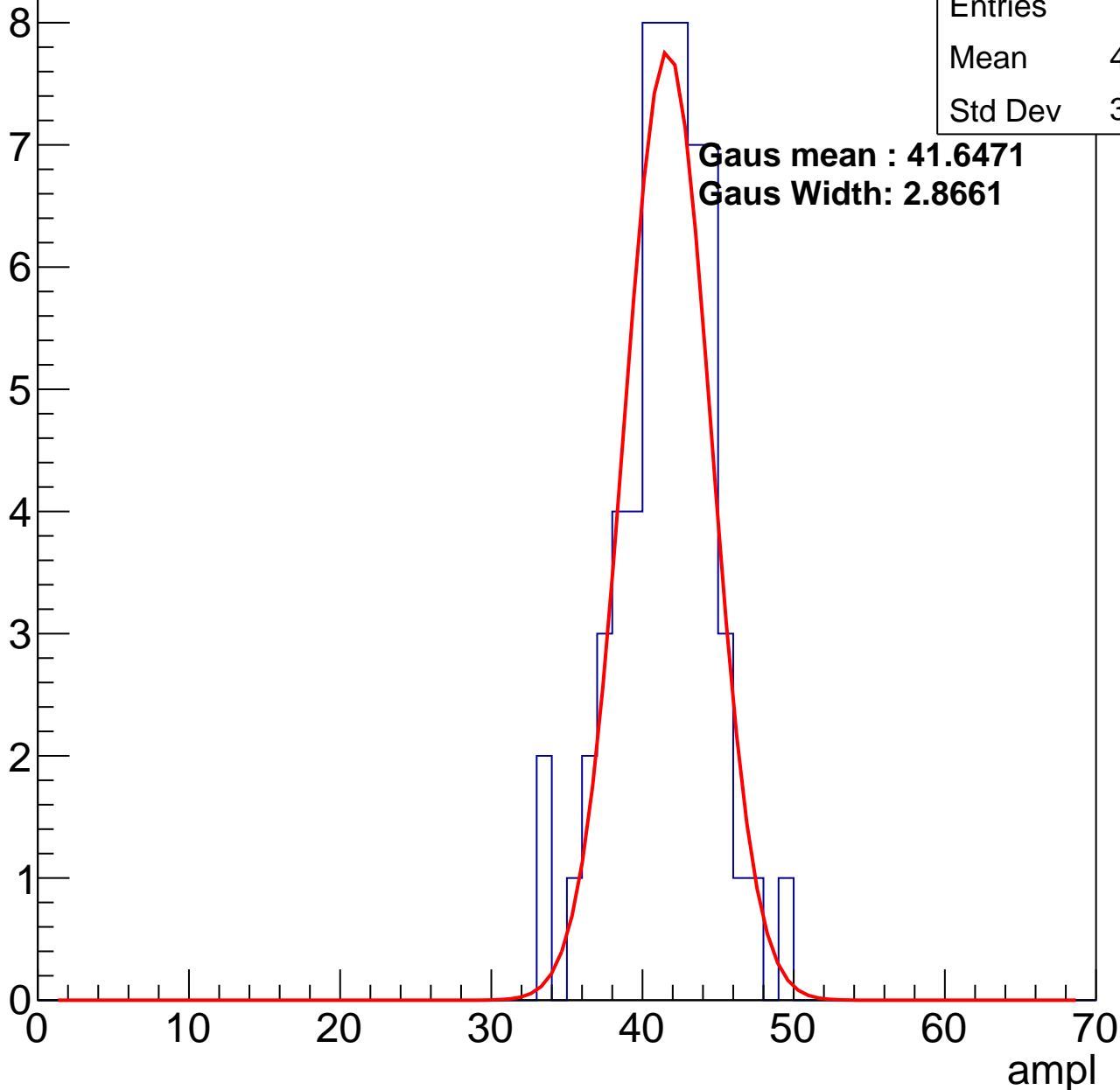
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.03
Std Dev	3.146

Gaus mean : 41.6471

Gaus Width: 2.8661



B1L101S, U6-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

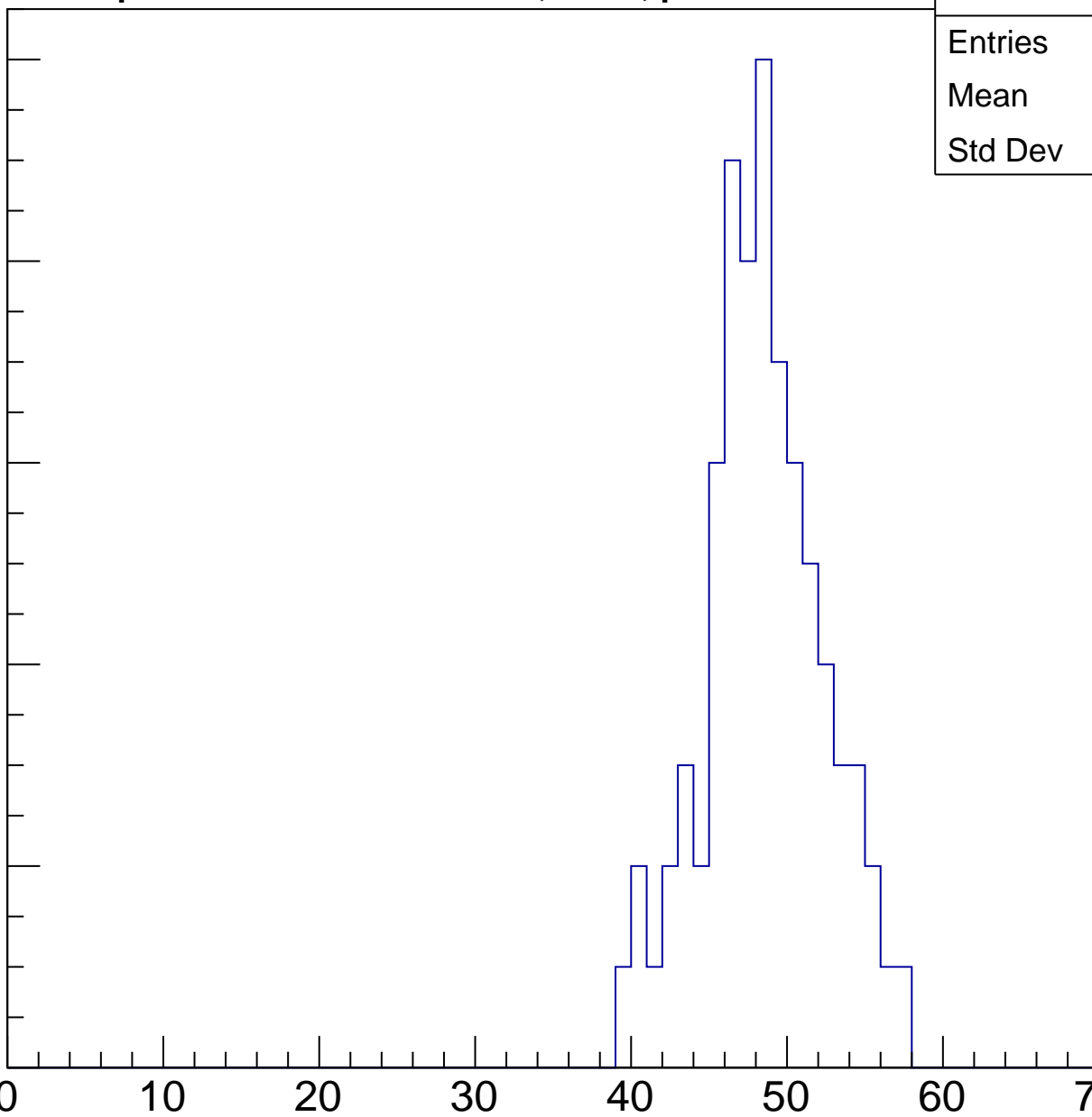
Entries	76
Mean	48.04
Std Dev	3.806

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

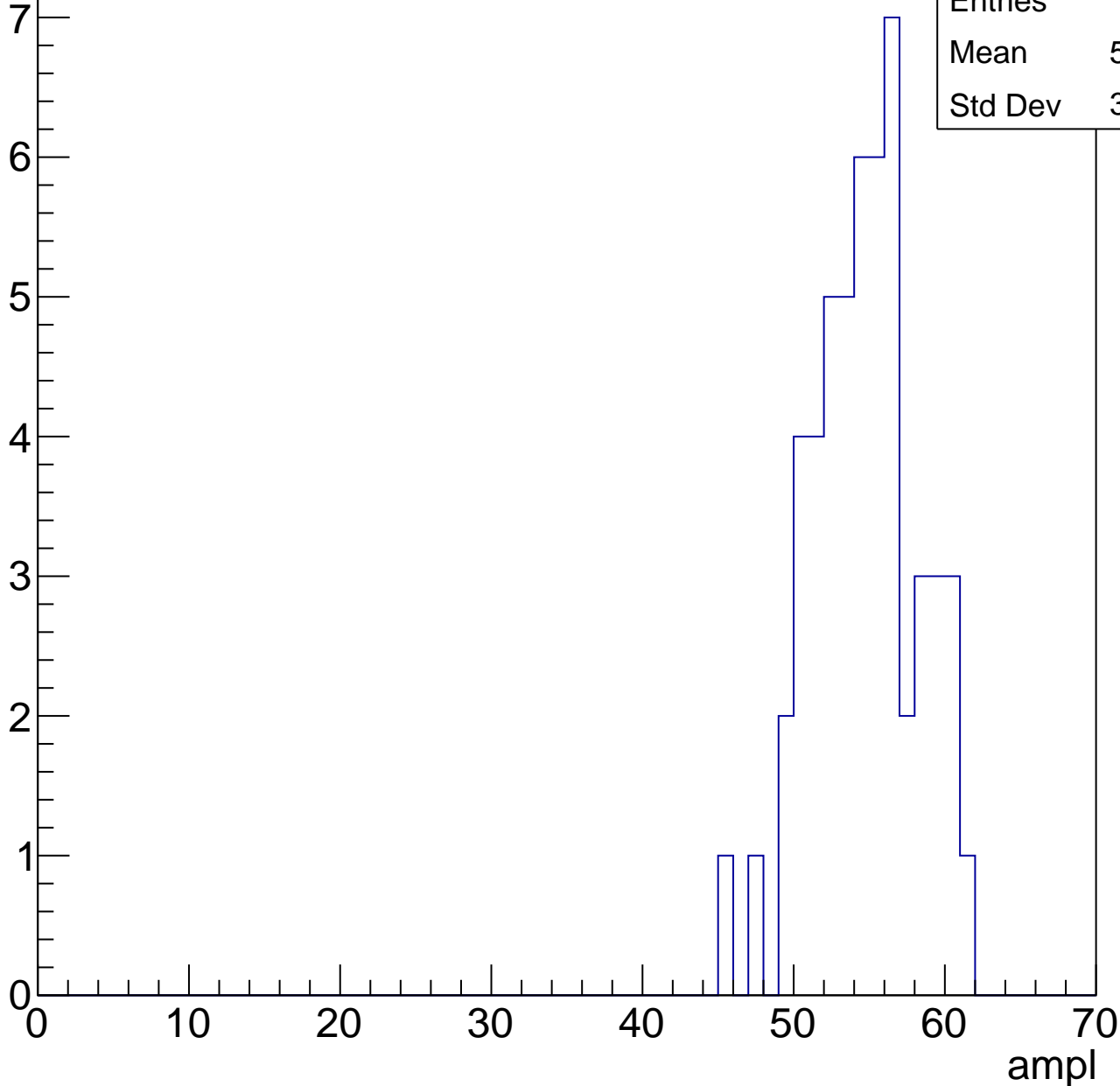


B1L101S, U6-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	54.17
Std Dev	3.463



B1L101S, U6-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	52
Mean	58.98
Std Dev	3.041

ampl

0

10

20

30

40

50

60

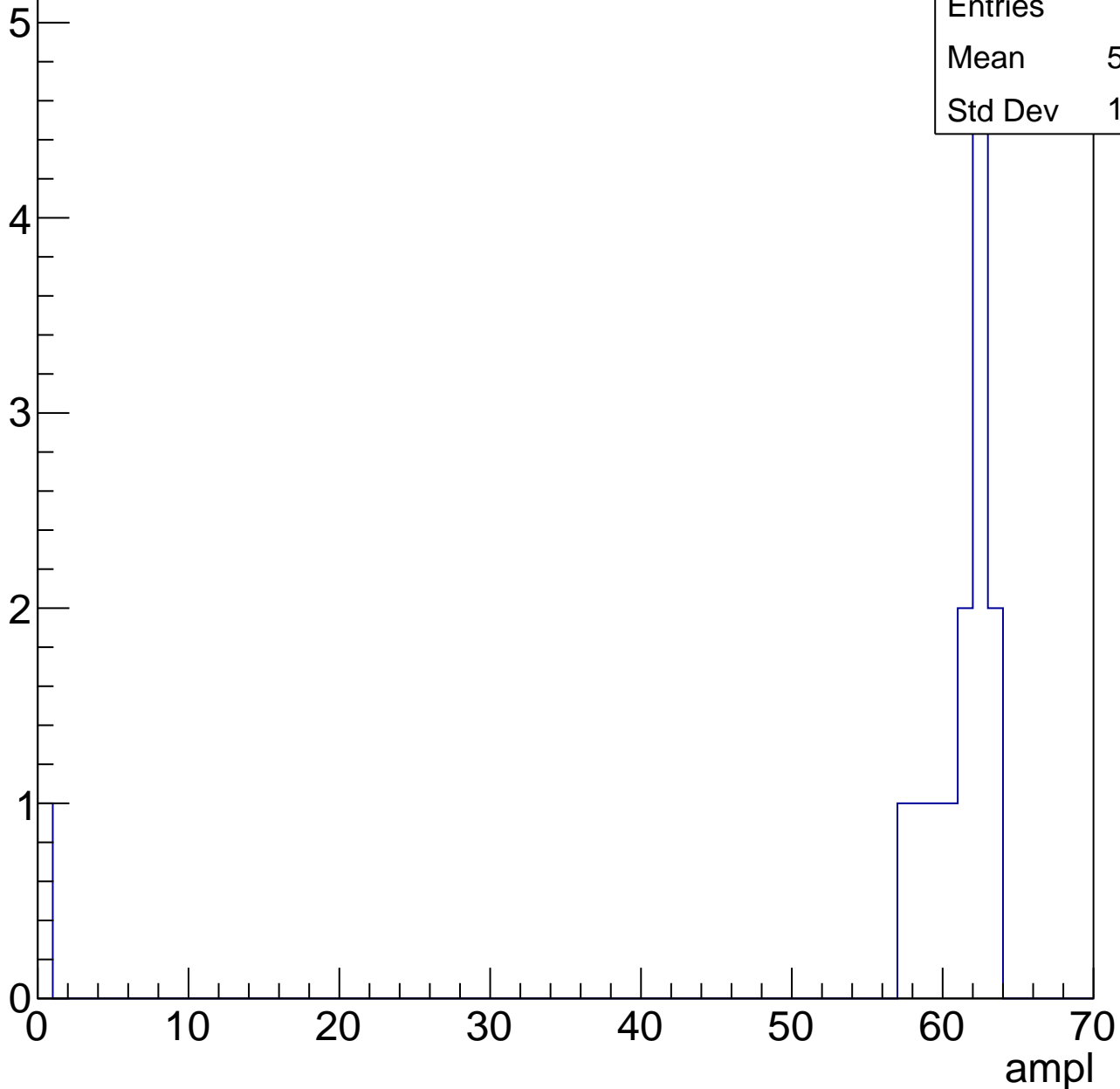
70

B1L101S, U6-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	56.57
Std Dev	15.79



B1L101S, U6-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch105, adc0

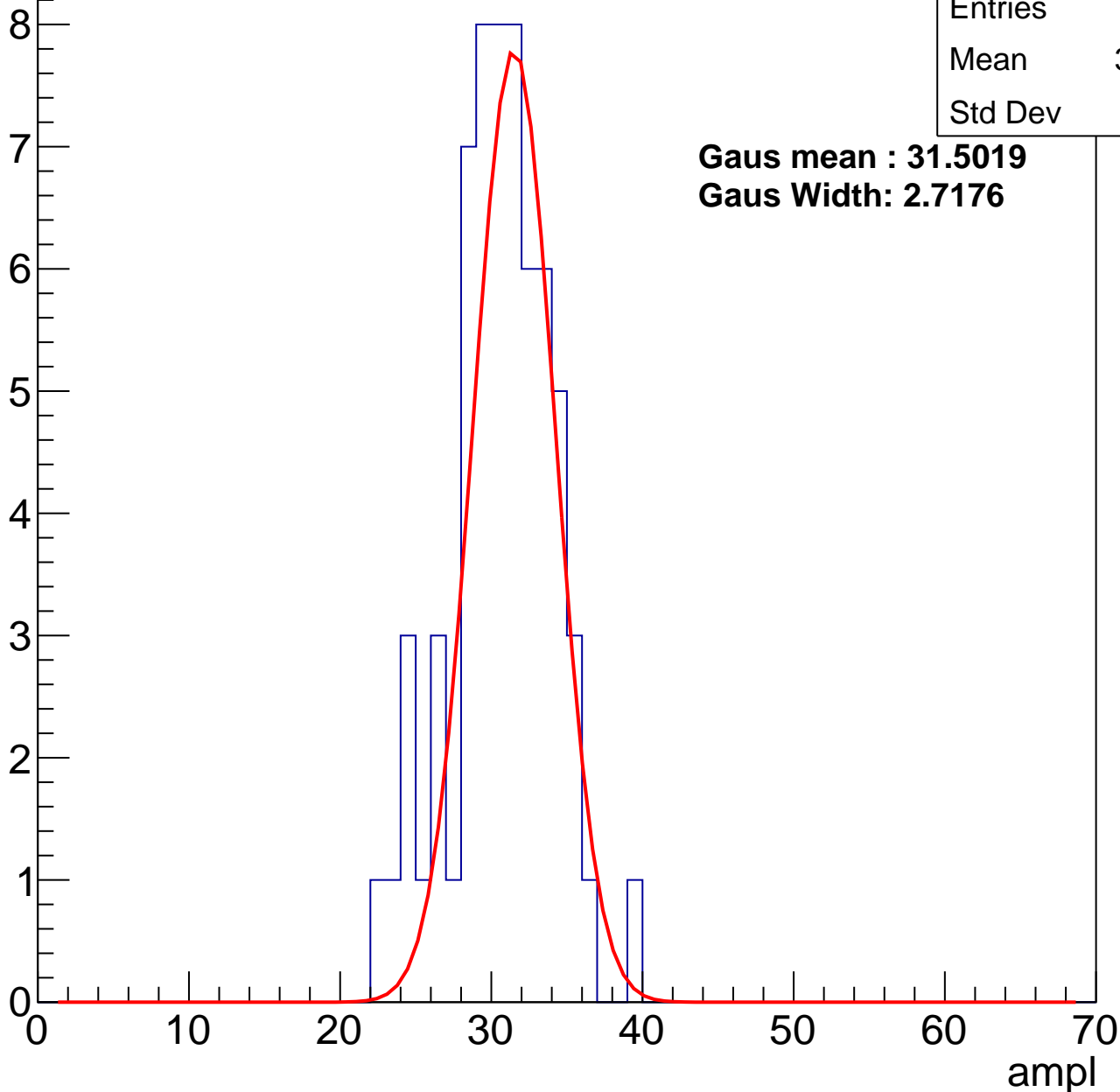
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.21
Std Dev	3.32

Gaus mean : 31.5019

Gaus Width: 2.7176



B1L101S, U6-ch105, adc1

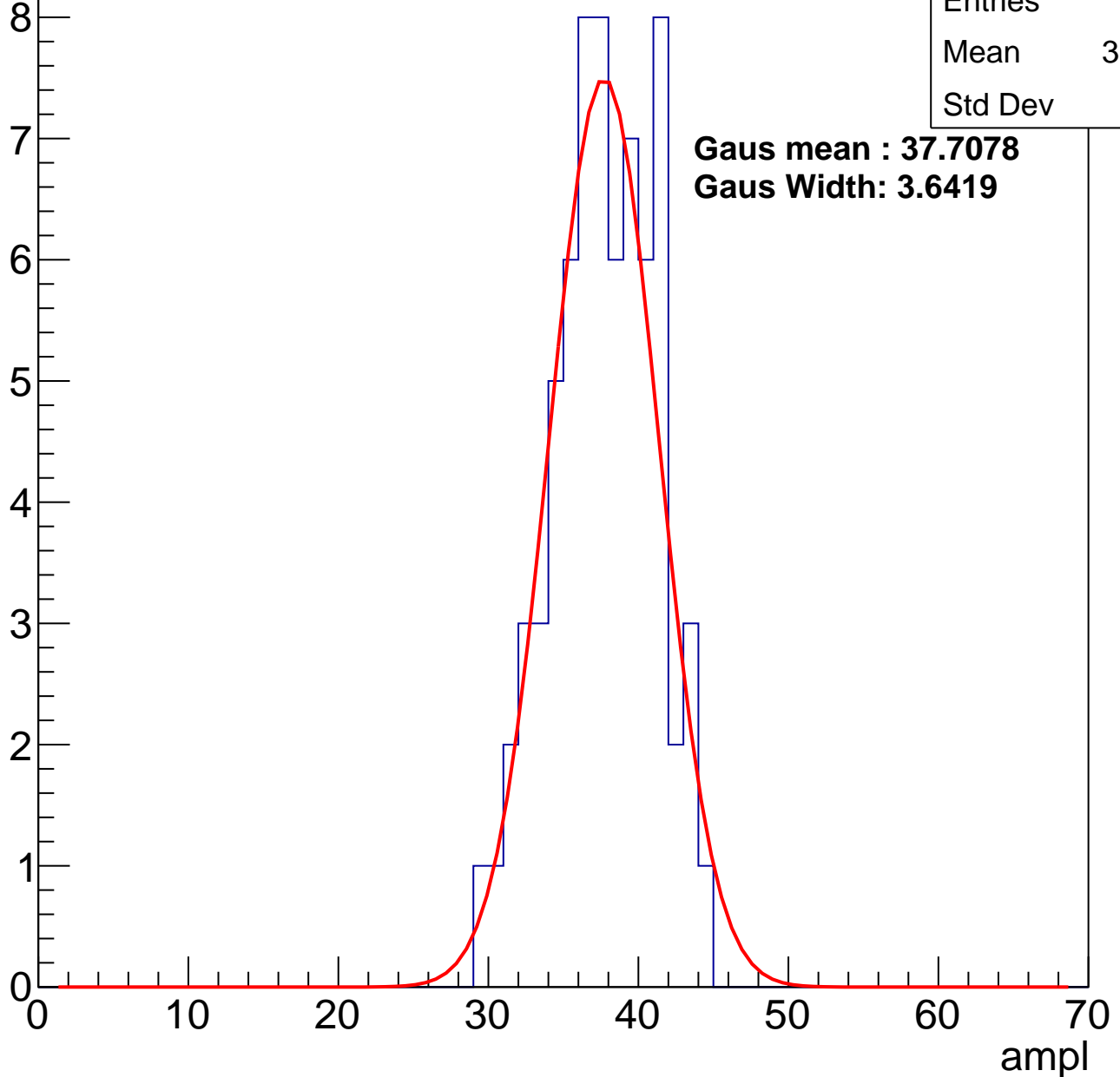
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.23
Std Dev	3.39

Gaus mean : 37.7078

Gaus Width: 3.6419



B1L101S, U6-ch105, adc2

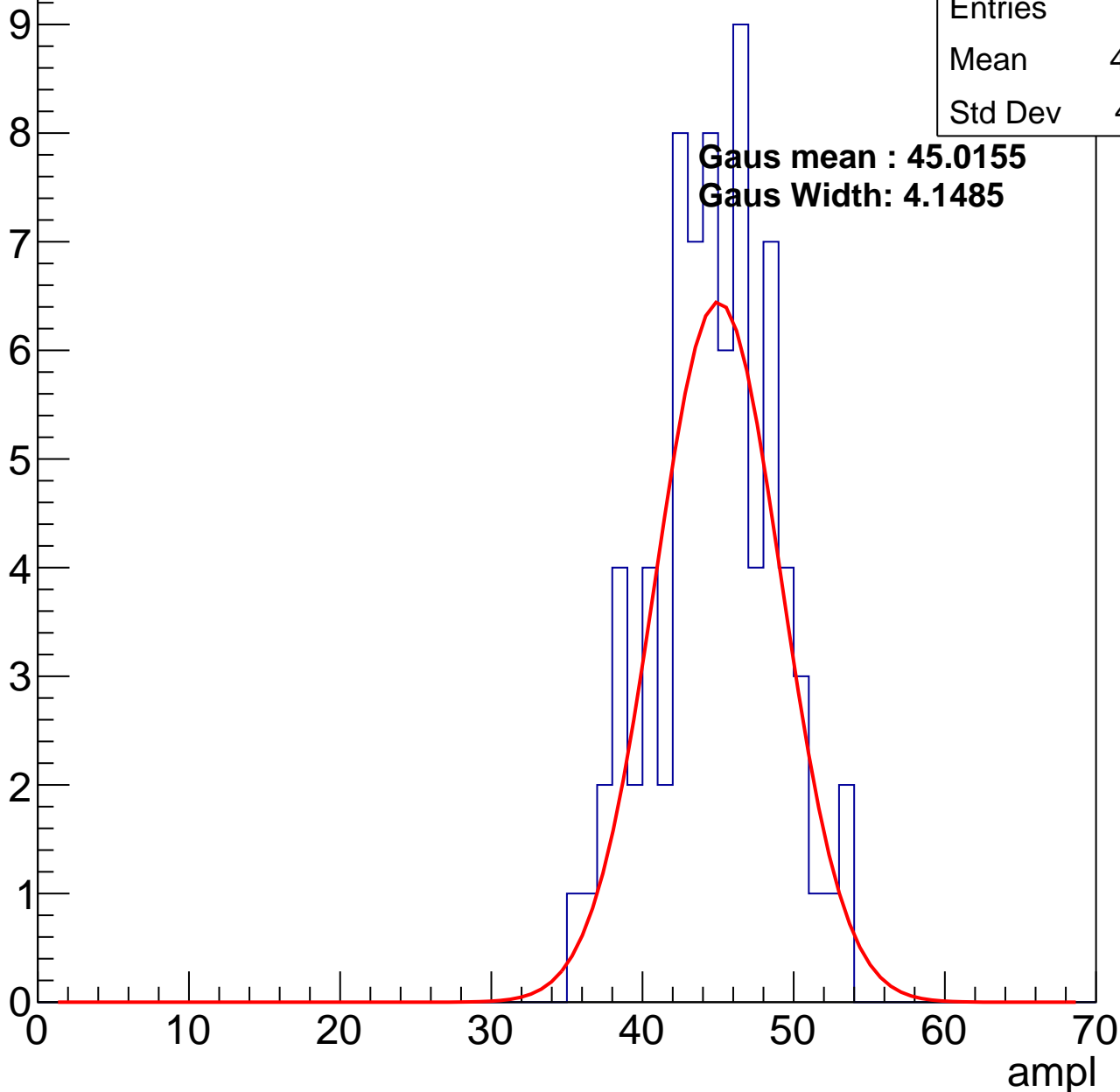
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	44.33
Std Dev	4.011

Gaus mean : 45.0155

Gaus Width: 4.1485

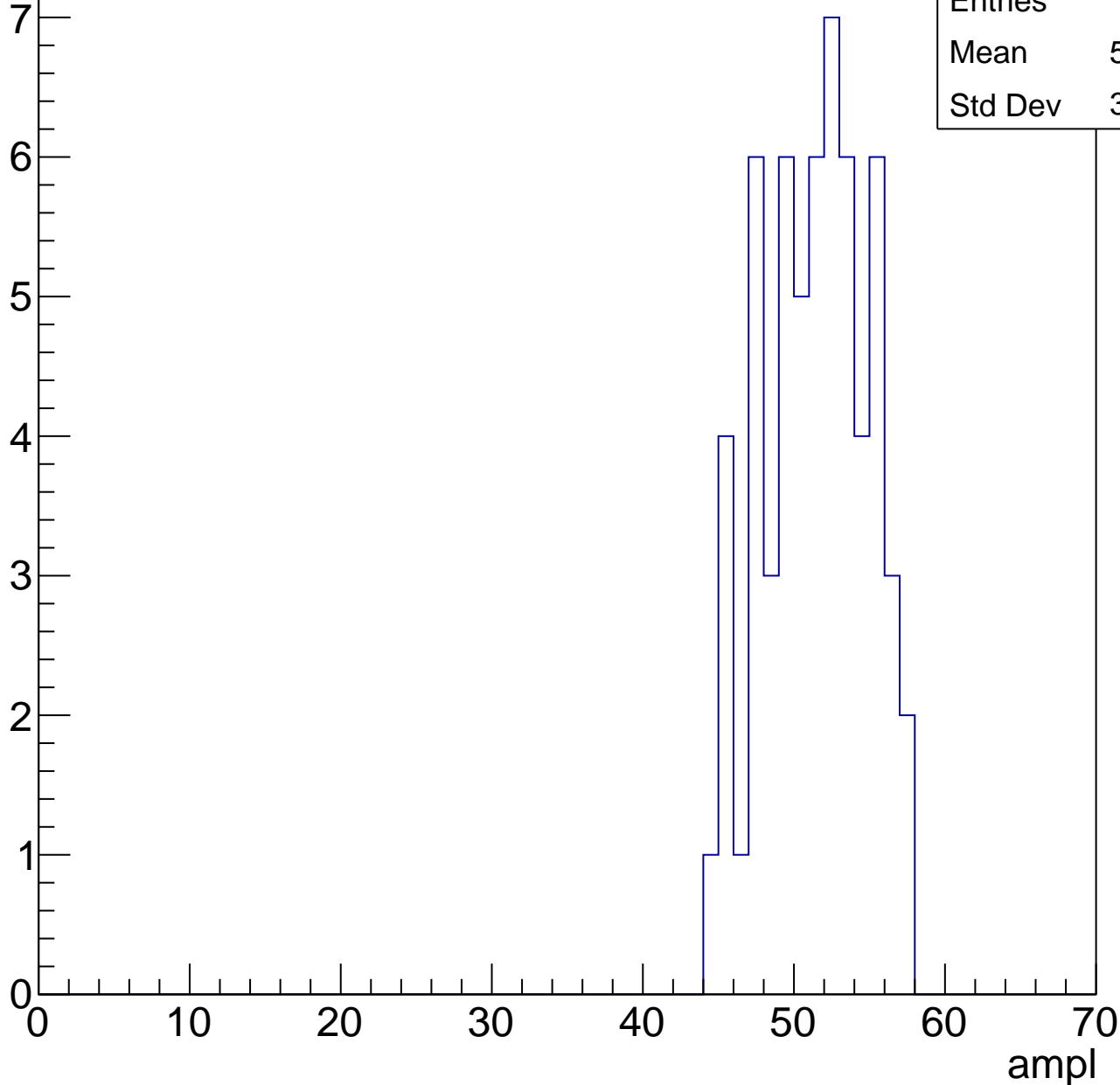


B1L101S, U6-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.93
Std Dev	3.366

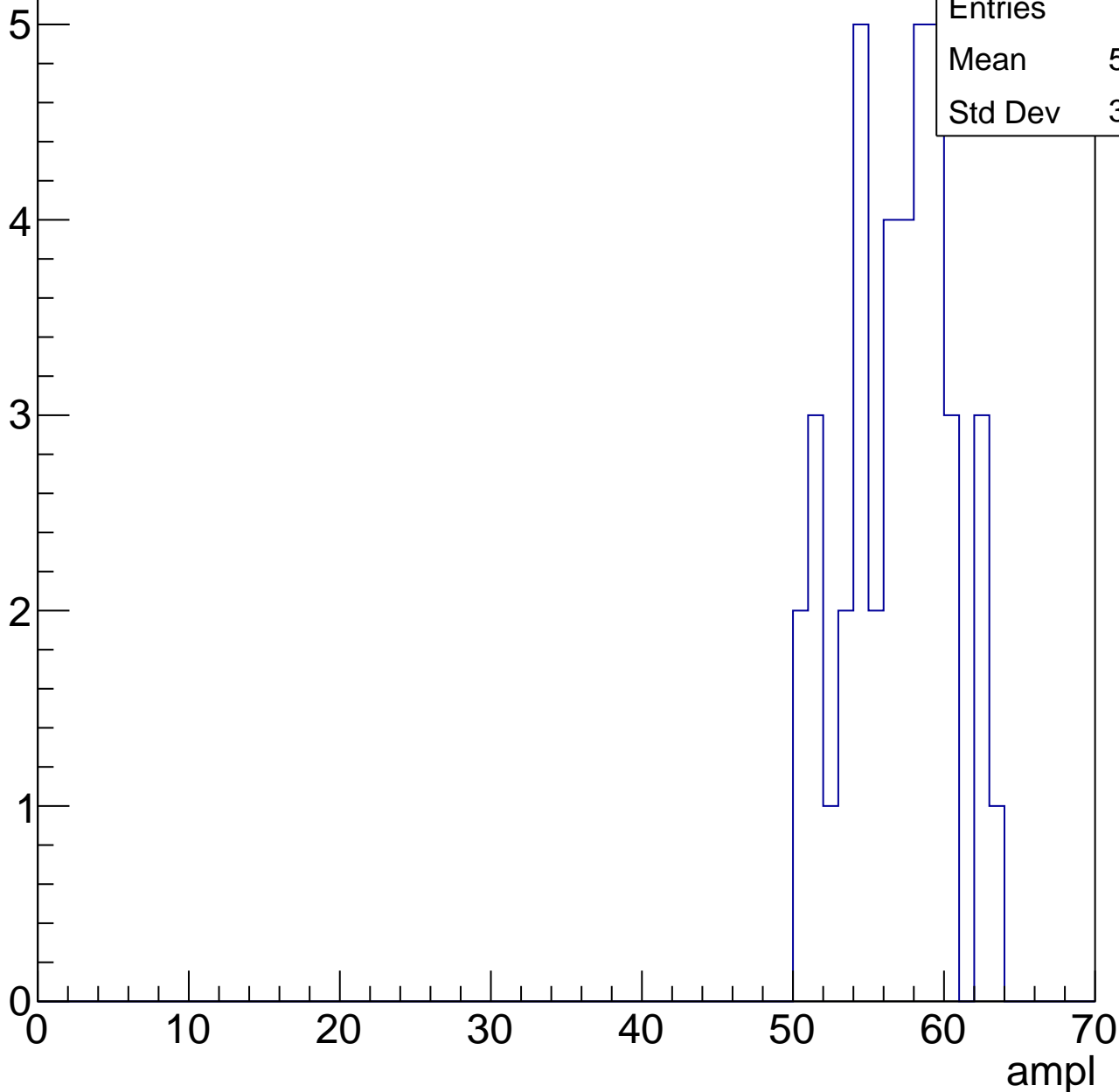


B1L101S, U6-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	56.42
Std Dev	3.427

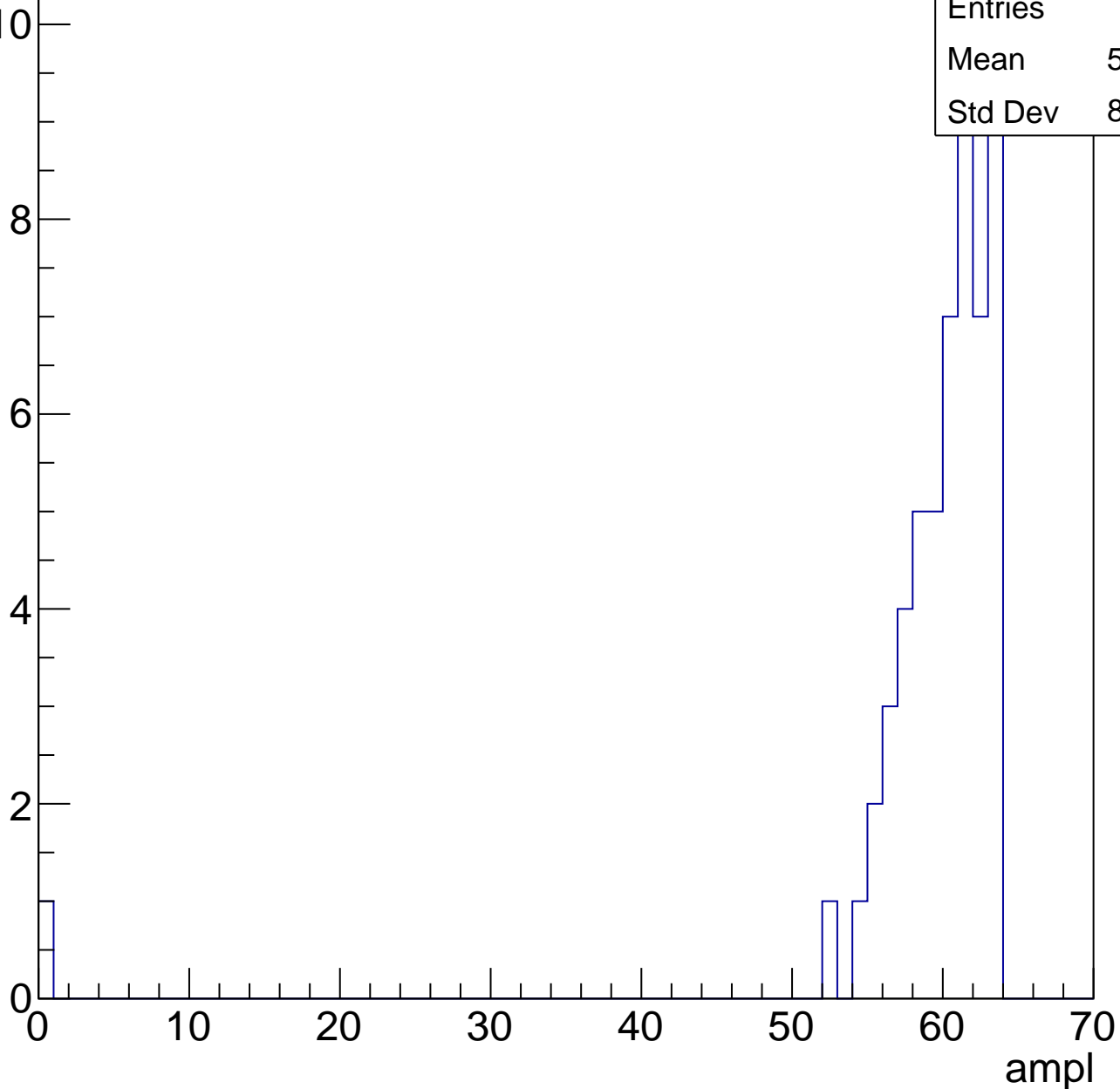


B1L101S, U6-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	58.69
Std Dev	8.403



B1L101S, U6-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch106, adc0

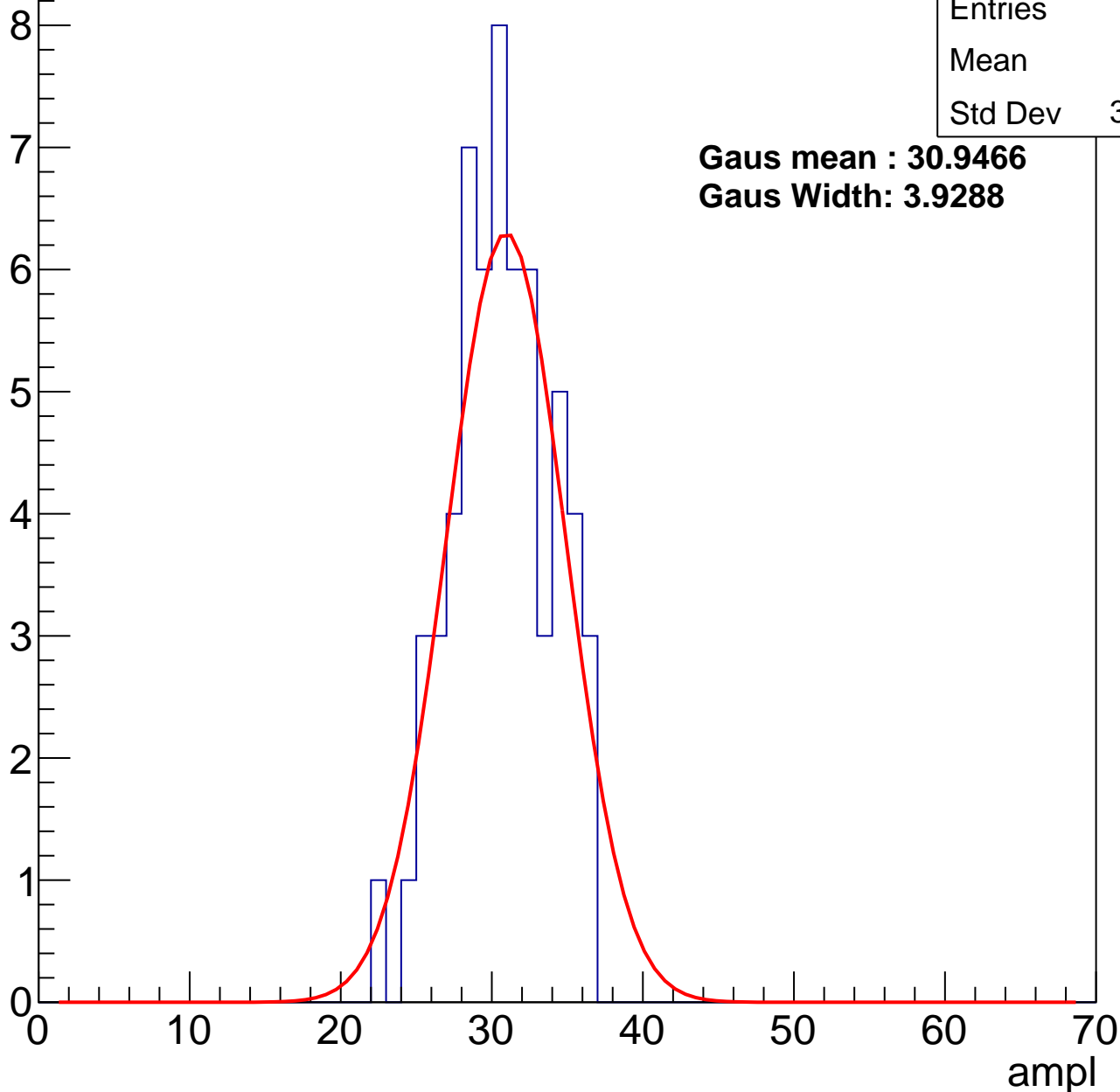
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.2
Std Dev	3.255

Gaus mean : 30.9466

Gaus Width: 3.9288



B1L101S, U6-ch106, adc1

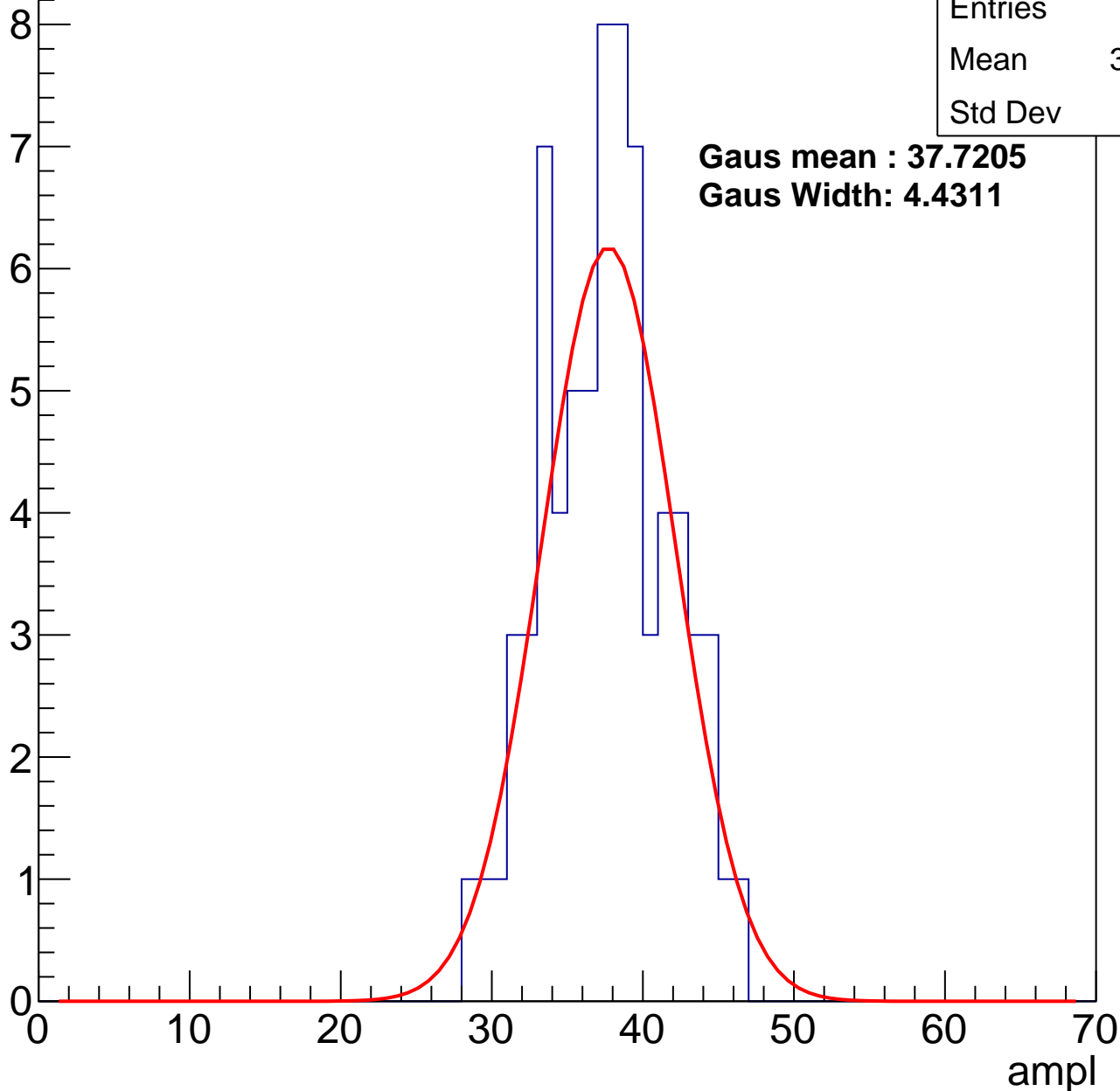
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.15
Std Dev	4.04

Gaus mean : 37.7205

Gaus Width: 4.4311

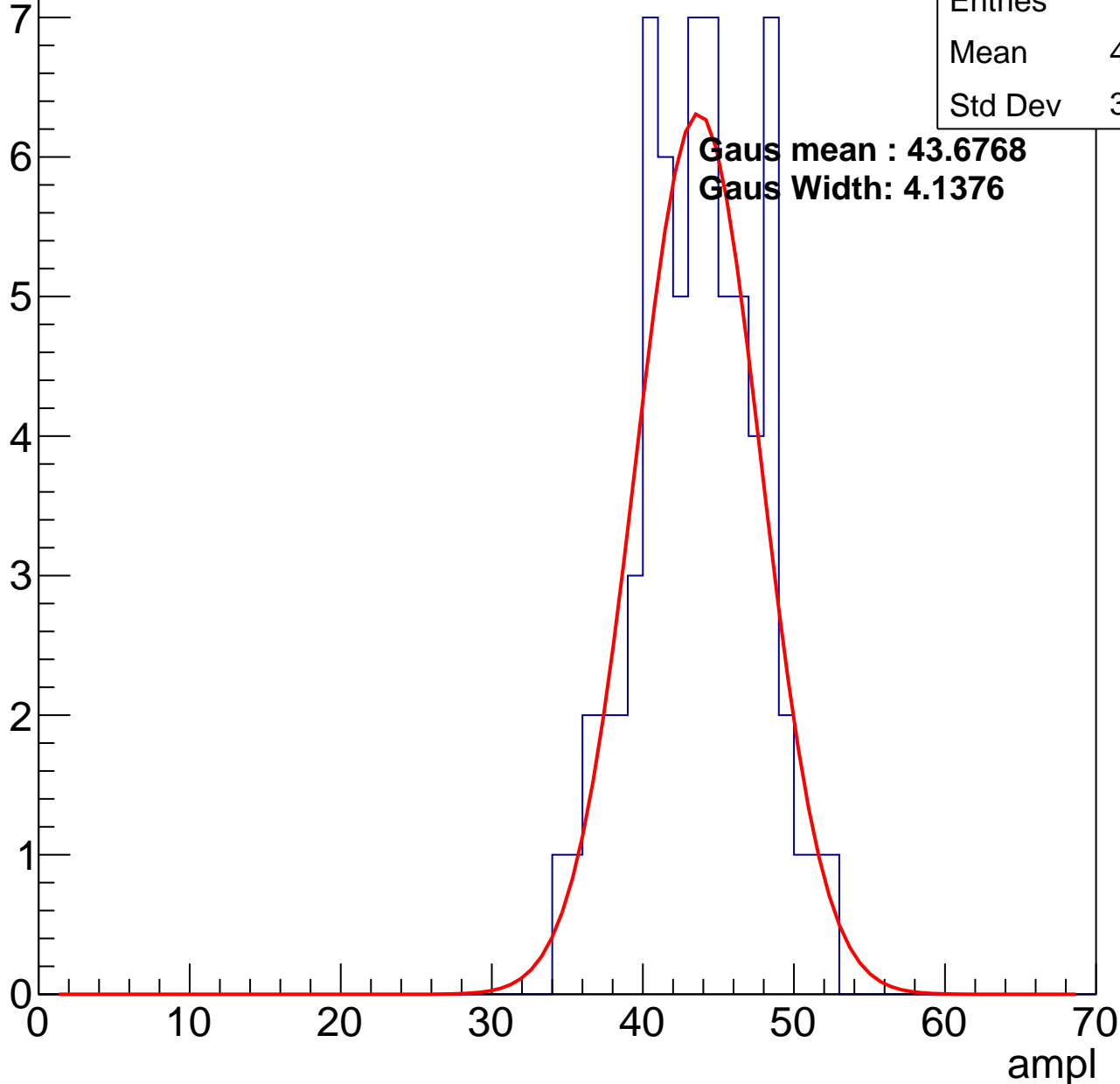


B1L101S, U6-ch106, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.23
Std Dev	3.953

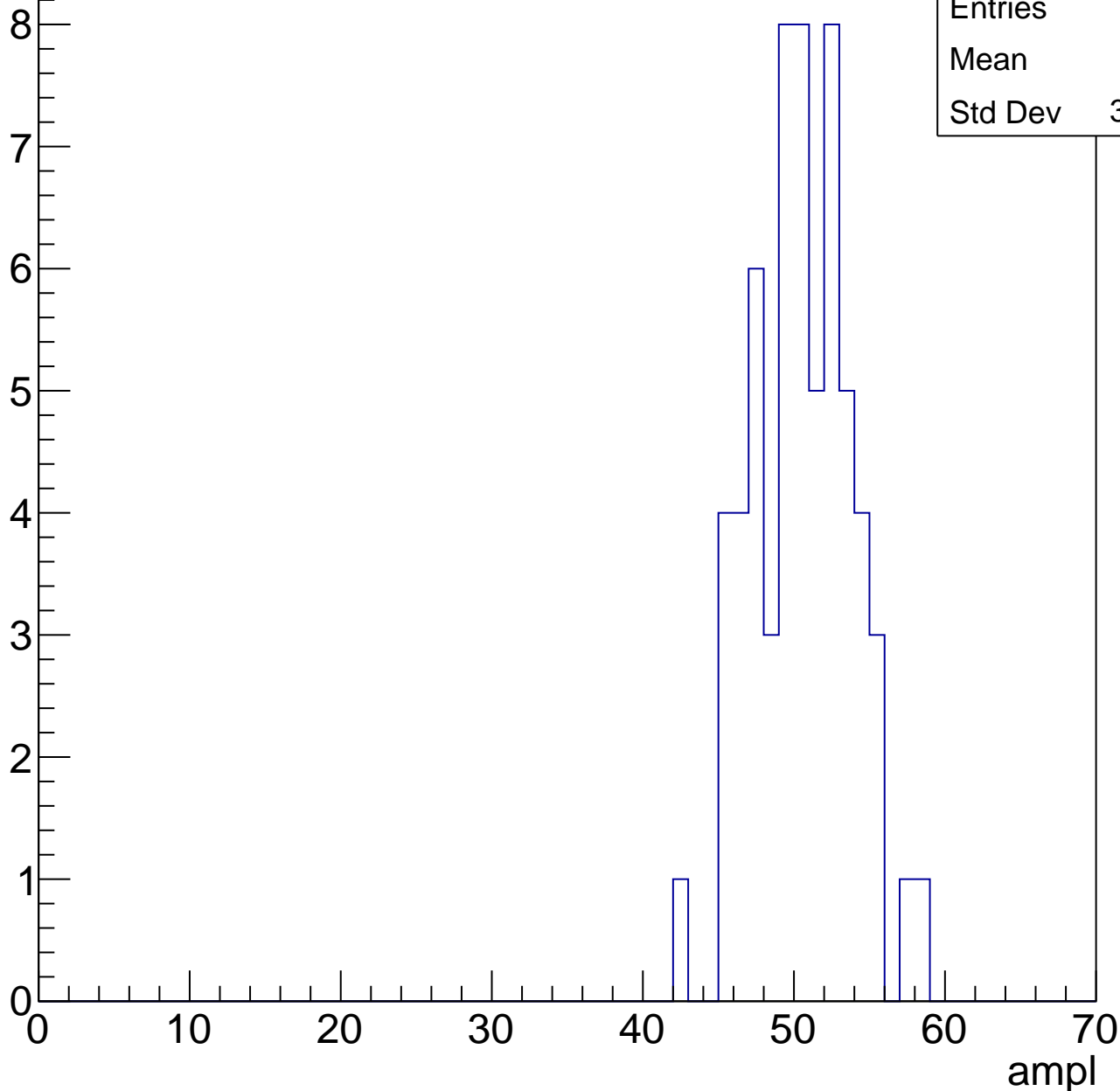


B1L101S, U6-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

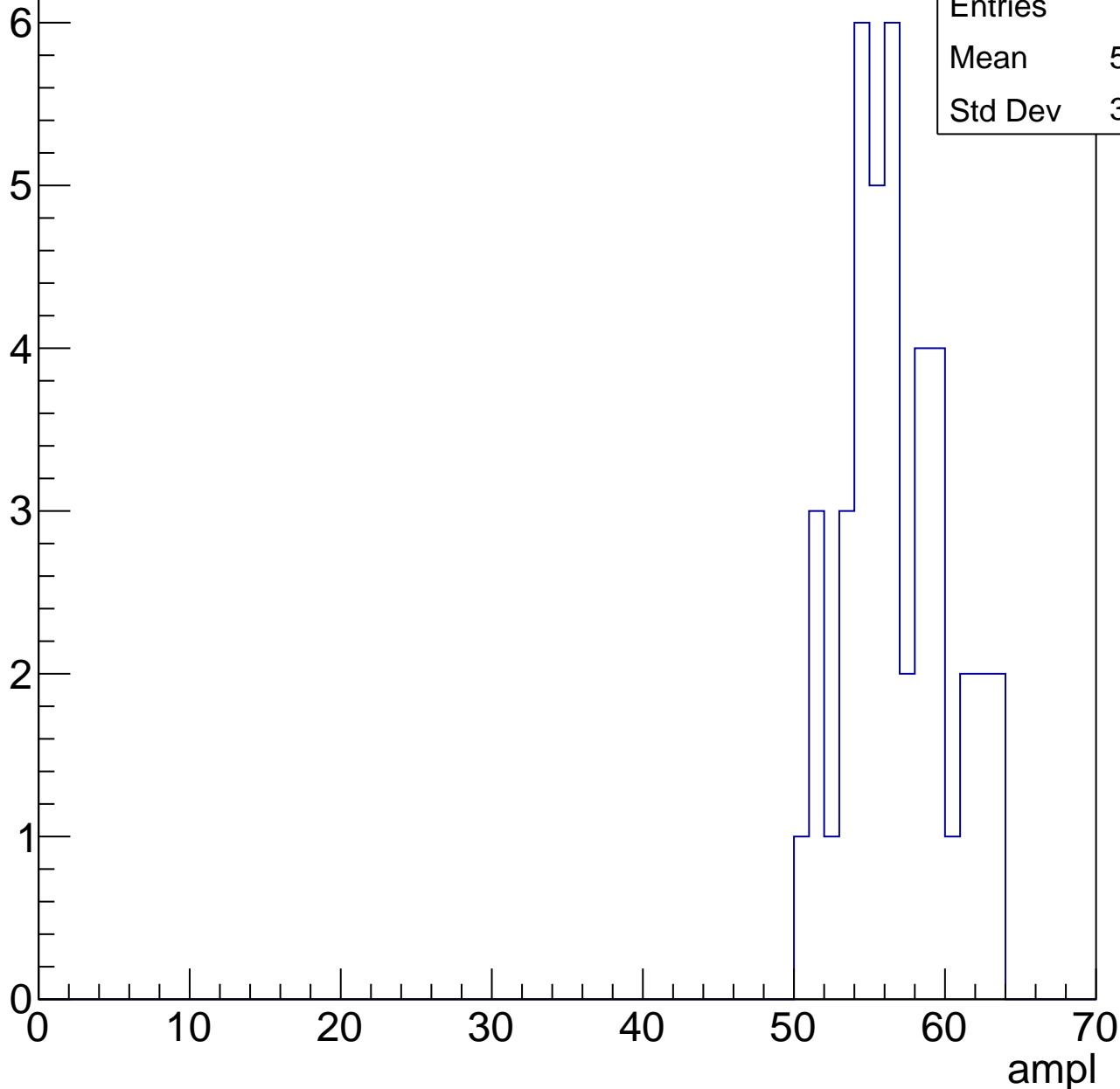
Entries	61
Mean	50.1
Std Dev	3.227



B1L101S, U6-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

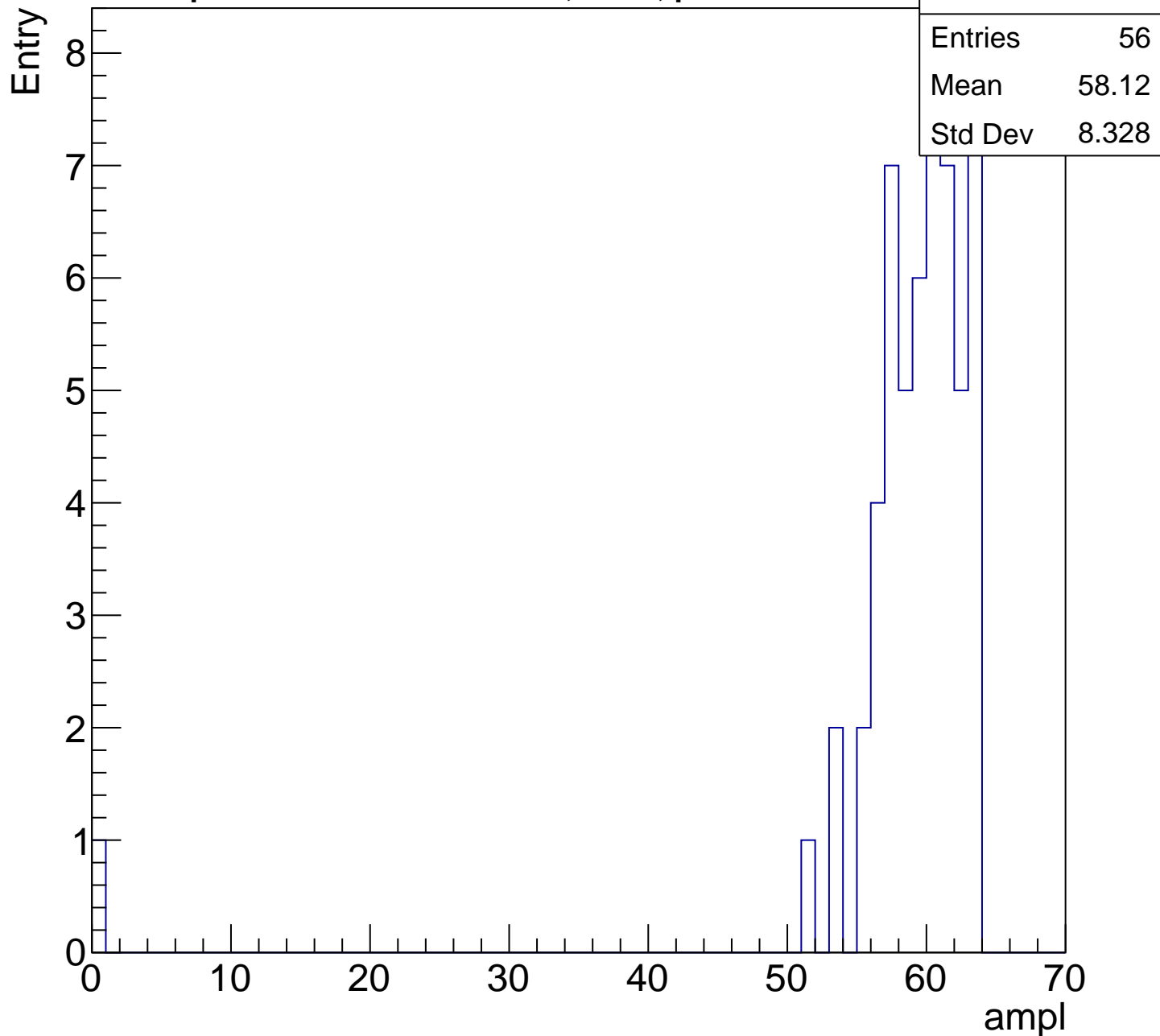
Entry



Entries	42
Mean	56.26
Std Dev	3.346

B1L101S, U6-ch106, adc5

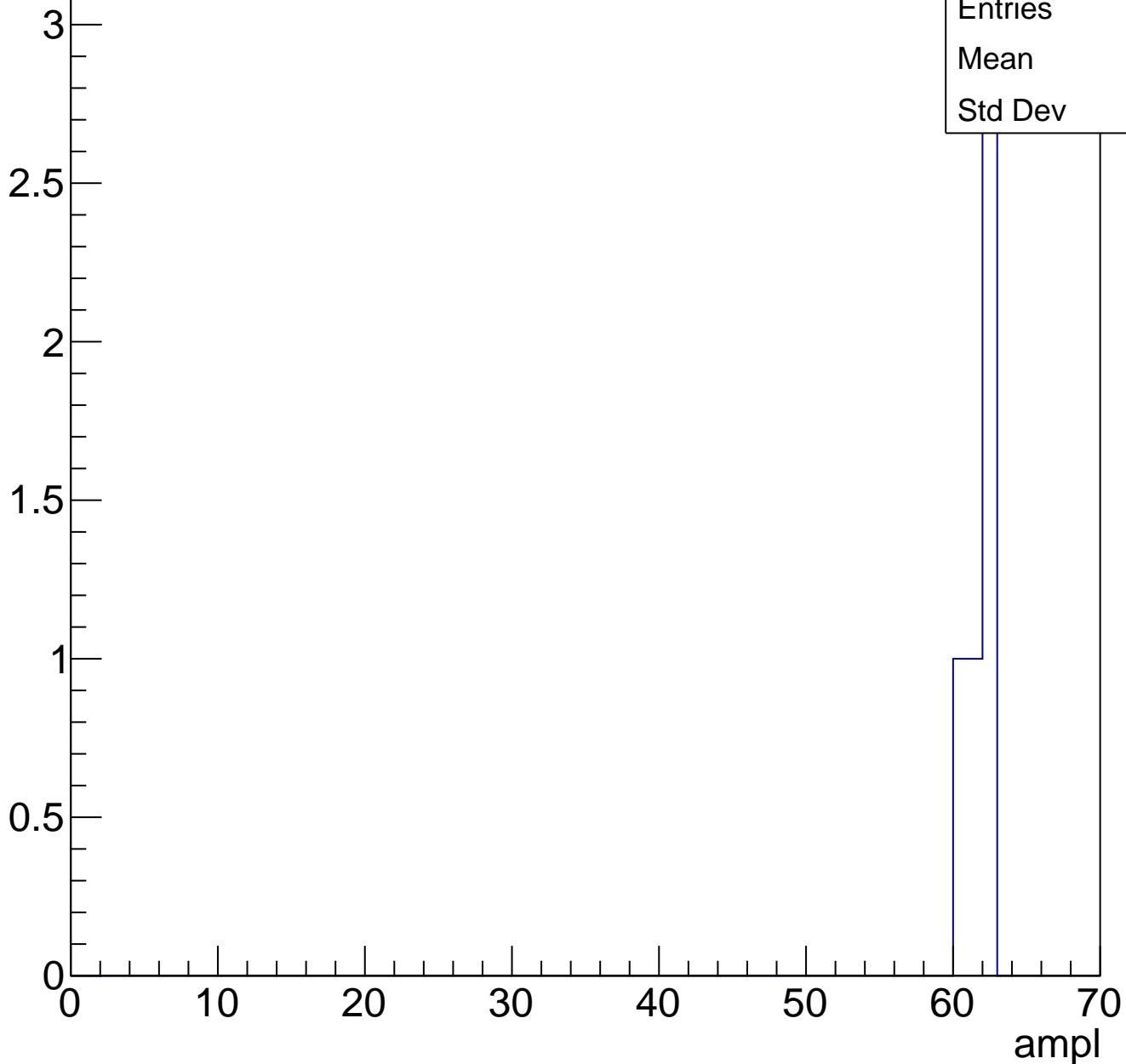
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch107, adc0

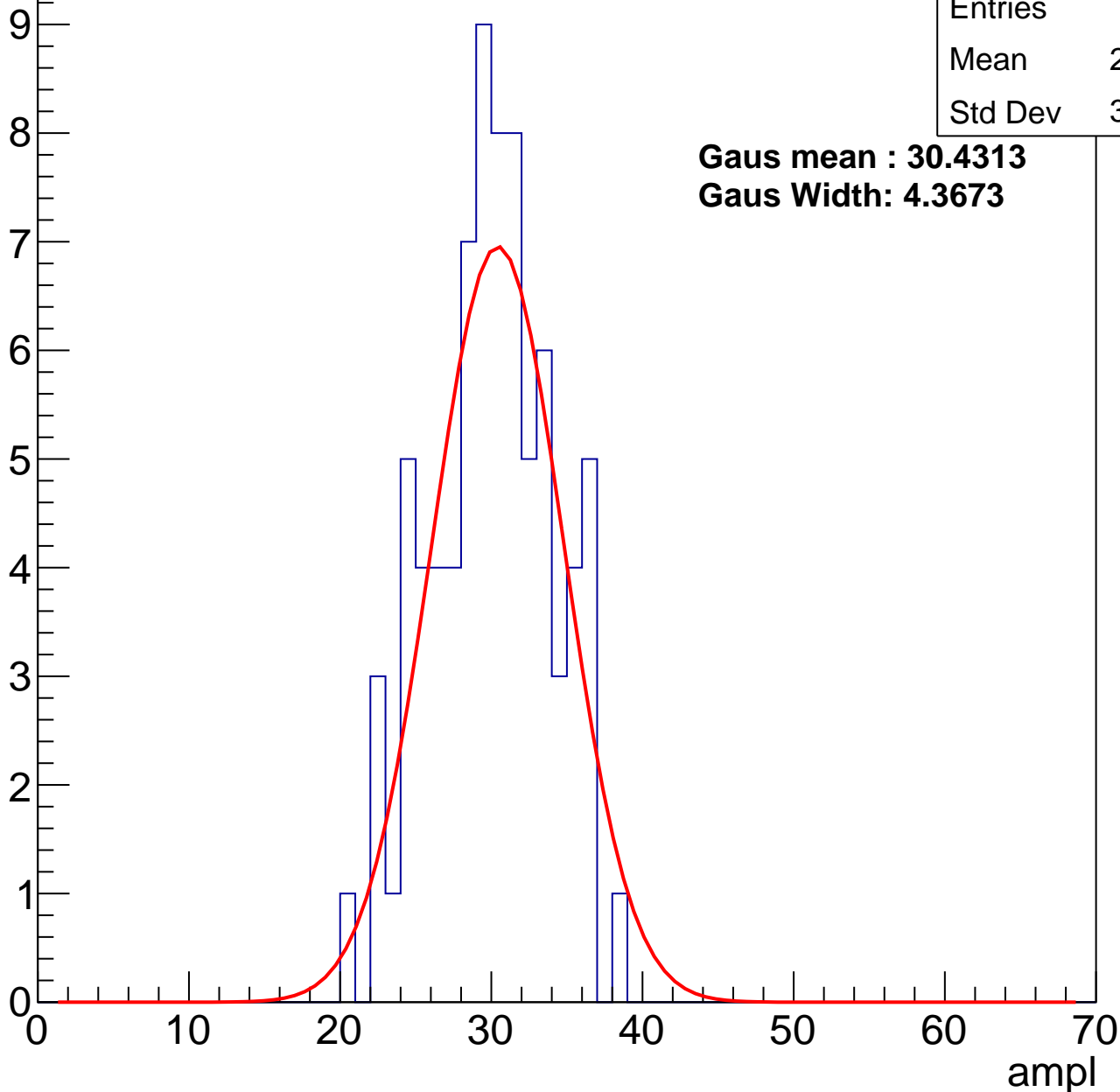
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.54
Std Dev	3.947

Gaus mean : 30.4313

Gaus Width: 4.3673



B1L101S, U6-ch107, adc1

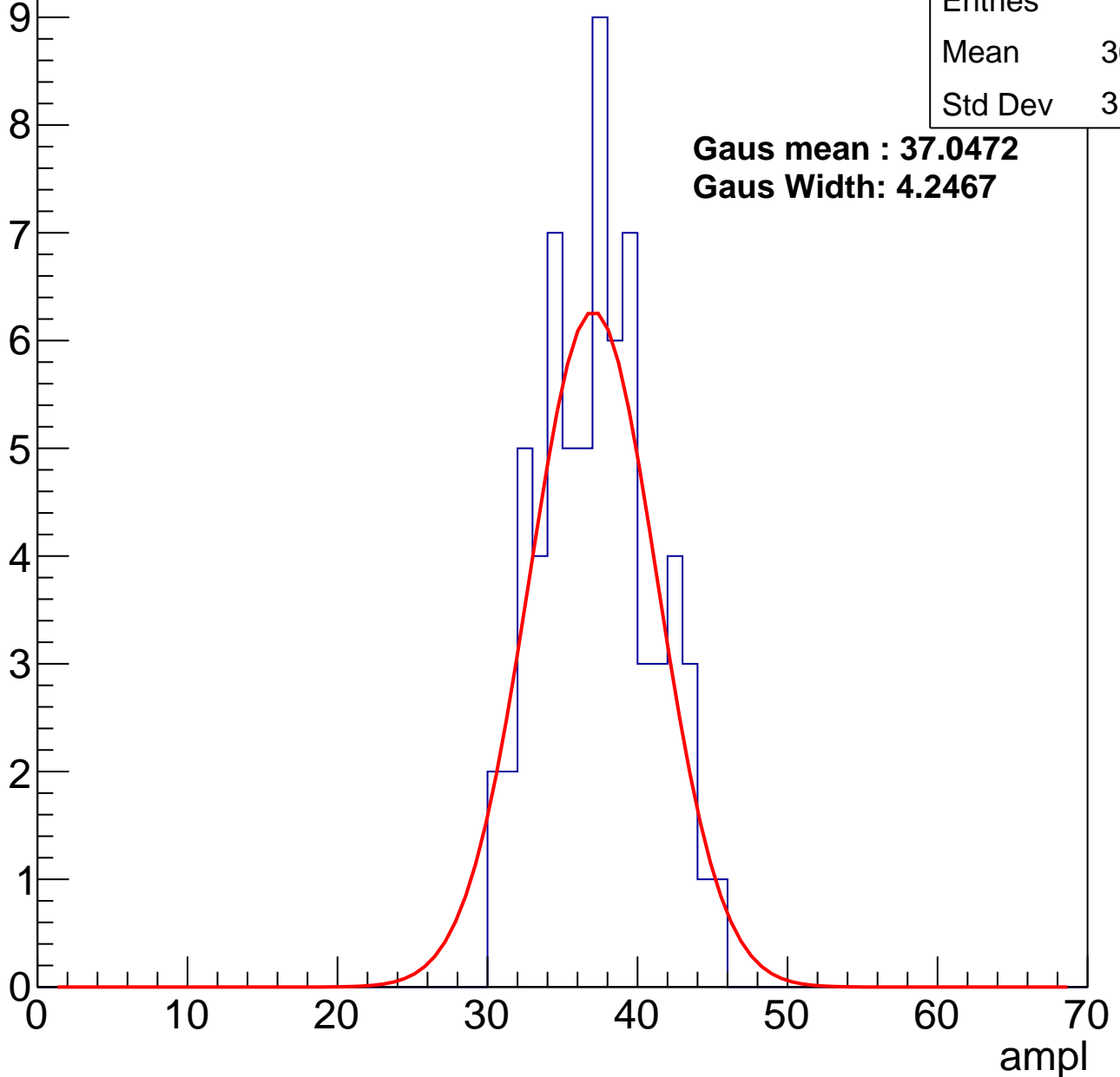
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.87
Std Dev	3.615

Gaus mean : 37.0472

Gaus Width: 4.2467



B1L101S, U6-ch107, adc2

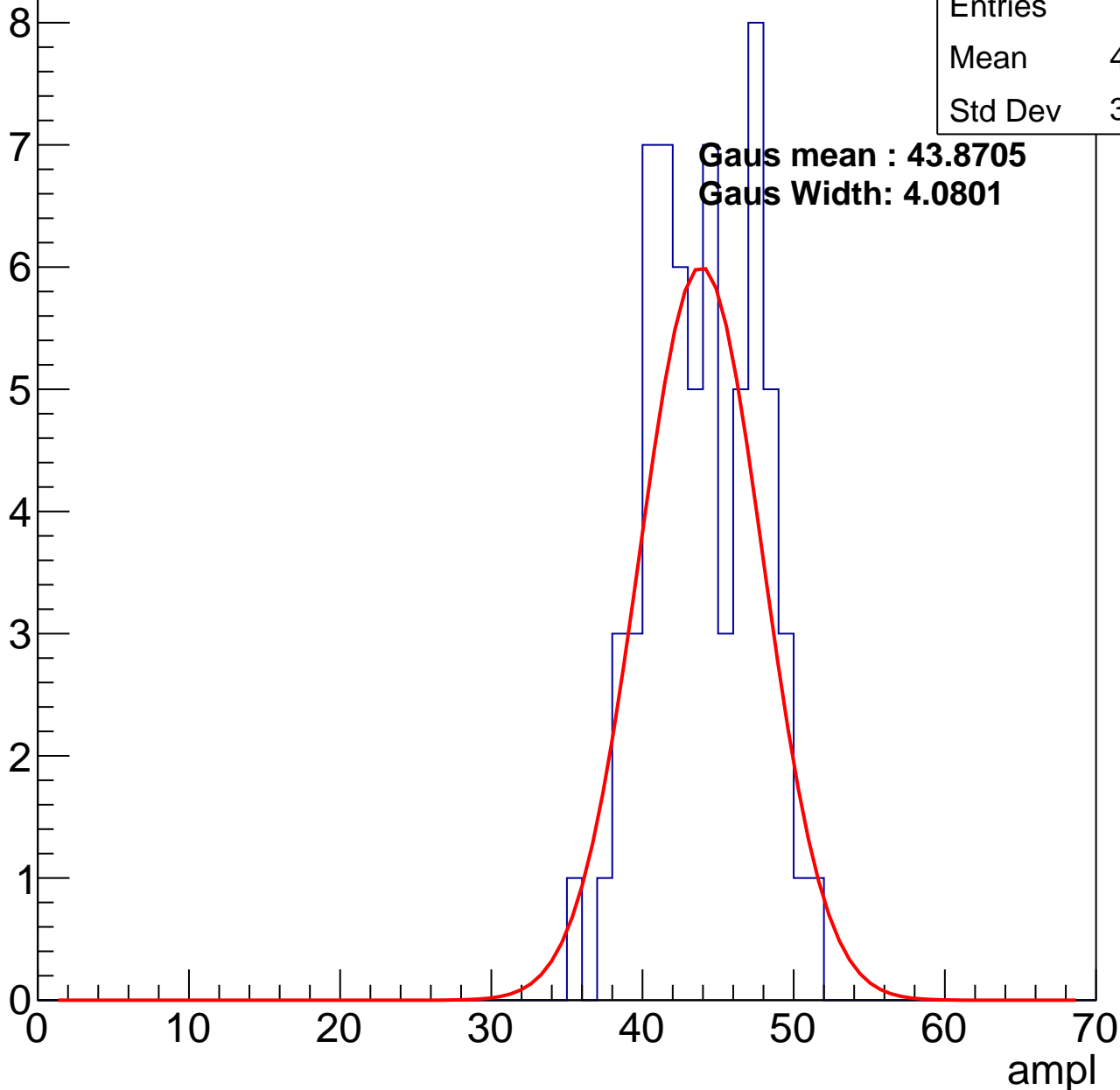
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.55
Std Dev	3.564

Gaus mean : 43.8705

Gaus Width: 4.0801

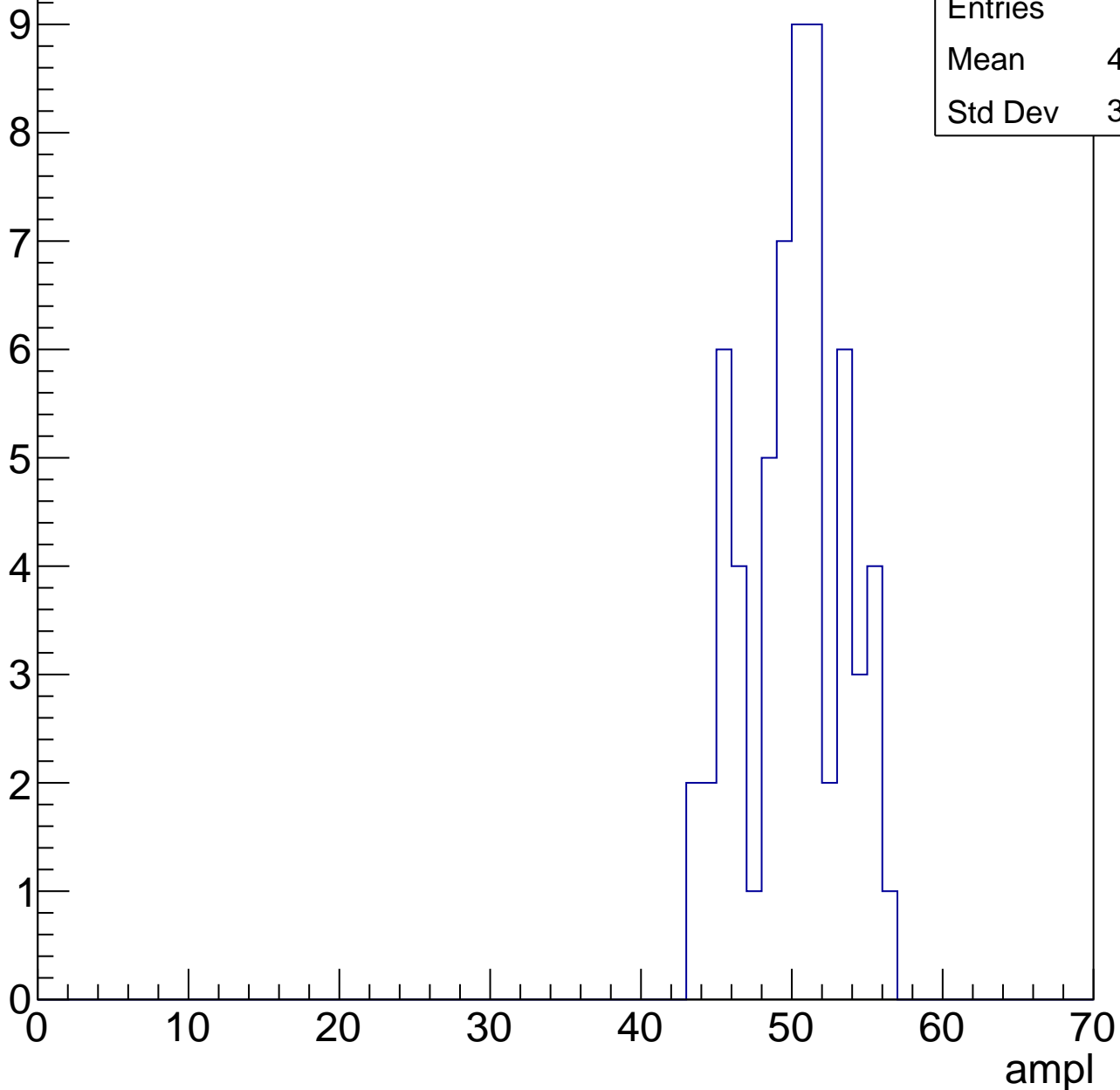


B1L101S, U6-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.62
Std Dev	3.305

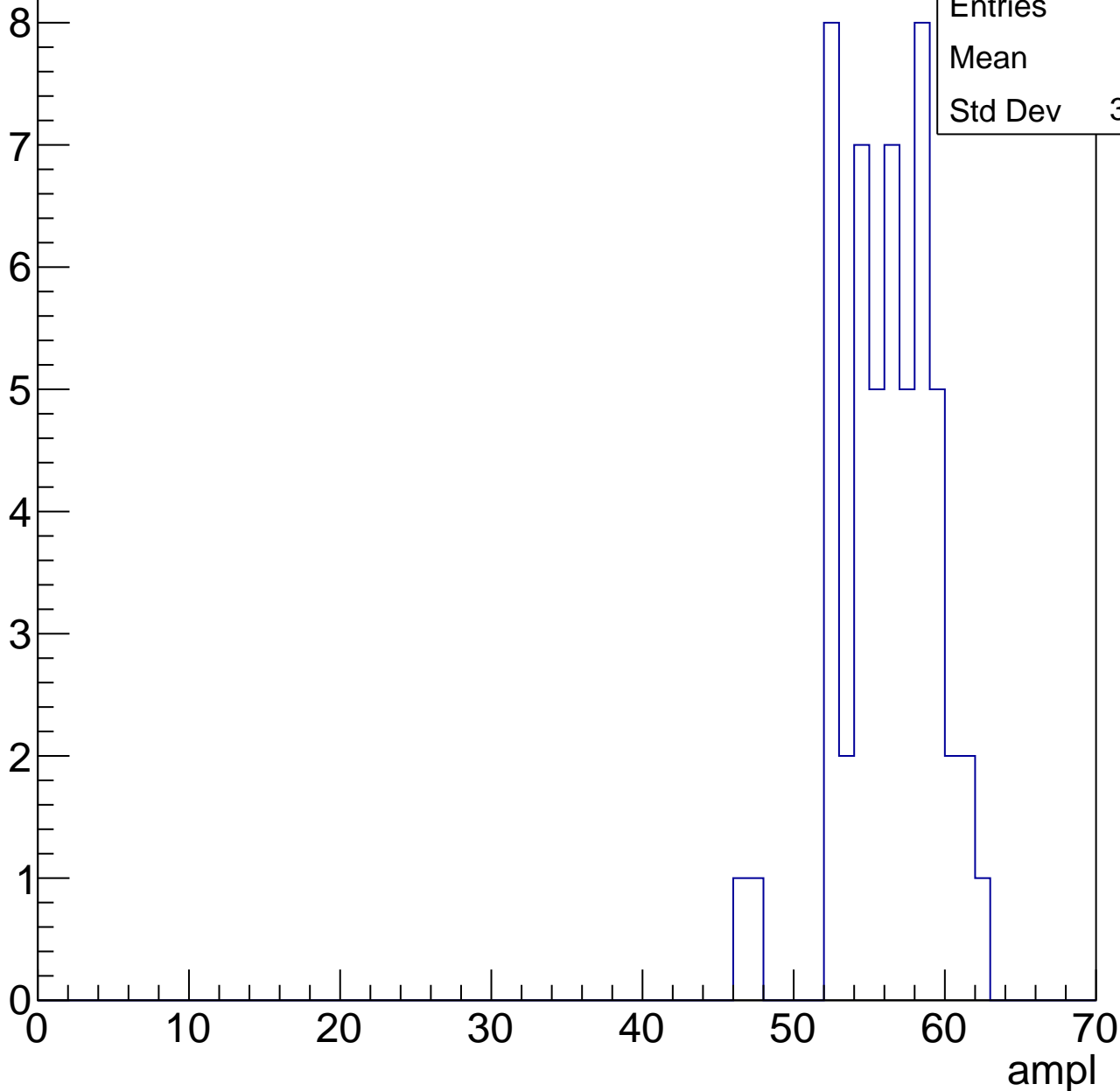


B1L101S, U6-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

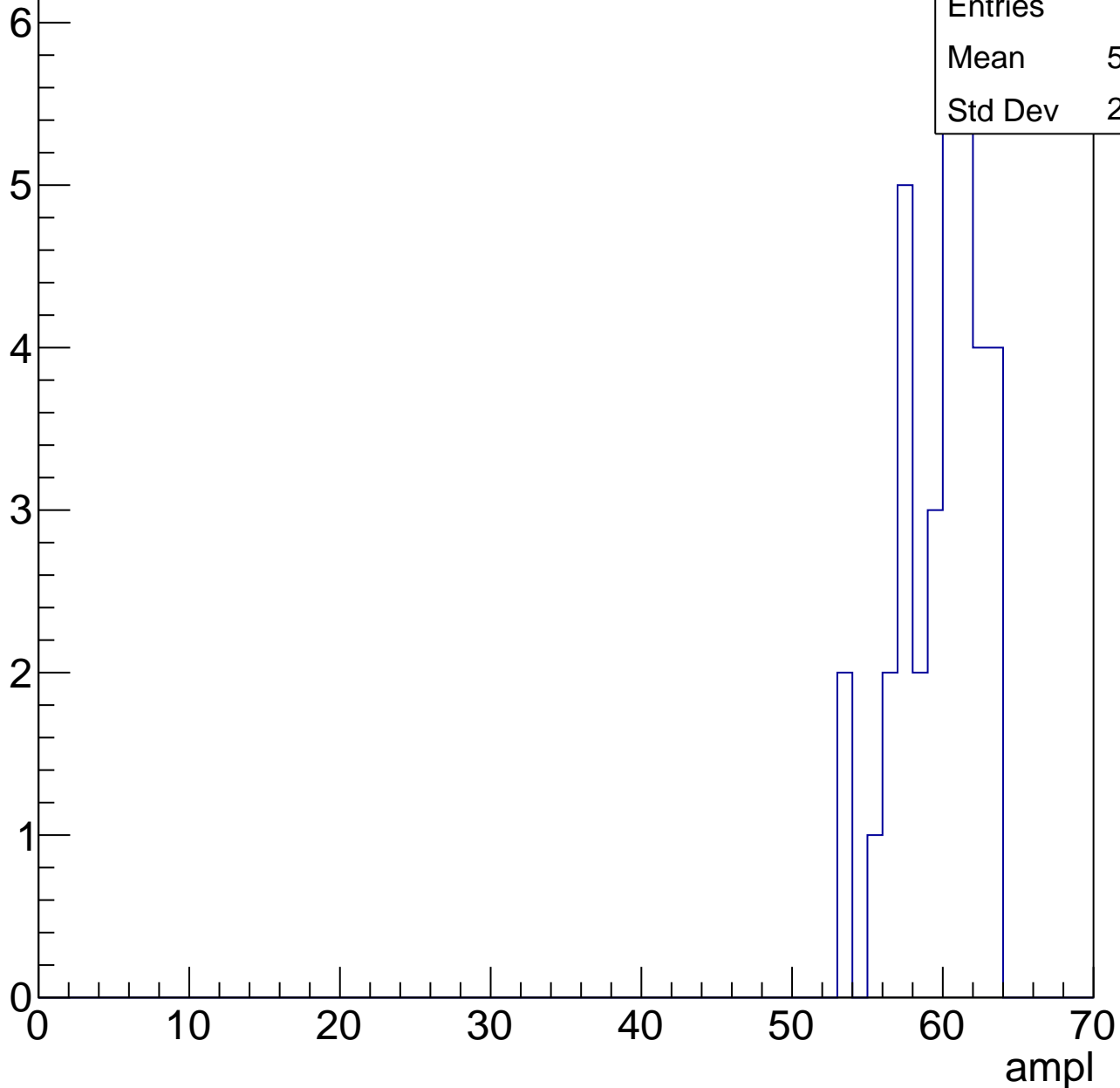
Entries	54
Mean	55.7
Std Dev	3.207



B1L101S, U6-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

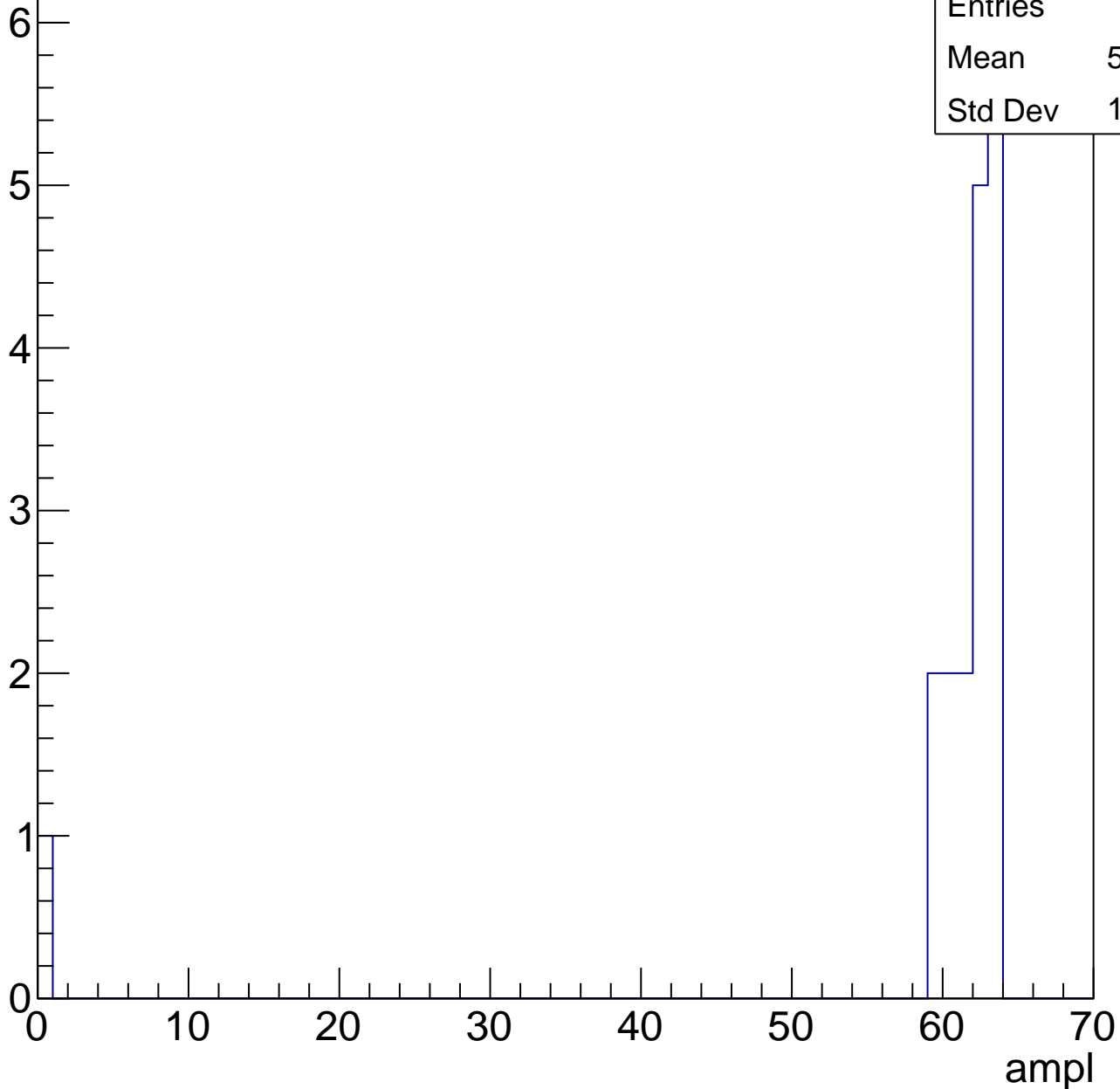


B1L101S, U6-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	58.22
Std Dev	14.18



B1L101S, U6-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch108, adc0

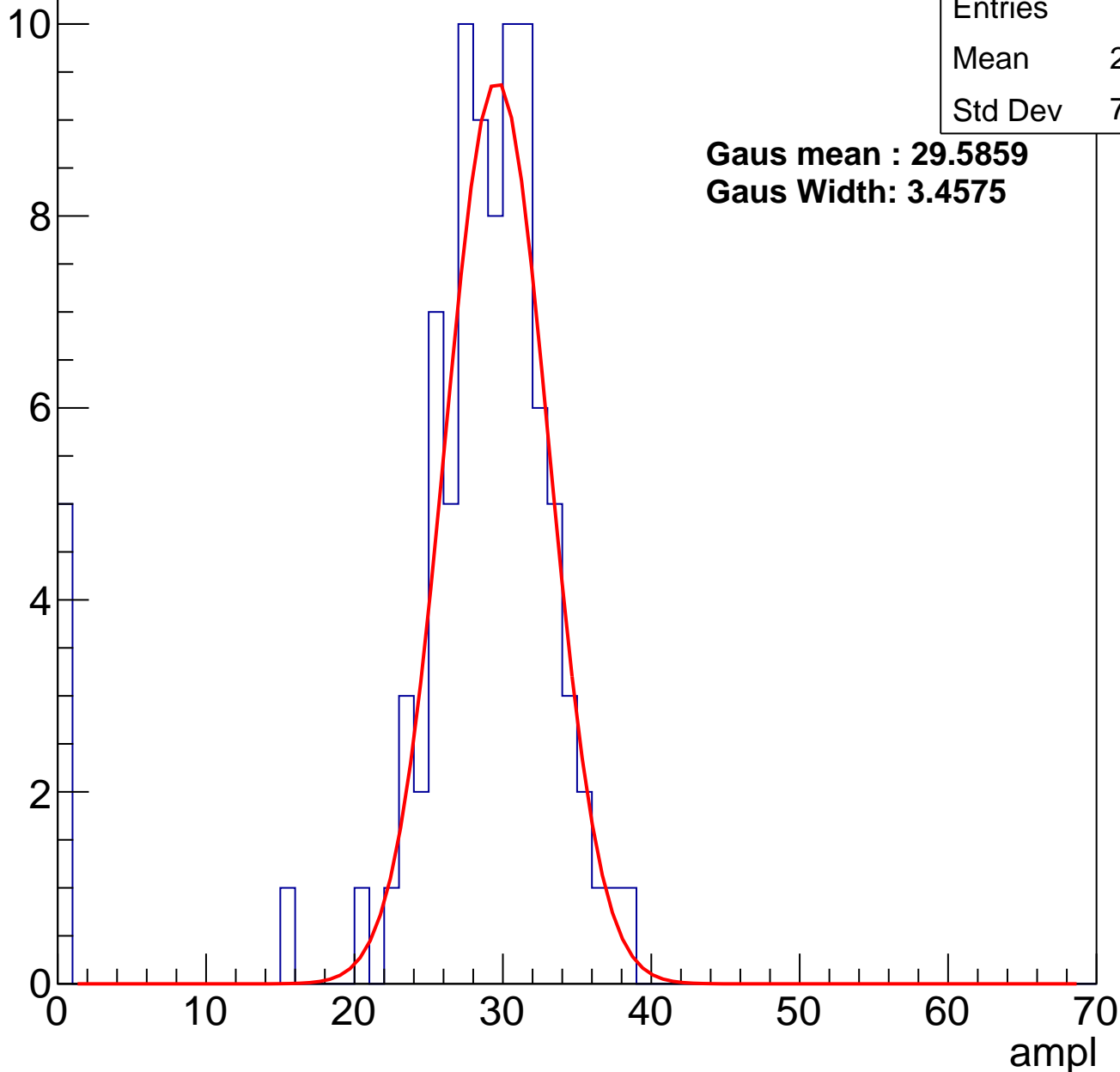
calib_packv5_042523_0143.root, FC#0, port D2

Entries	91
Mean	27.29
Std Dev	7.525

Gaus mean : 29.5859

Gaus Width: 3.4575

Entry



B1L101S, U6-ch108, adc1

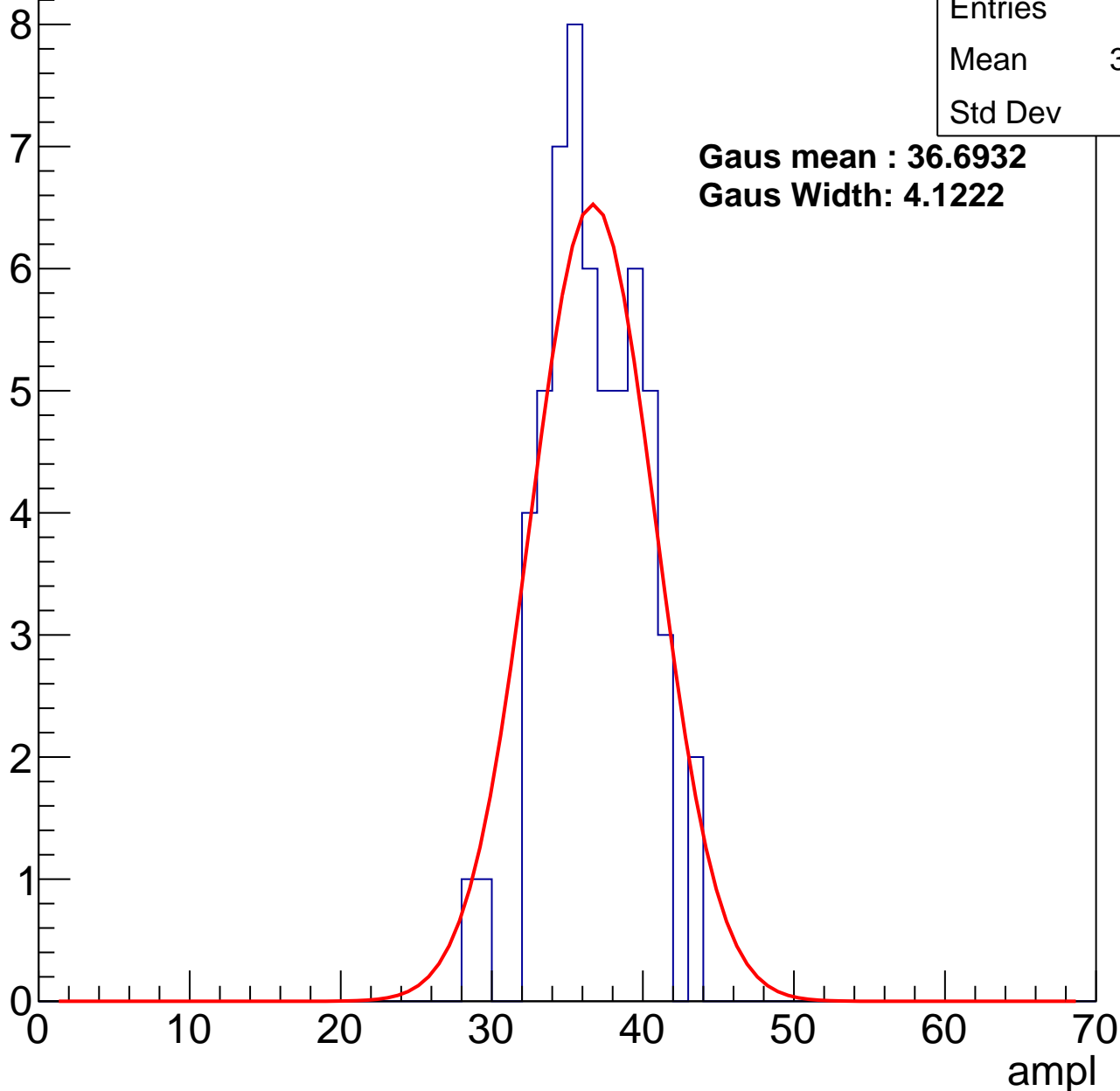
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.24
Std Dev	3.18

Gaus mean : 36.6932

Gaus Width: 4.1222



B1L101S, U6-ch108, adc2

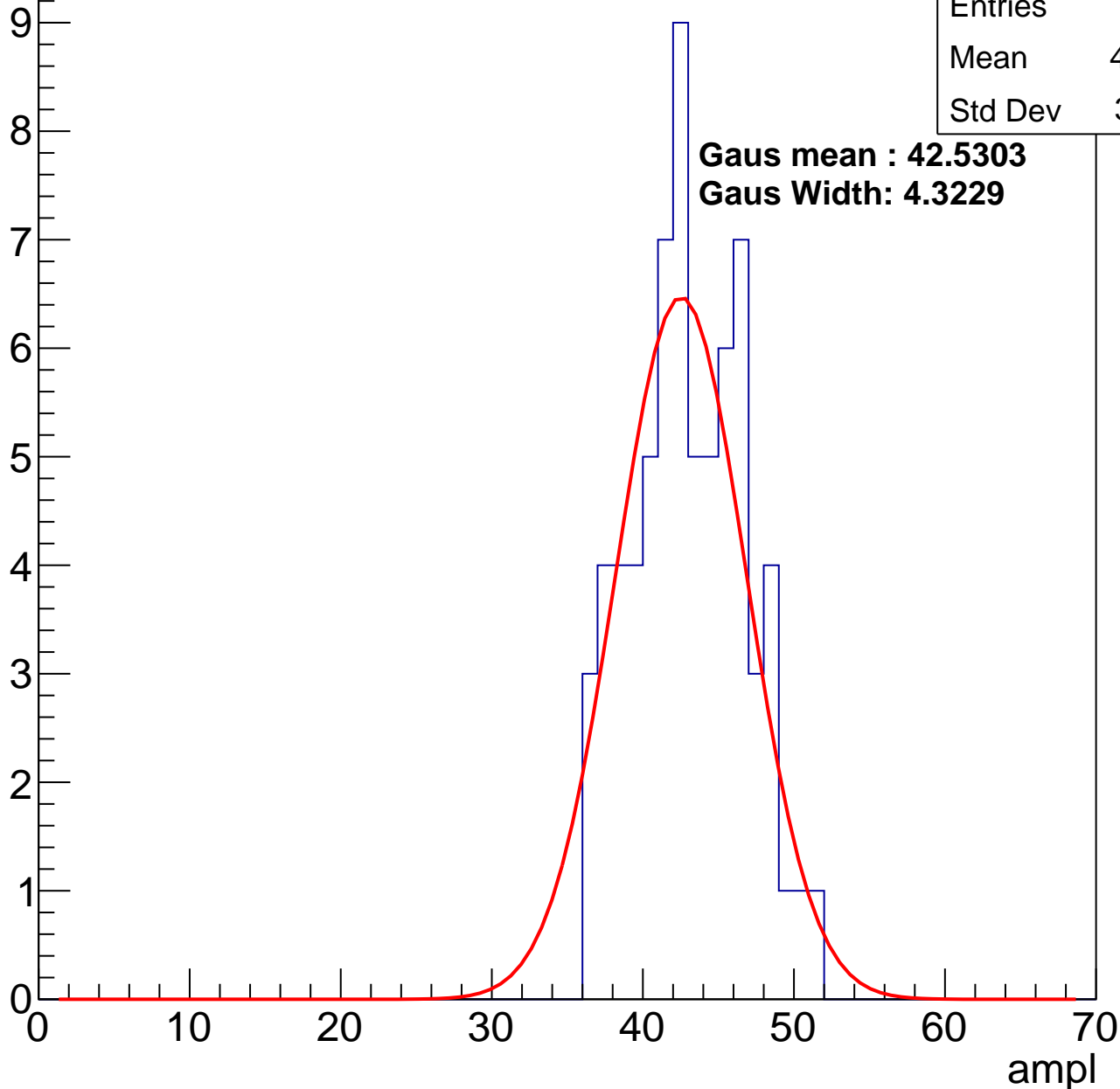
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.59
Std Dev	3.641

Gaus mean : 42.5303

Gaus Width: 4.3229

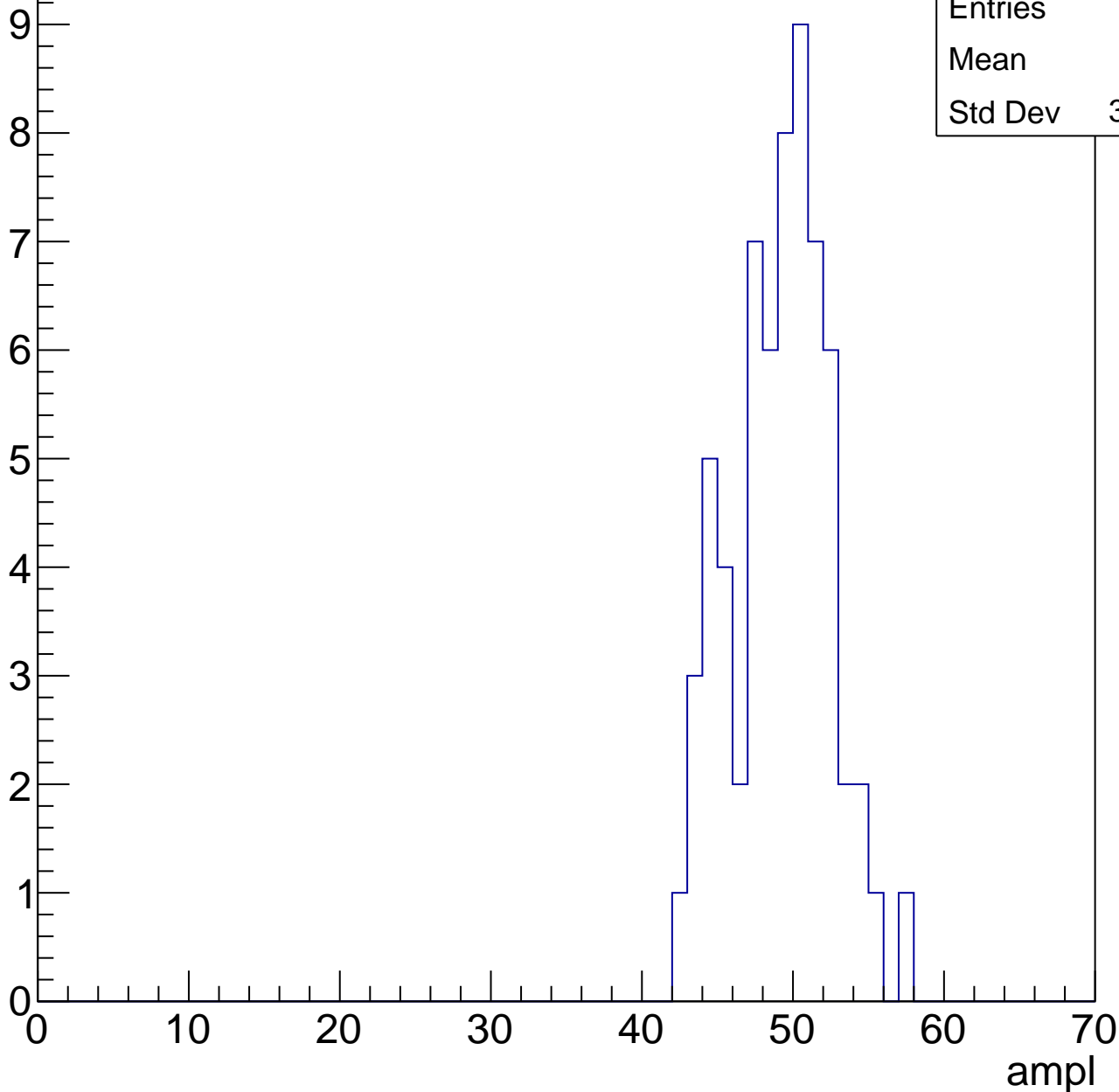


B1L101S, U6-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.7
Std Dev	3.224

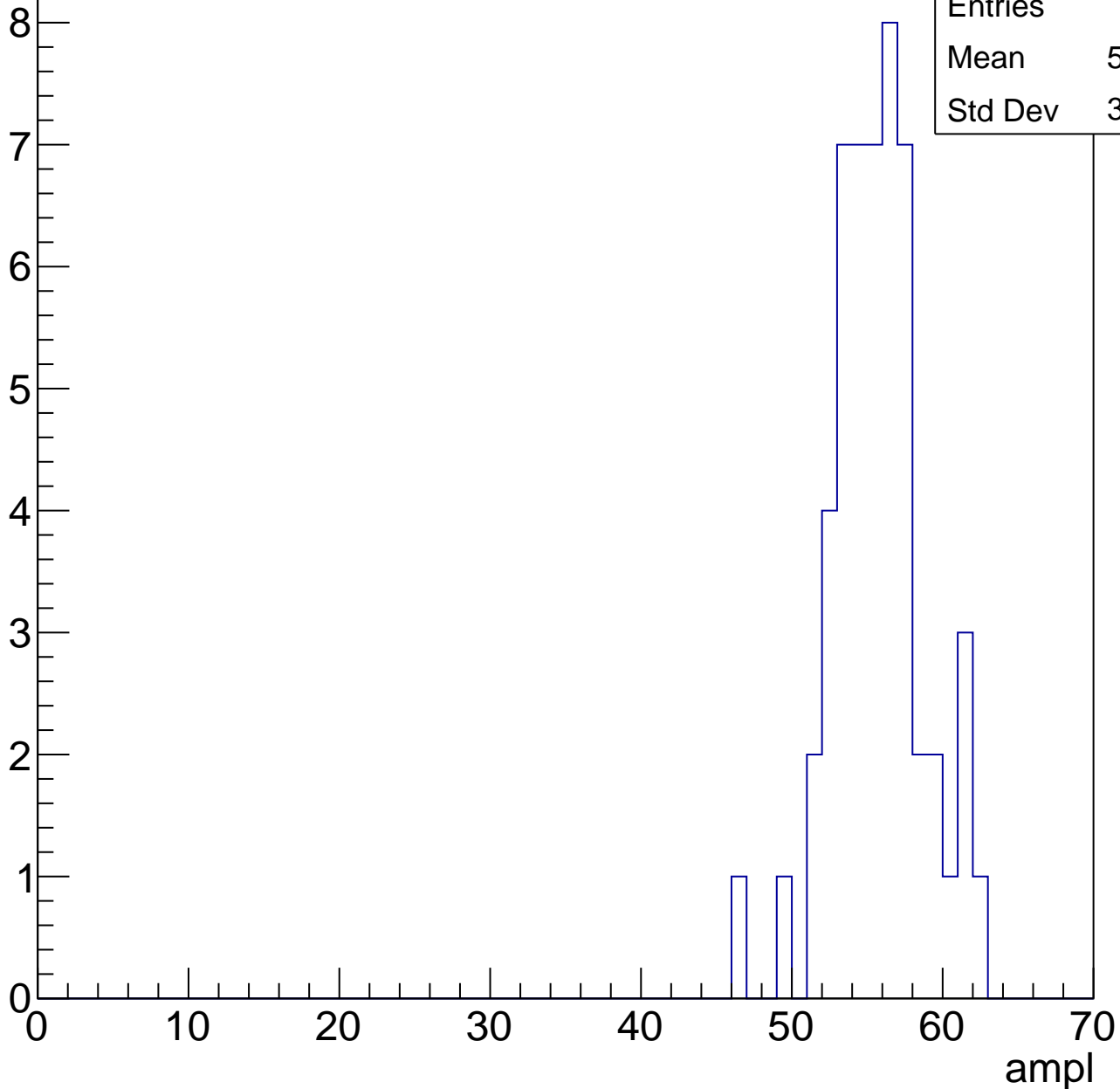


B1L101S, U6-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

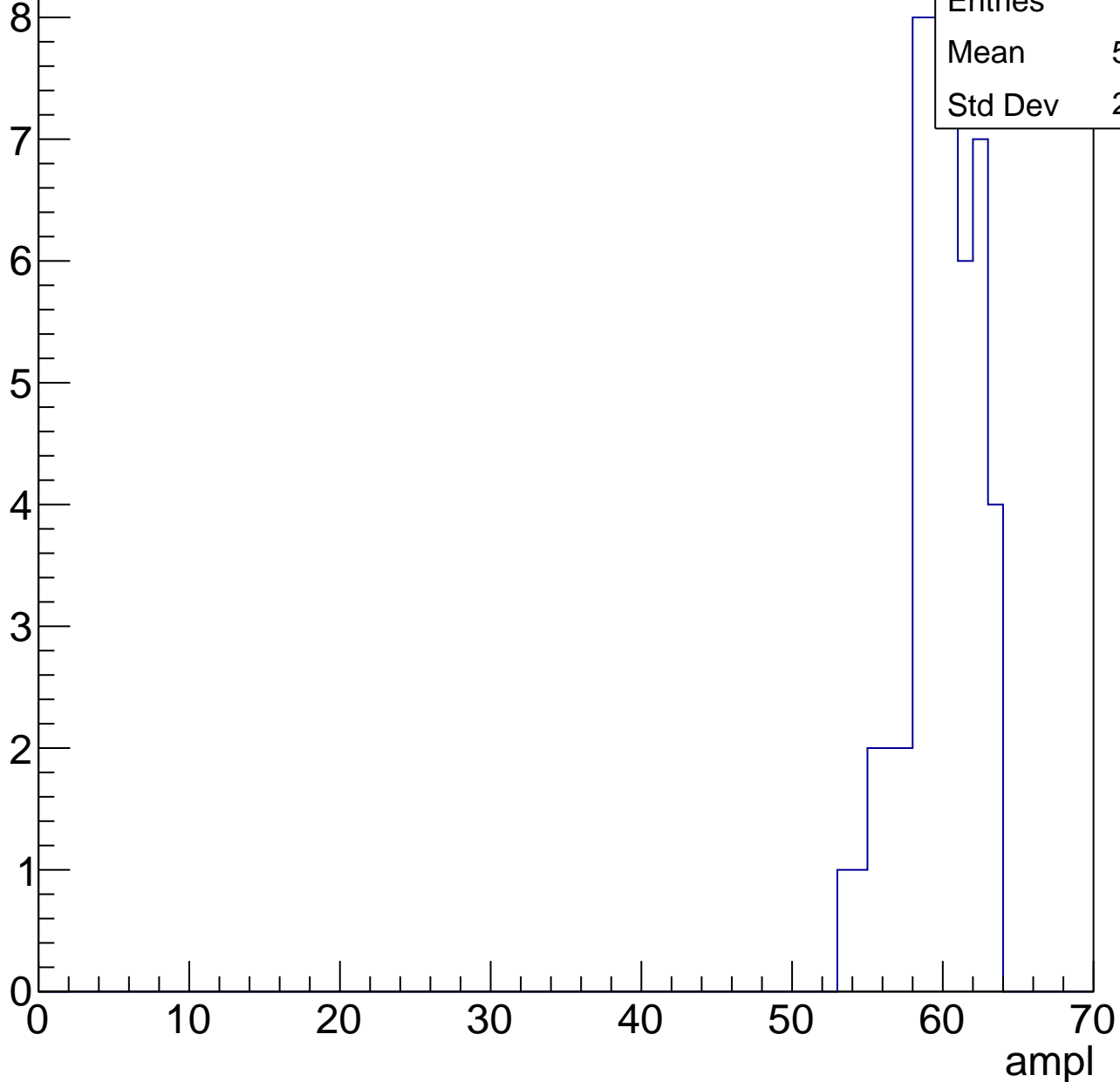
Entries	53
Mean	55.19
Std Dev	3.029



B1L101S, U6-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



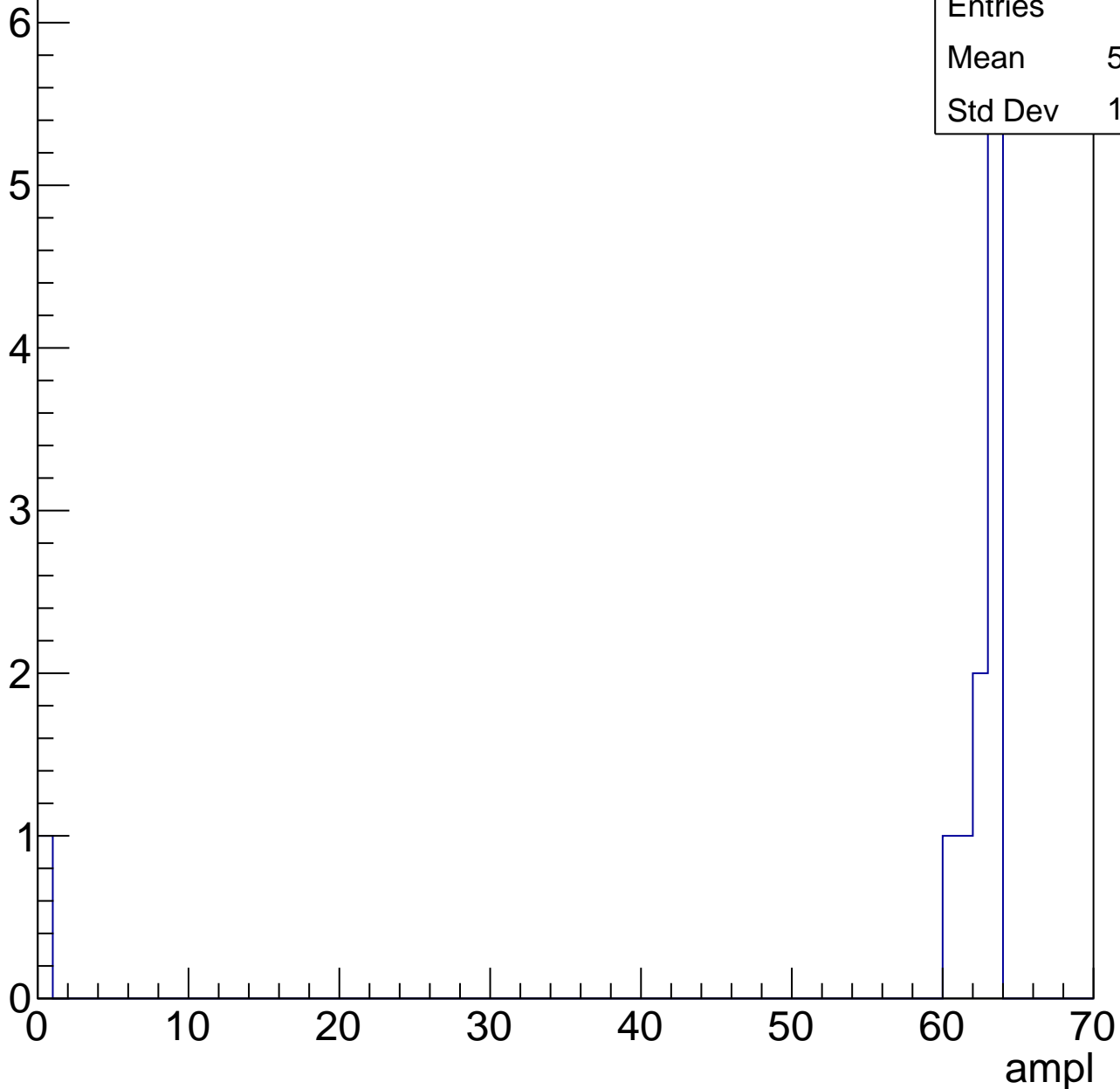
Entries	49
Mean	59.41
Std Dev	2.381

B1L101S, U6-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	56.64
Std Dev	17.94



B1L101S, U6-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch109, adc0

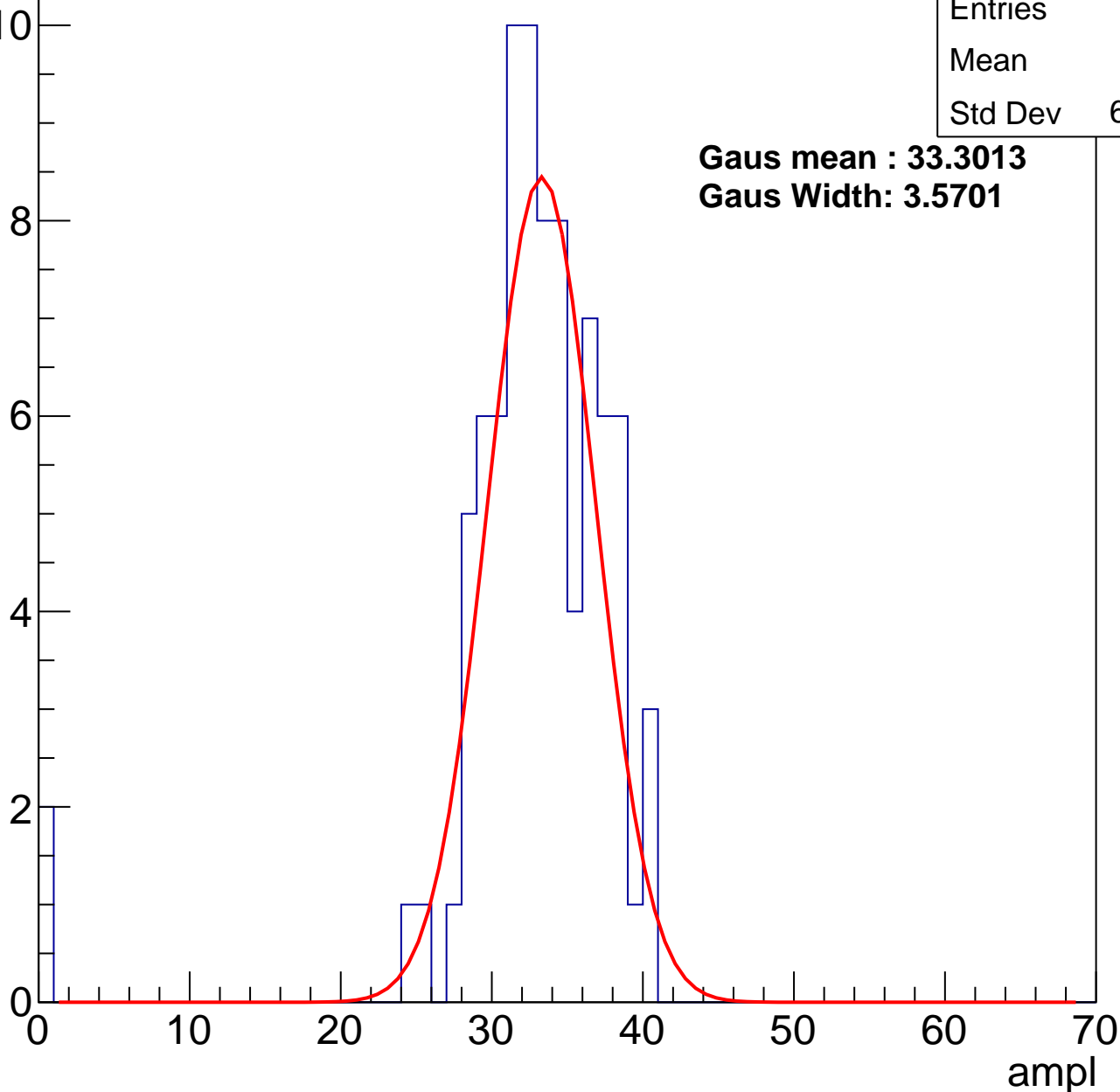
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	32.2
Std Dev	6.083

Gaus mean : 33.3013

Gaus Width: 3.5701



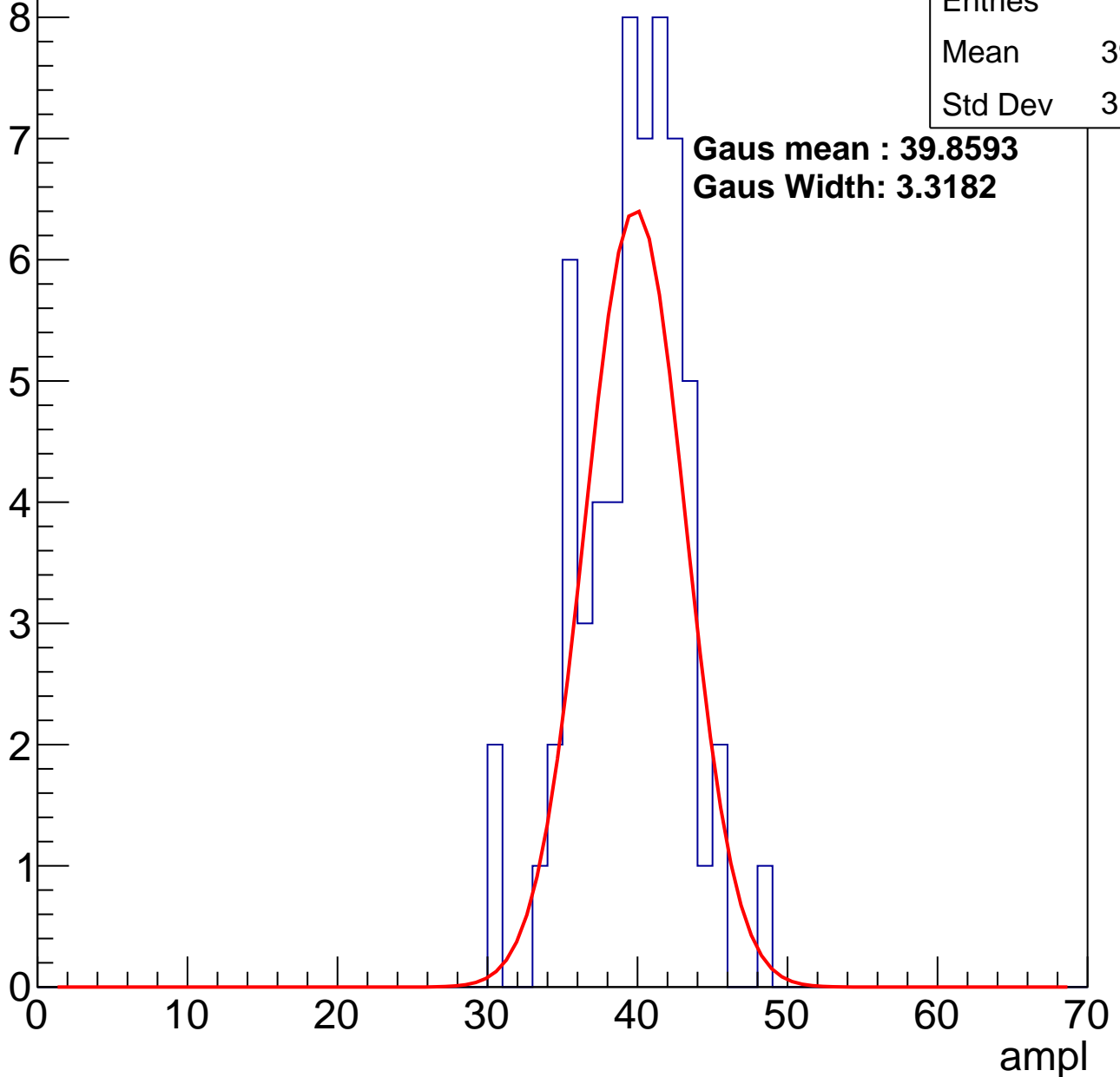
B1L101S, U6-ch109, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	39.18
Std Dev	3.504

Gaus mean : 39.8593
Gaus Width: 3.3182



B1L101S, U6-ch109, adc2

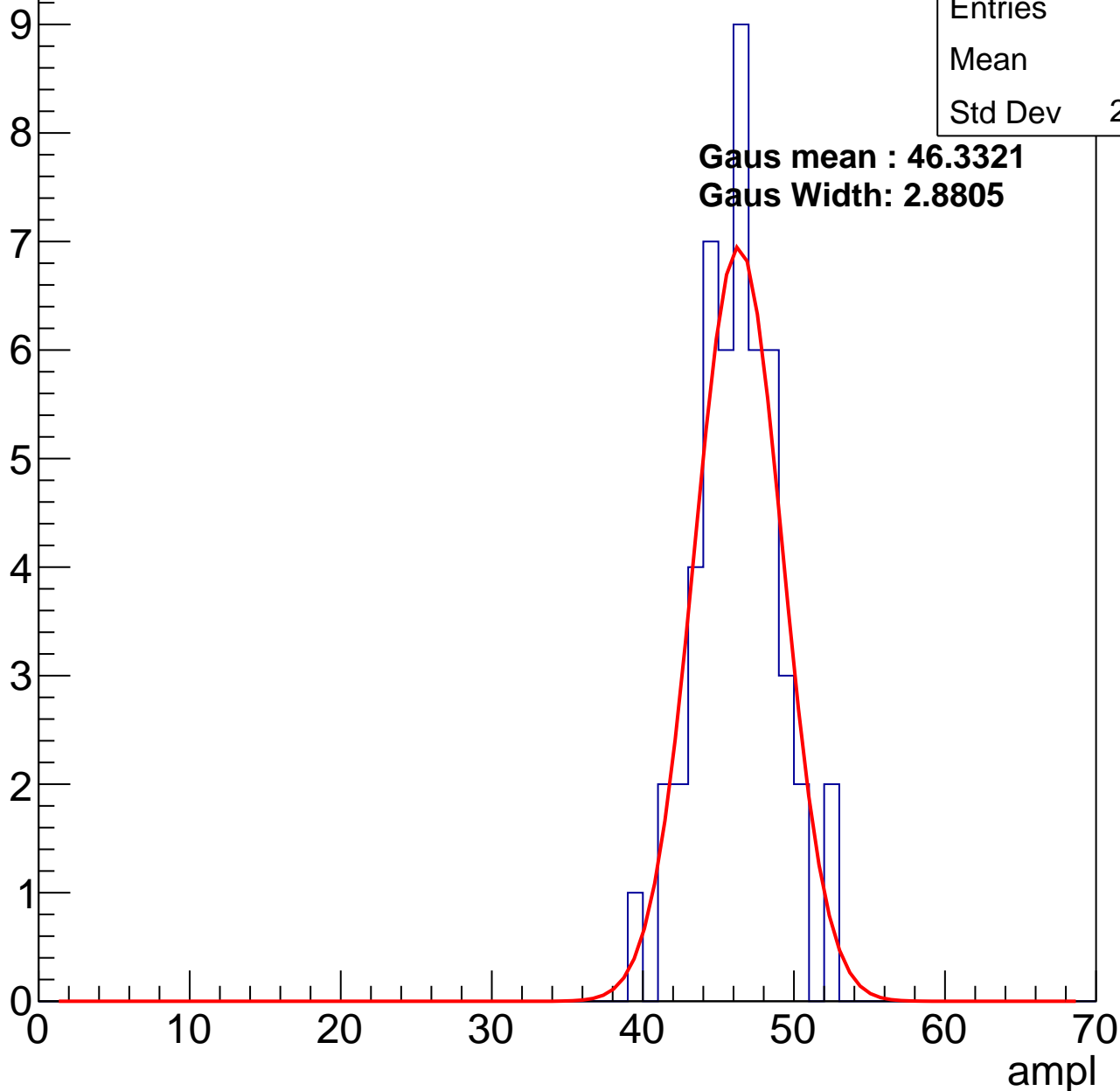
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	45.8
Std Dev	2.683

Gaus mean : 46.3321

Gaus Width: 2.8805

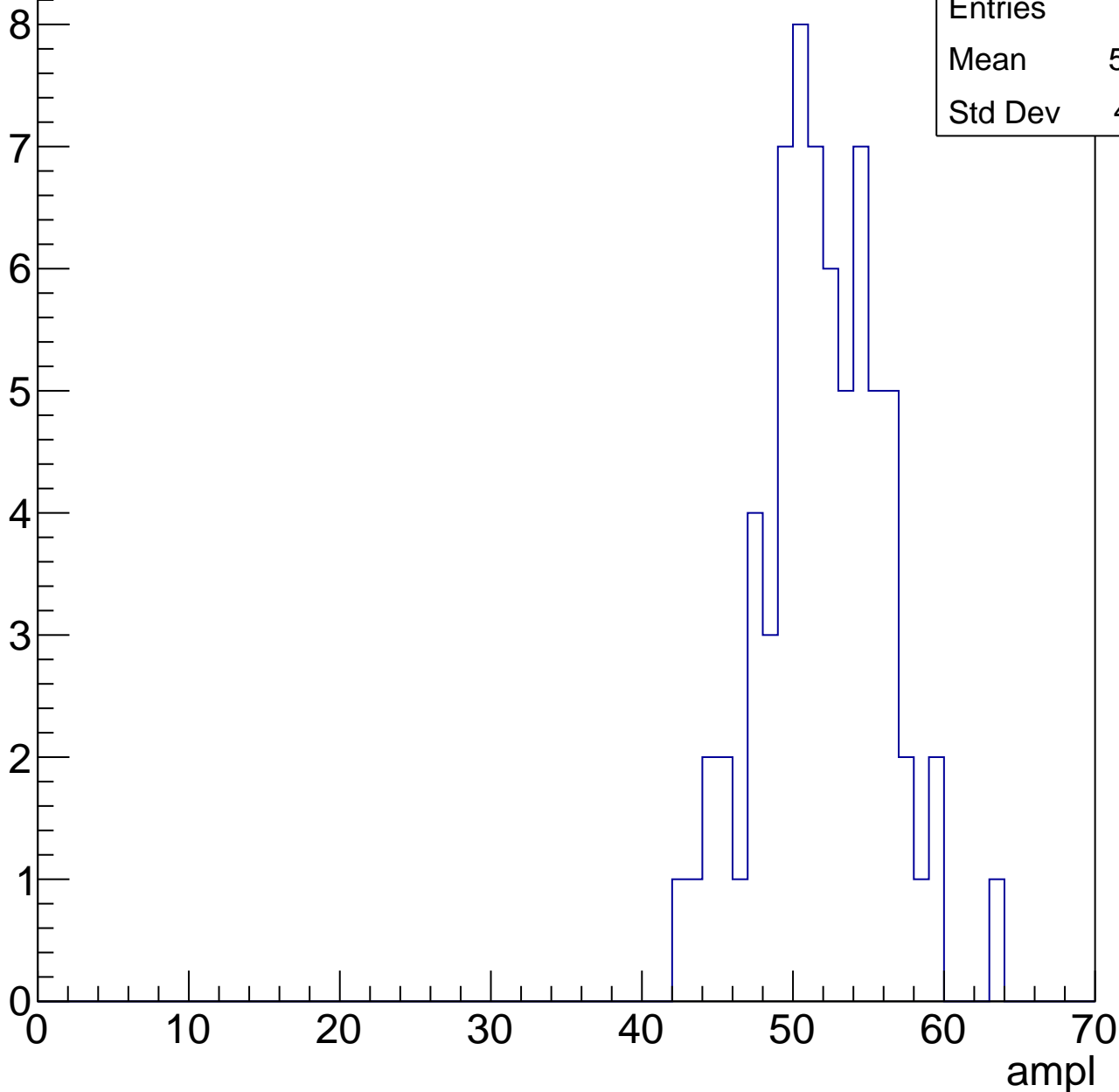


B1L101S, U6-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

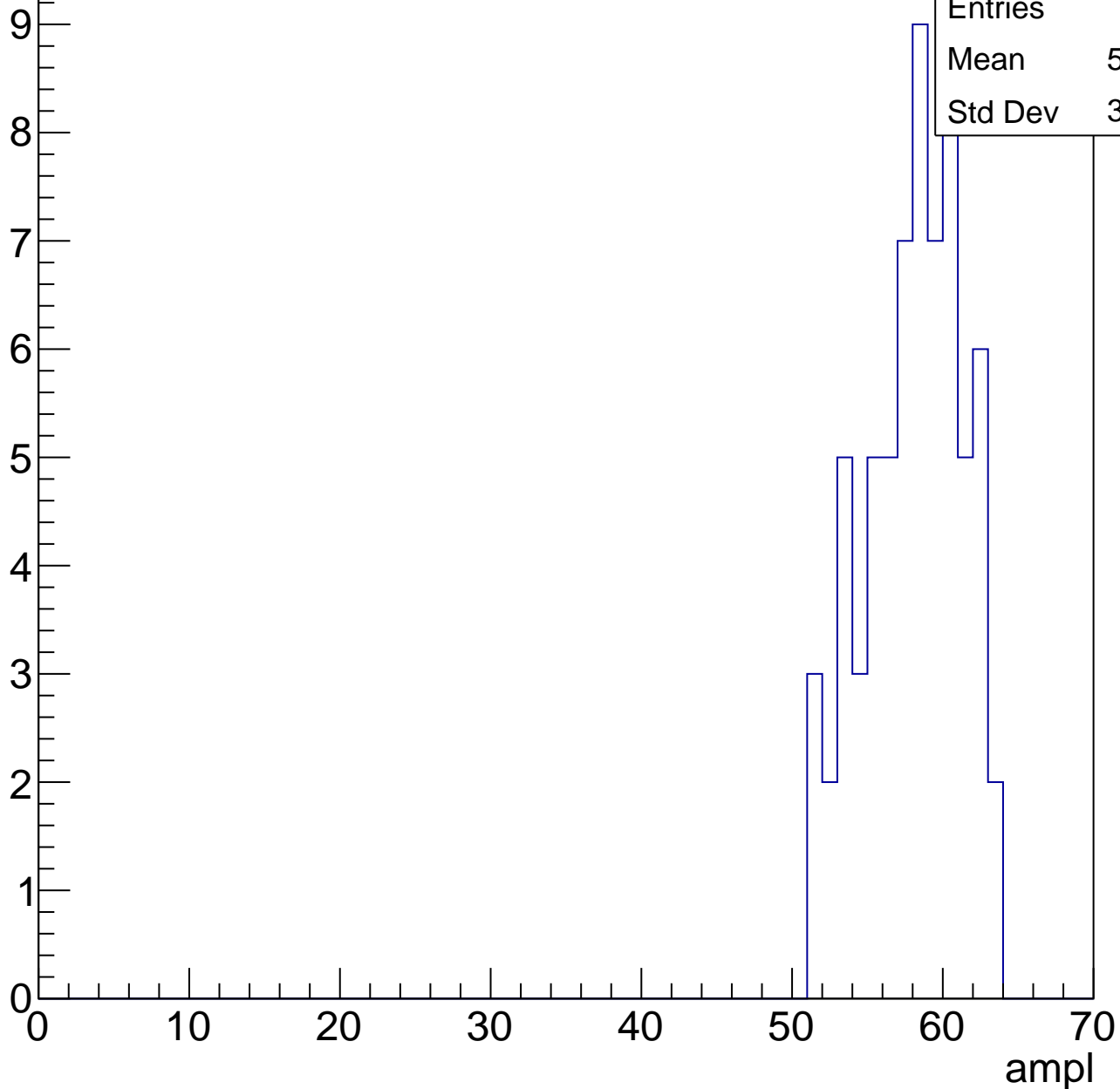
Entries	70
Mean	51.49
Std Dev	4.031



B1L101S, U6-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



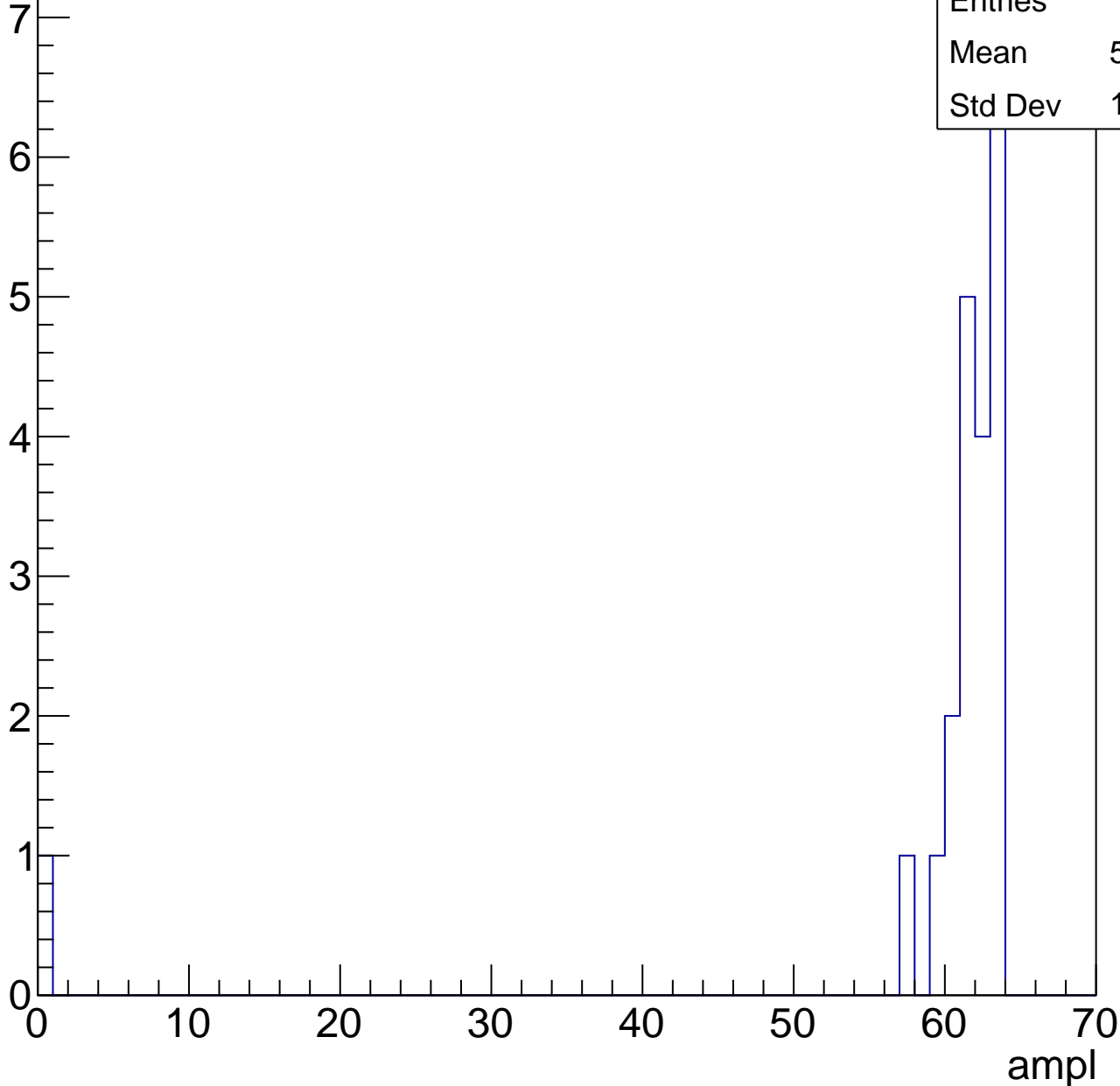
Entries	67
Mean	57.55
Std Dev	3.187

B1L101S, U6-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.57
Std Dev	13.19



B1L101S, U6-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U6-ch110, adc0

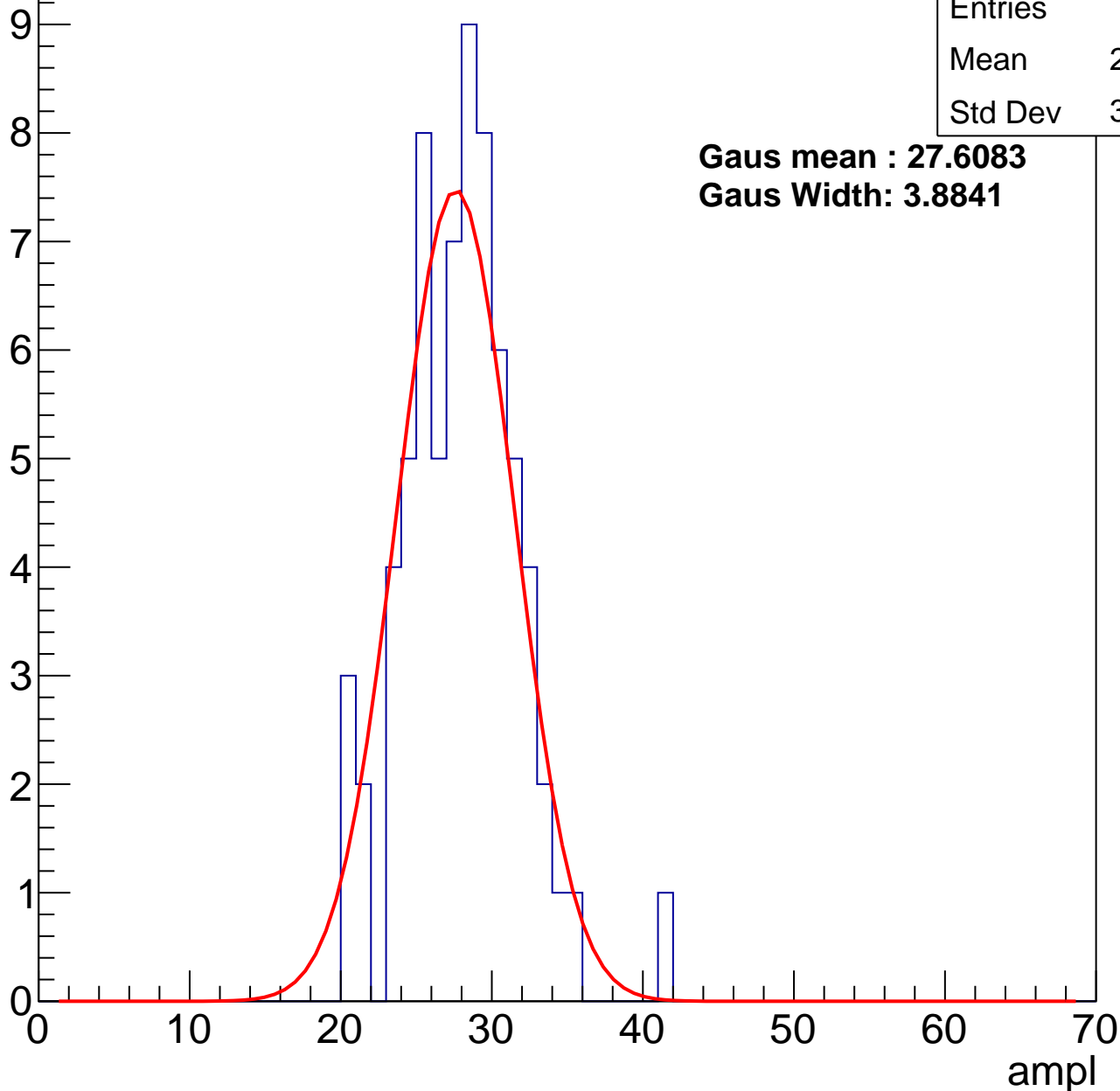
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	27.55
Std Dev	3.752

Gaus mean : 27.6083

Gaus Width: 3.8841



B1L101S, U6-ch110, adc1

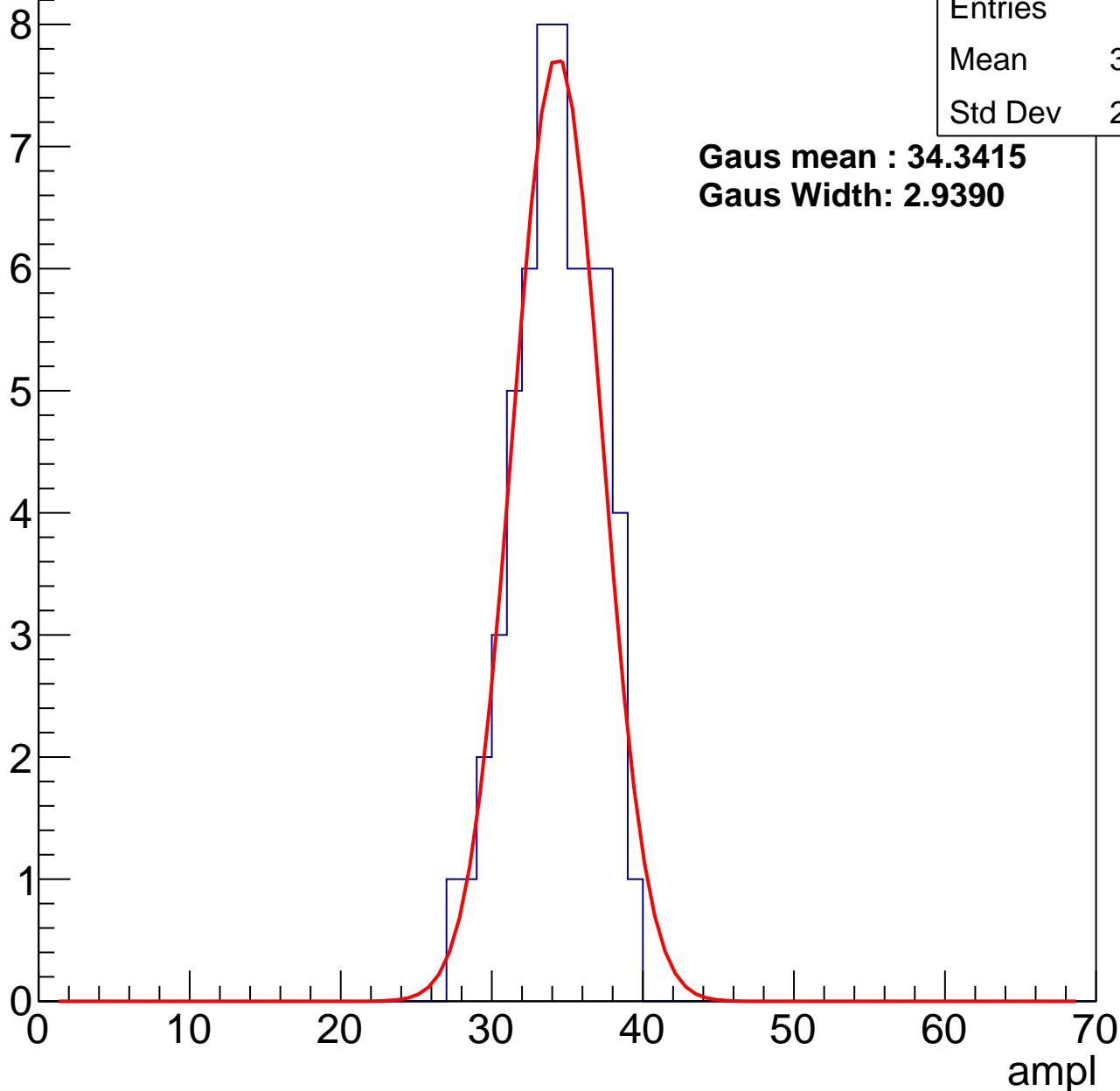
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	33.77
Std Dev	2.747

Gaus mean : 34.3415

Gaus Width: 2.9390



B1L101S, U6-ch110, adc2

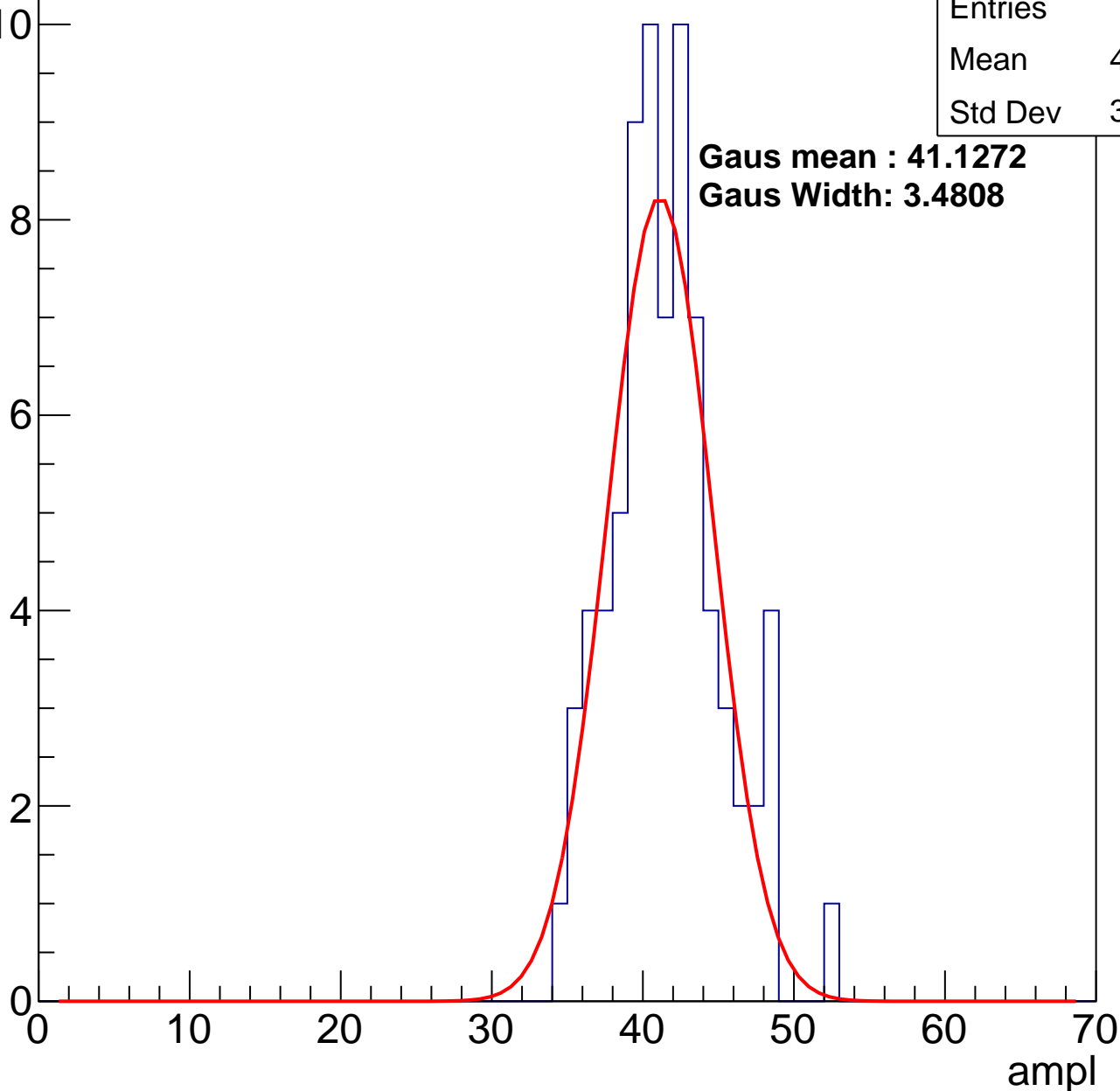
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	41.07
Std Dev	3.577

Gaus mean : 41.1272

Gaus Width: 3.4808

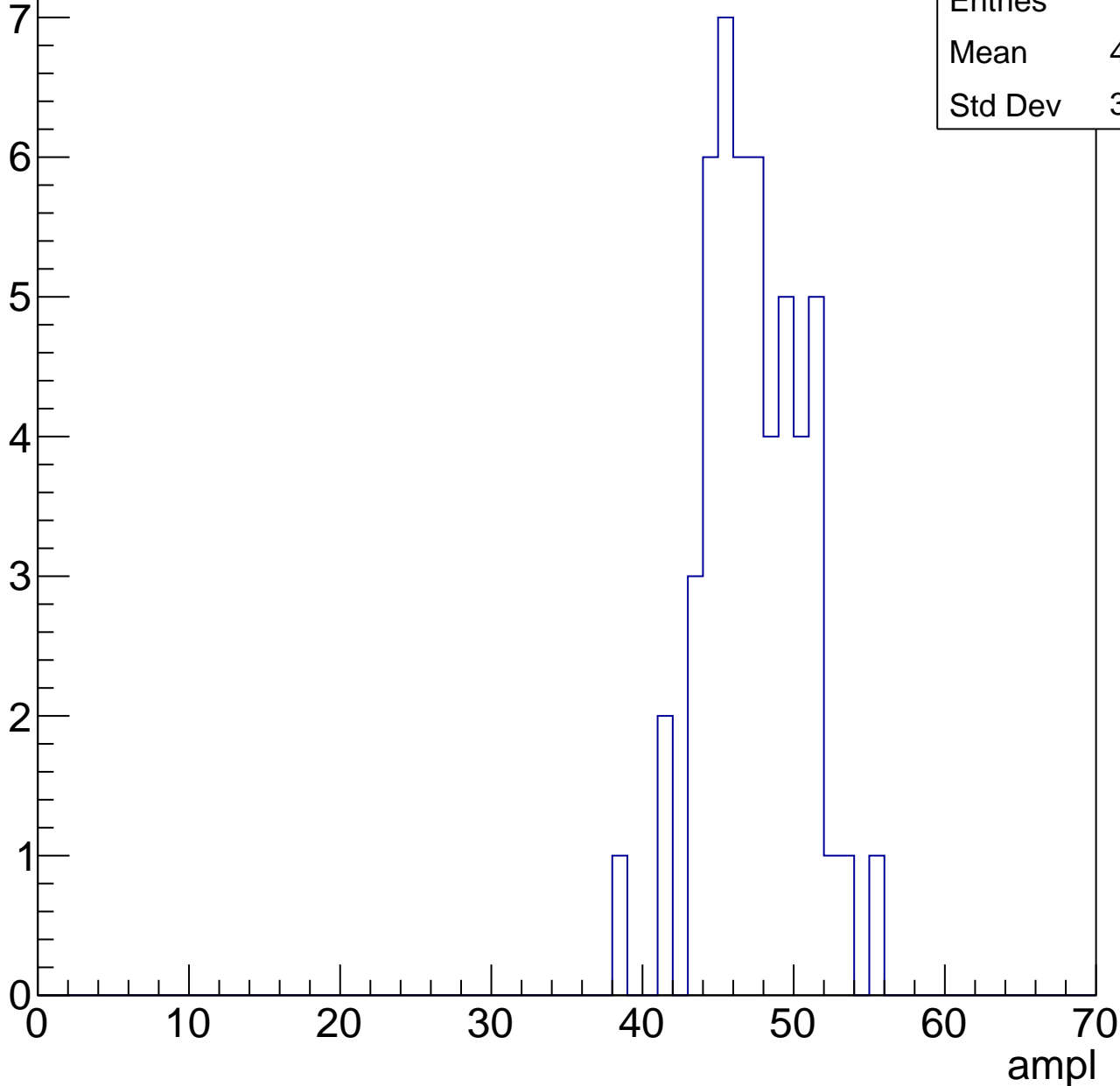


B1L101S, U6-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	46.88
Std Dev	3.262

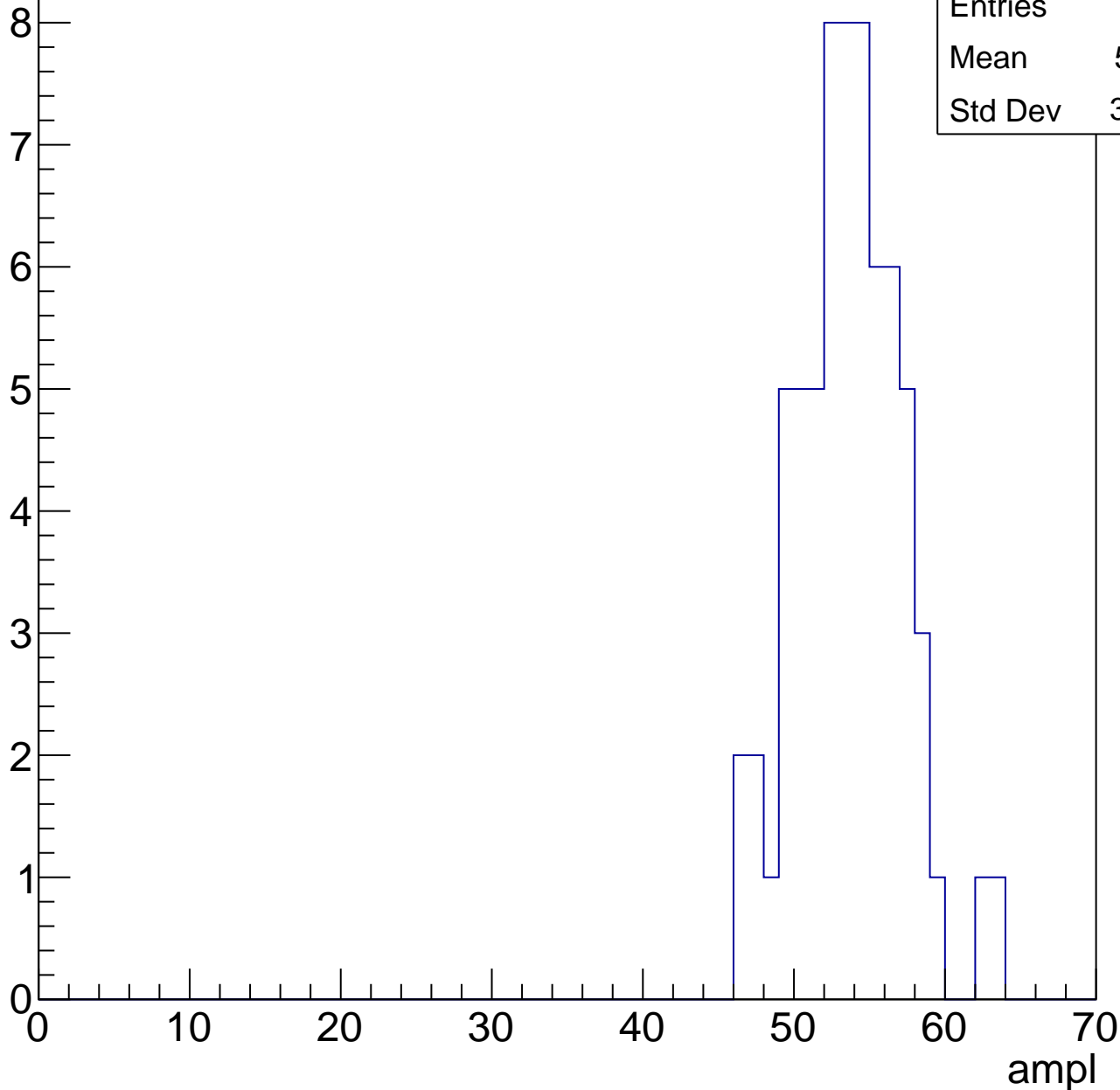


B1L101S, U6-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	53.21
Std Dev	3.462

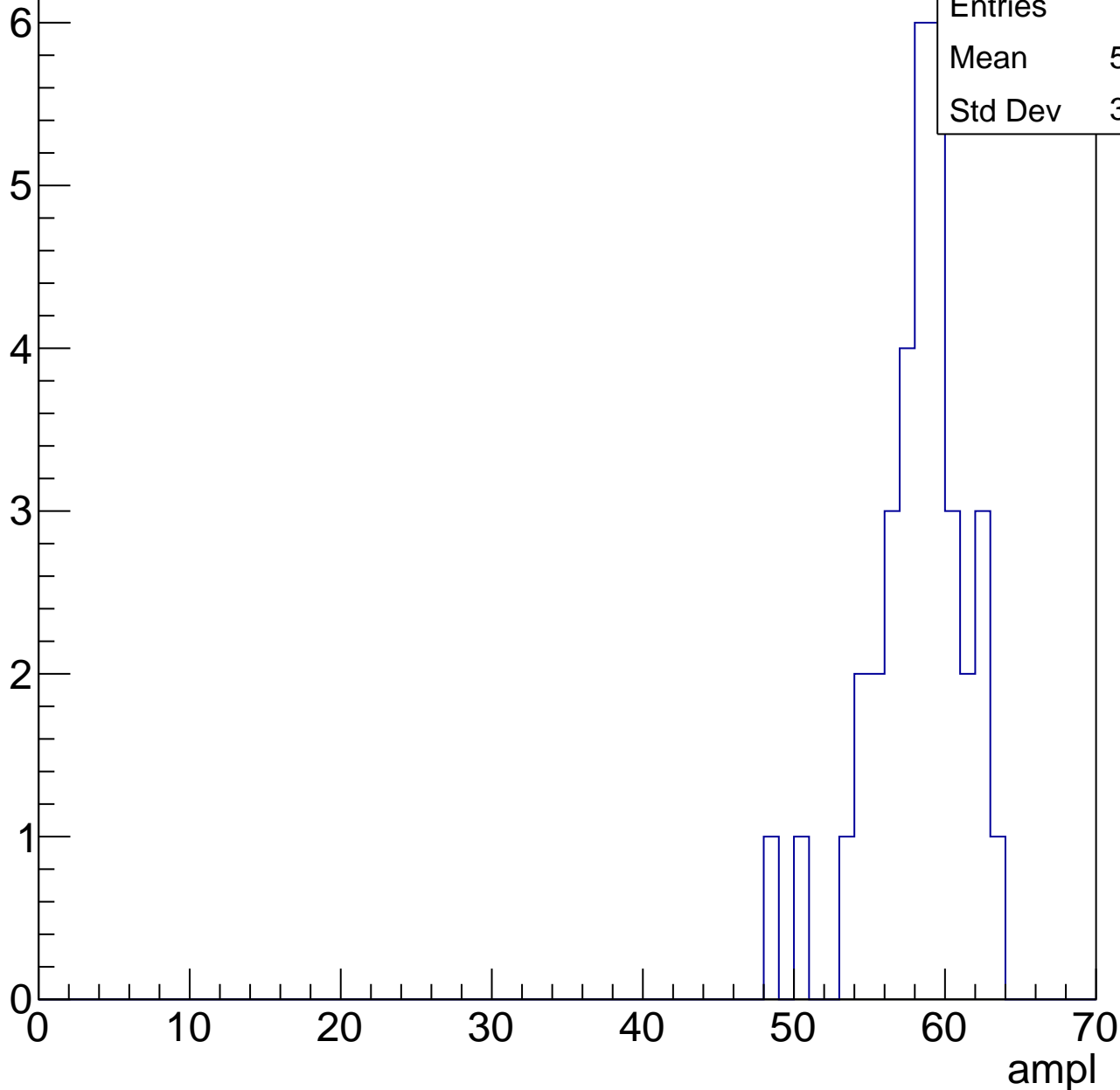


B1L101S, U6-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	57.66
Std Dev	3.207



B1L101S, U6-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.59
Std Dev	1.998

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L101S, U6-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

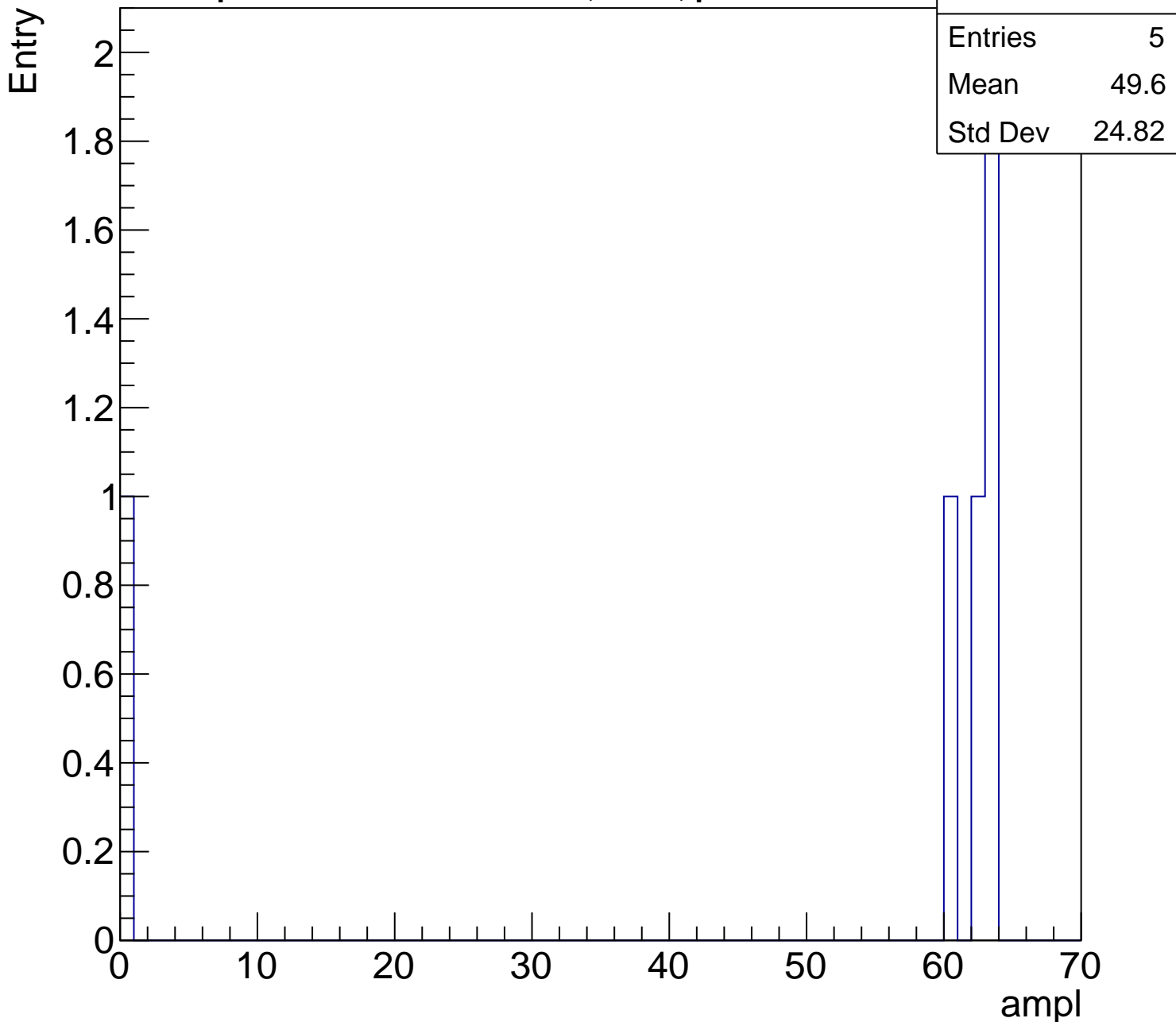
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl



B1L101S, U6-ch111, adc0

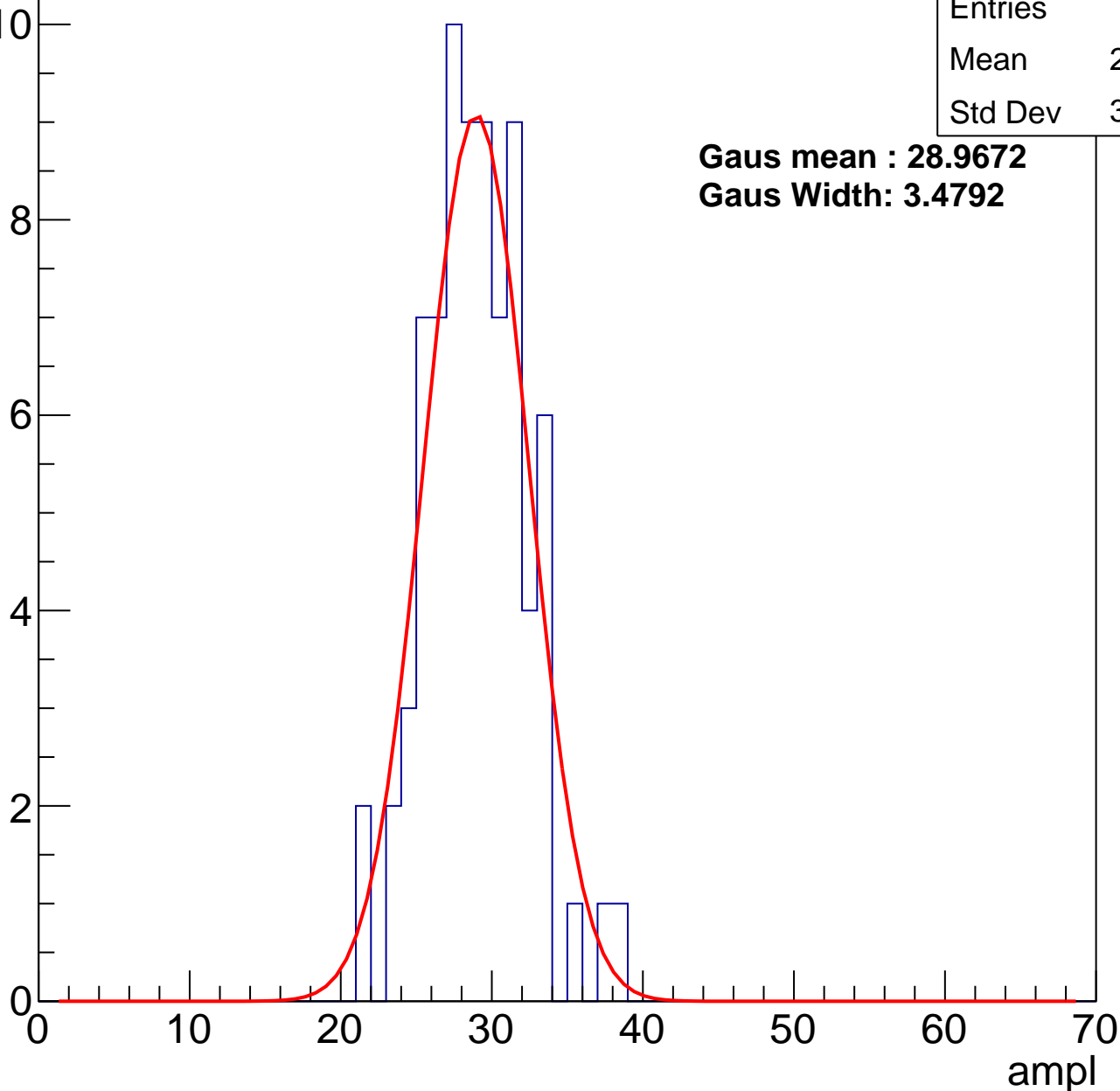
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.53
Std Dev	3.277

Gaus mean : 28.9672

Gaus Width: 3.4792



B1L101S, U6-ch111, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	36.48
Std Dev	3.599

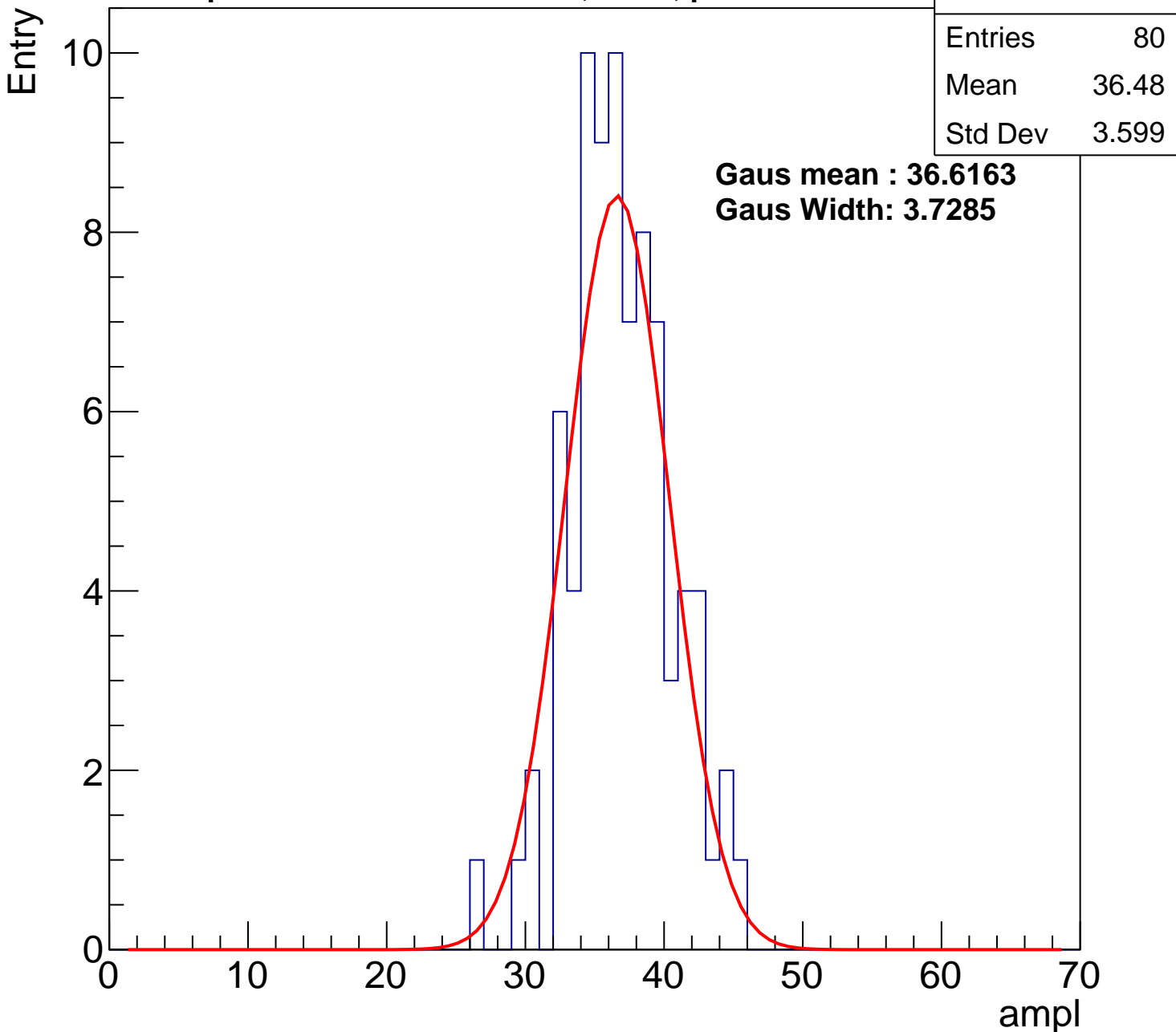
Gaus mean : 36.6163
Gaus Width: 3.7285

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch111, adc2

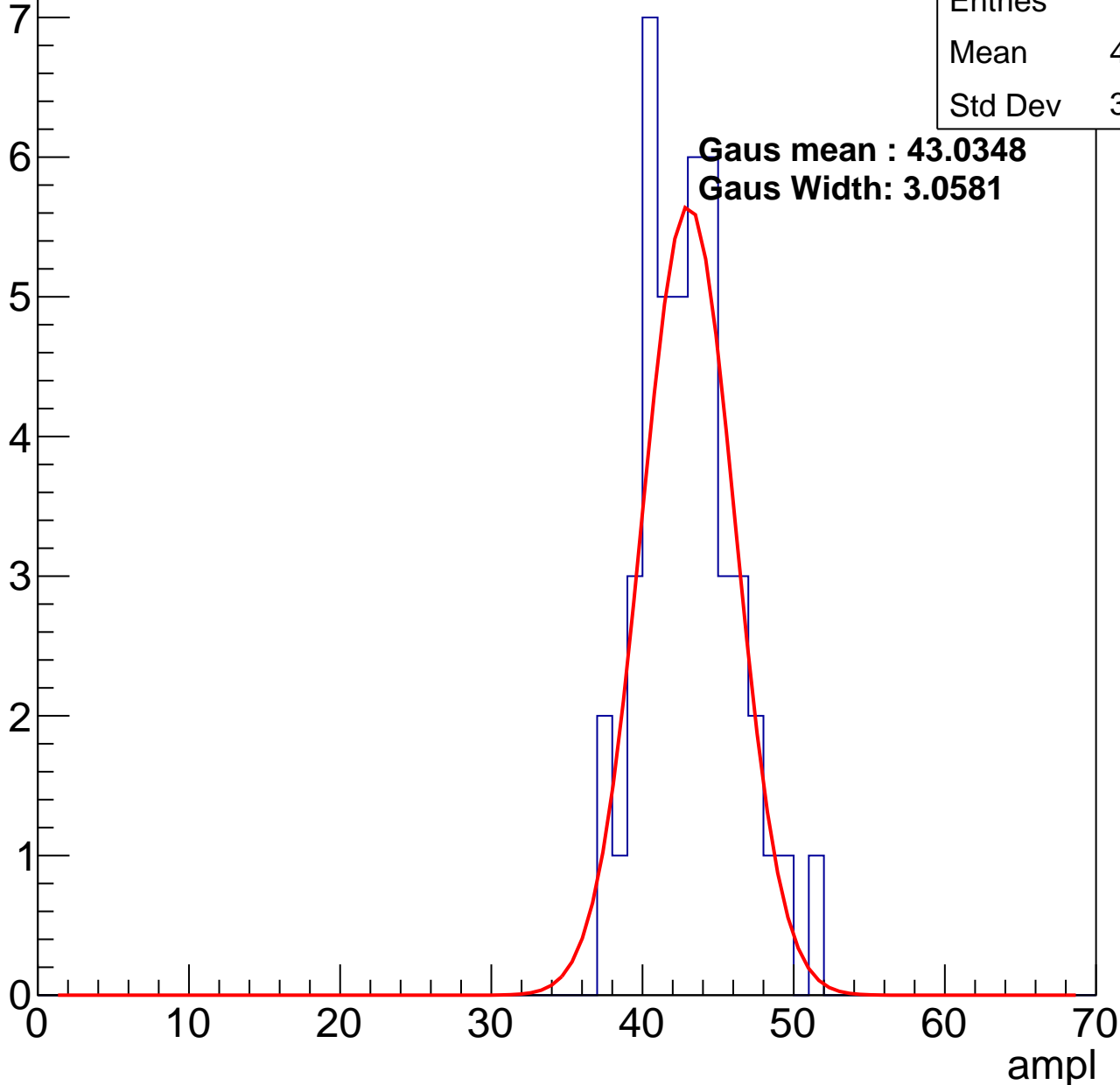
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	42.63
Std Dev	3.053

Gaus mean : 43.0348

Gaus Width: 3.0581

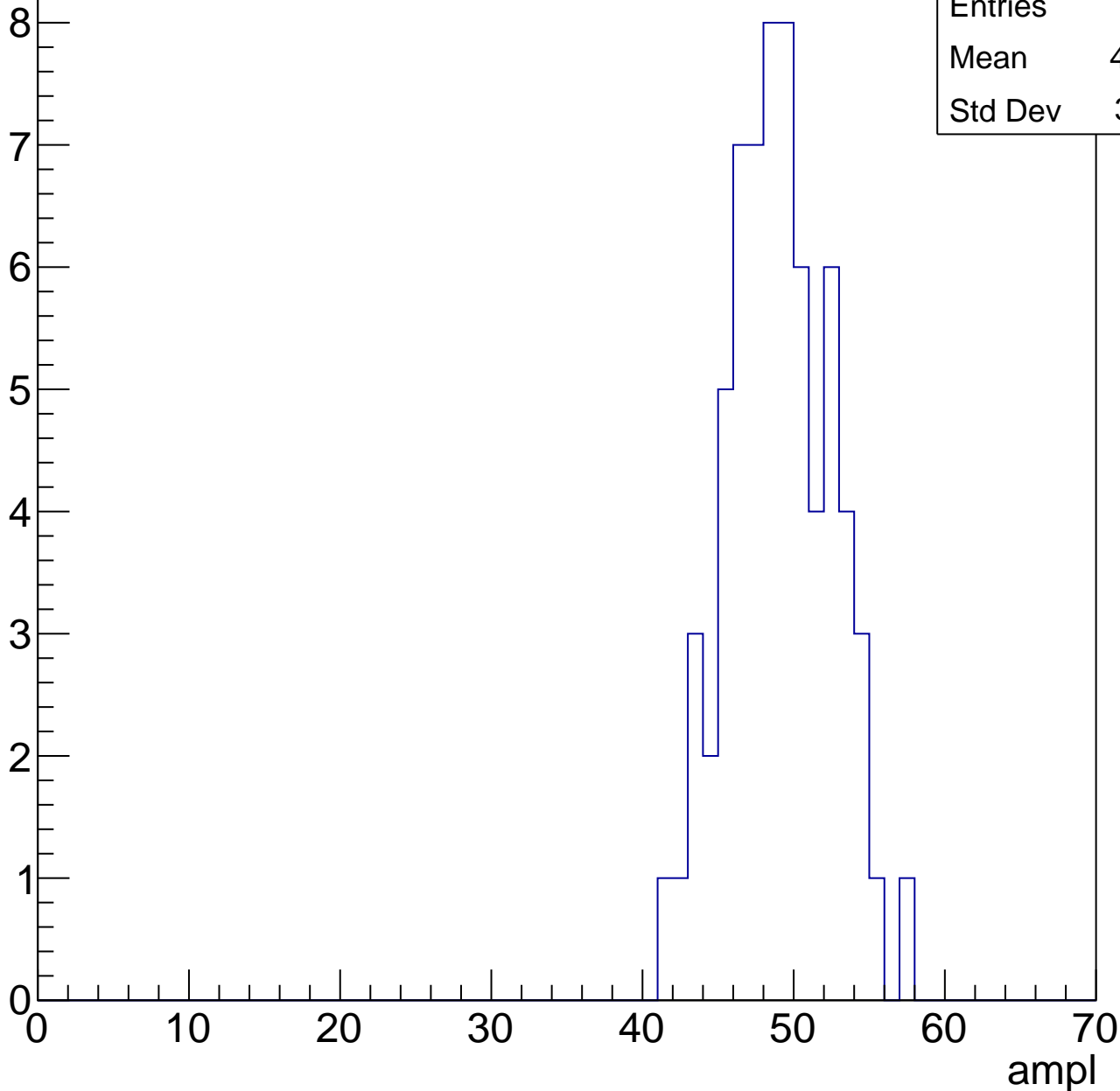


B1L101S, U6-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	48.57
Std Dev	3.351



B1L101S, U6-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	54.64
Std Dev	3.47

Entry

10

8

6

4

2

0

0

10

20

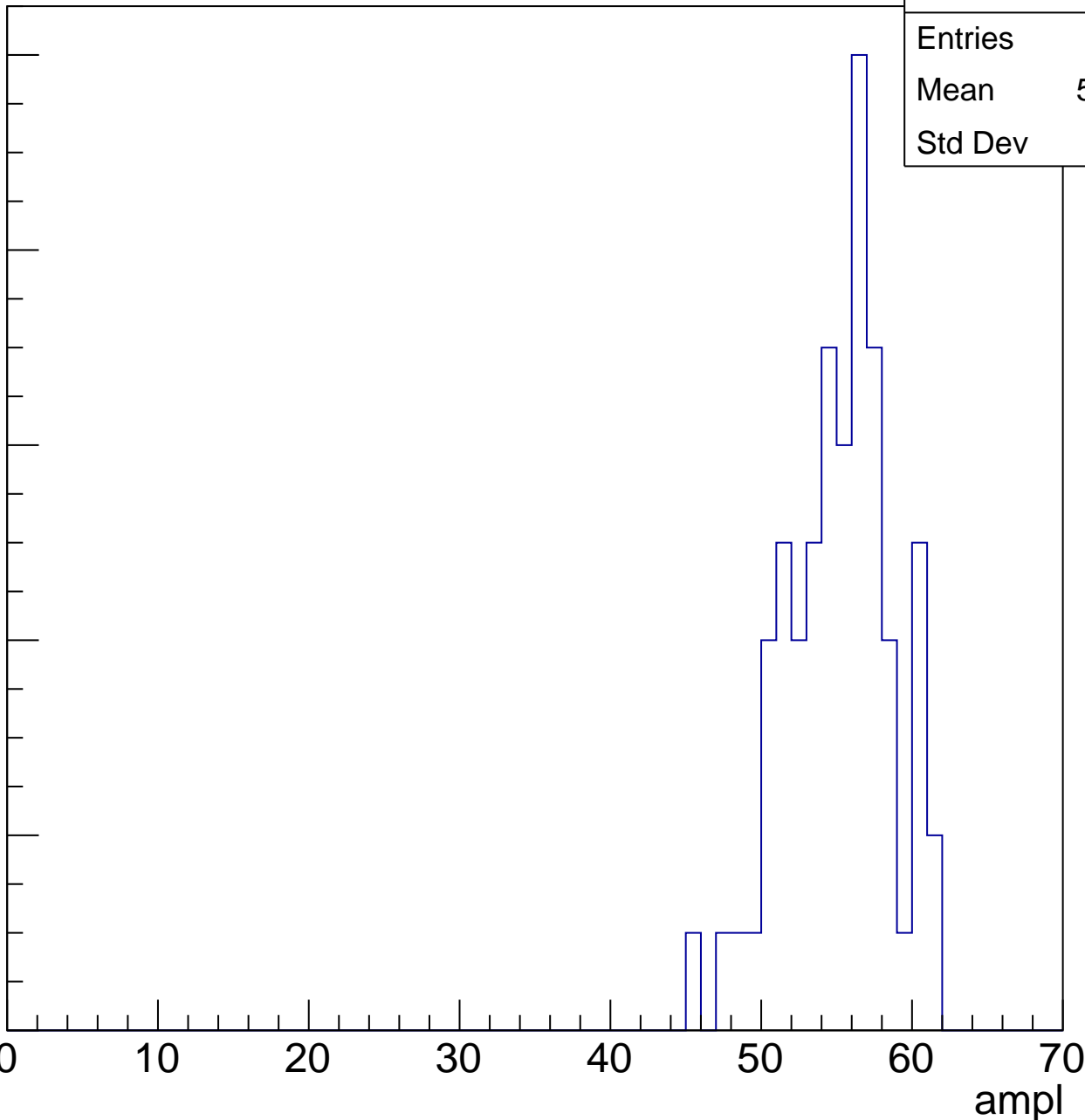
30

40

50

60

ampl



B1L101S, U6-ch111, adc5

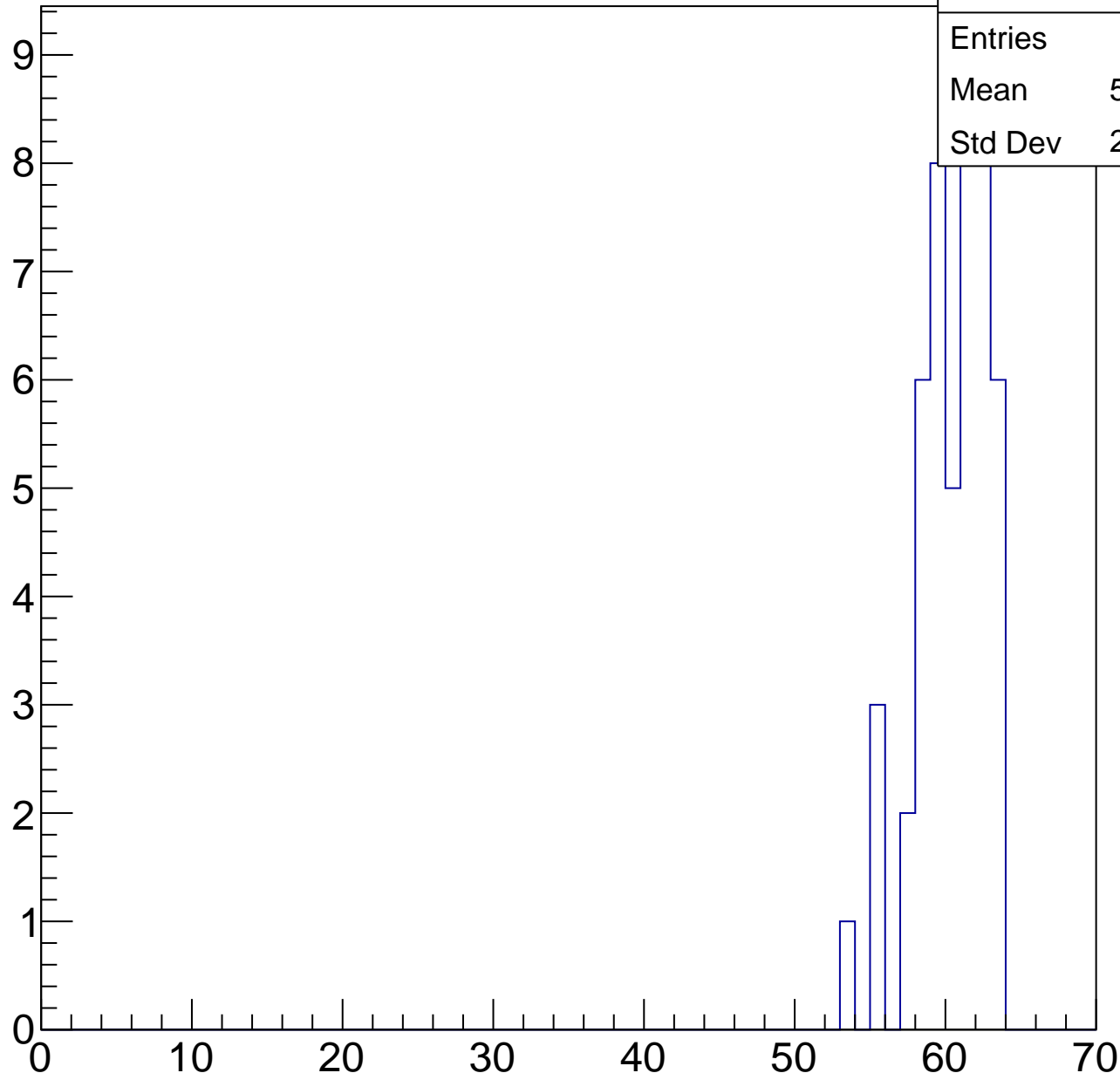
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.92
Std Dev	2.379

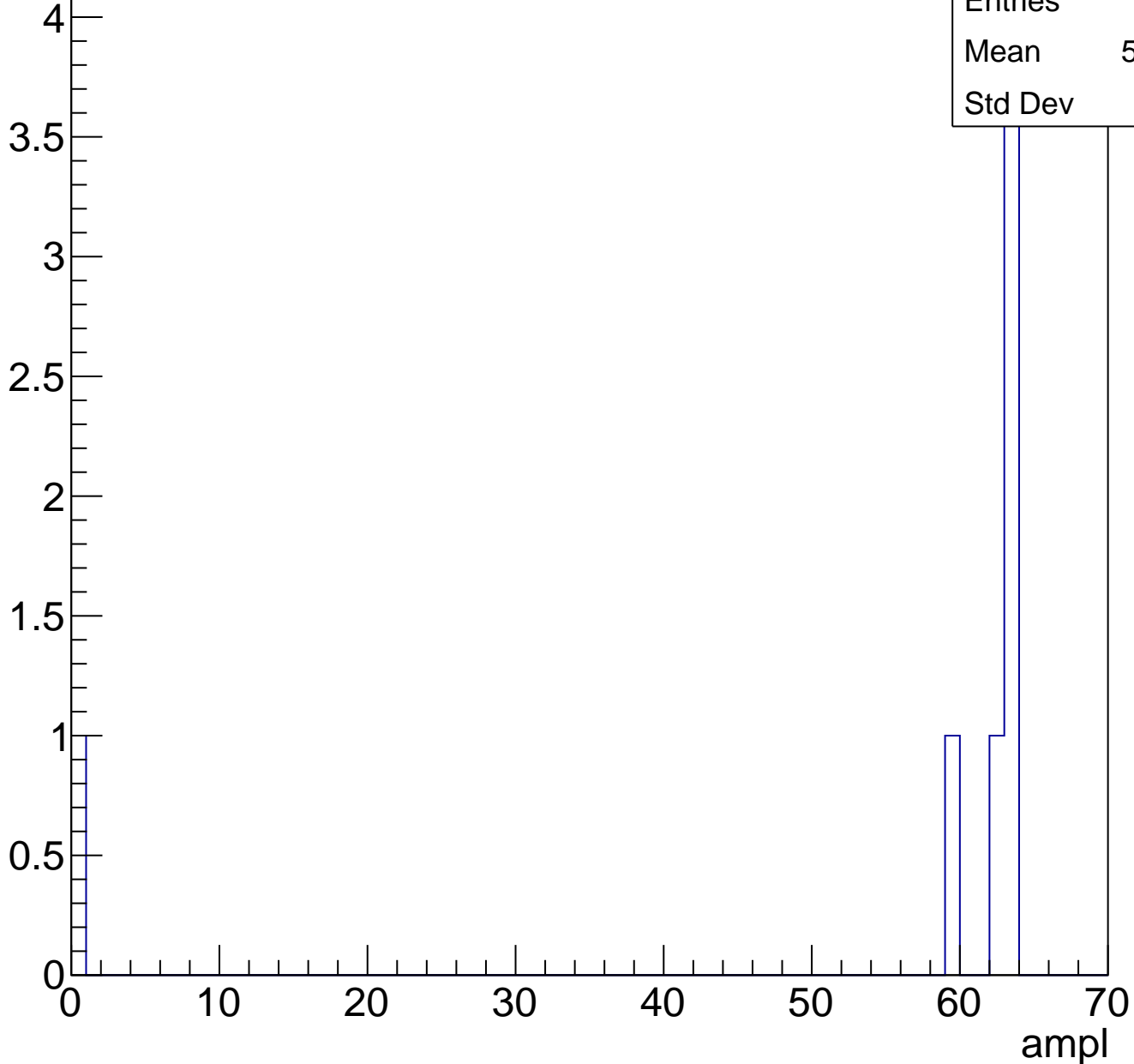
ampl



B1L101S, U6-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch112, adc0

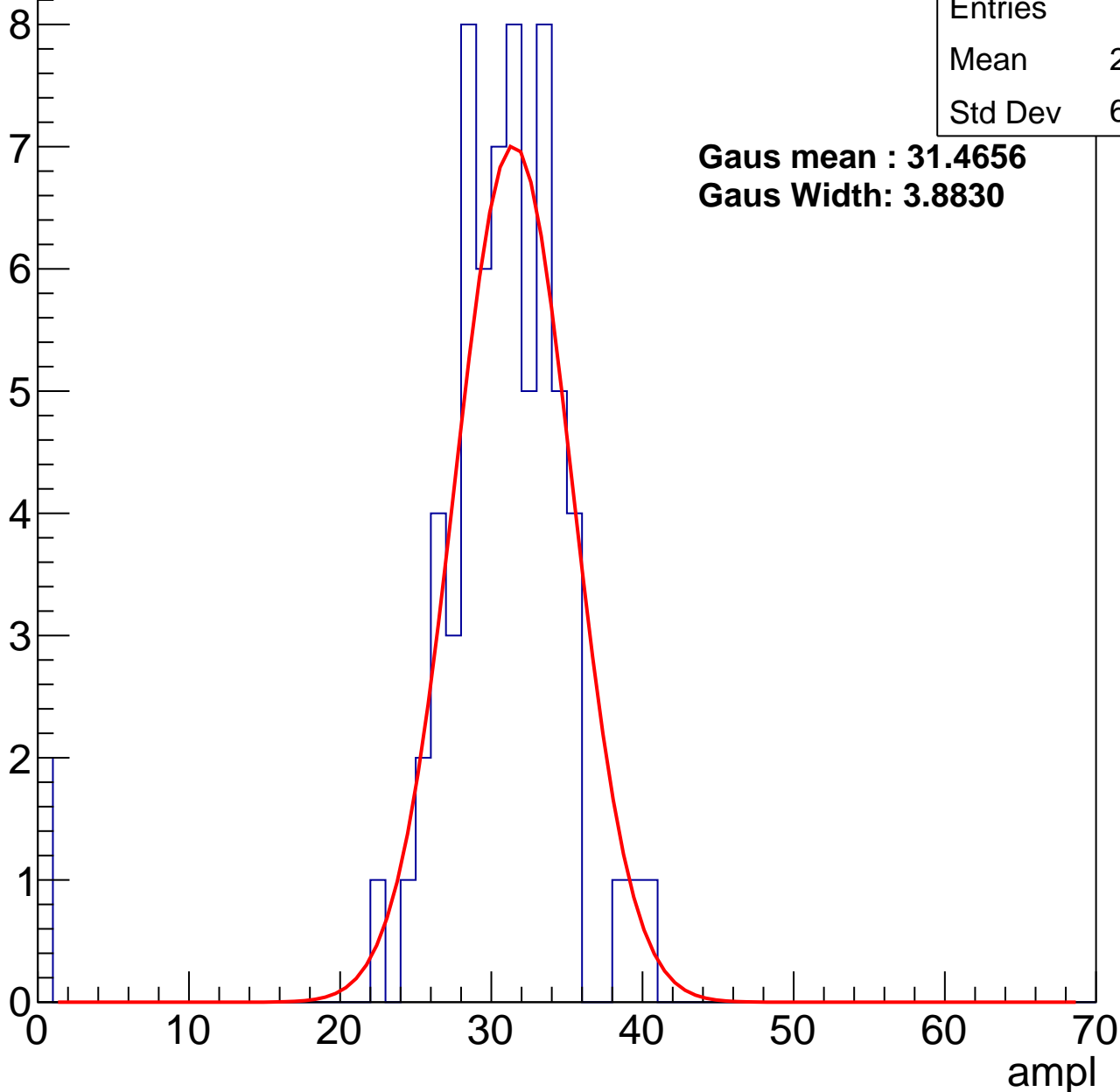
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.67
Std Dev	6.226

Gaus mean : 31.4656

Gaus Width: 3.8830



B1L101S, U6-ch112, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	37.05
Std Dev	4.157

Gaus mean : 37.5523

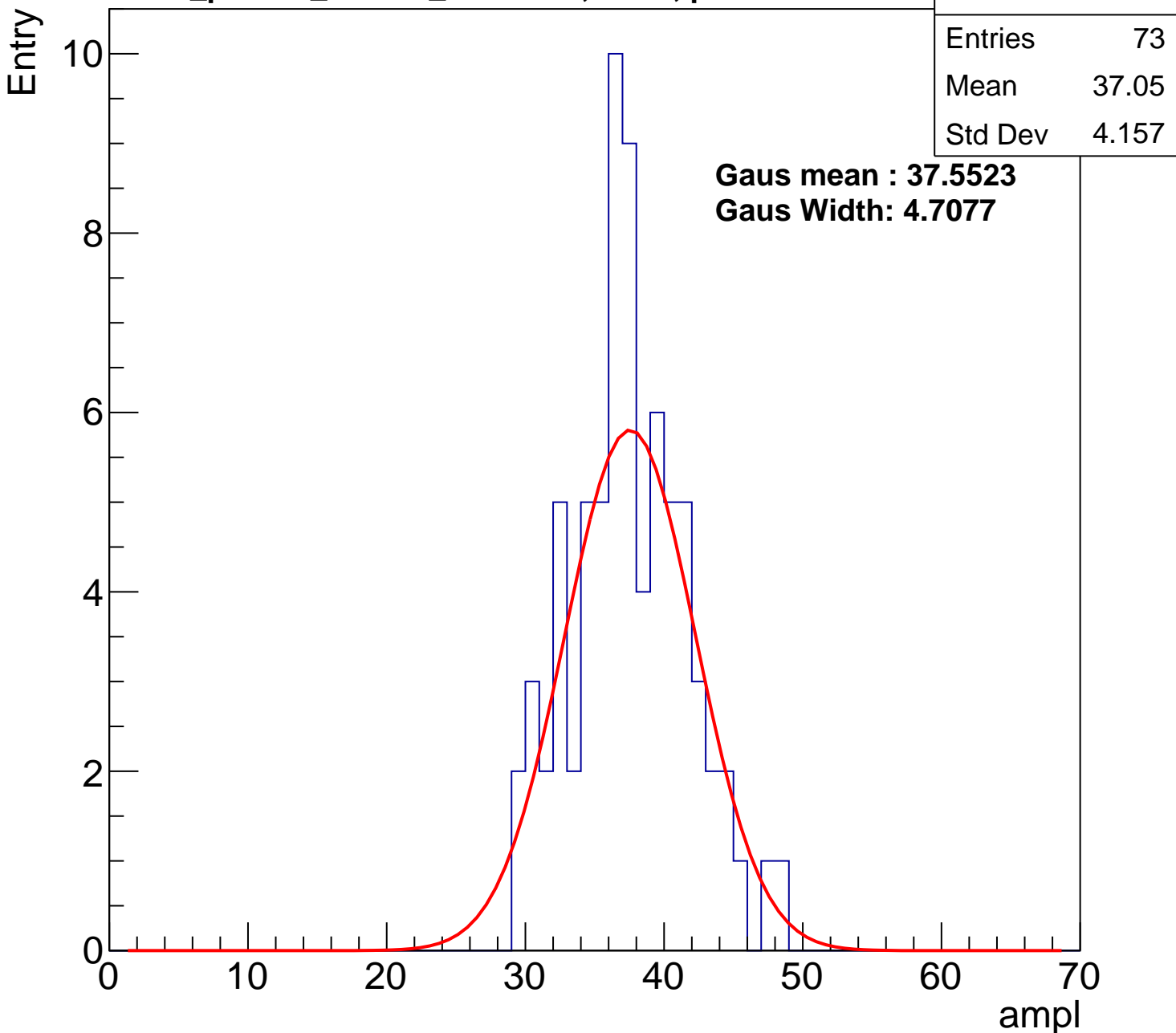
Gaus Width: 4.7077

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

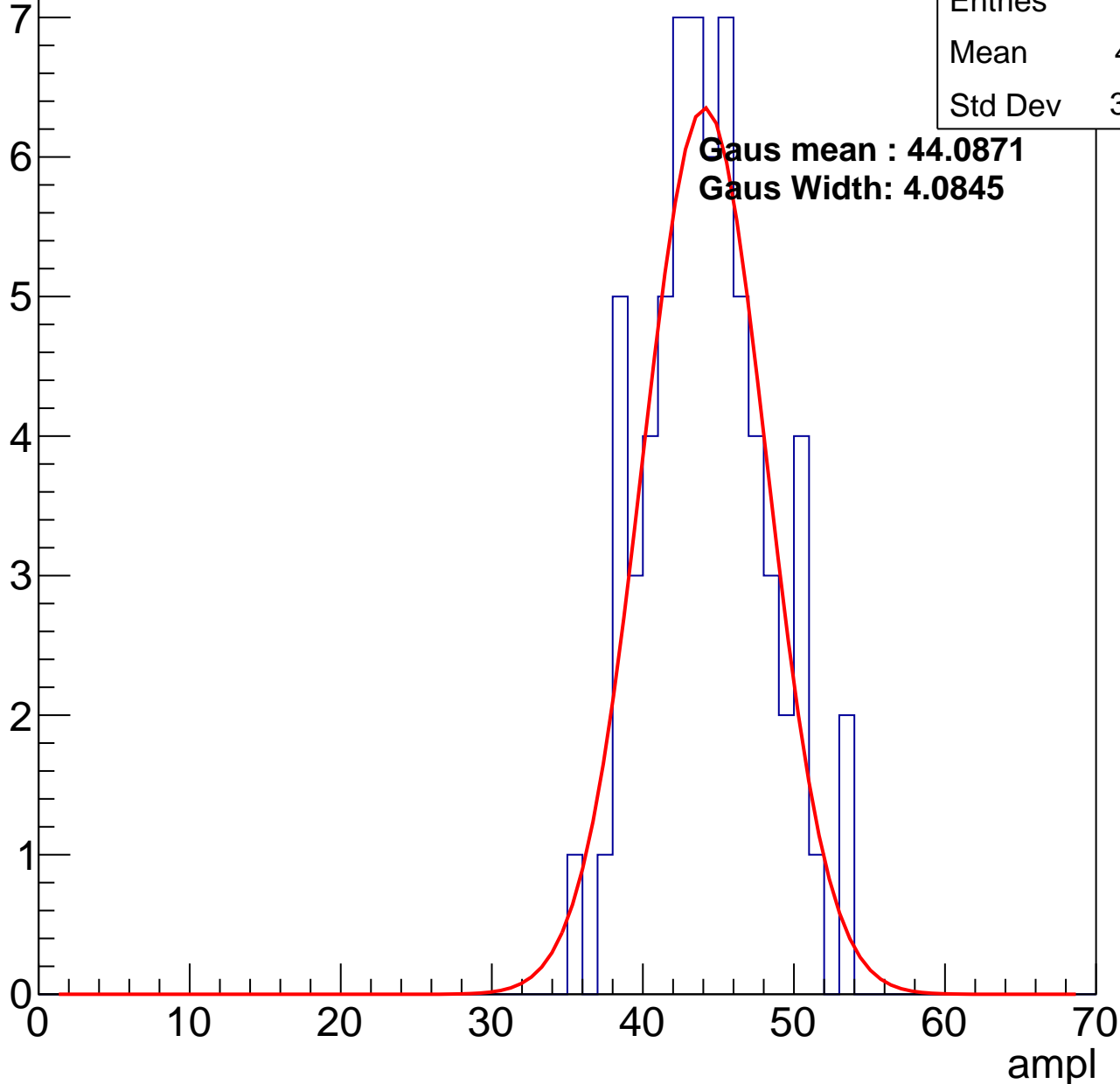


B1L101S, U6-ch112, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.81
Std Dev	3.945

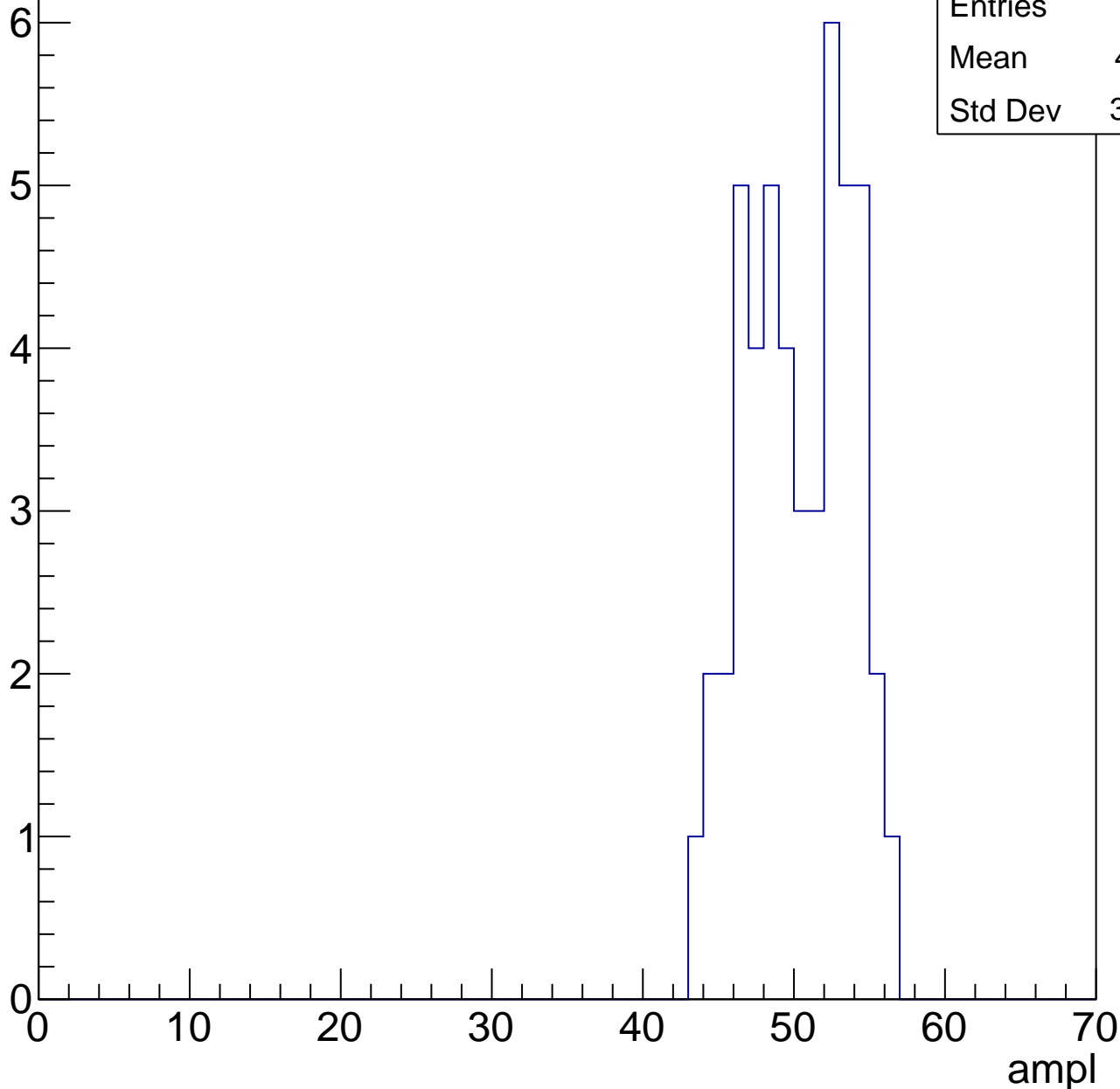


B1L101S, U6-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

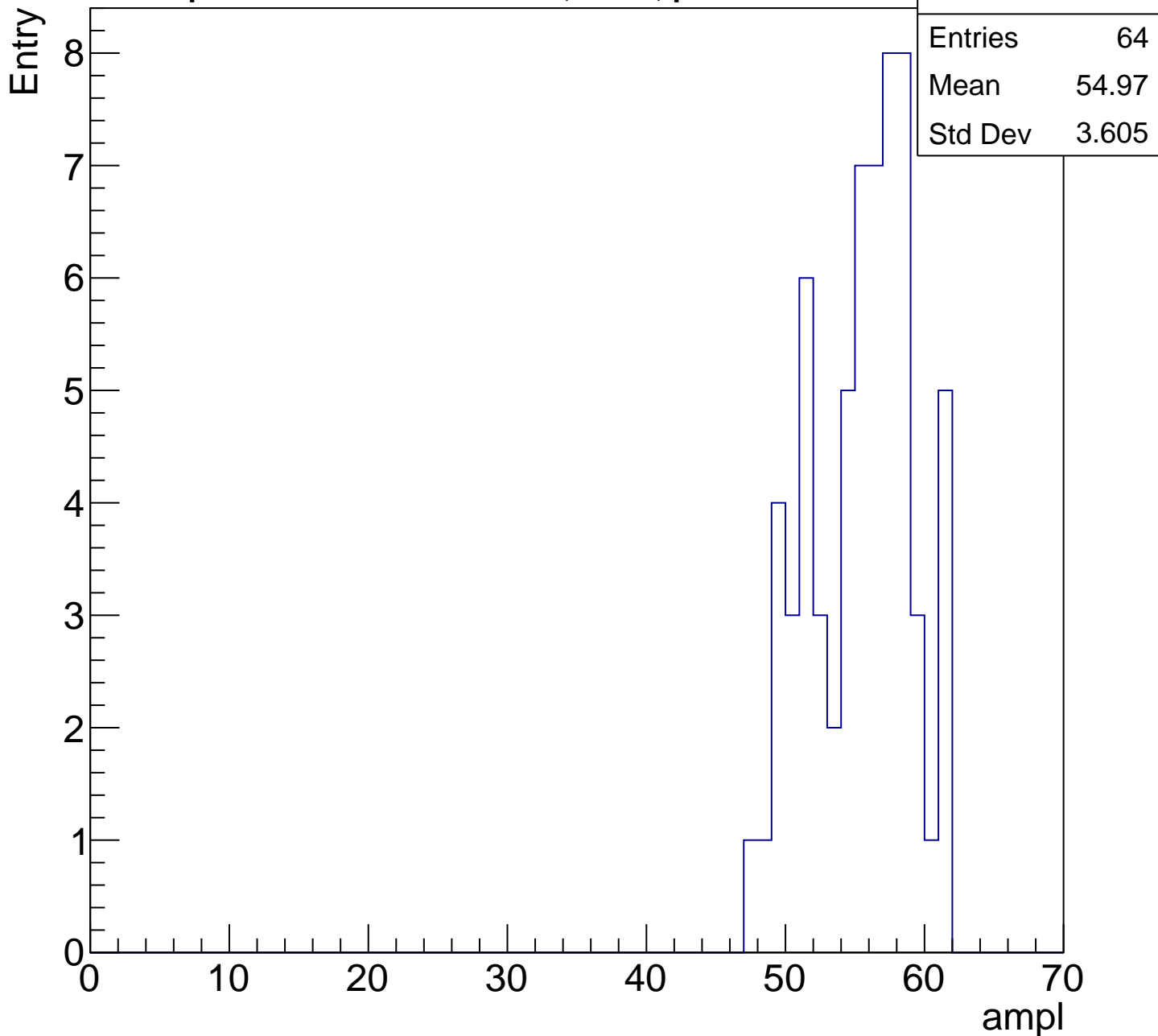
Entry

Entries	48
Mean	49.81
Std Dev	3.377



B1L101S, U6-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

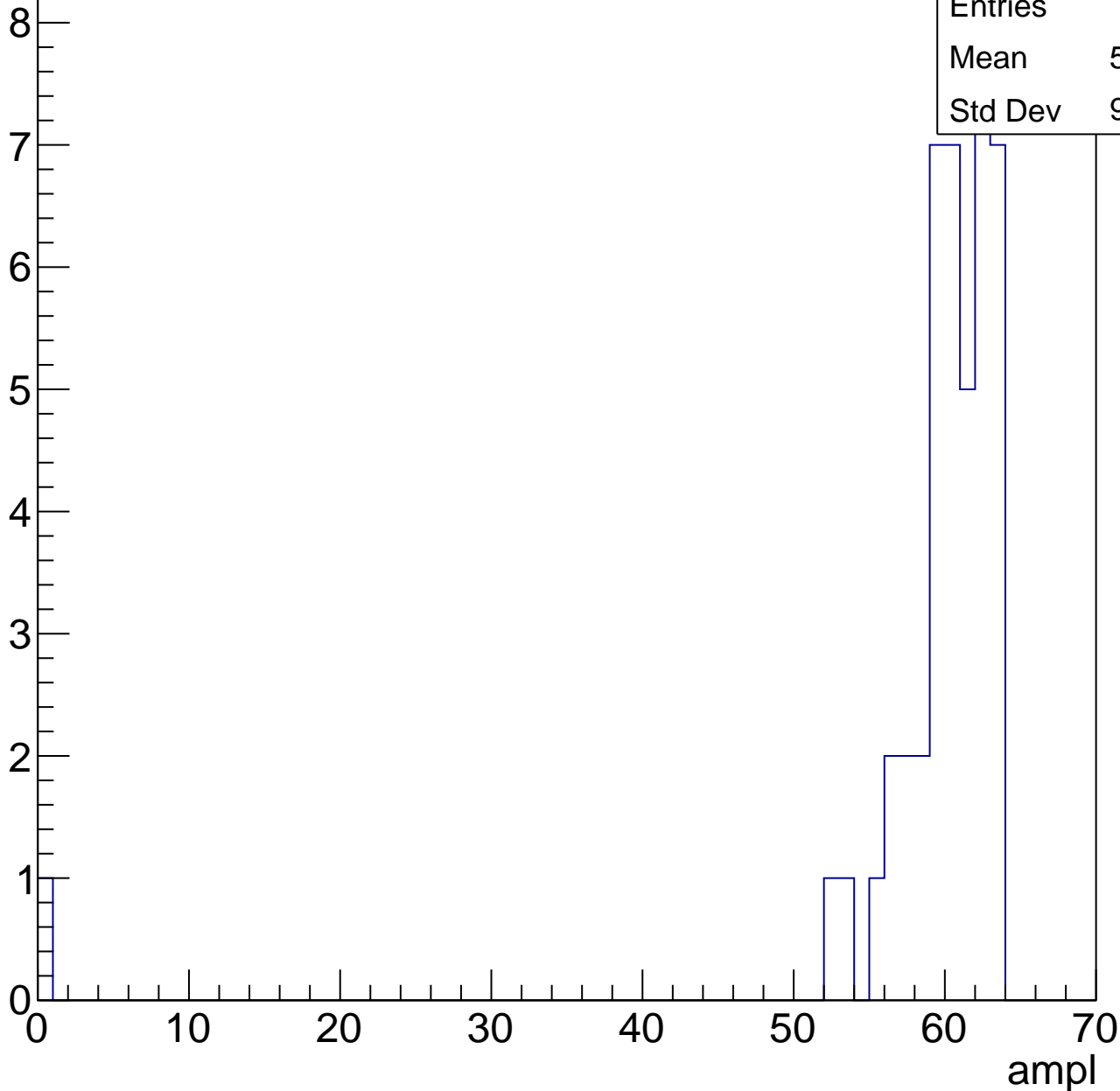


B1L101S, U6-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

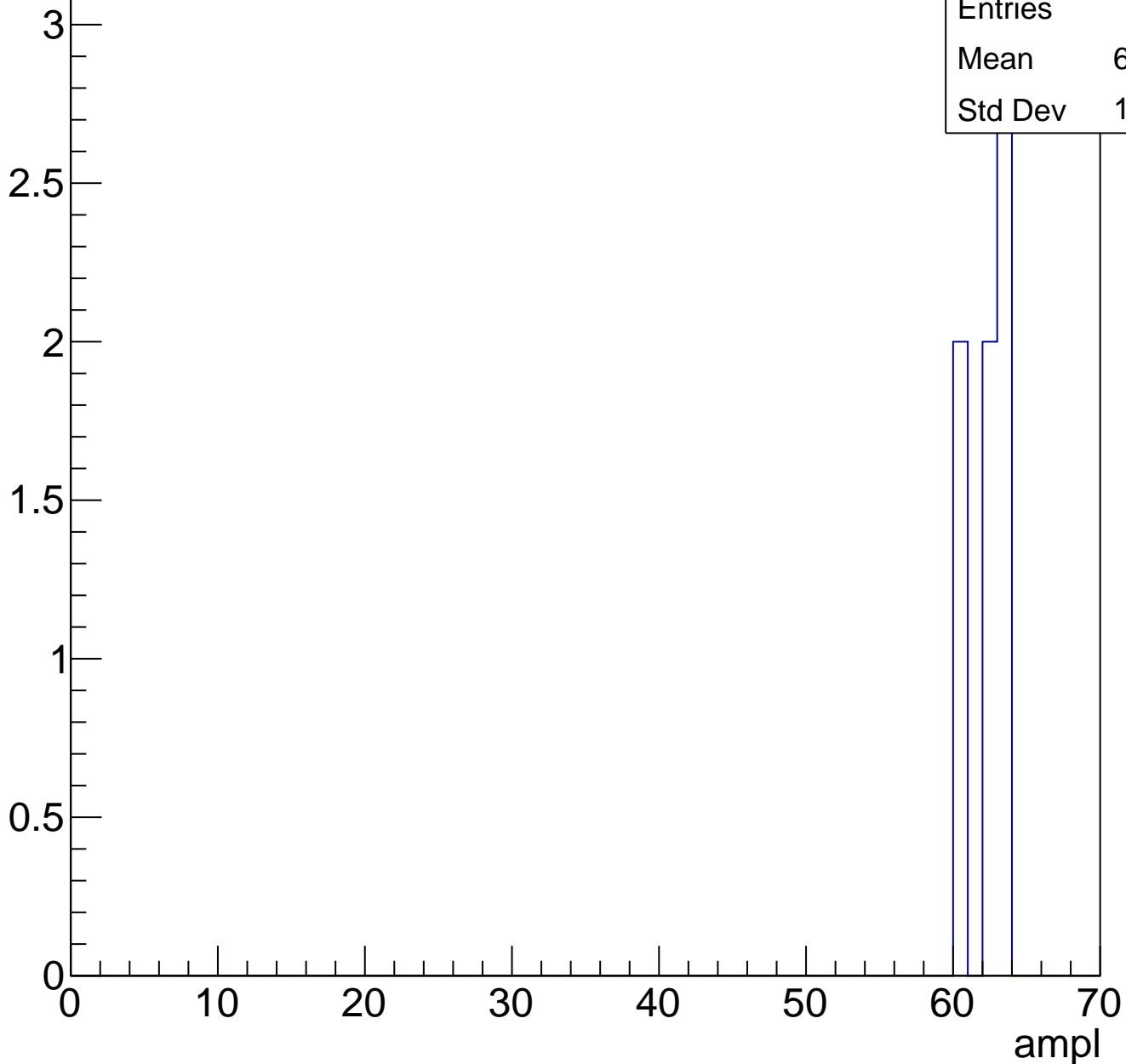
Entries	44
Mean	58.57
Std Dev	9.309



B1L101S, U6-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch113, adc0

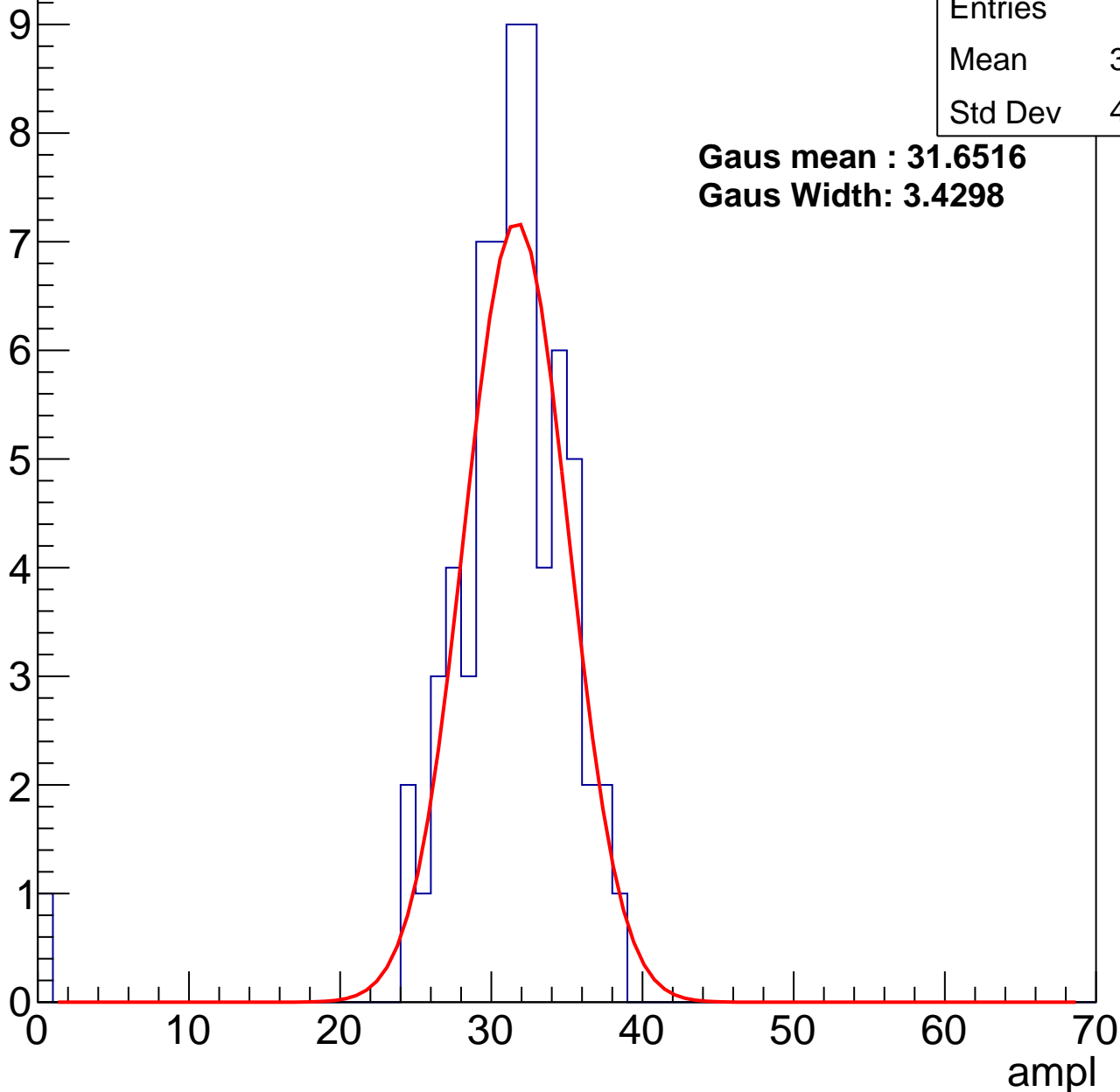
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.58
Std Dev	4.945

Gaus mean : 31.6516

Gaus Width: 3.4298



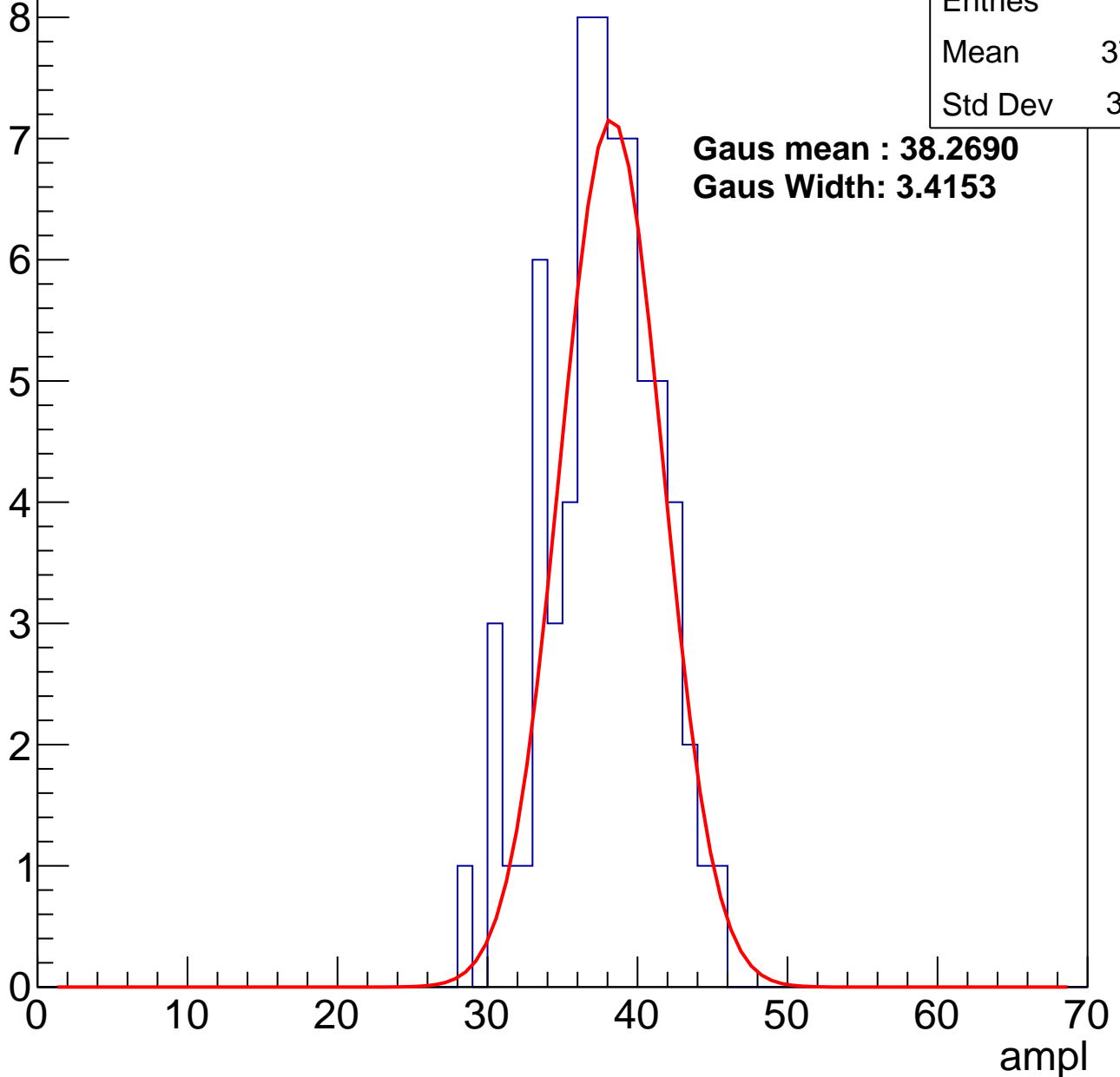
B1L101S, U6-ch113, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.19
Std Dev	3.621

Gaus mean : 38.2690
Gaus Width: 3.4153



B1L101S, U6-ch113, adc2

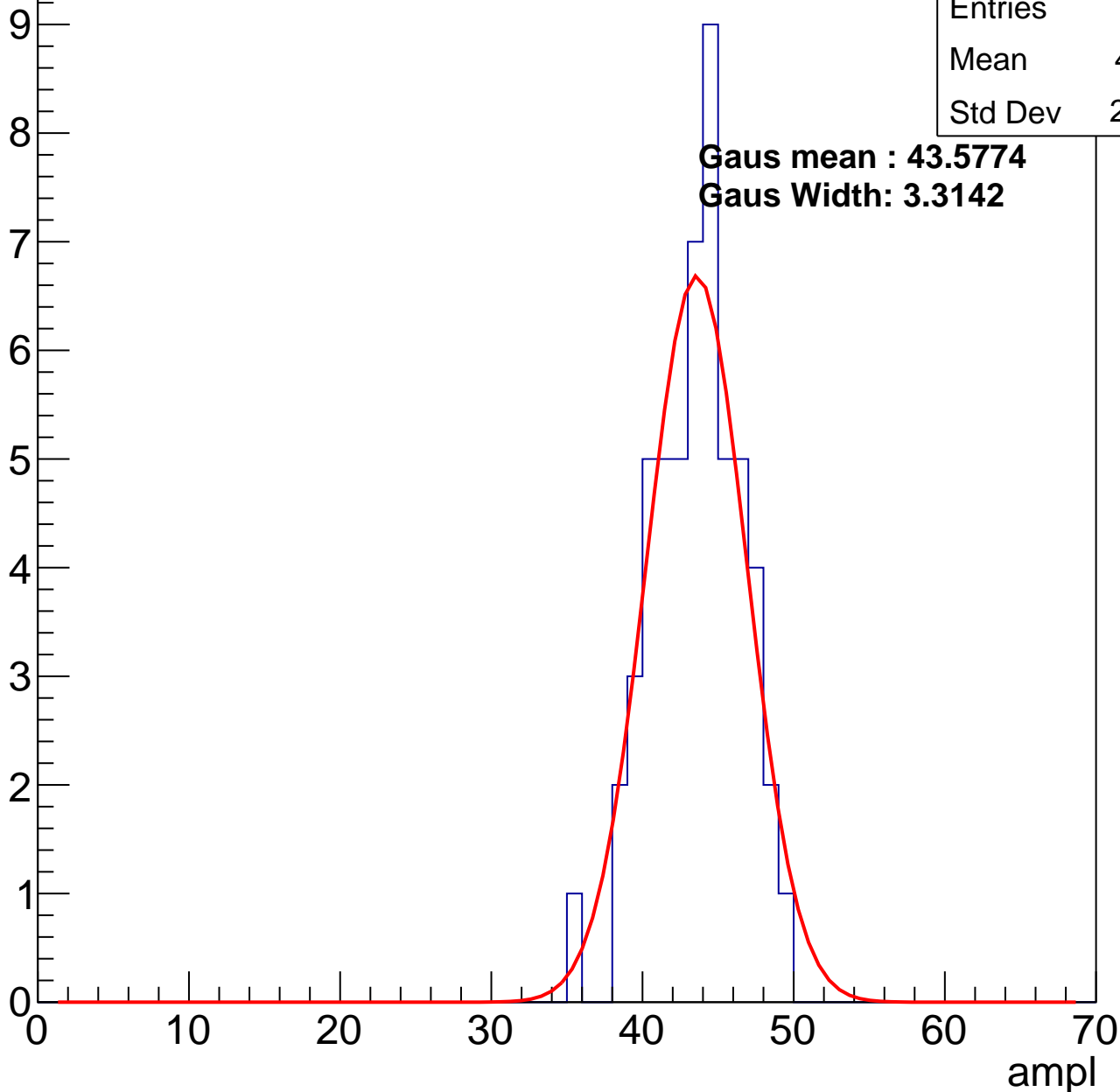
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.11
Std Dev	2.904

Gaus mean : 43.5774

Gaus Width: 3.3142

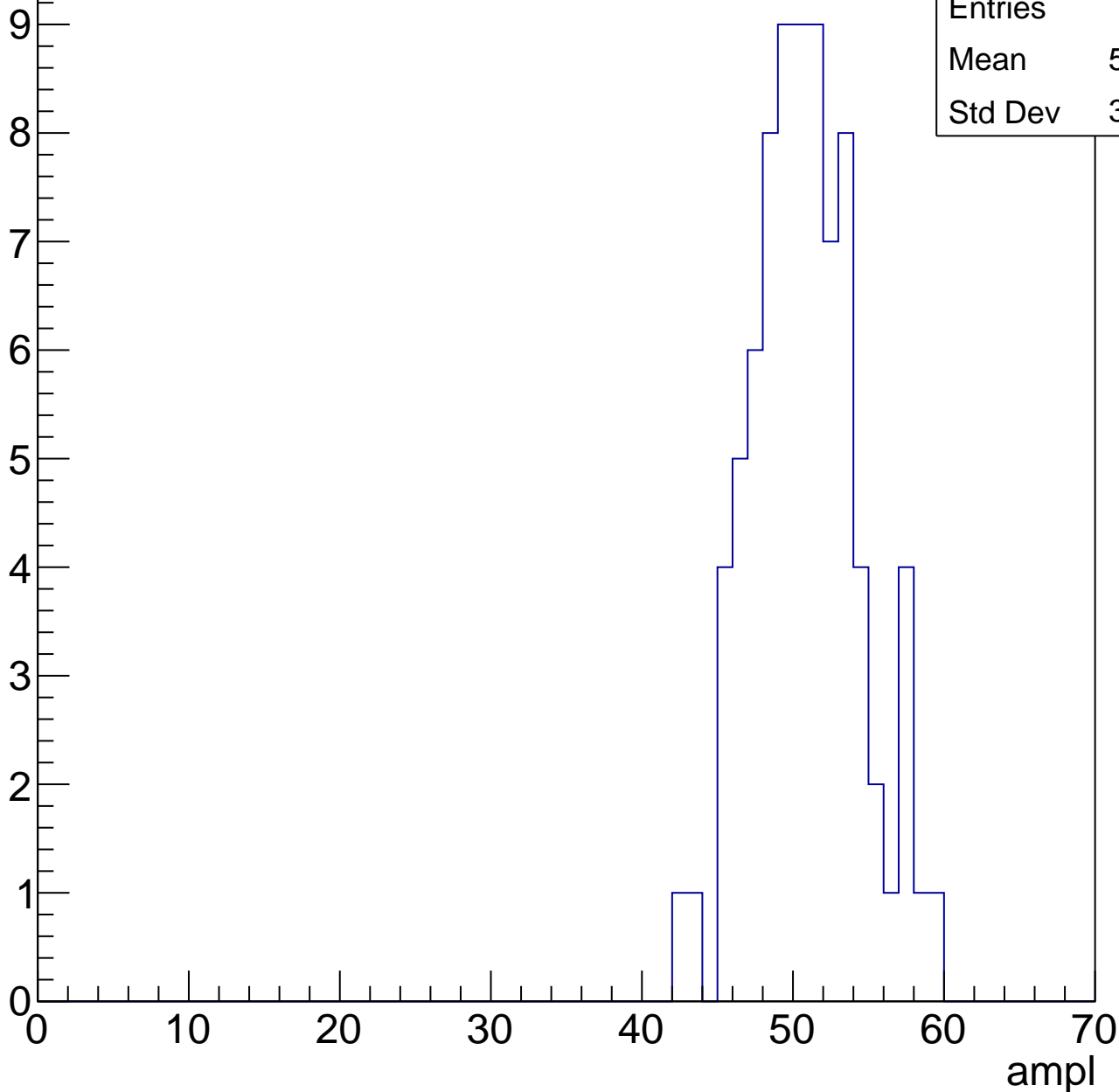


B1L101S, U6-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	50.33
Std Dev	3.499

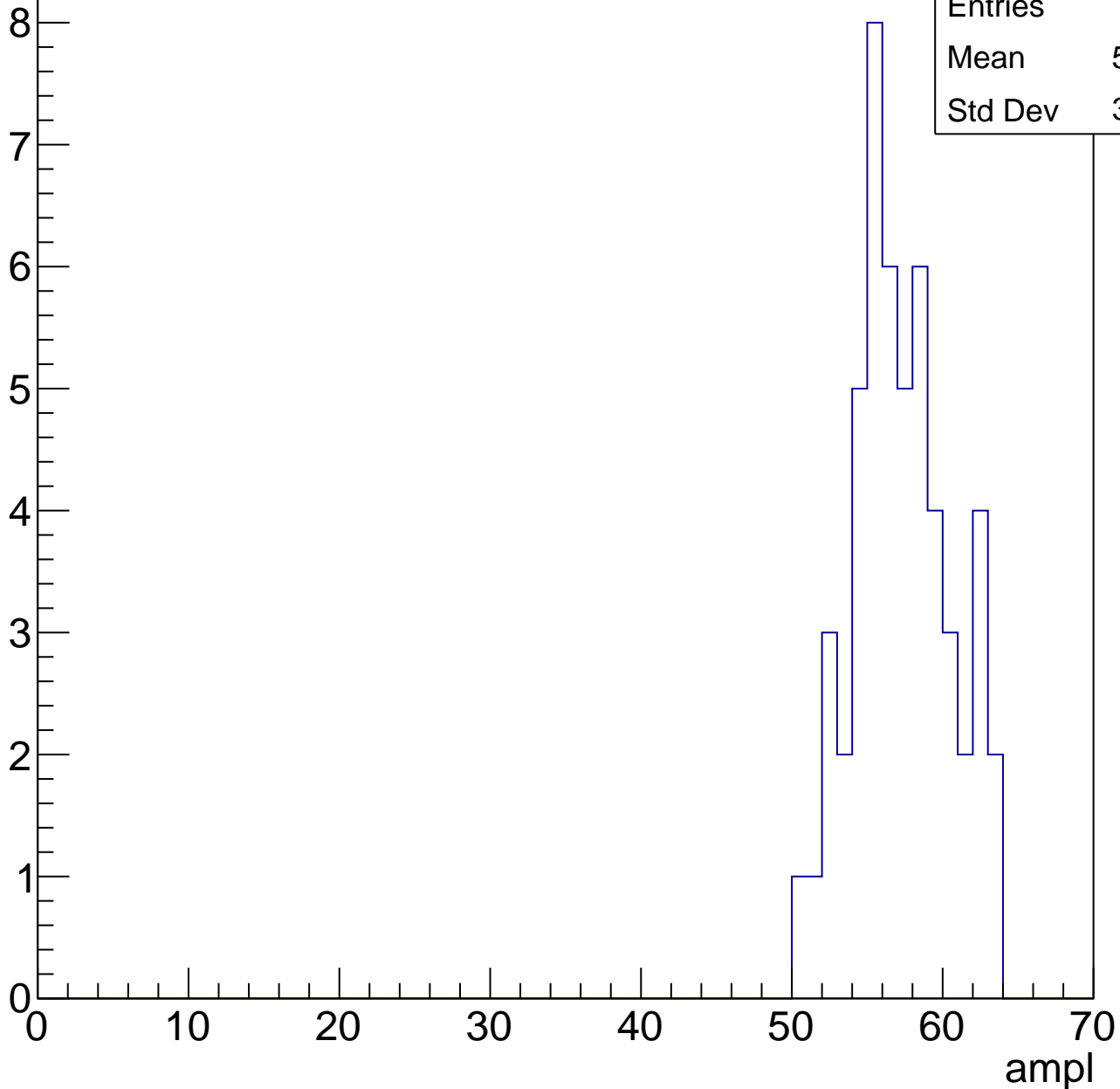


B1L101S, U6-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.81
Std Dev	3.181

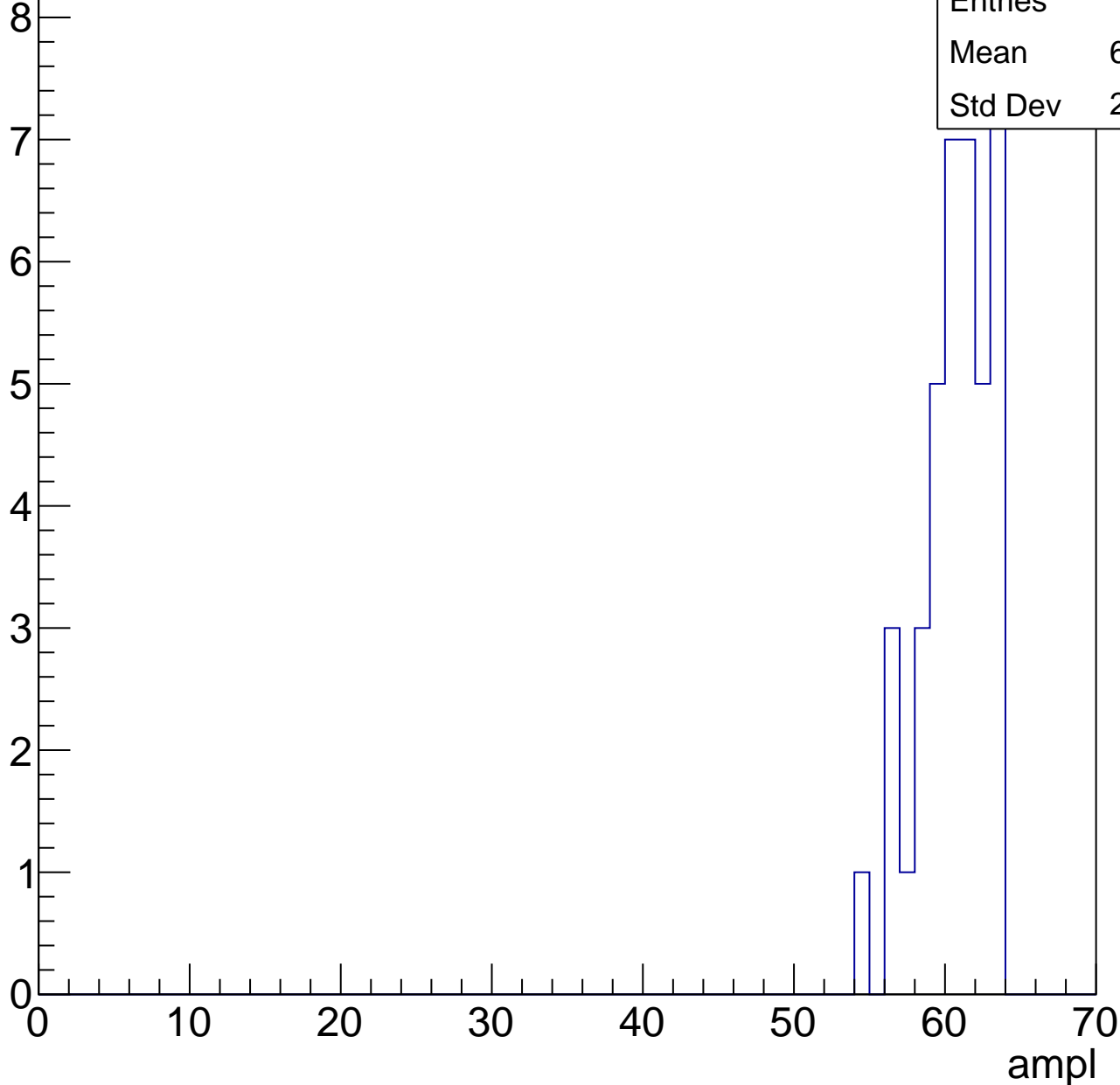


B1L101S, U6-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

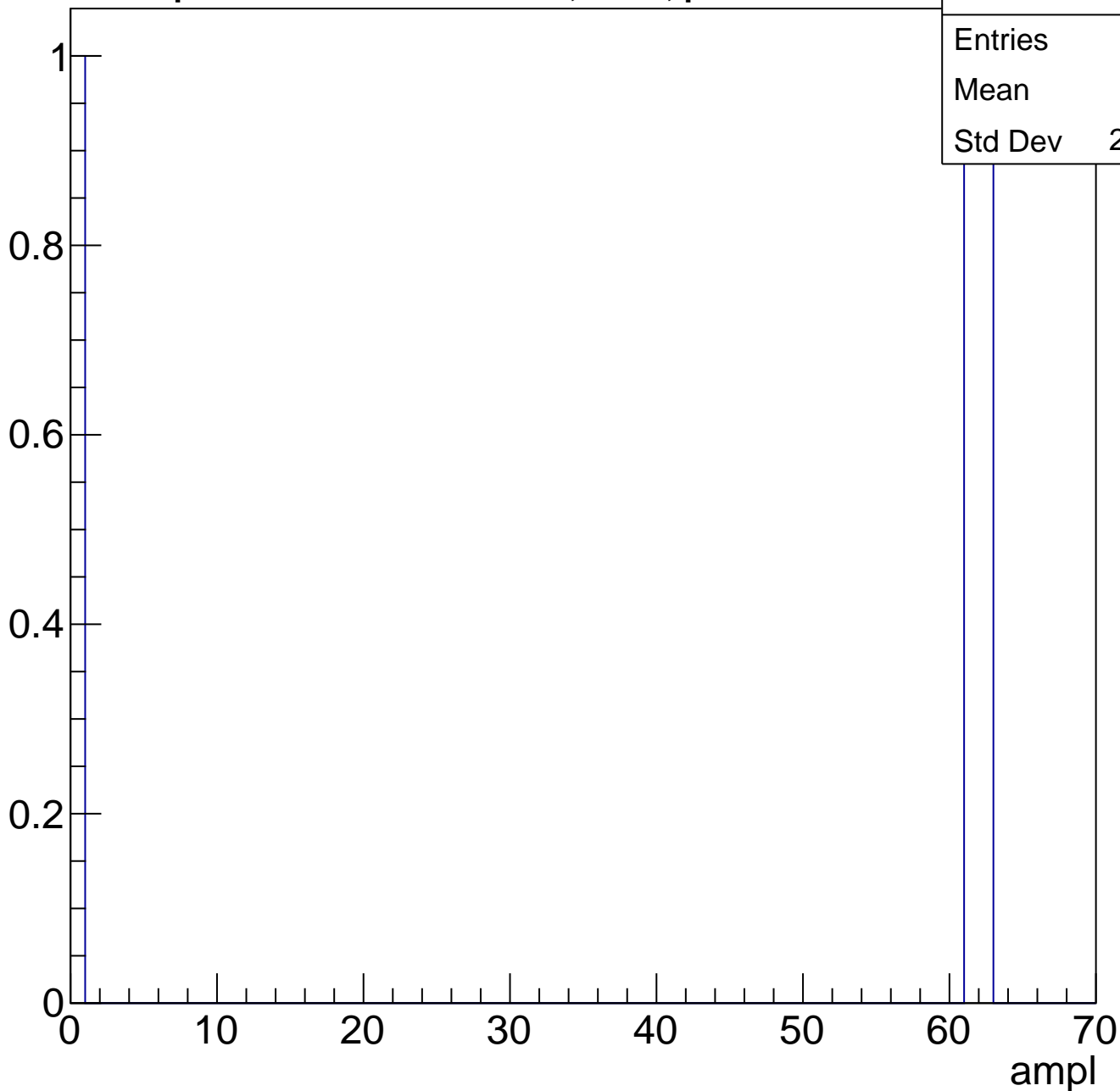
Entries	40
Mean	60.23
Std Dev	2.275



B1L101S, U6-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch114, adc0

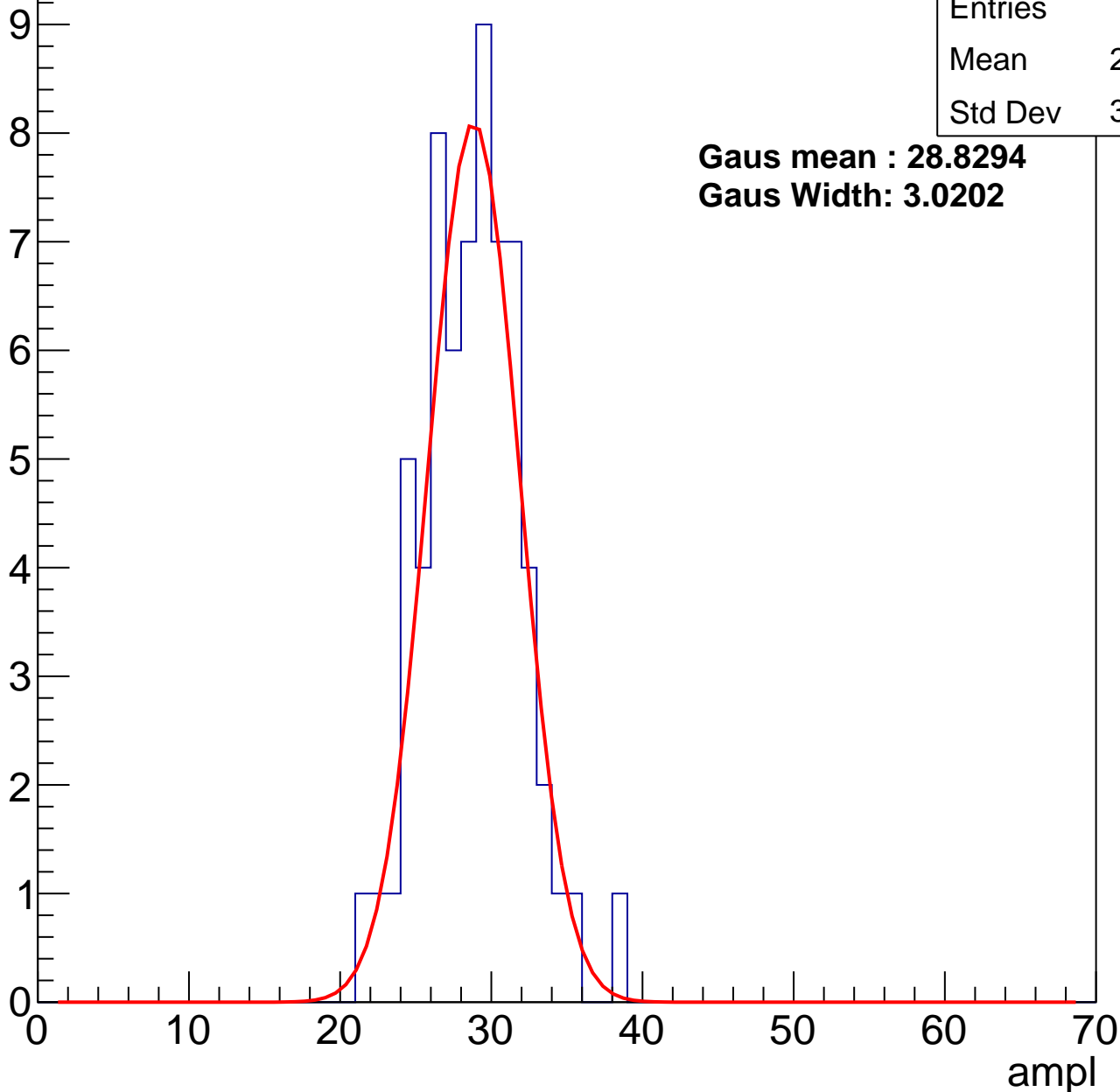
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.32
Std Dev	3.177

Gaus mean : 28.8294

Gaus Width: 3.0202



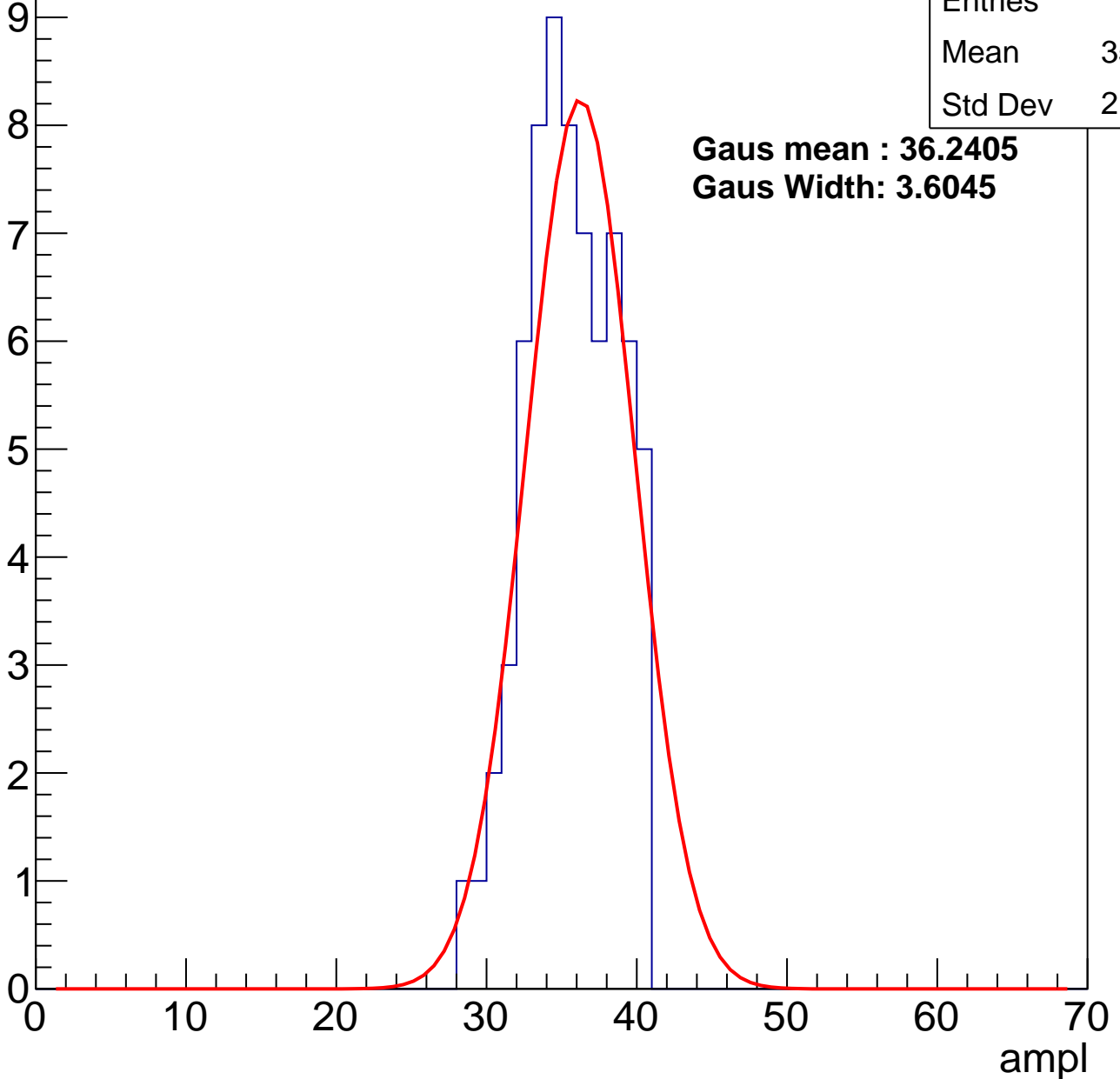
B1L101S, U6-ch114, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.16
Std Dev	2.927

Gaus mean : 36.2405
Gaus Width: 3.6045



B1L101S, U6-ch114, adc2

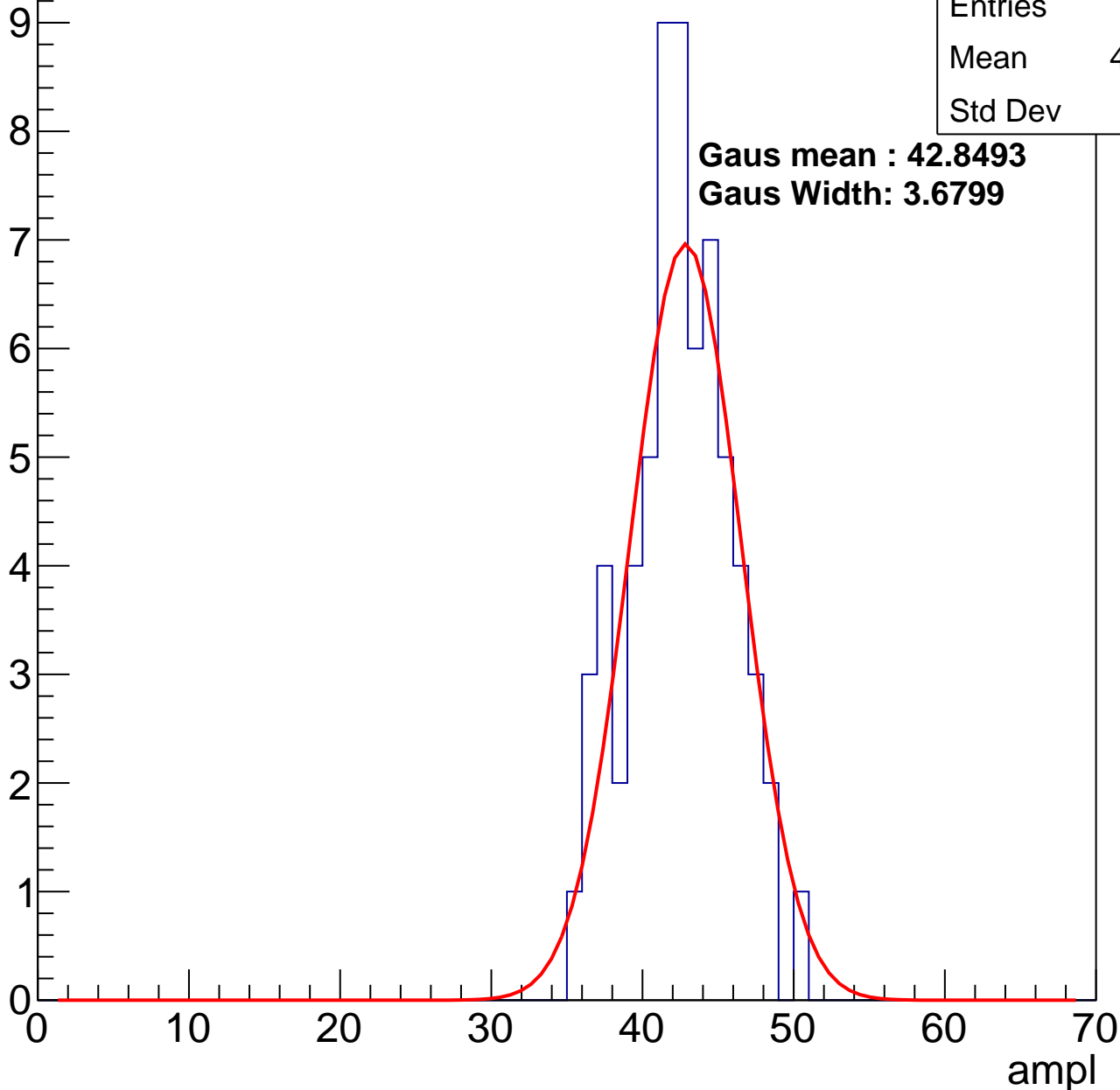
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.03
Std Dev	3.3

Gaus mean : 42.8493

Gaus Width: 3.6799

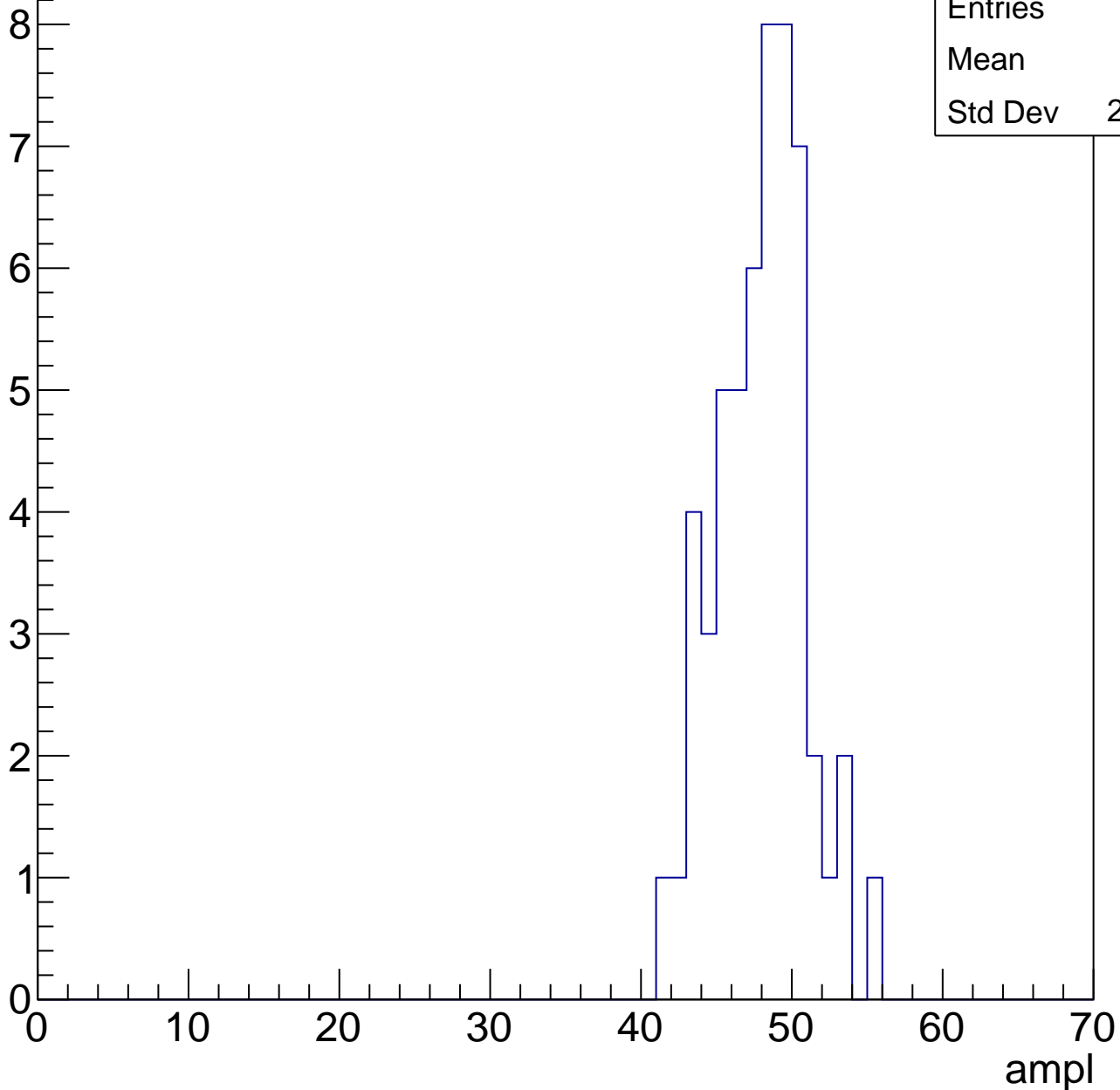


B1L101S, U6-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	47.5
Std Dev	2.917

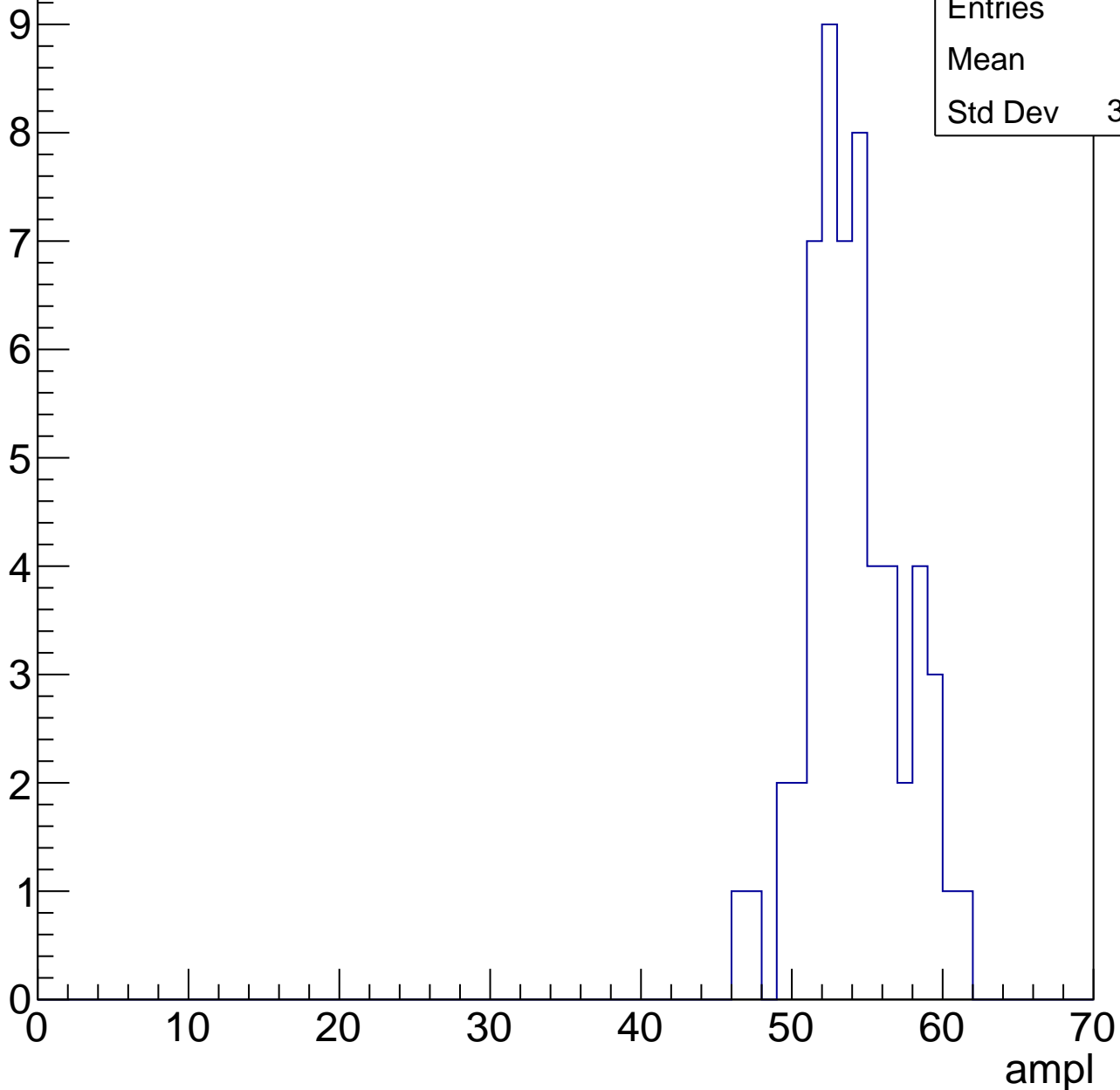


B1L101S, U6-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

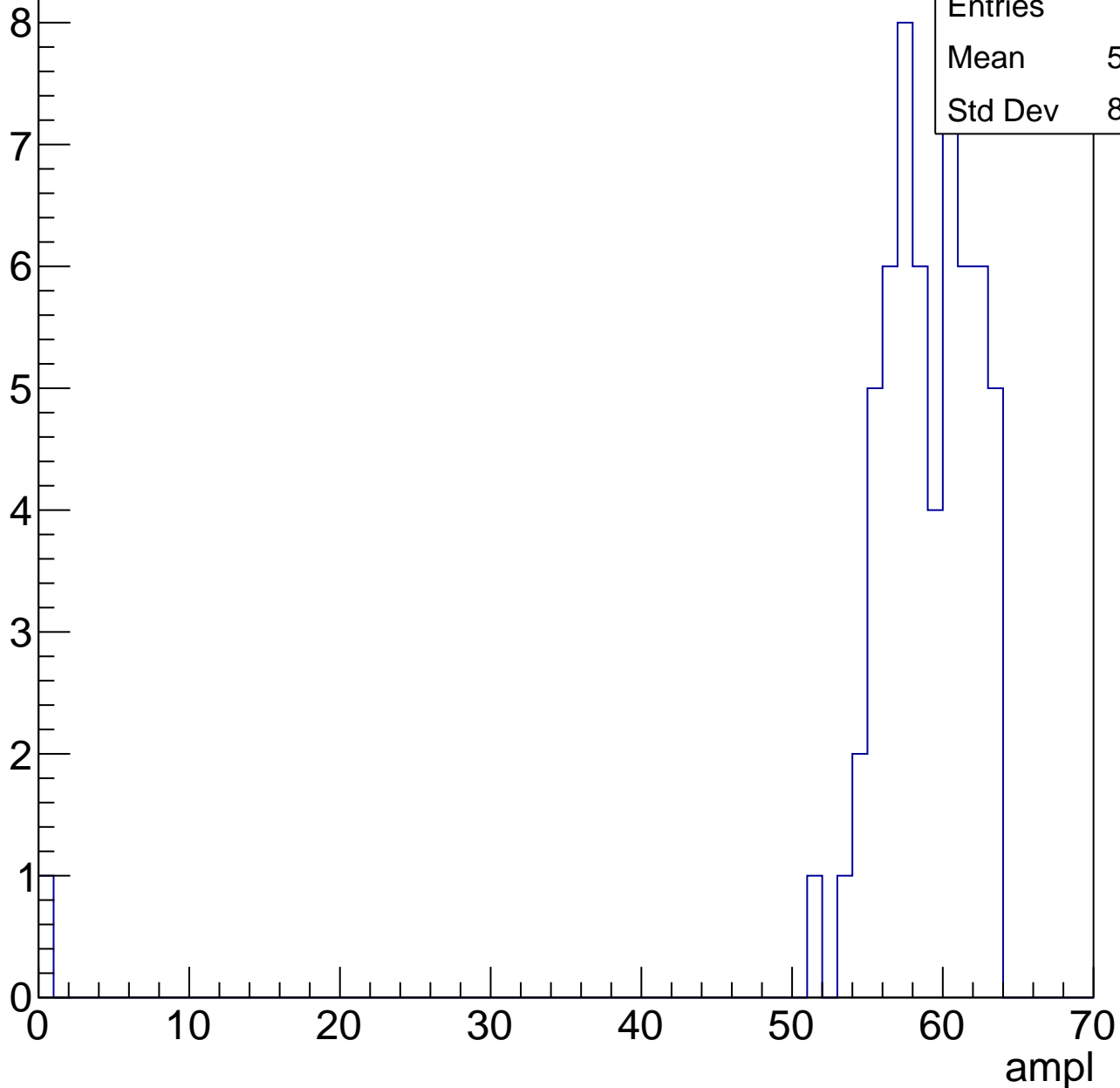
Entries	56
Mean	53.7
Std Dev	3.156



B1L101S, U6-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

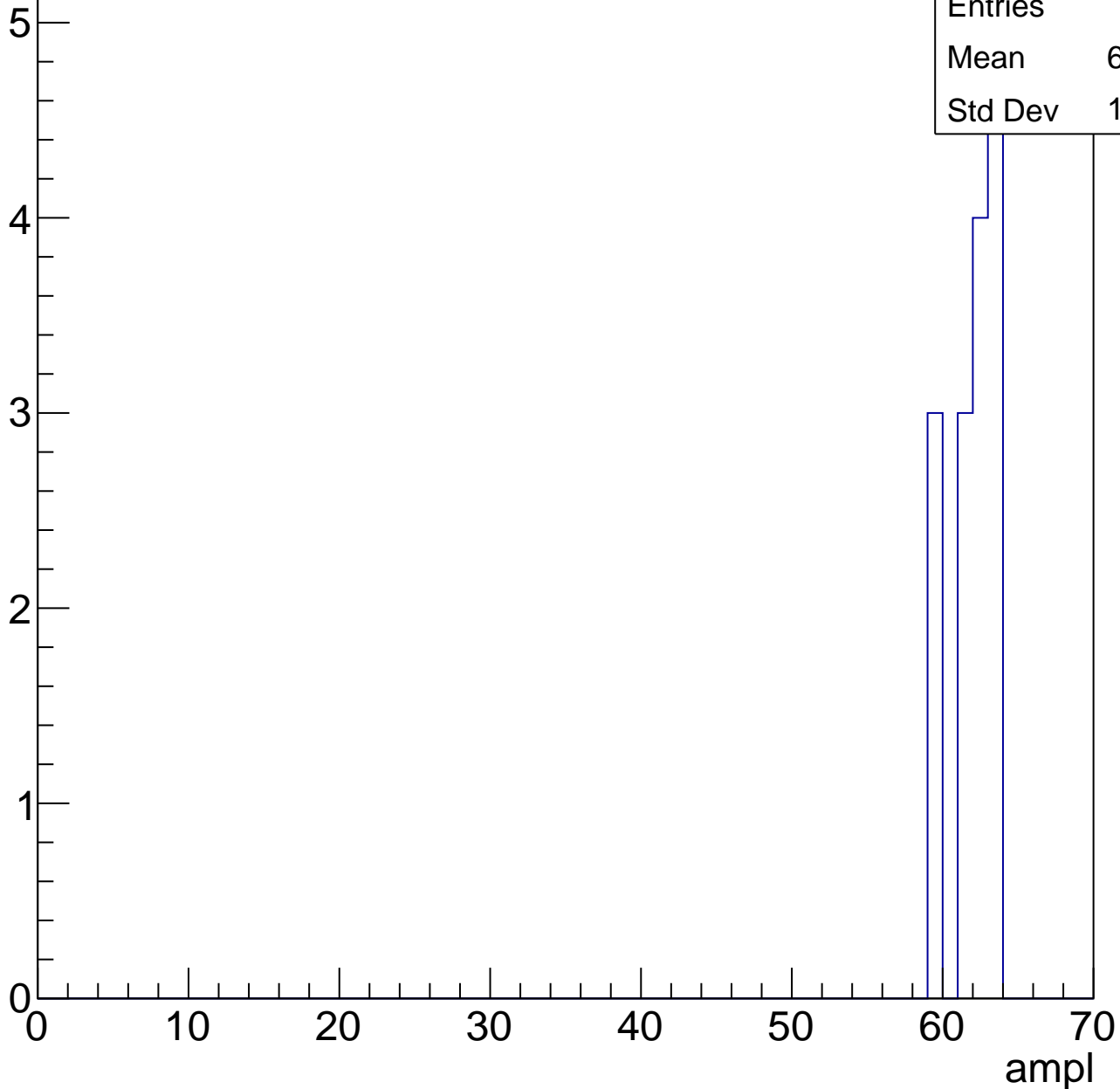


B1L101S, U6-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

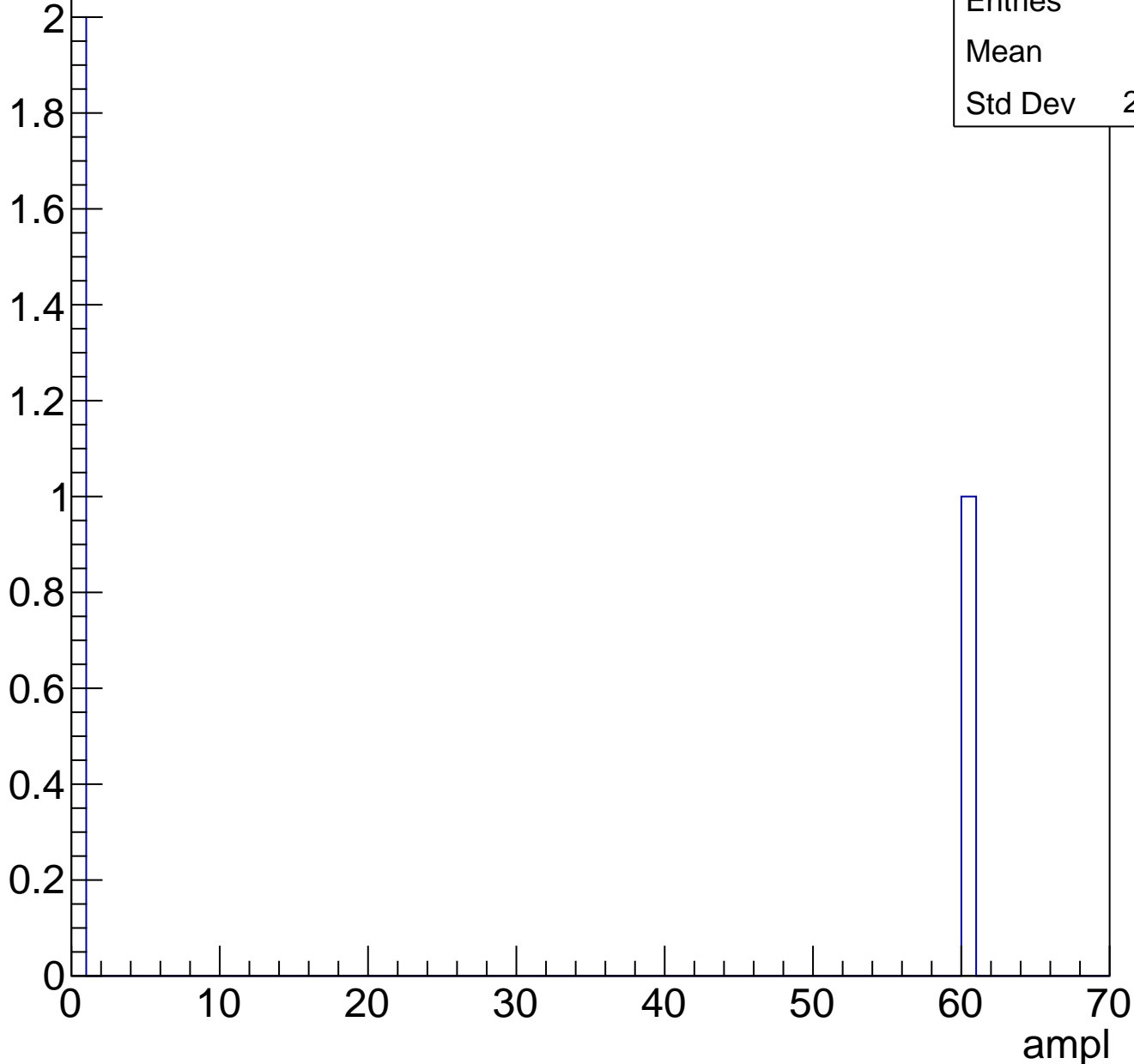
Entries	15
Mean	61.53
Std Dev	1.454



B1L101S, U6-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	20
Std Dev	28.28

B1L101S, U6-ch115, adc0

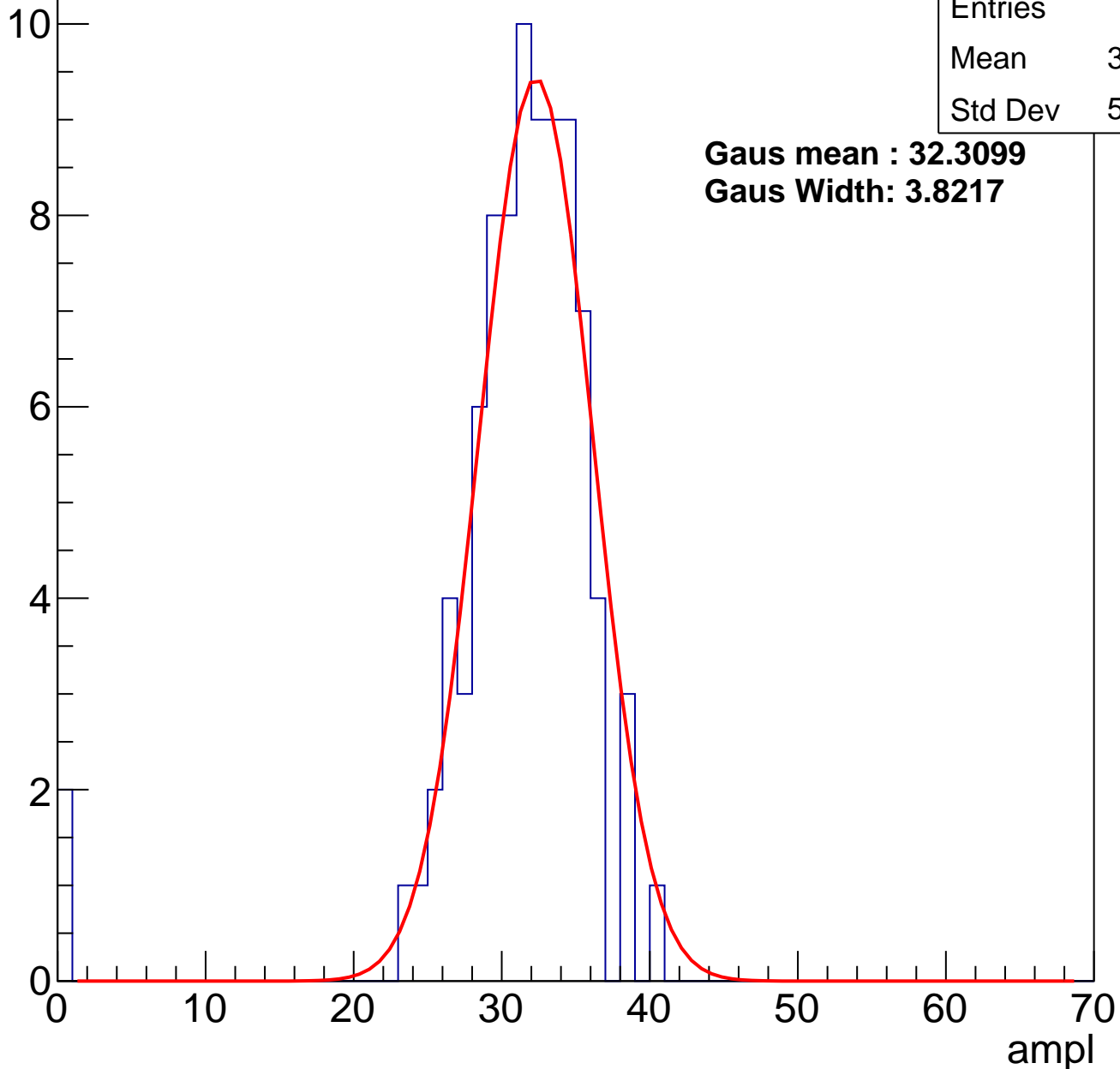
calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	30.64
Std Dev	5.777

Gaus mean : 32.3099

Gaus Width: 3.8217

Entry



B1L101S, U6-ch115, adc1

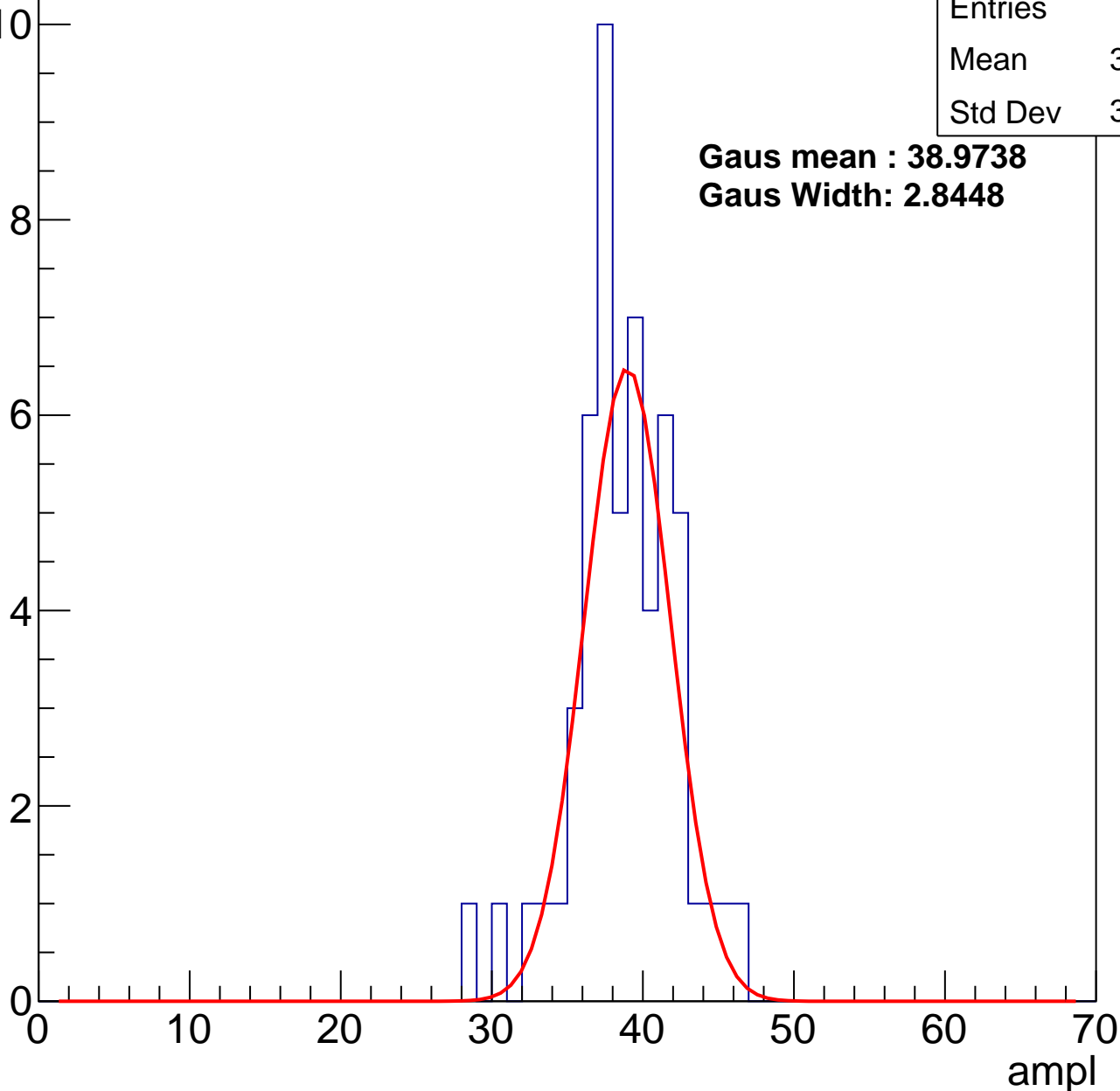
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	38.27
Std Dev	3.382

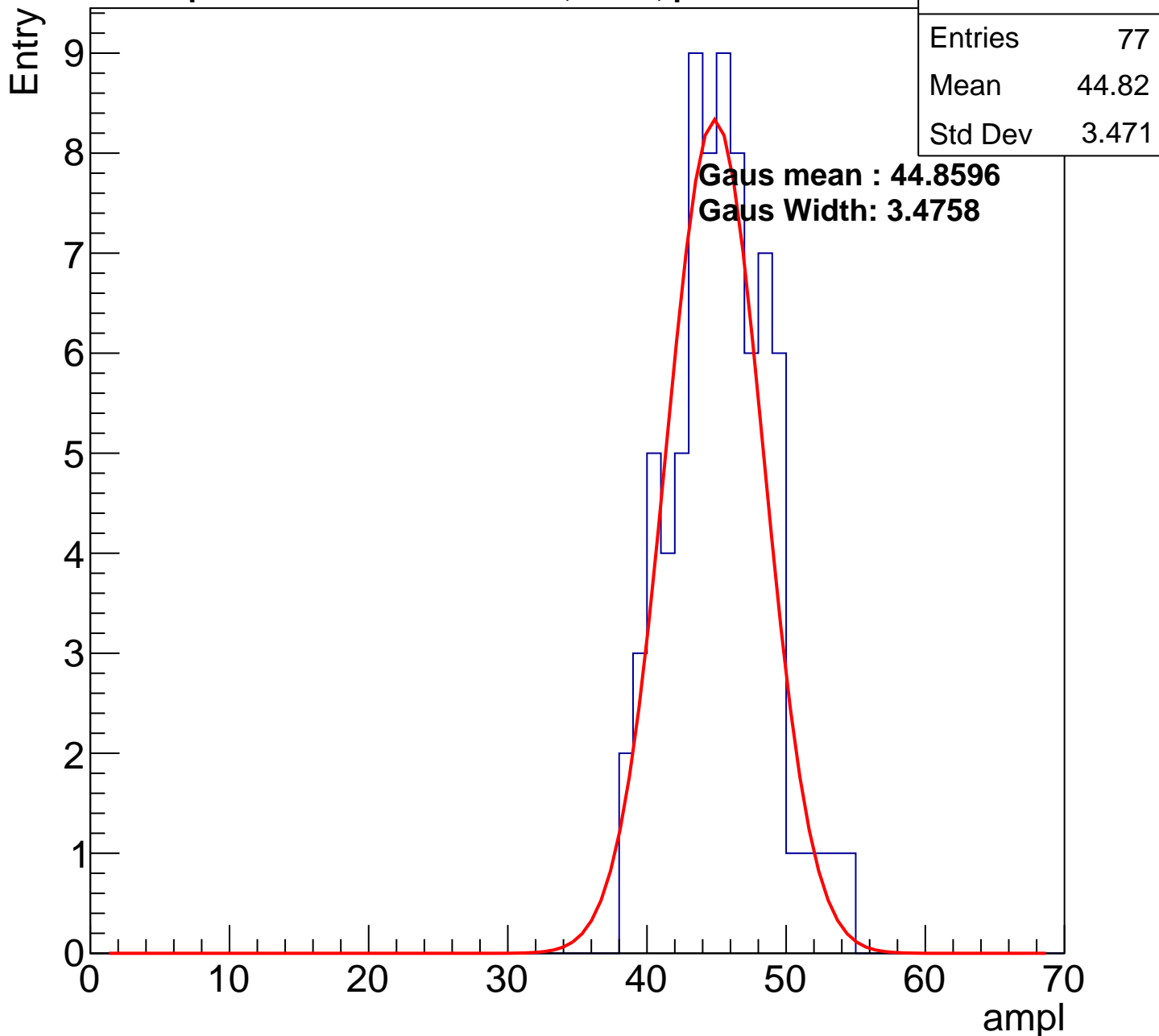
Gaus mean : 38.9738

Gaus Width: 2.8448



B1L101S, U6-ch115, adc2

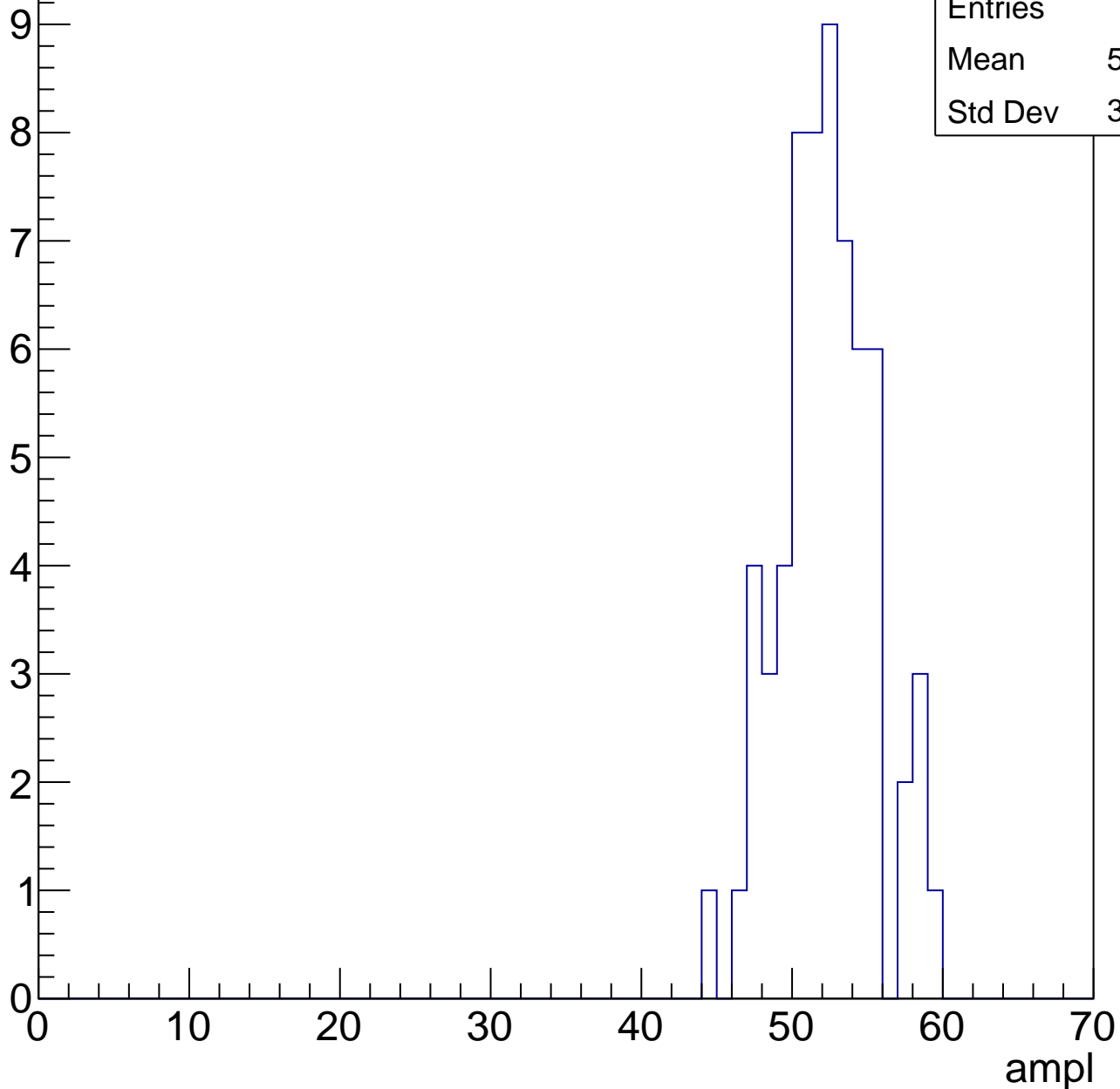
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



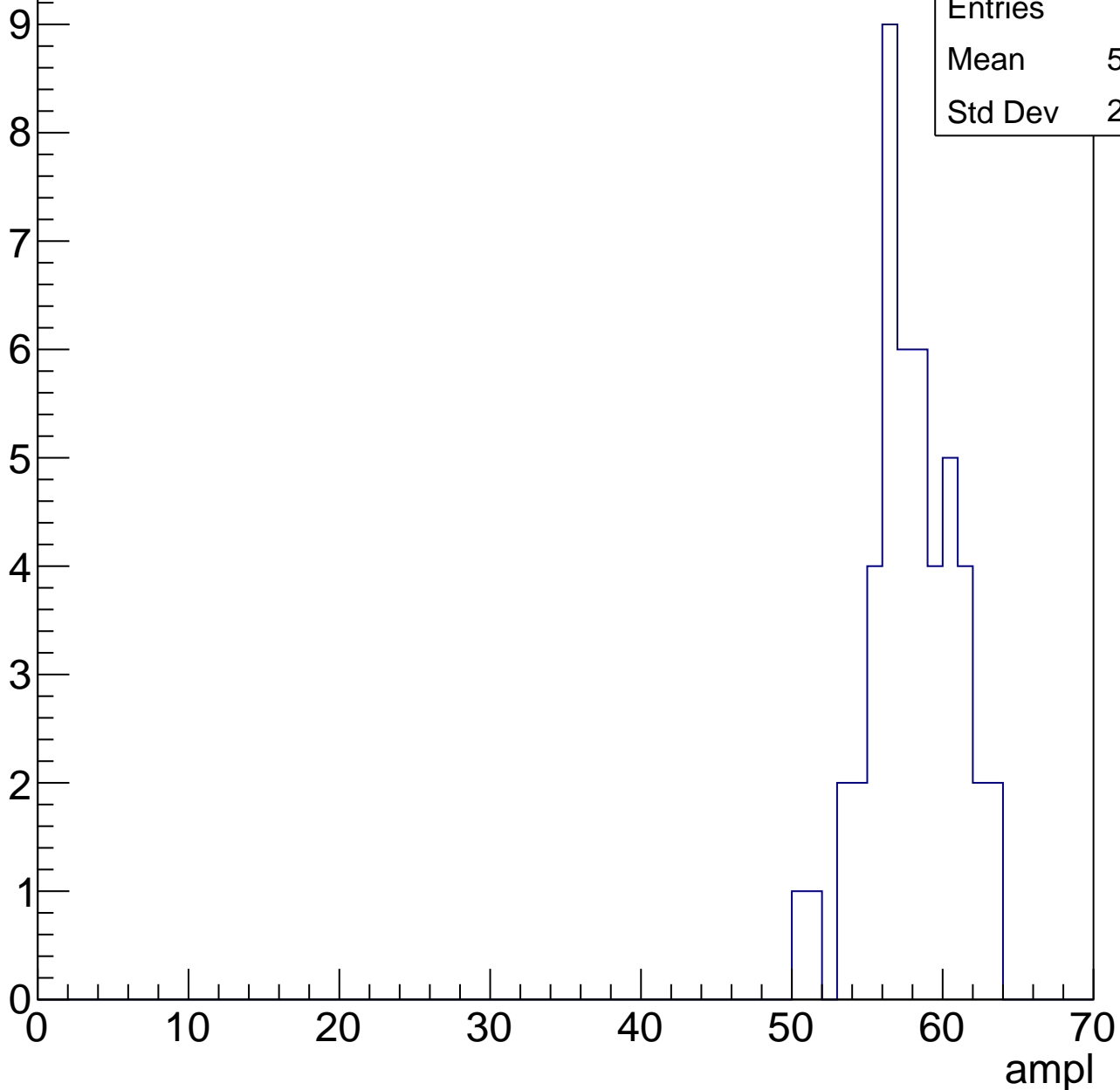
Entries	63
Mean	51.84
Std Dev	3.123

B1L101S, U6-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.48
Std Dev	2.894

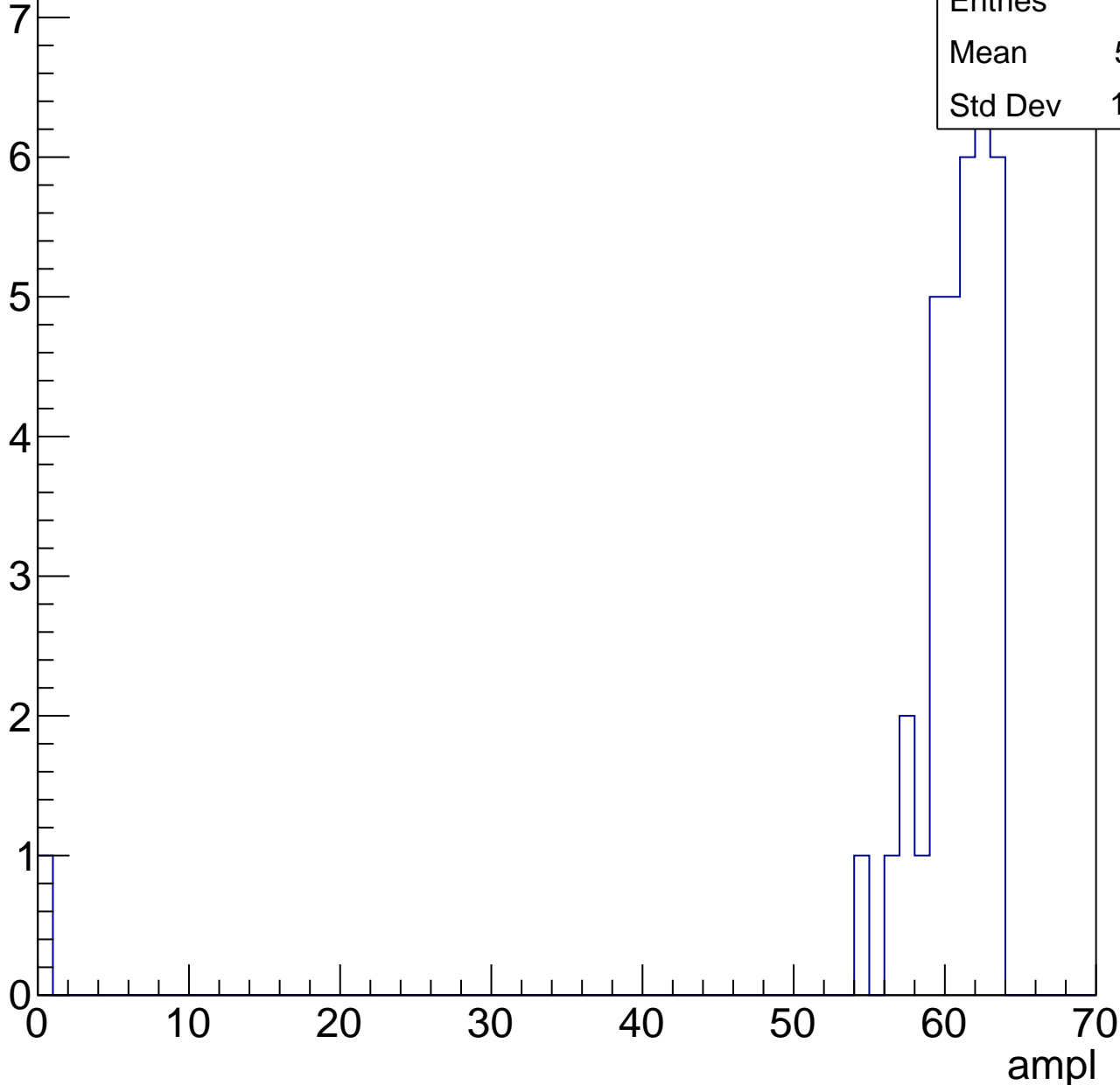


B1L101S, U6-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.71
Std Dev	10.29



B1L101S, U6-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch116, adc0

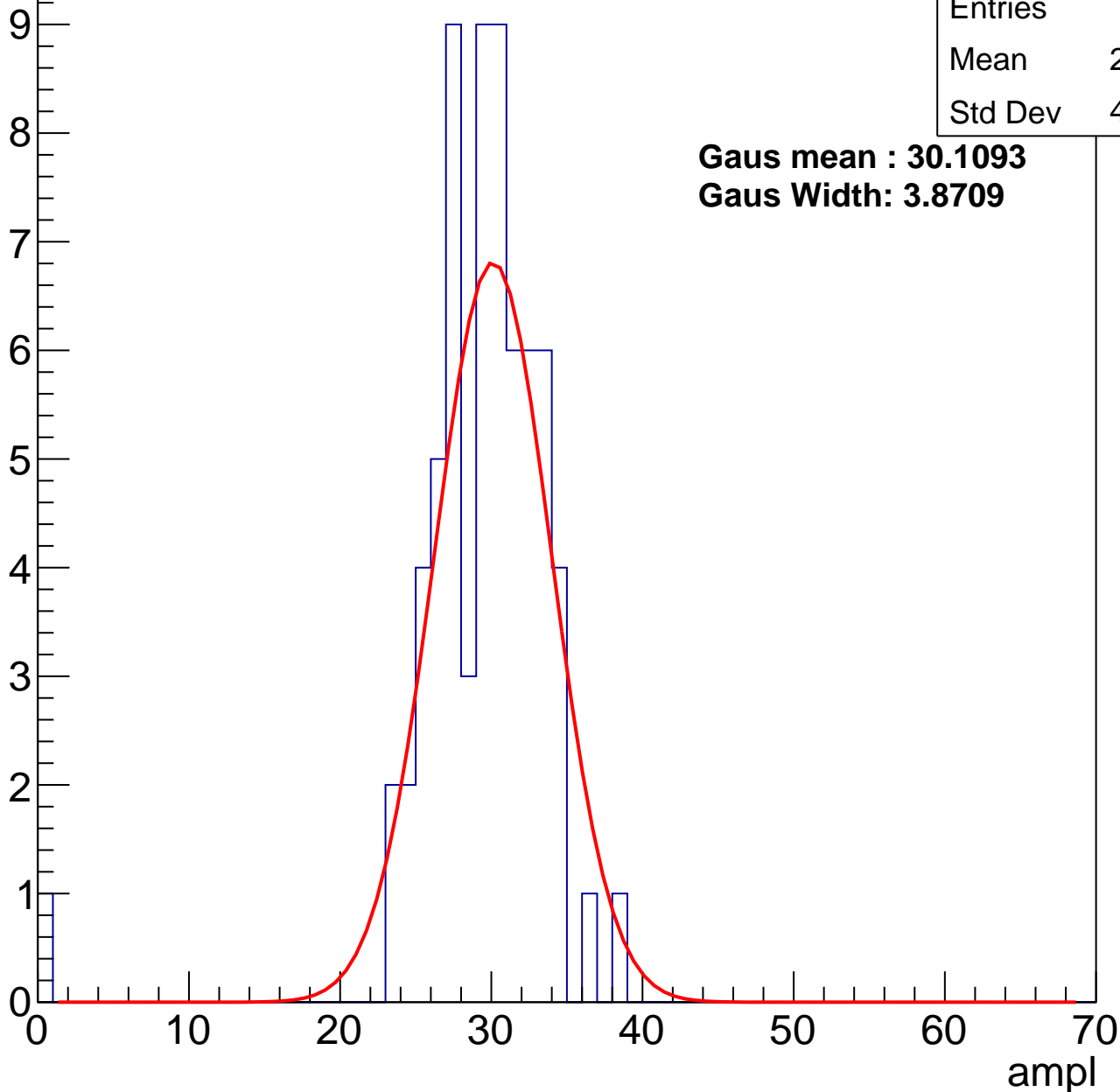
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.94
Std Dev	4.734

Gaus mean : 30.1093

Gaus Width: 3.8709



B1L101S, U6-ch116, adc1

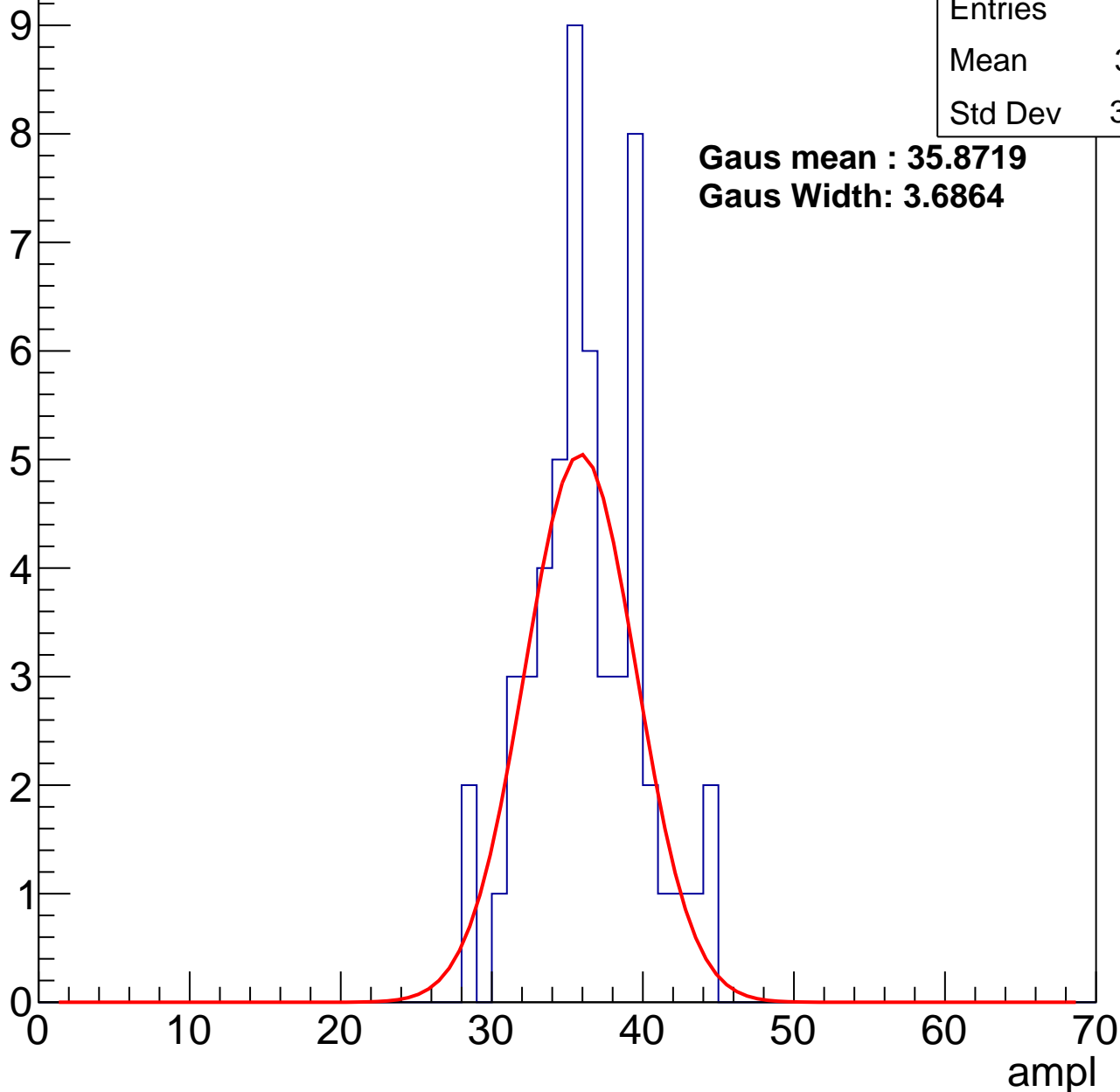
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	35.91
Std Dev	3.622

Gaus mean : 35.8719

Gaus Width: 3.6864



B1L101S, U6-ch116, adc2

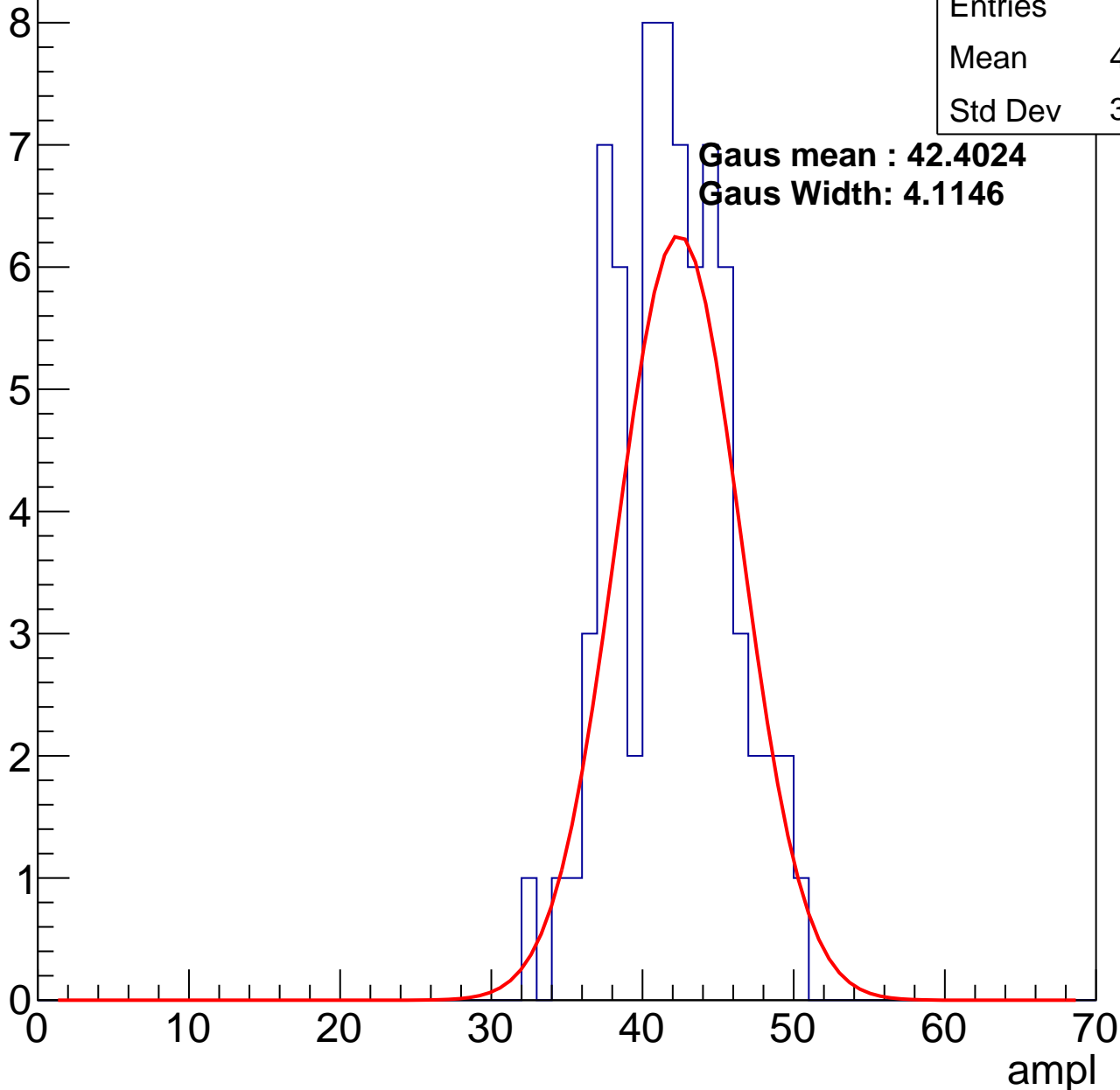
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.48
Std Dev	3.793

Gaus mean : 42.4024

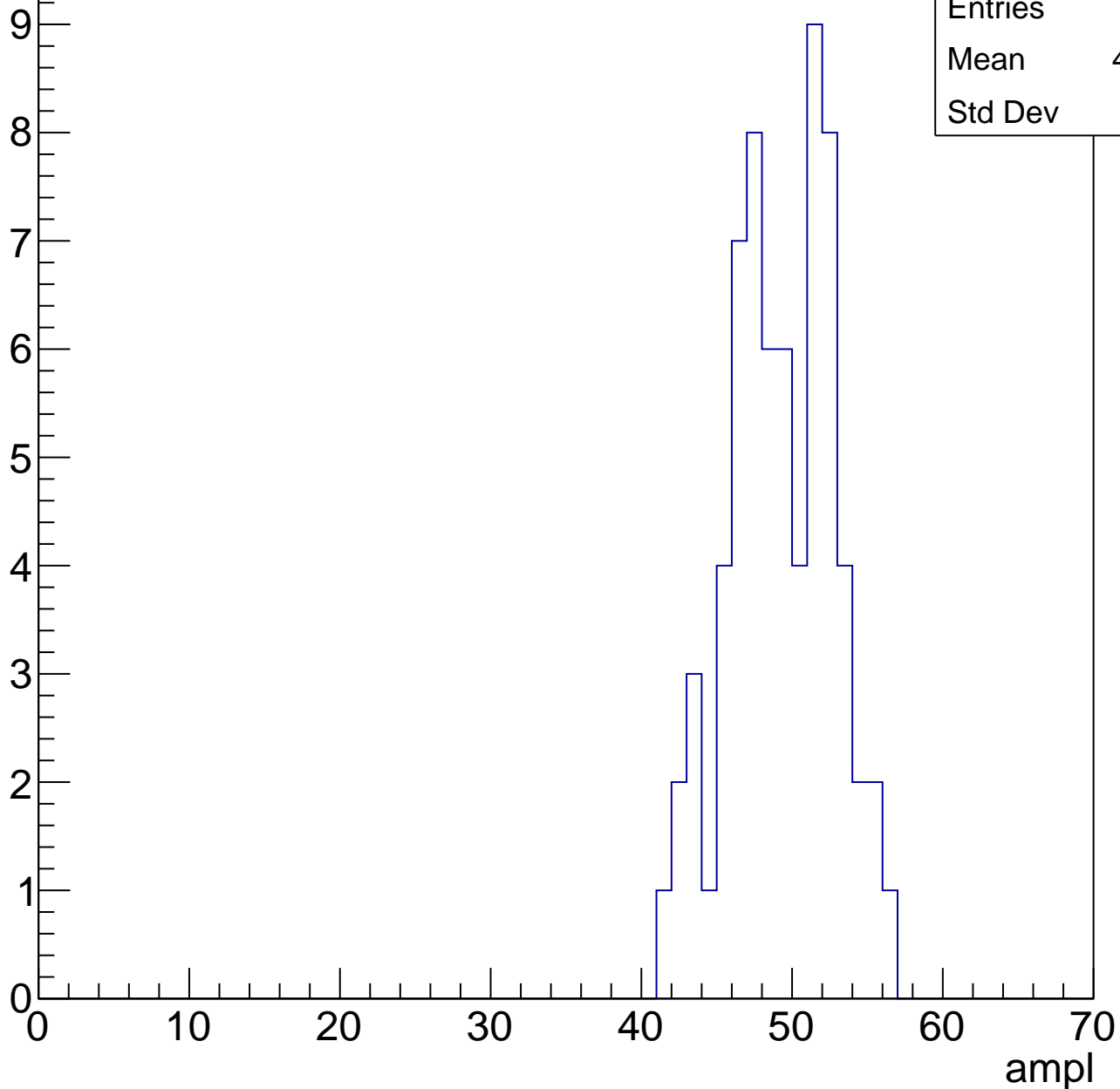
Gaus Width: 4.1146



B1L101S, U6-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

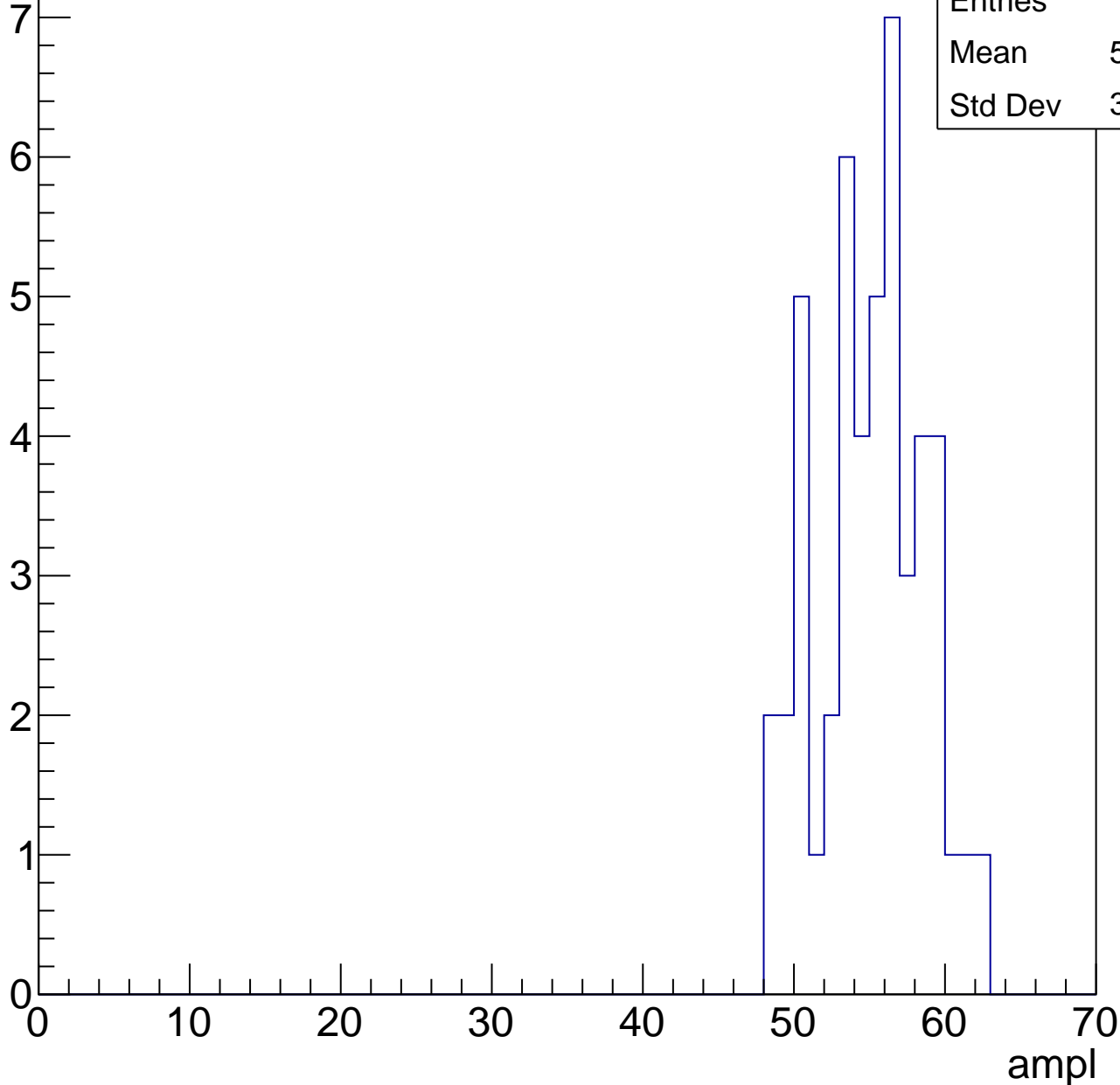


Entries	68
Mean	48.81
Std Dev	3.44

B1L101S, U6-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



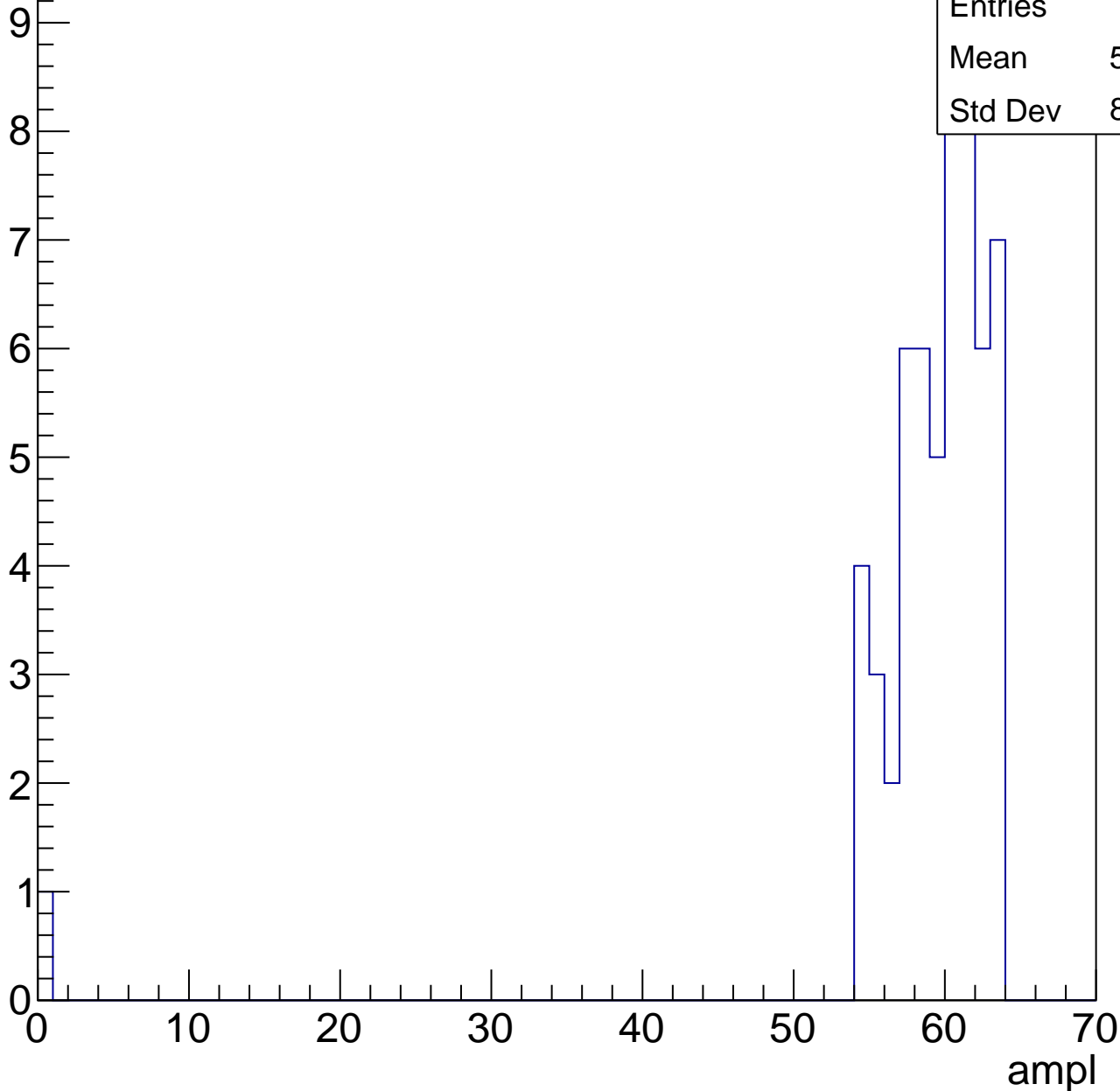
Entries	48
Mean	54.62
Std Dev	3.474

B1L101S, U6-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

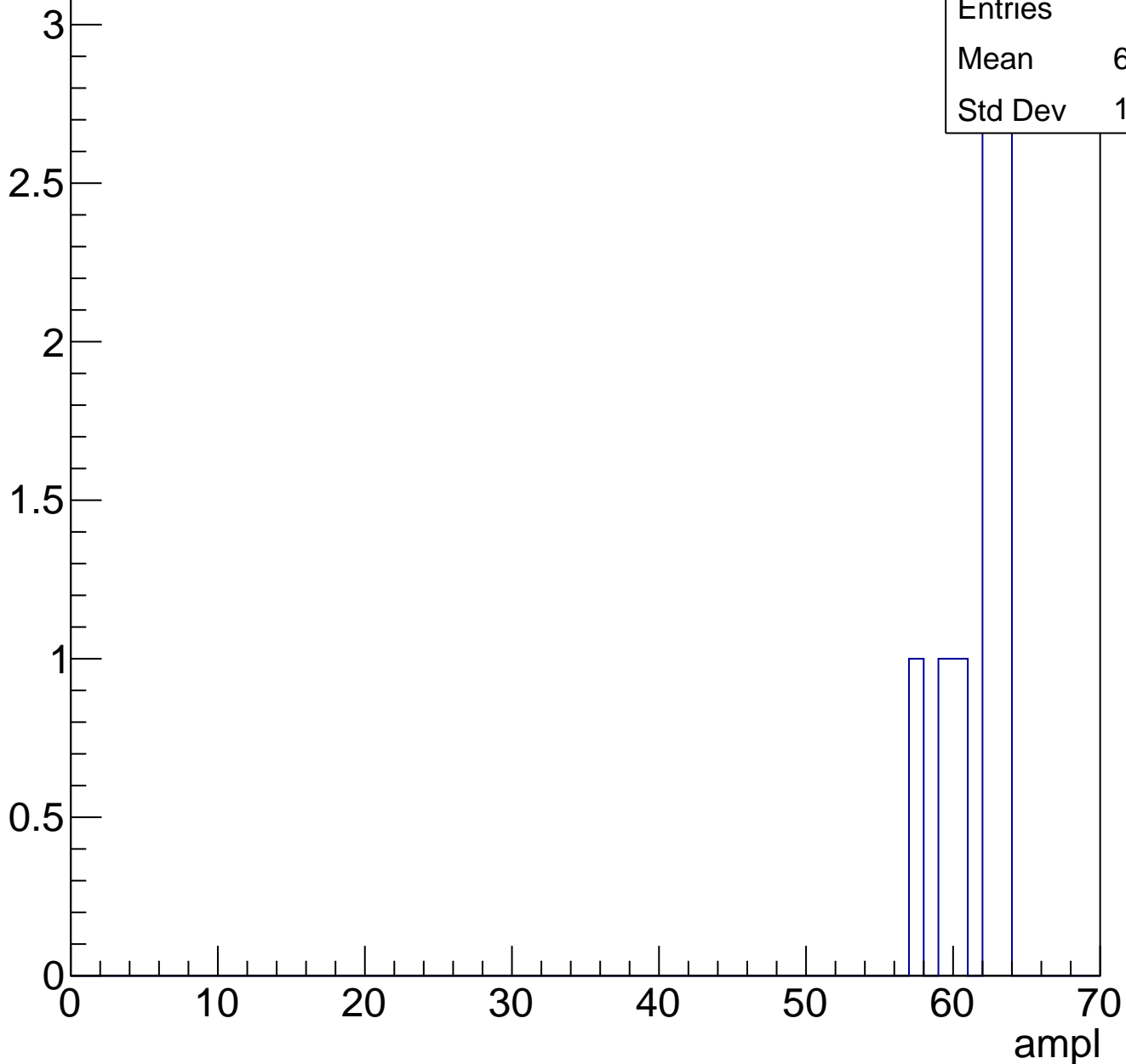
Entries	57
Mean	58.25
Std Dev	8.223



B1L101S, U6-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

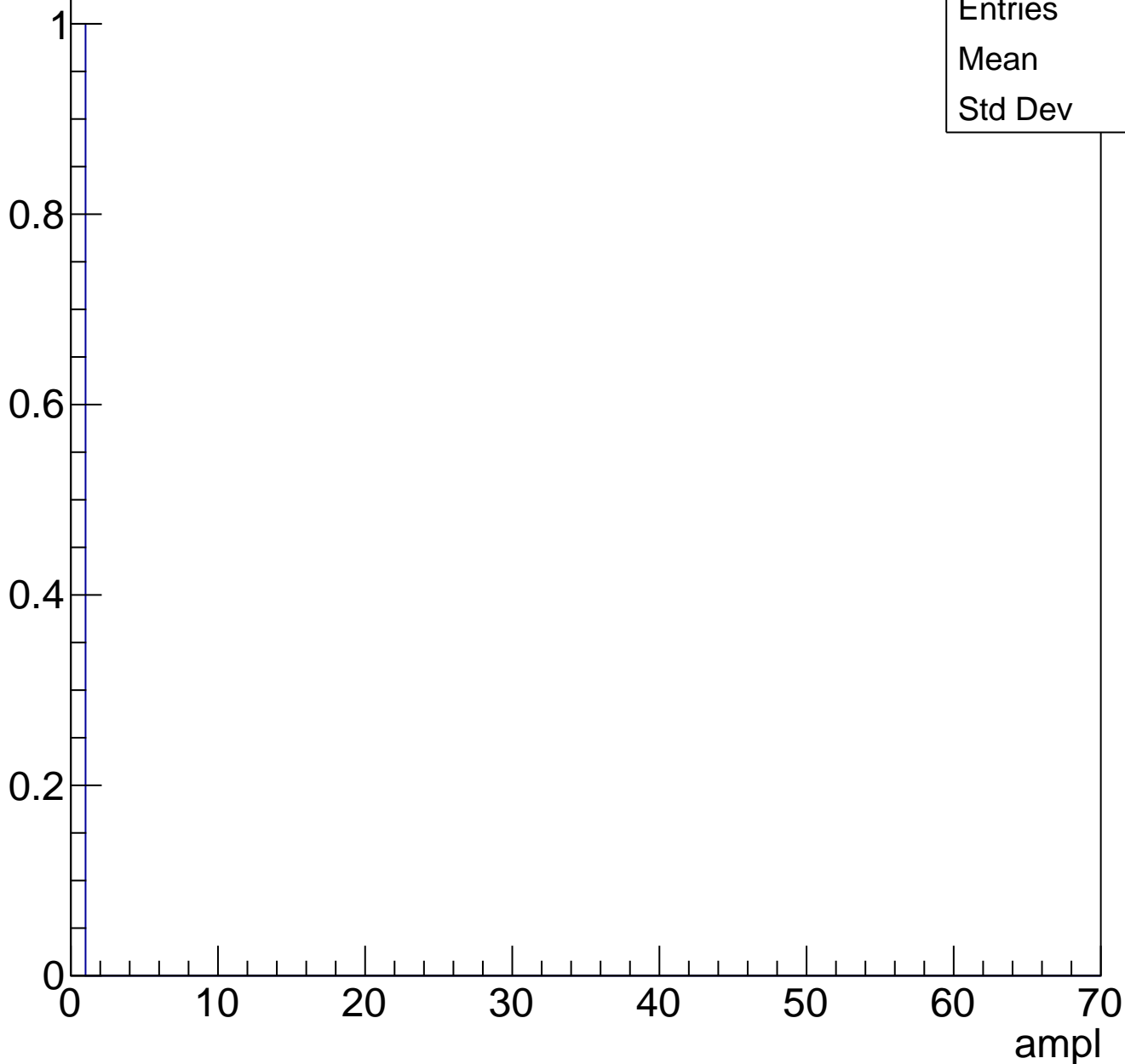
Entry



B1L101S, U6-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch117, adc0

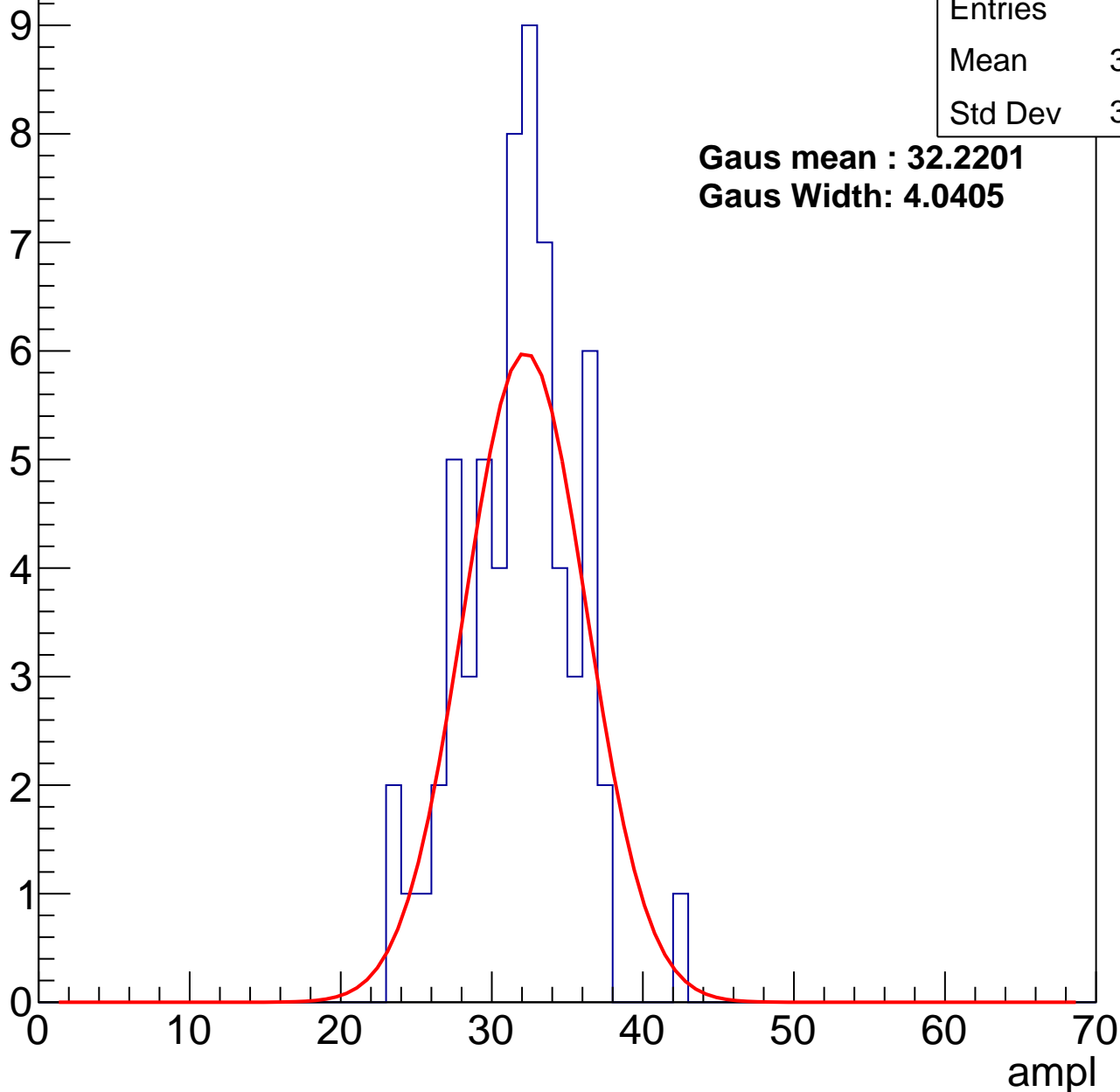
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.29
Std Dev	3.679

Gaus mean : 32.2201

Gaus Width: 4.0405



B1L101S, U6-ch117, adc1

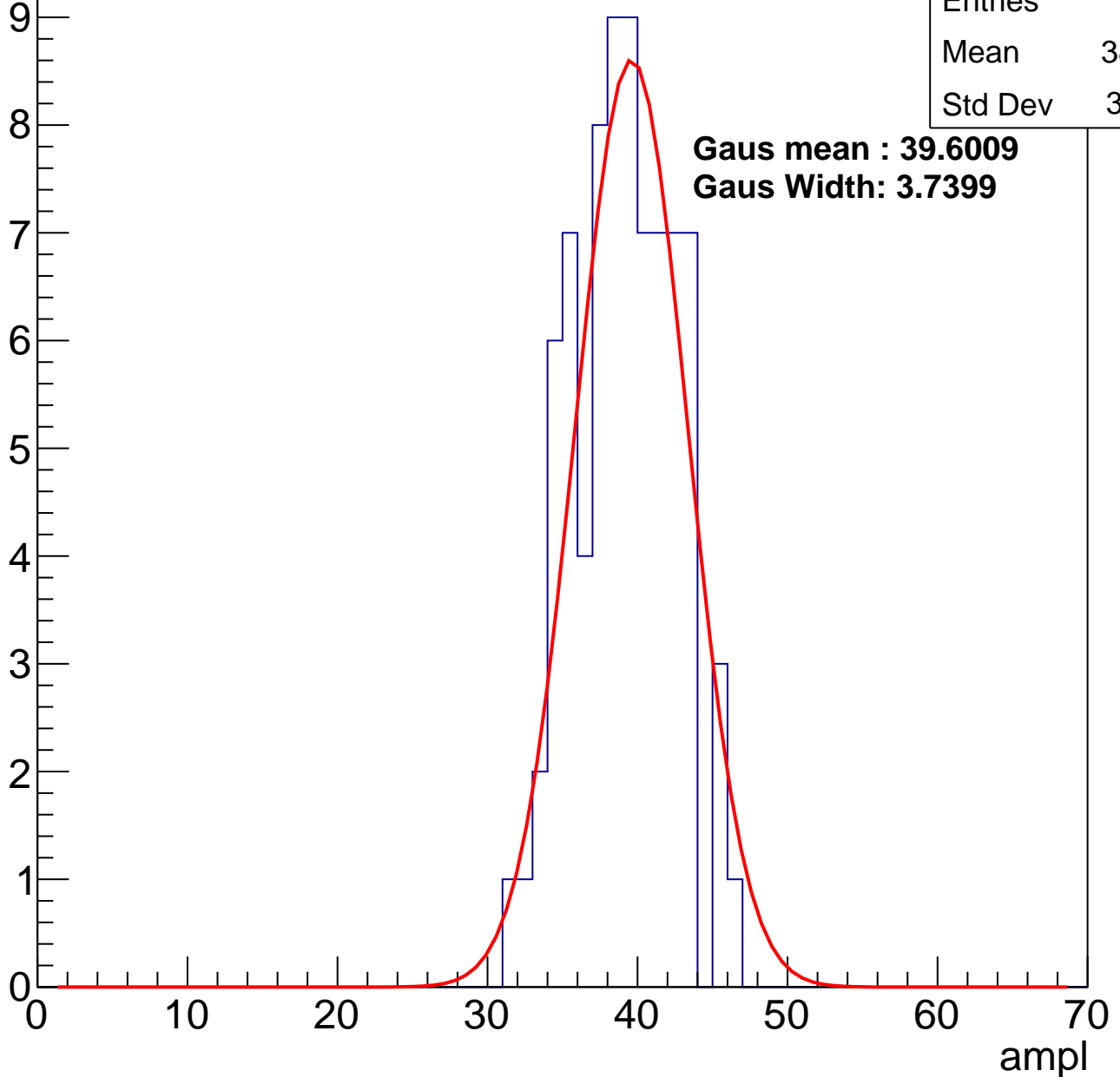
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	38.66
Std Dev	3.341

Gaus mean : 39.6009

Gaus Width: 3.7399



B1L101S, U6-ch117, adc2

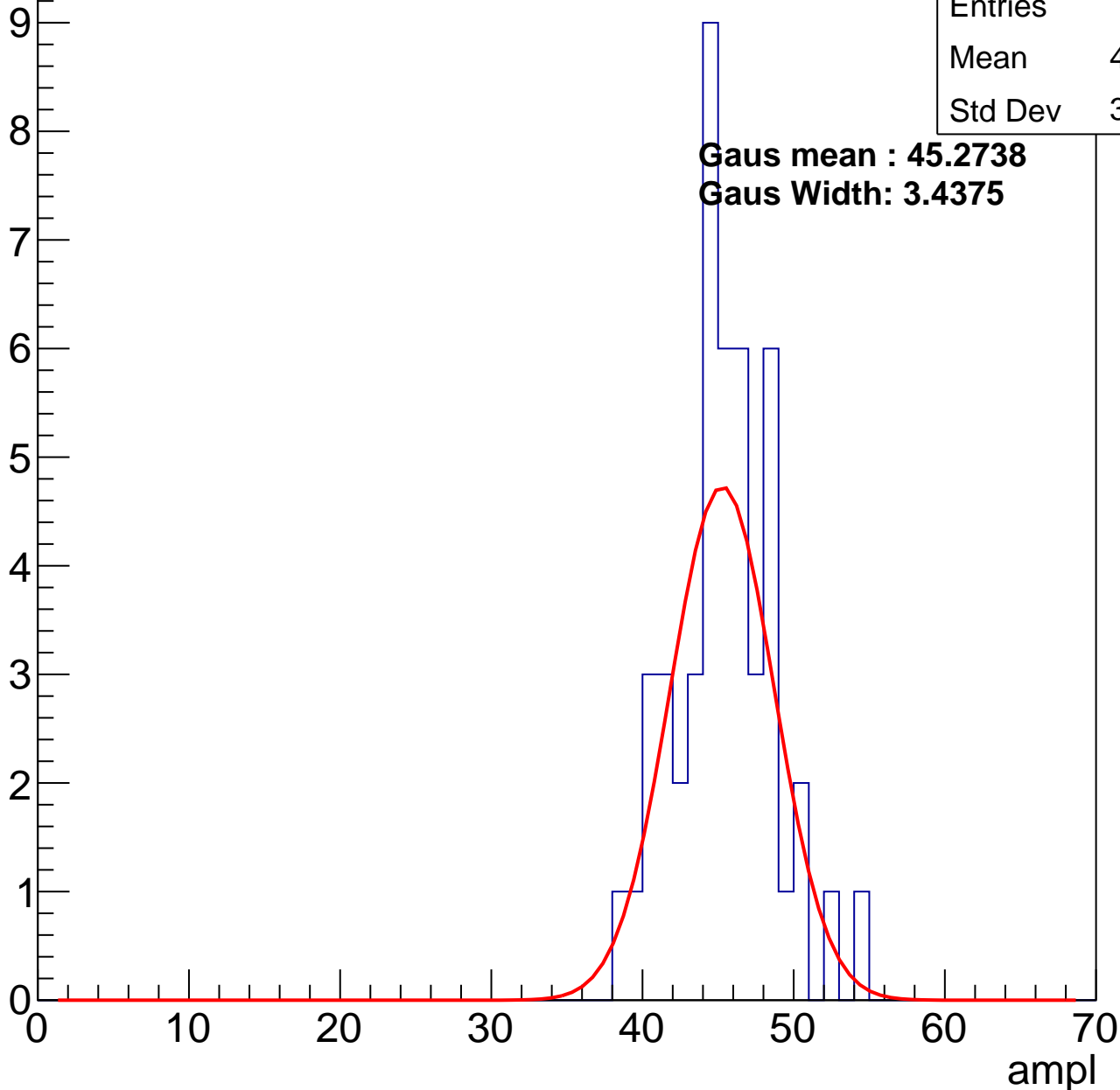
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	44.98
Std Dev	3.275

Gaus mean : 45.2738

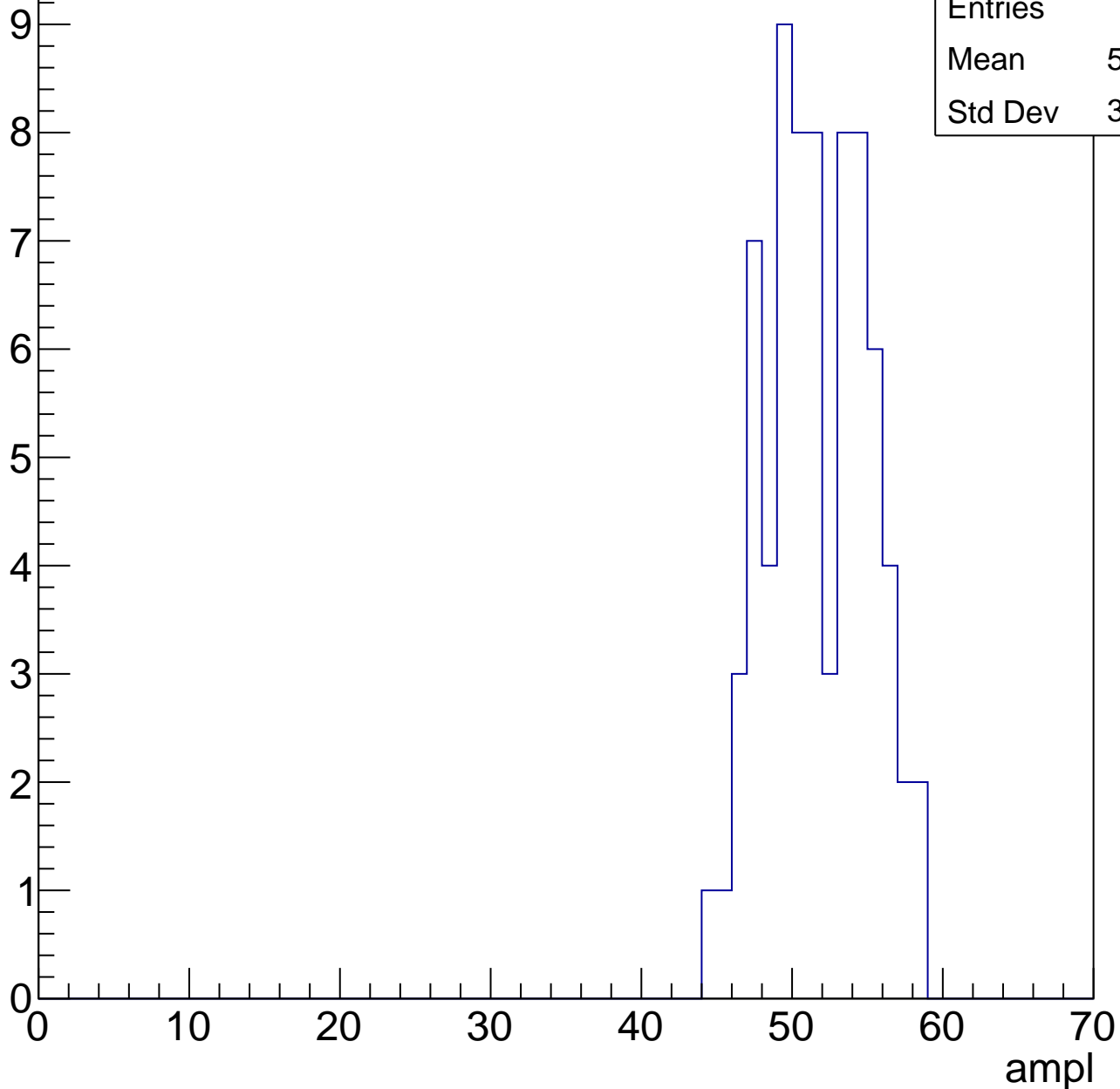
Gaus Width: 3.4375



B1L101S, U6-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



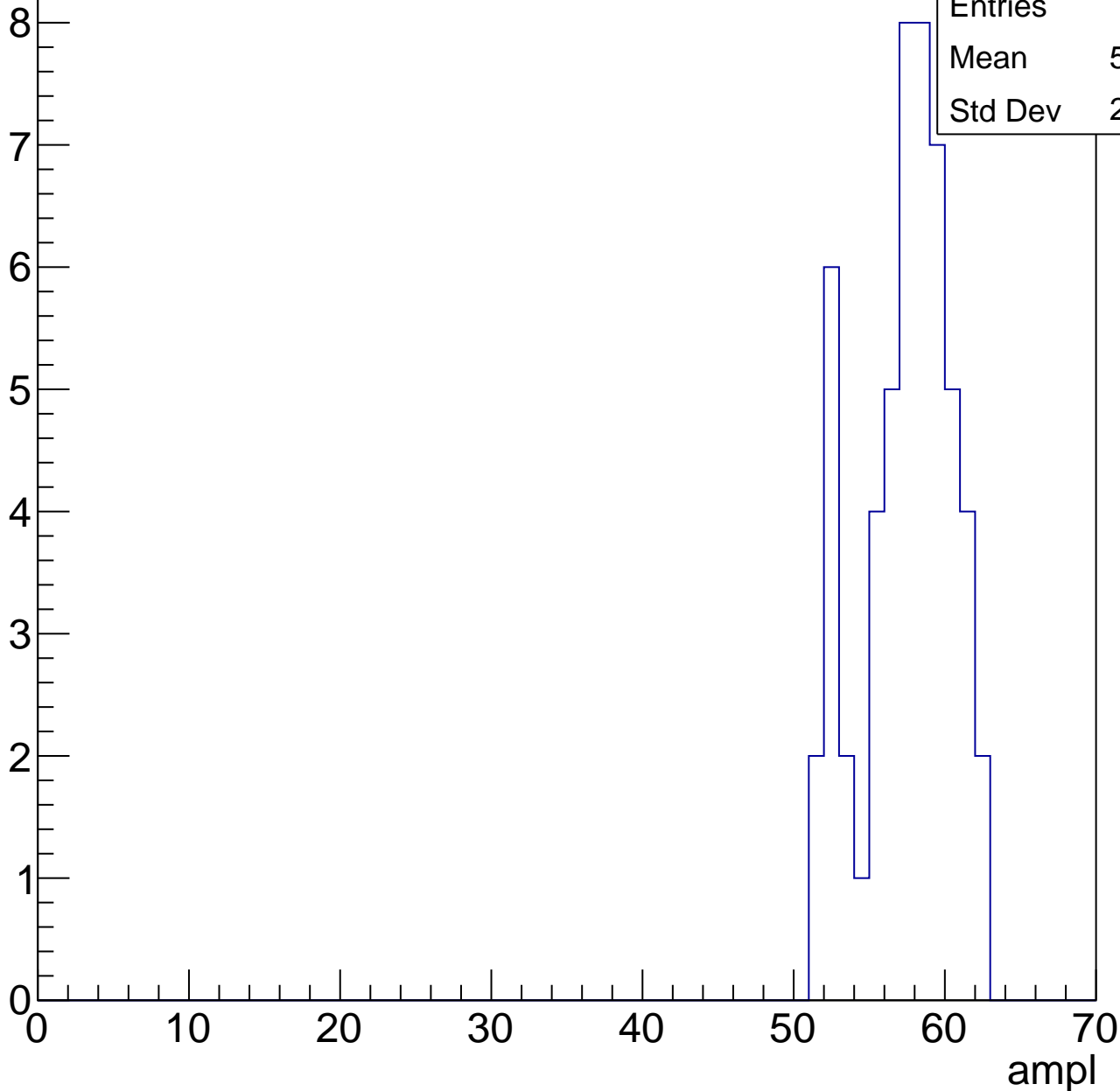
Entries	74
Mean	51.26
Std Dev	3.329

B1L101S, U6-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.94
Std Dev	2.978

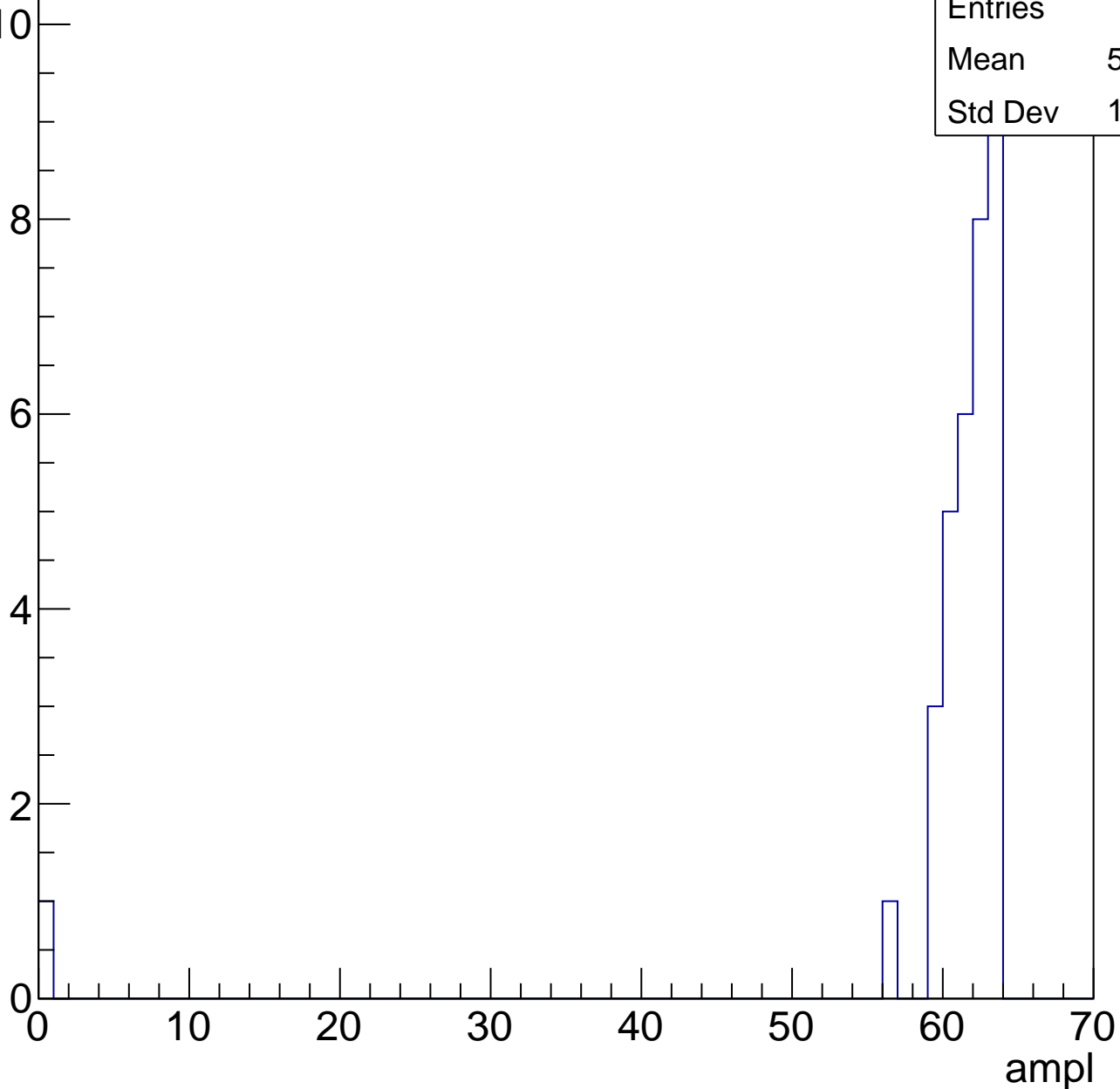


B1L101S, U6-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.56
Std Dev	10.49



B1L101S, U6-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch118, adc0

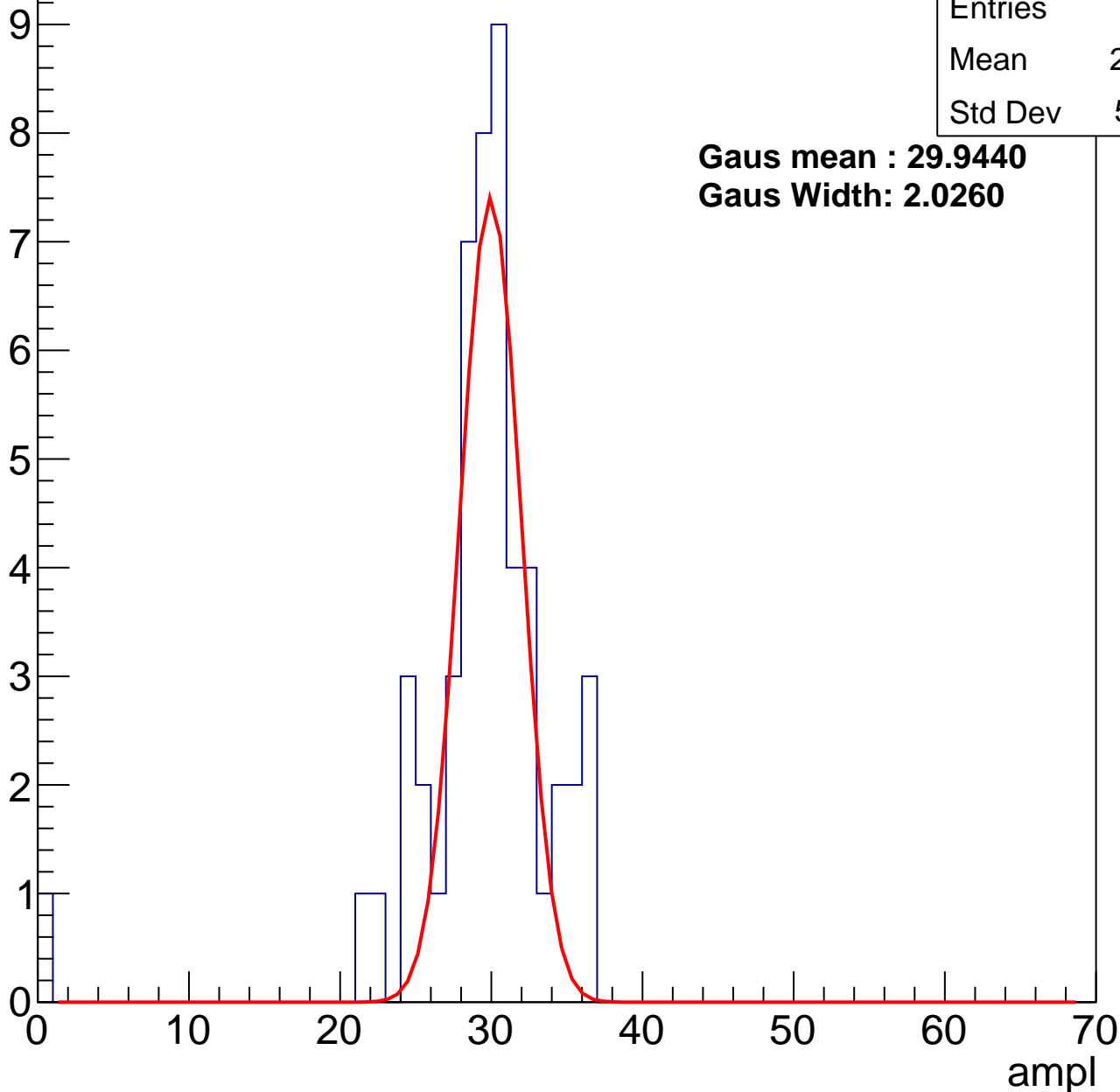
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	28.87
Std Dev	5.251

Gaus mean : 29.9440

Gaus Width: 2.0260



B1L101S, U6-ch118, adc1

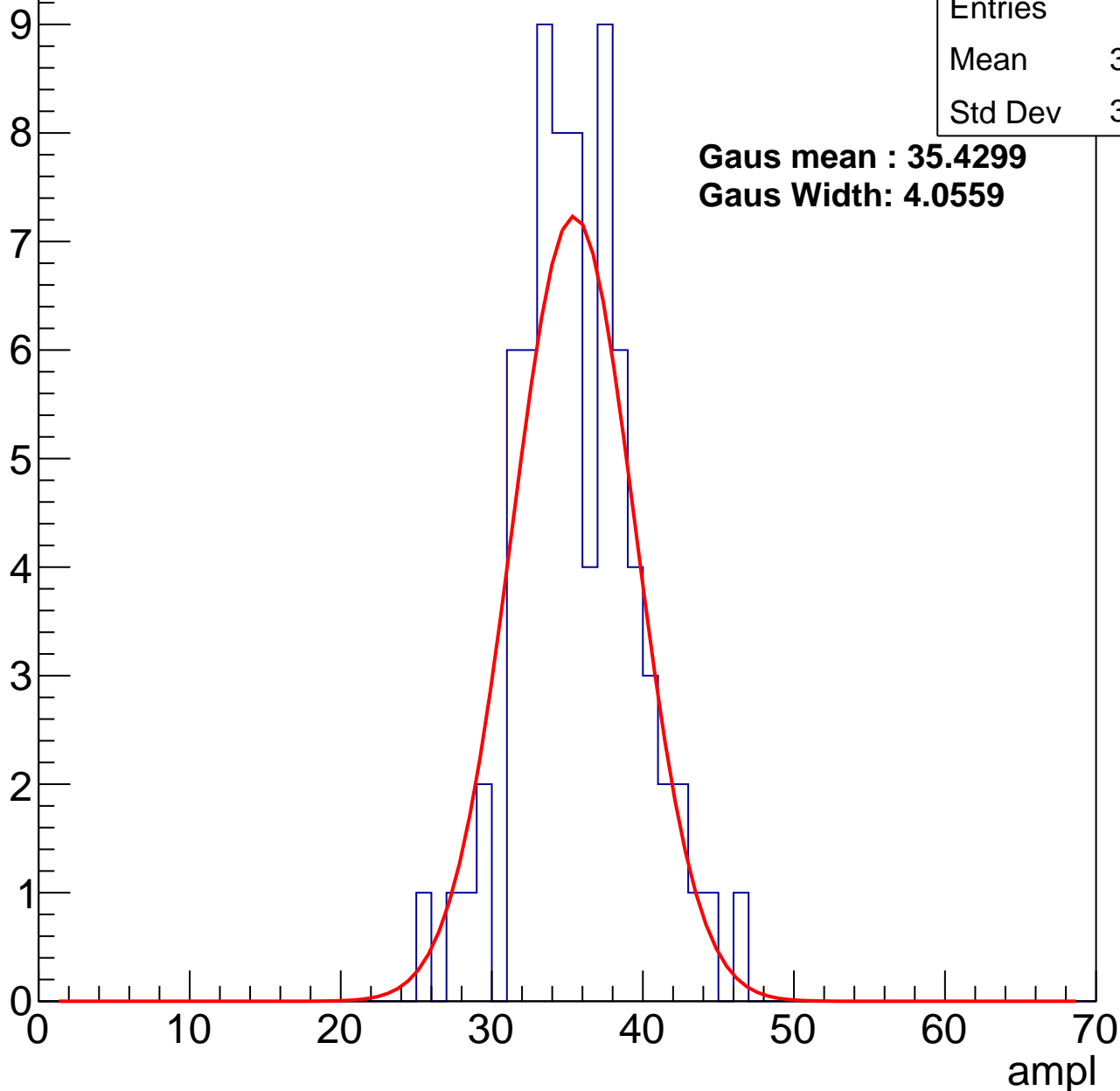
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.27
Std Dev	3.903

Gaus mean : 35.4299

Gaus Width: 4.0559

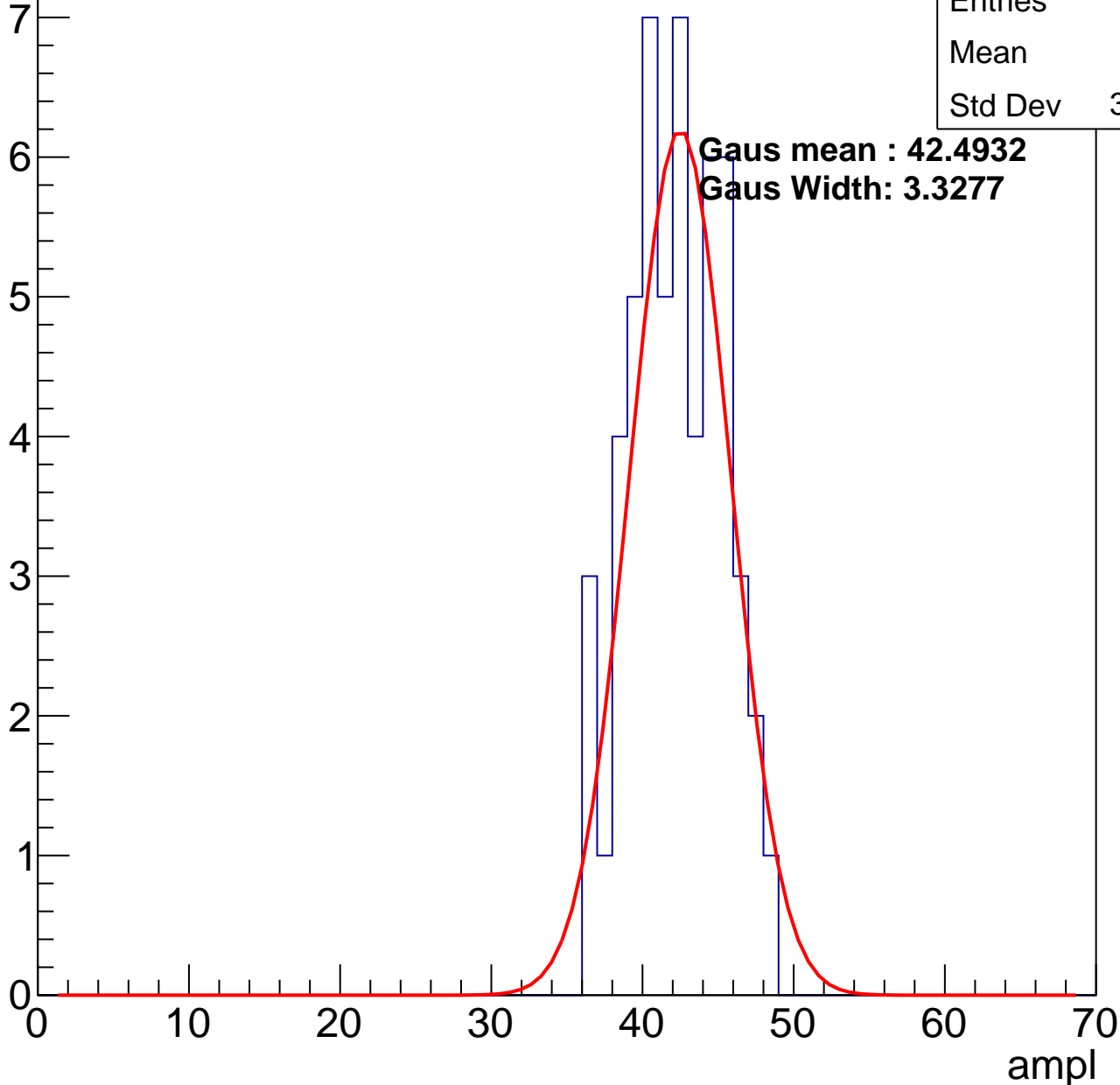


B1L101S, U6-ch118, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.8
Std Dev	3.009

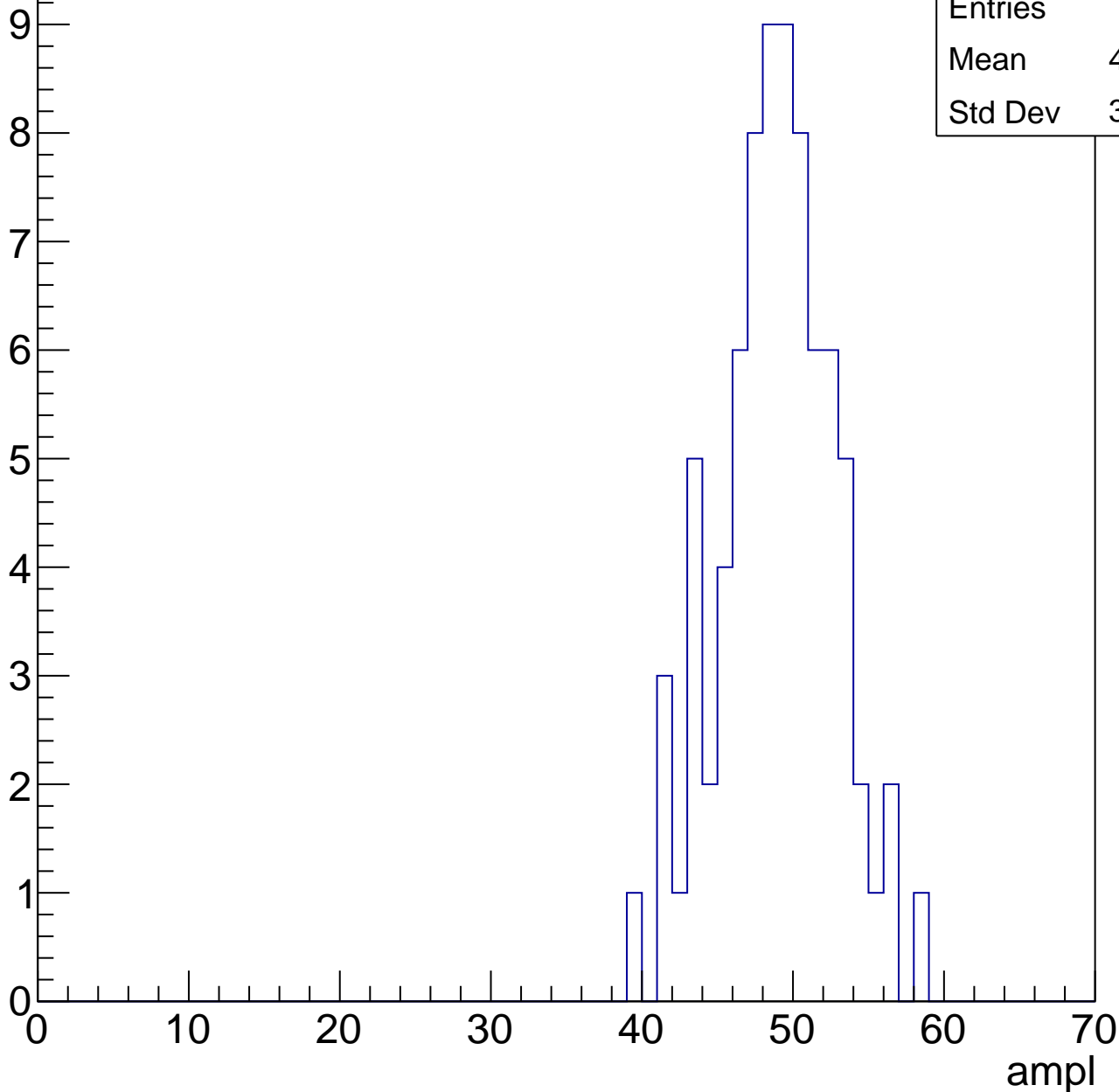


B1L101S, U6-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	48.46
Std Dev	3.795

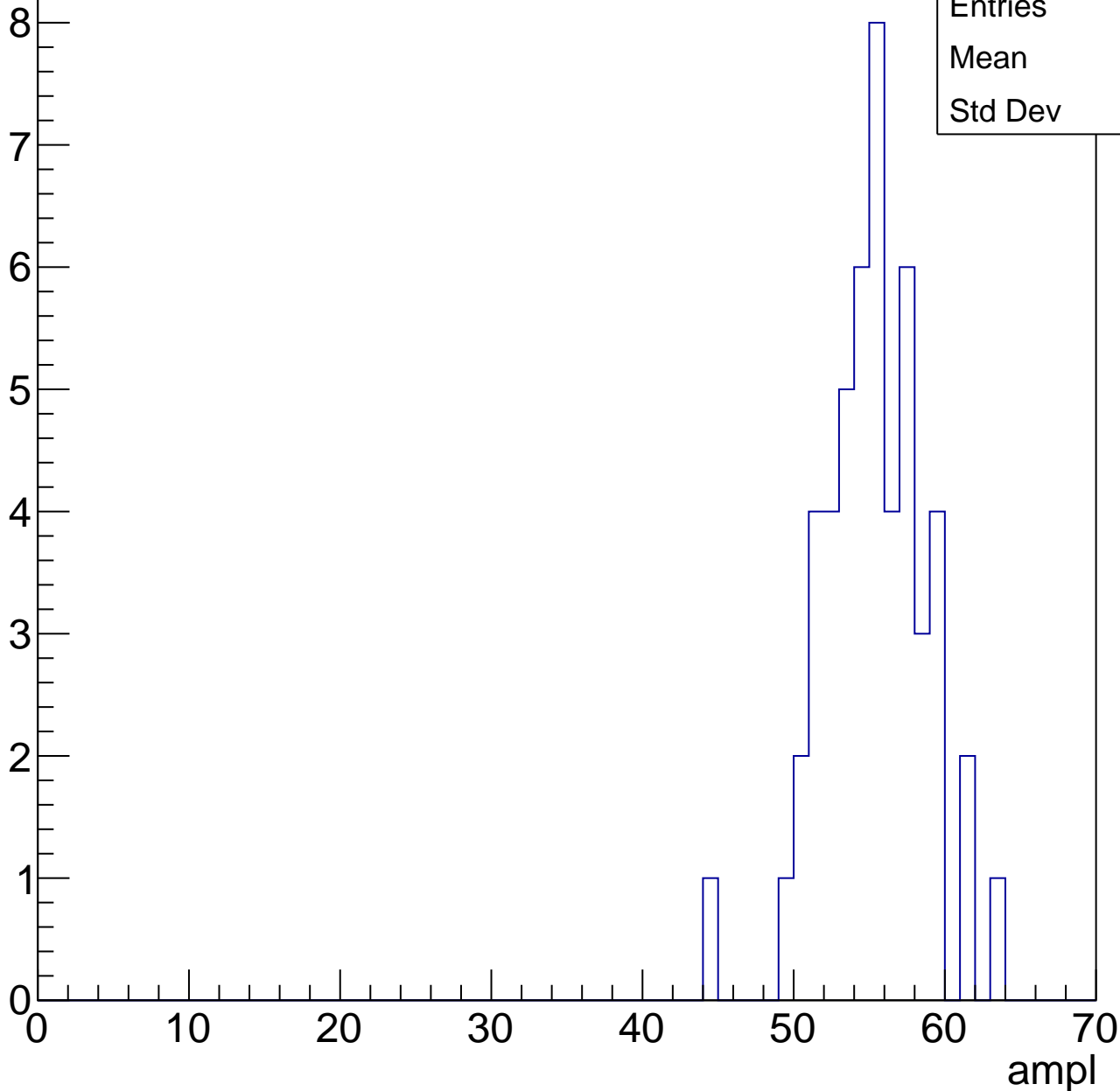


B1L101S, U6-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	54.8
Std Dev	3.39

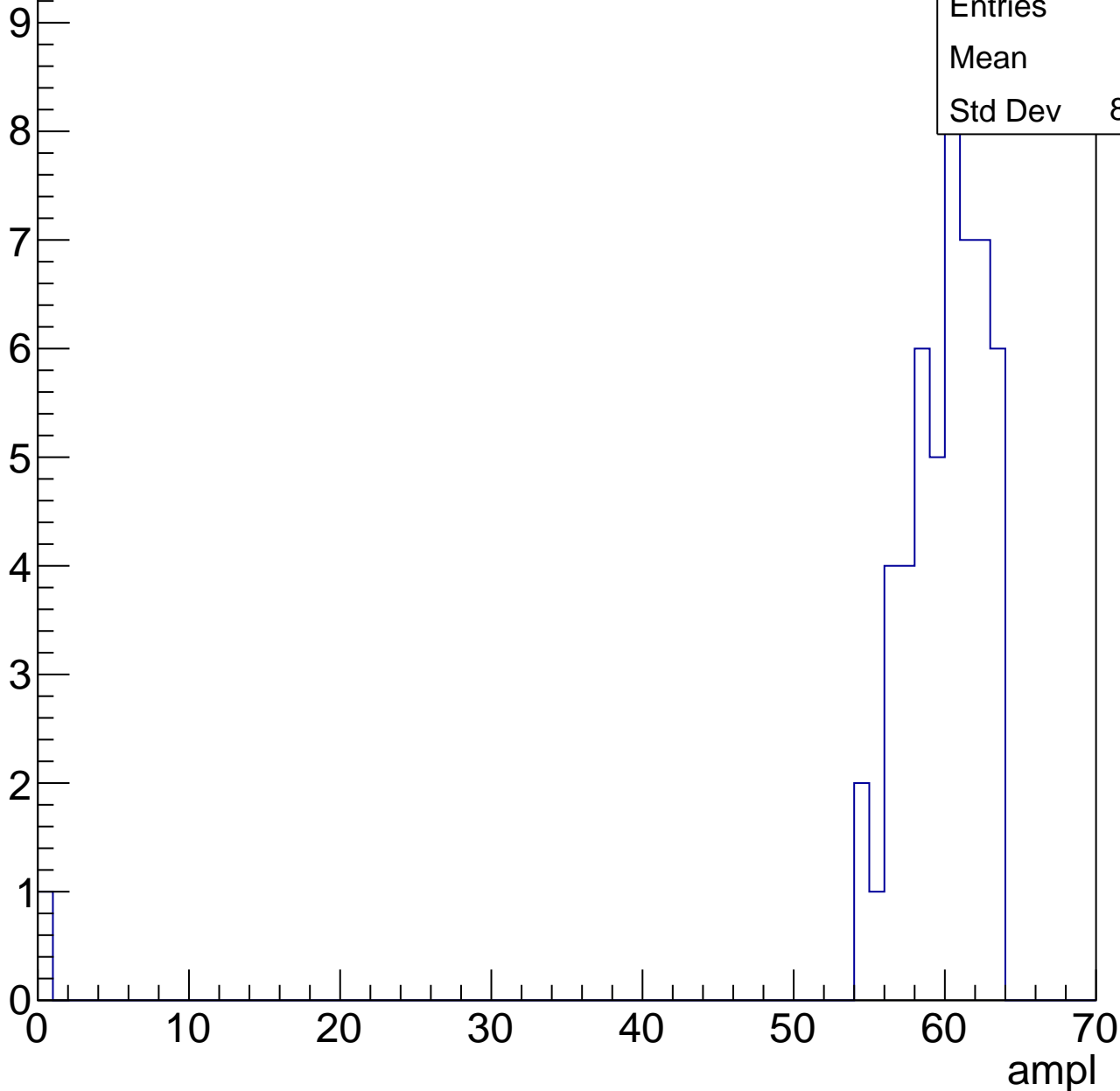


B1L101S, U6-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

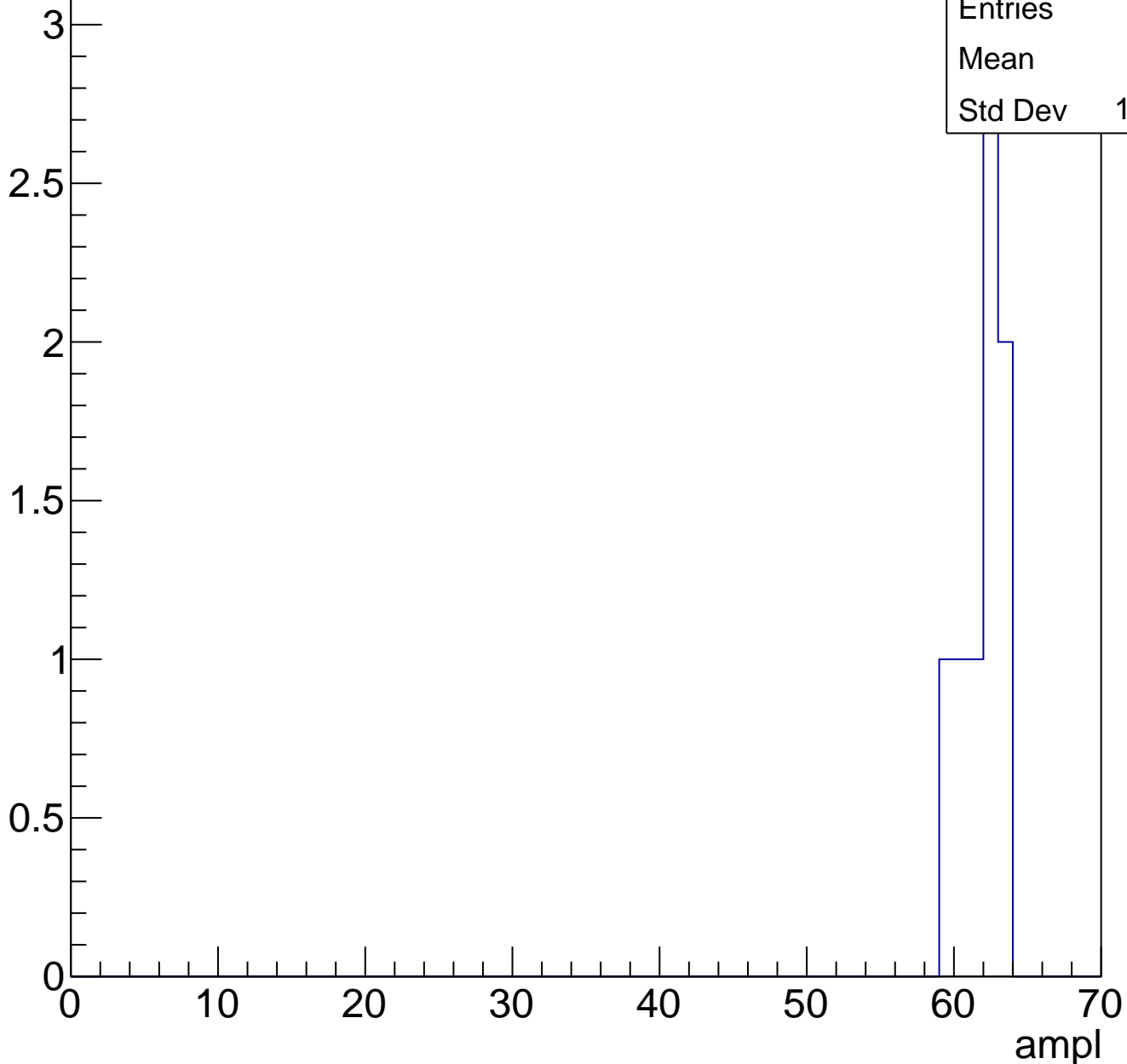
Entries	52
Mean	58.4
Std Dev	8.529



B1L101S, U6-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch119, adc0

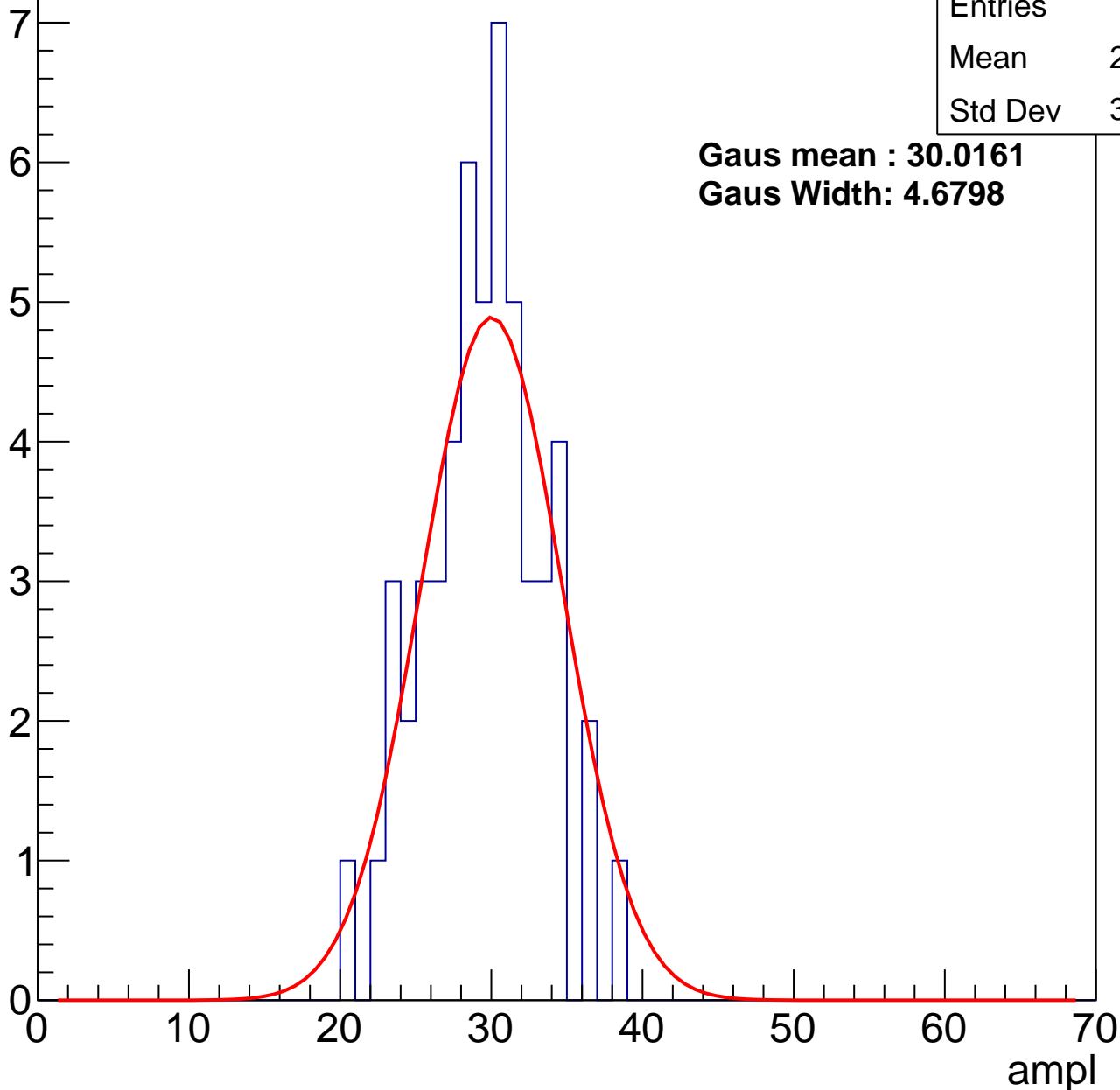
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.04
Std Dev	3.802

Gaus mean : 30.0161

Gaus Width: 4.6798



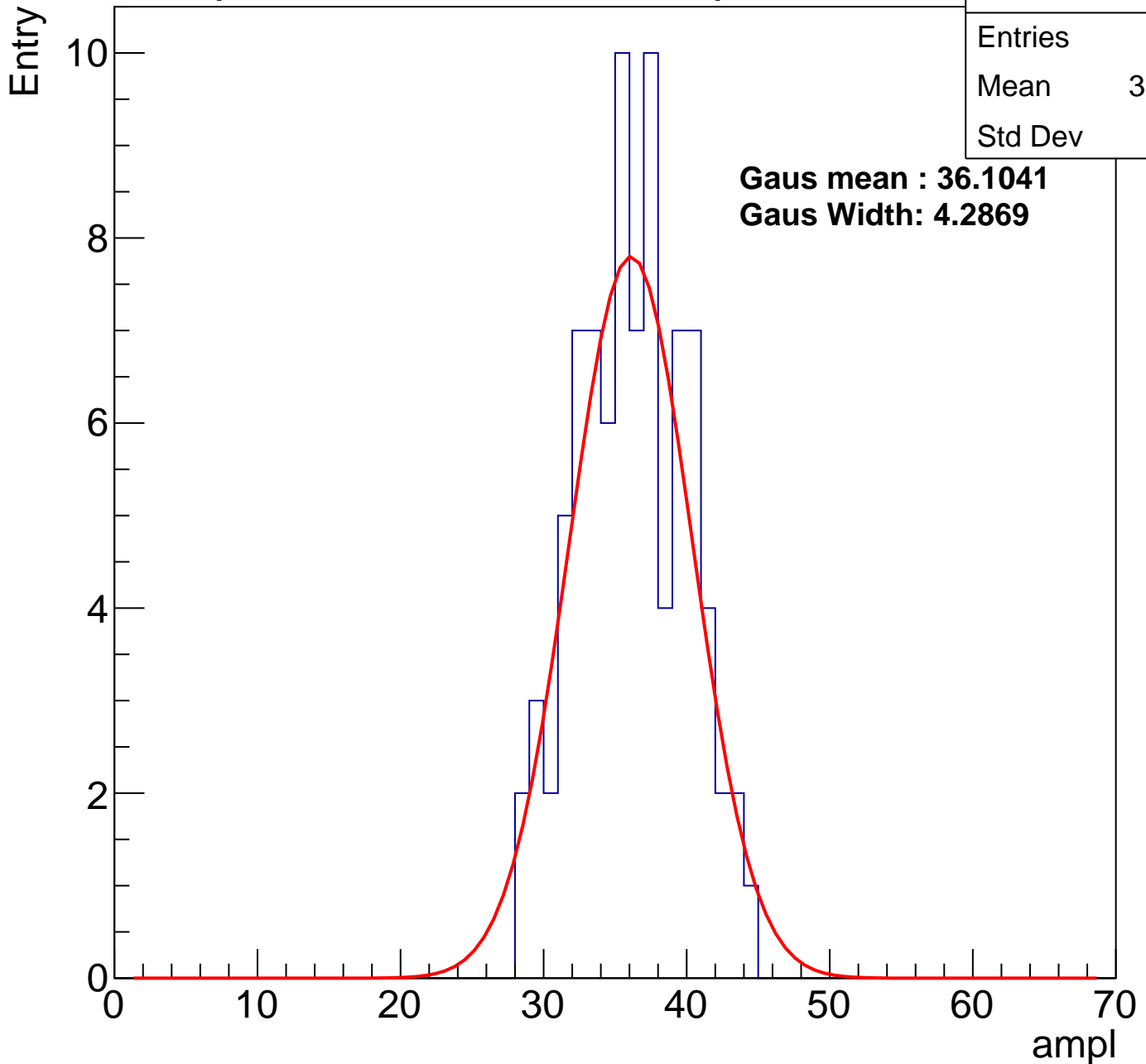
B1L101S, U6-ch119, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	35.72
Std Dev	3.75

Gaus mean : 36.1041

Gaus Width: 4.2869

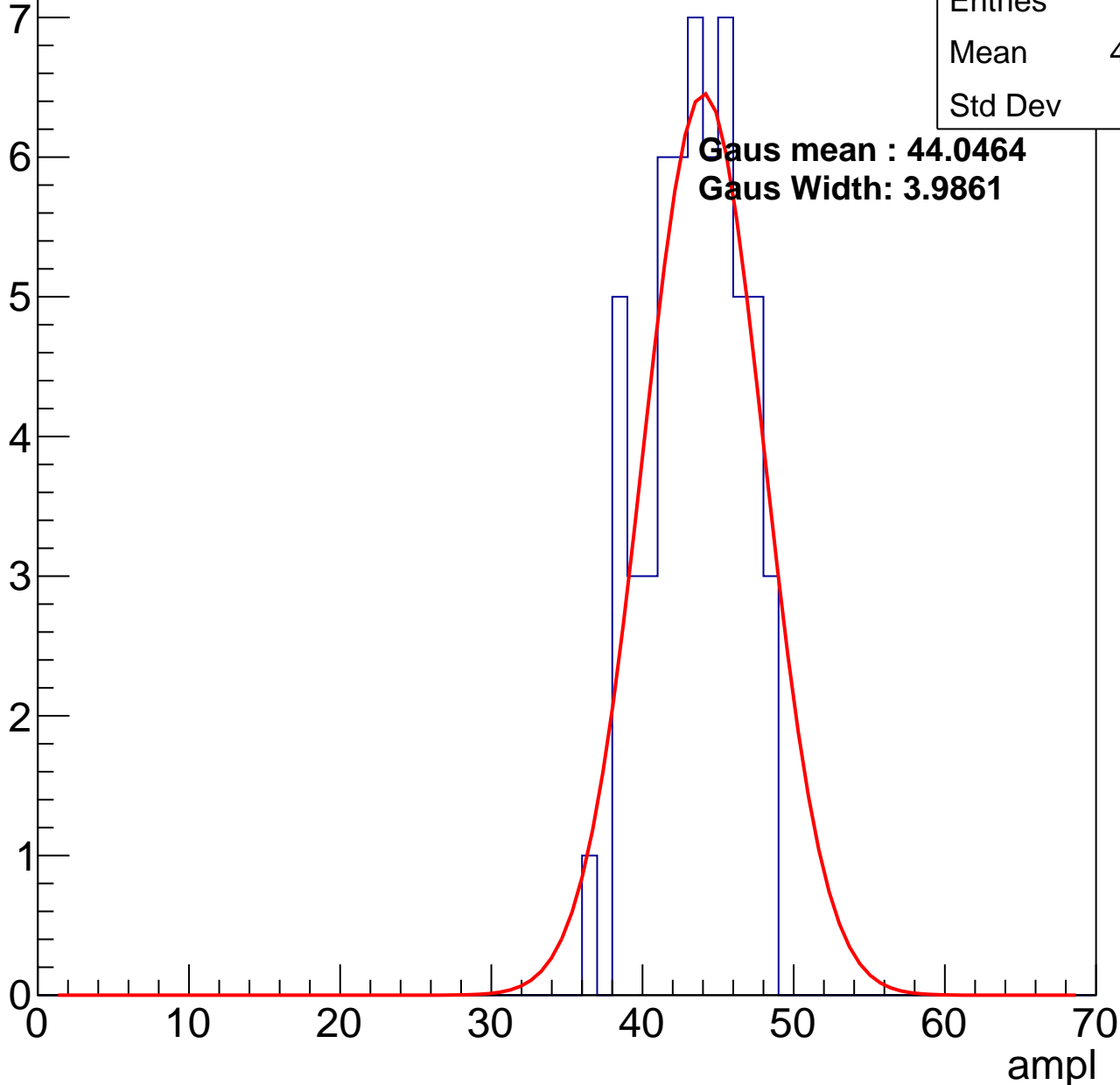


B1L101S, U6-ch119, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.98
Std Dev	3



B1L101S, U6-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

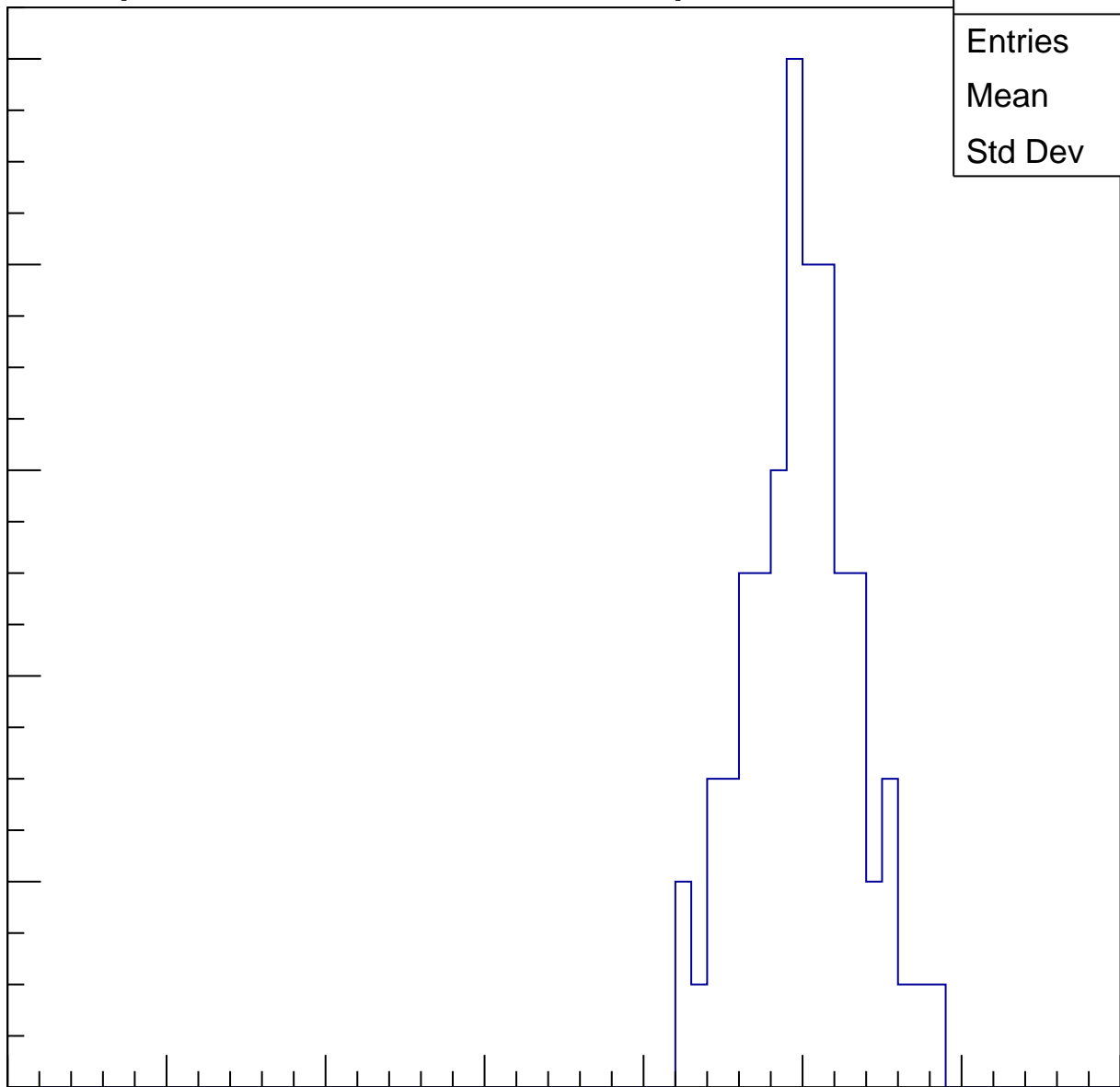
Entries	69
Mean	49.48
Std Dev	3.467

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

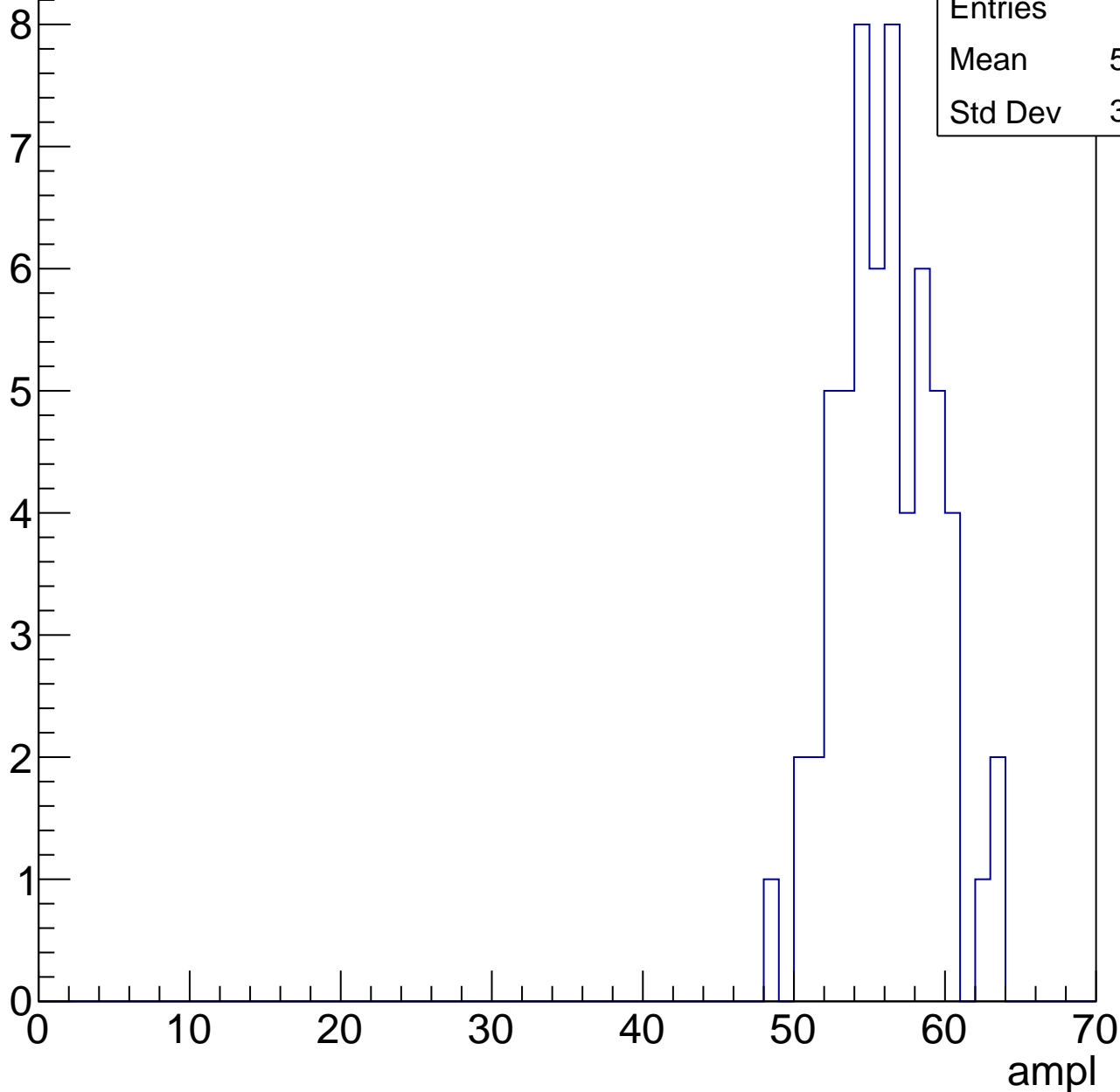


B1L101S, U6-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.66
Std Dev	3.224



B1L101S, U6-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

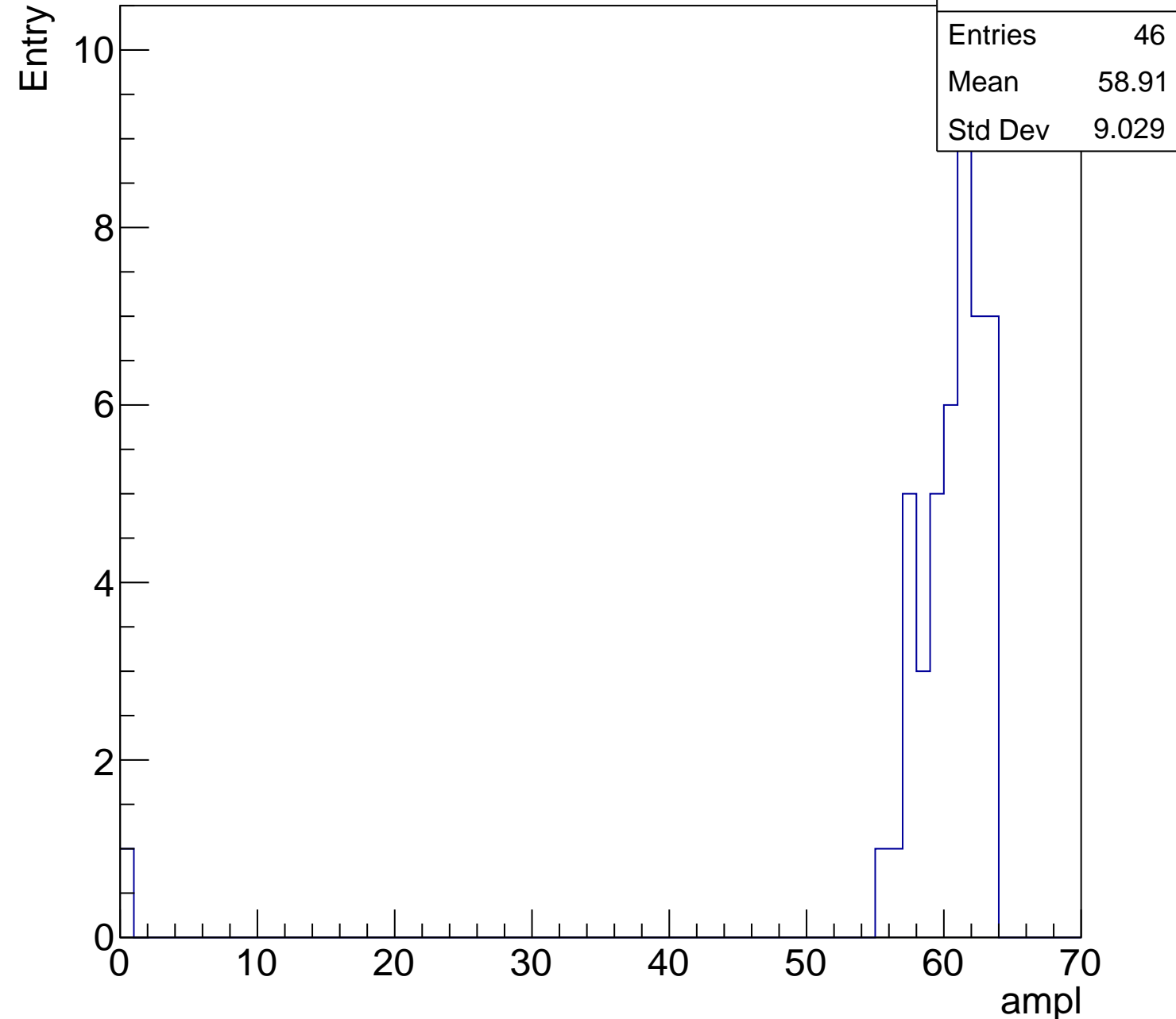
40

50

60

ampl

Entries	46
Mean	58.91
Std Dev	9.029



B1L101S, U6-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U6-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

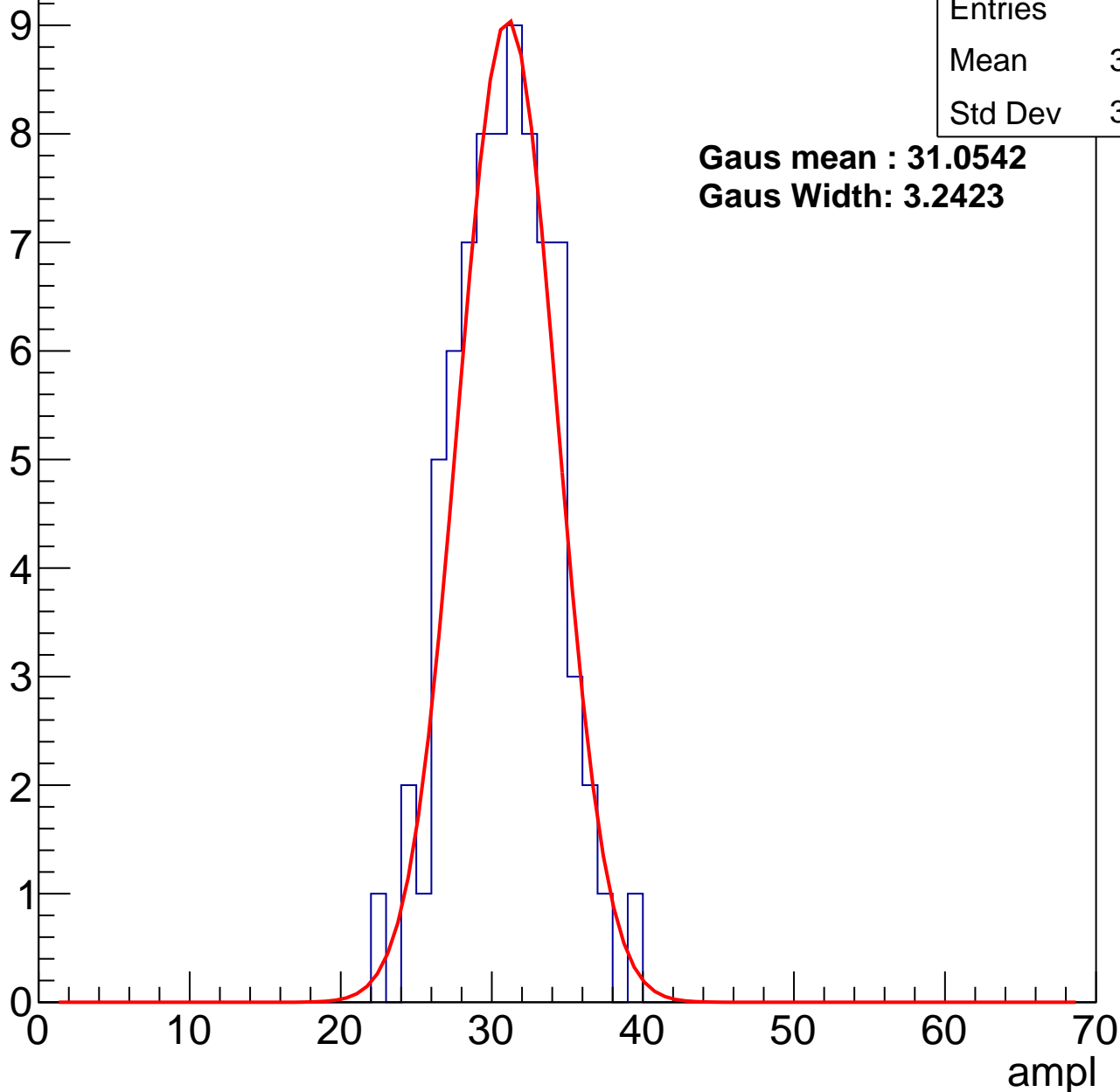


Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch120, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch120, adc1

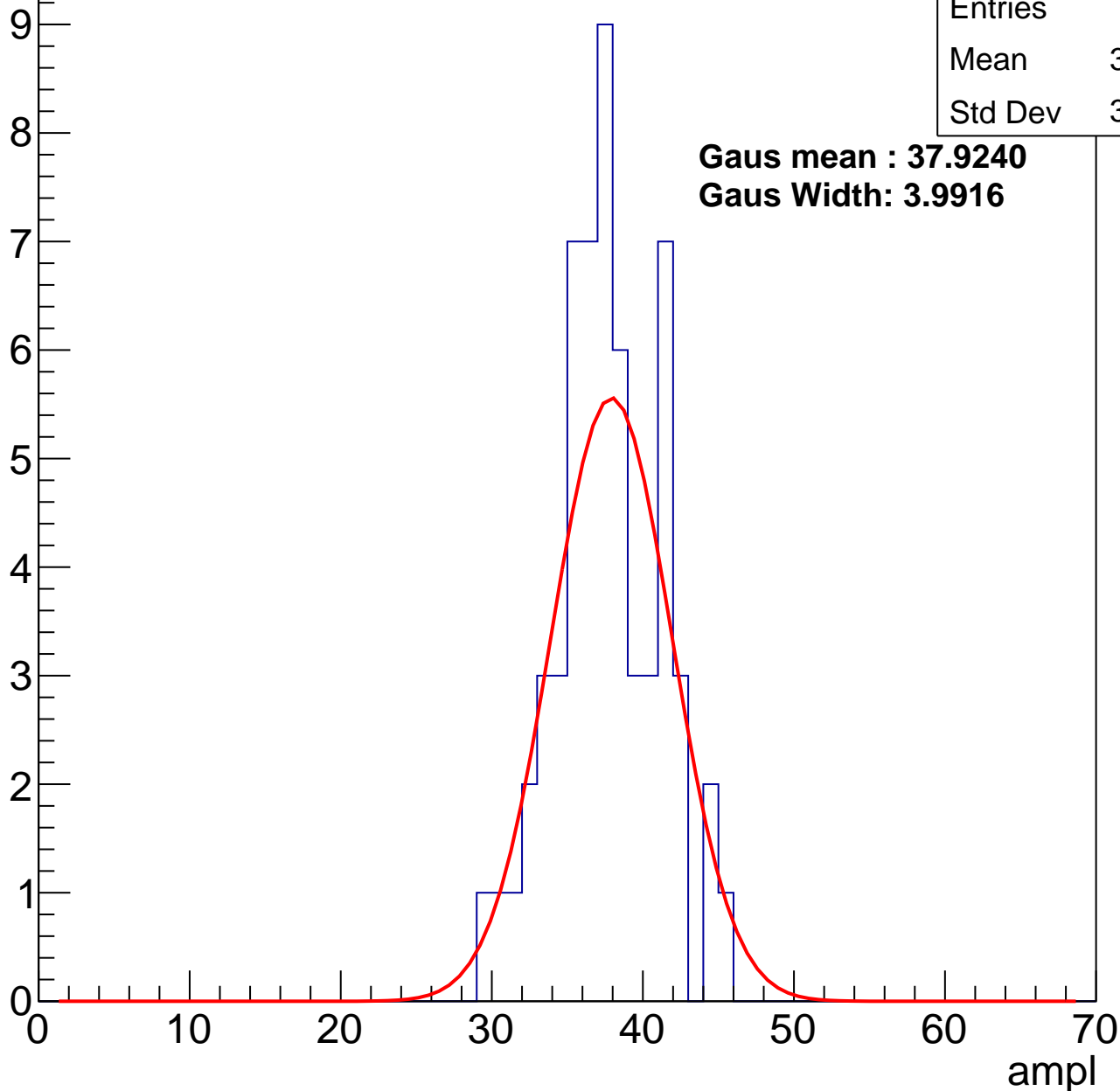
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.22
Std Dev	3.445

Gaus mean : 37.9240

Gaus Width: 3.9916



B1L101S, U6-ch120, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	43.35
Std Dev	3.523

Gaus mean : 43.6336

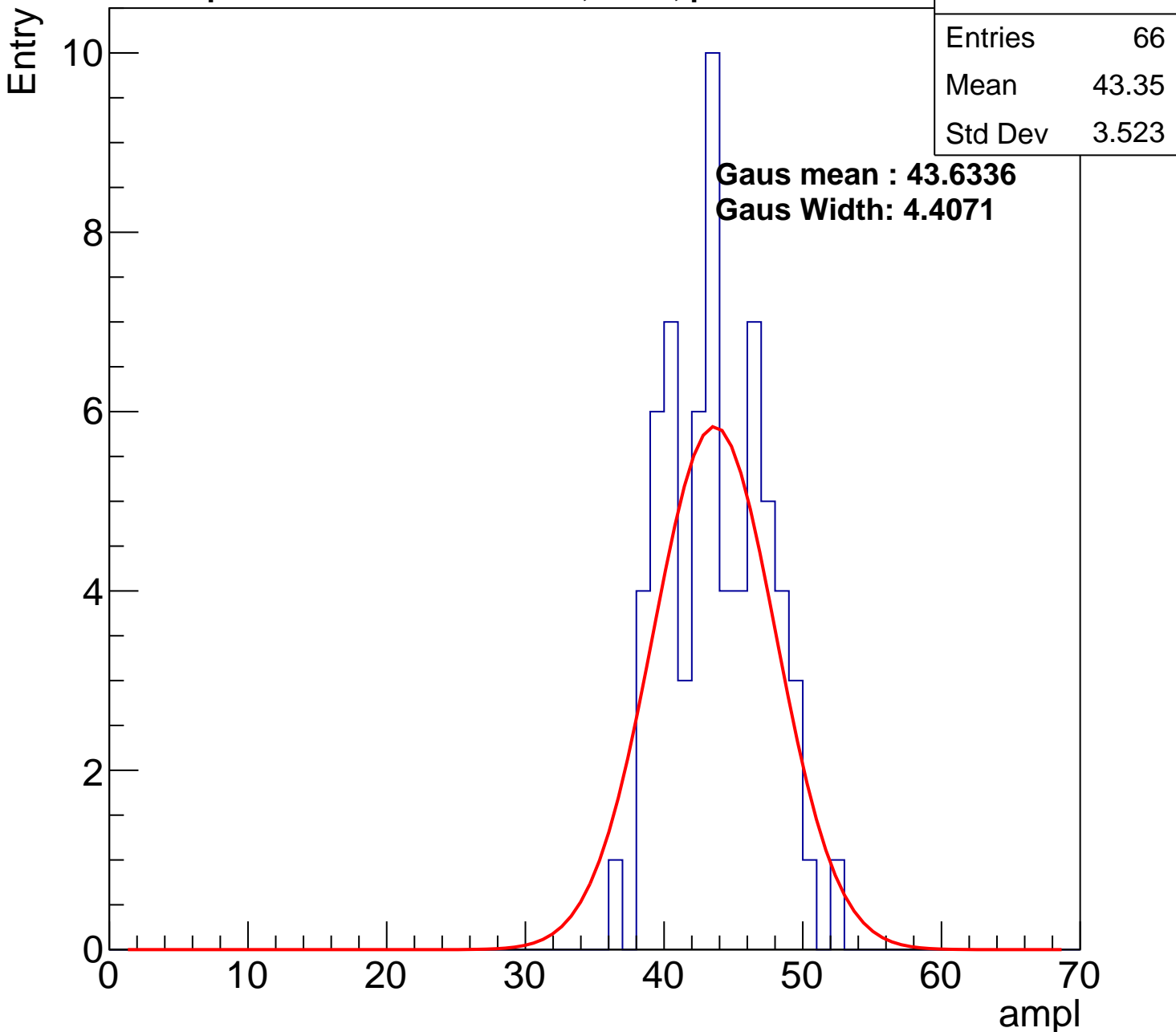
Gaus Width: 4.4071

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

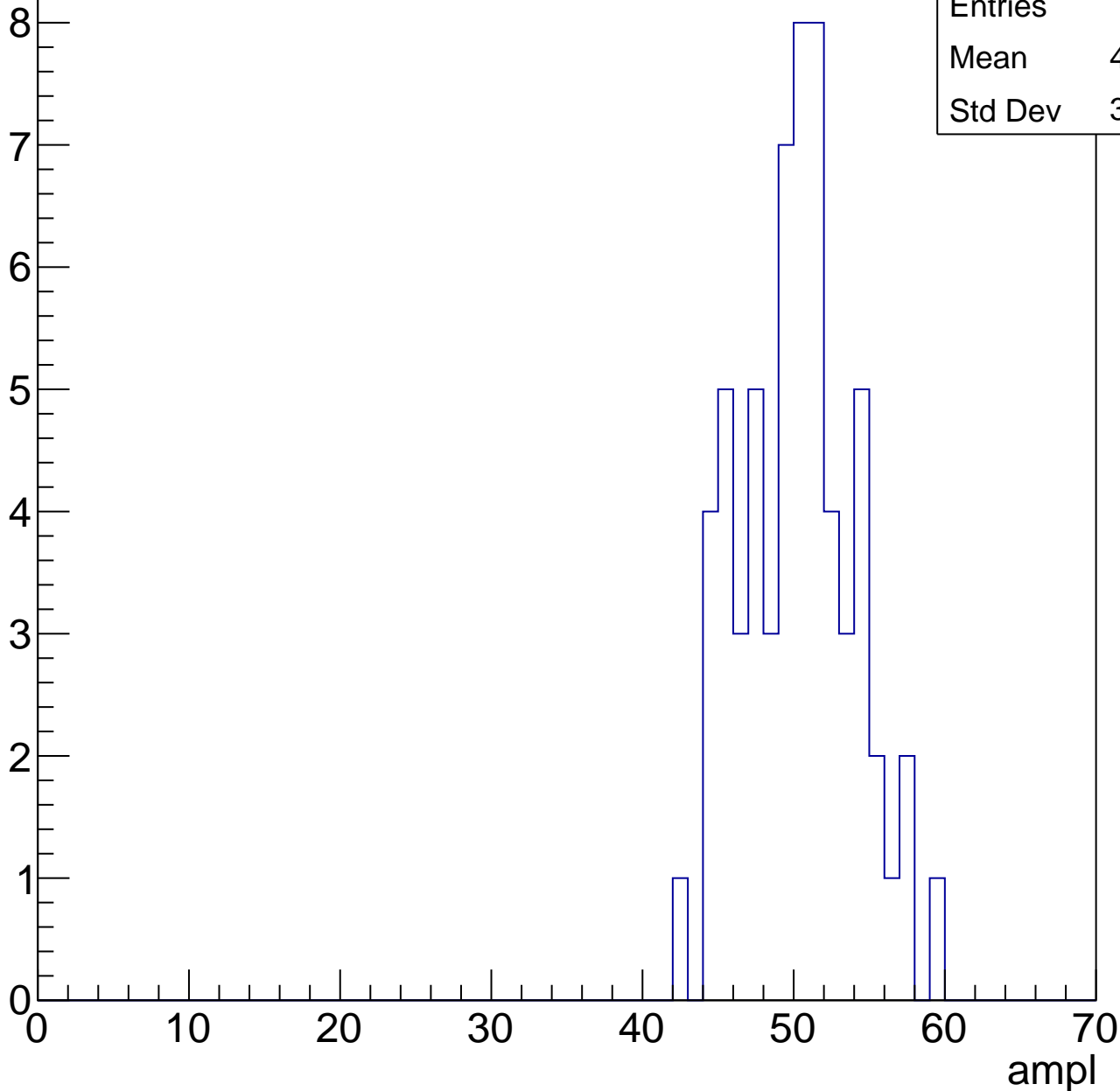


B1L101S, U6-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.79
Std Dev	3.677

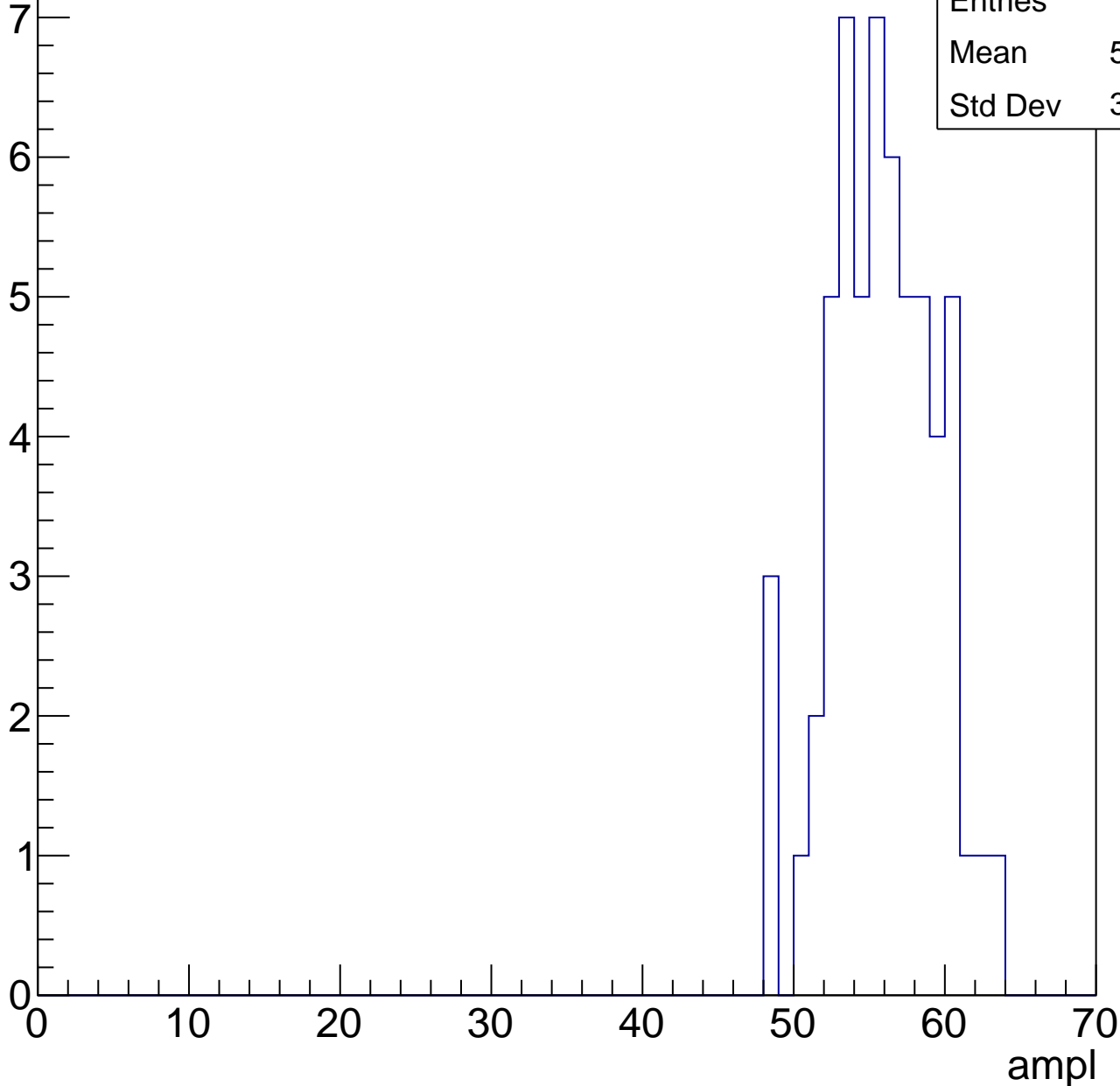


B1L101S, U6-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.43
Std Dev	3.425

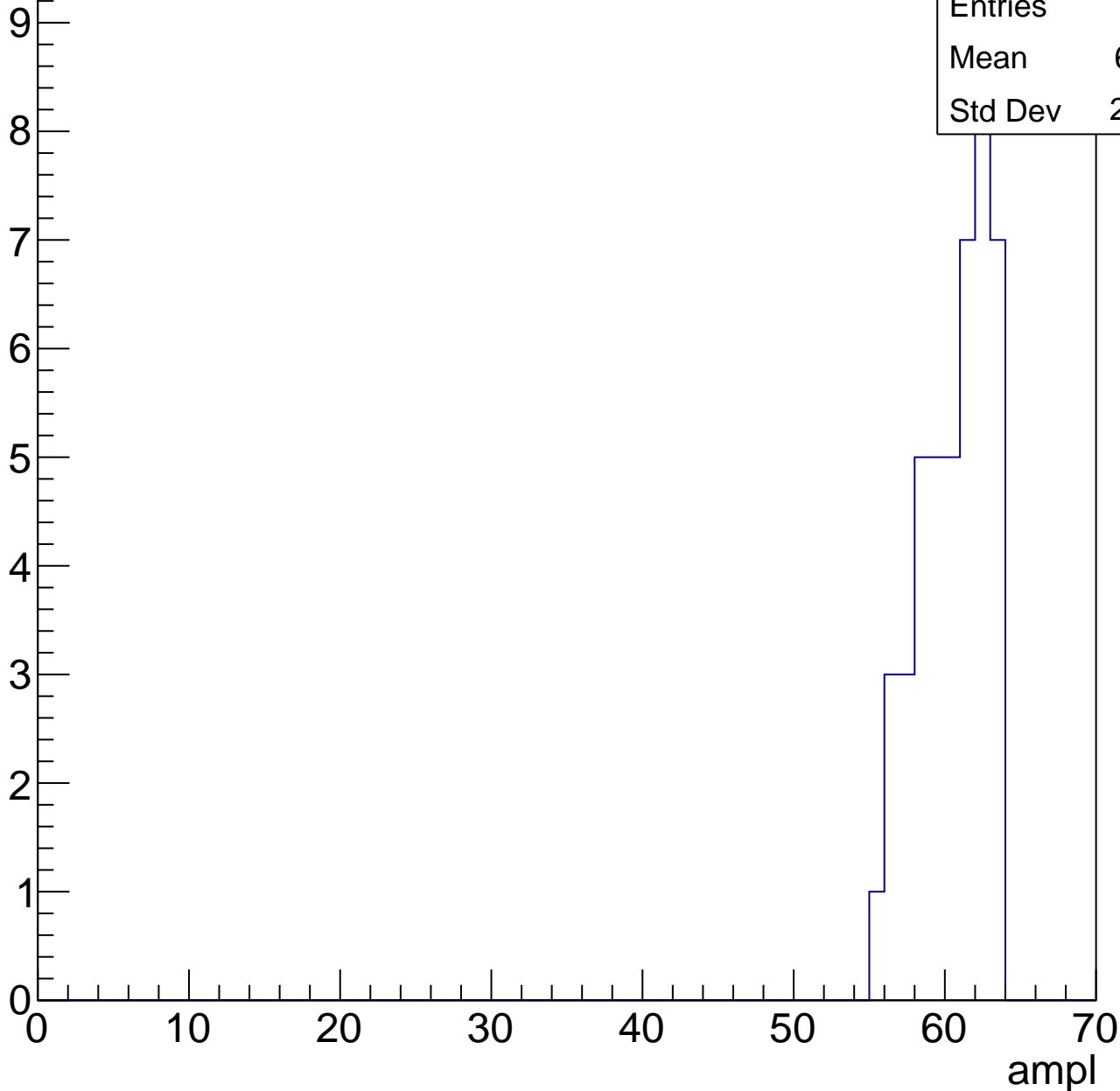


B1L101S, U6-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

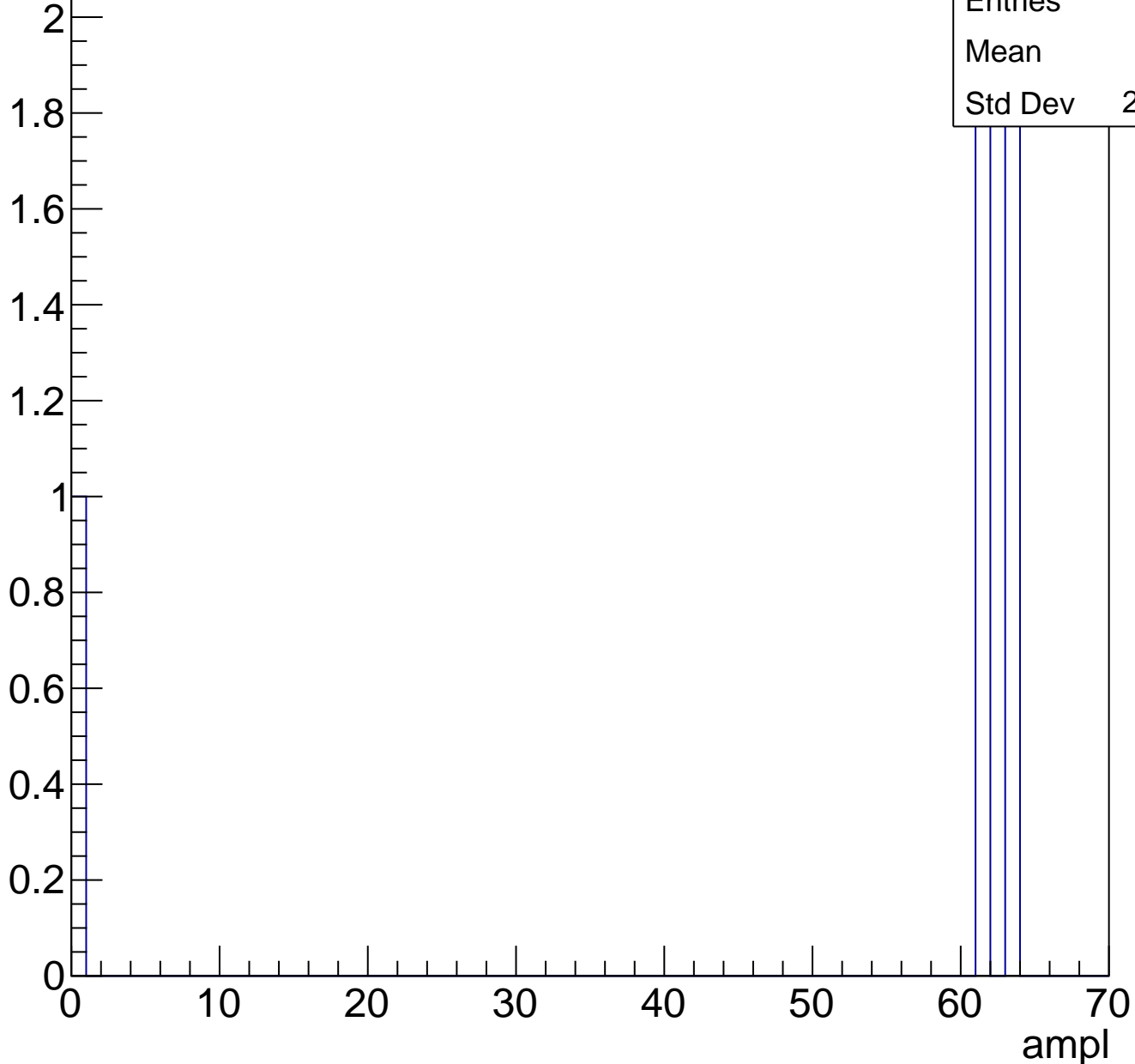
Entries	45
Mean	60.11
Std Dev	2.263



B1L101S, U6-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch121, adc0

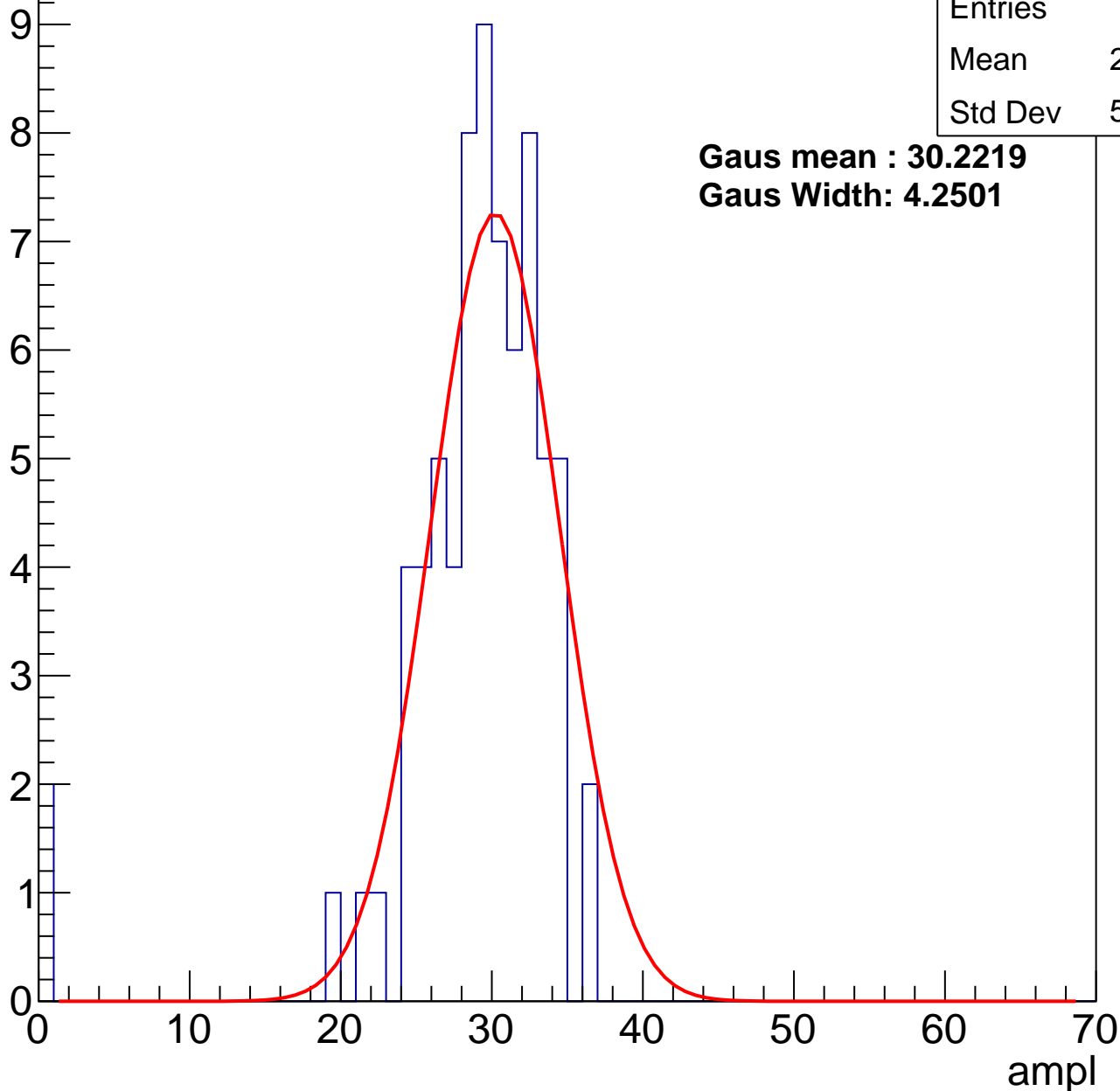
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.33
Std Dev	5.895

Gaus mean : 30.2219

Gaus Width: 4.2501



B1L101S, U6-ch121, adc1

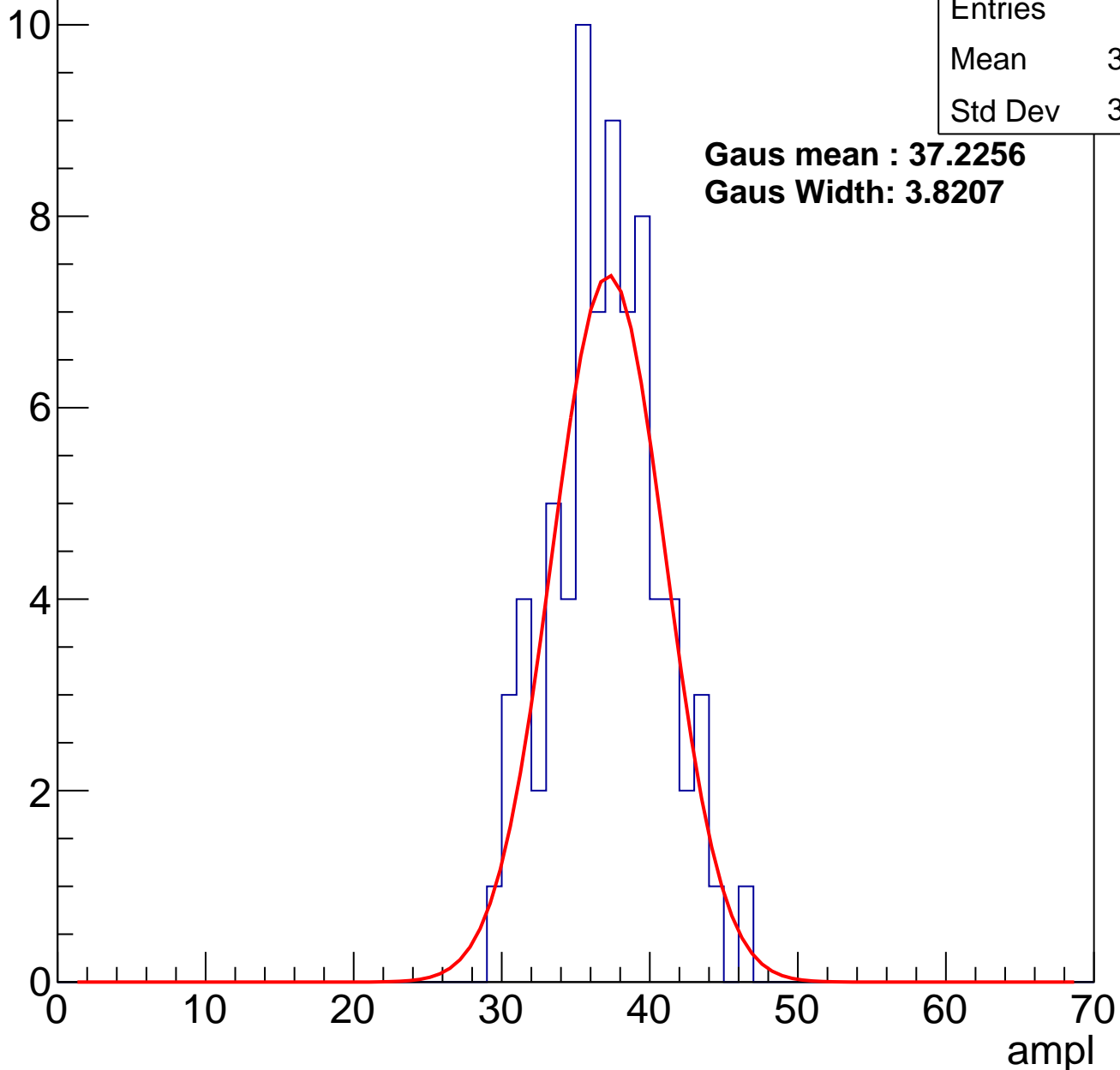
calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.64
Std Dev	3.625

Gaus mean : 37.2256

Gaus Width: 3.8207

Entry



B1L101S, U6-ch121, adc2

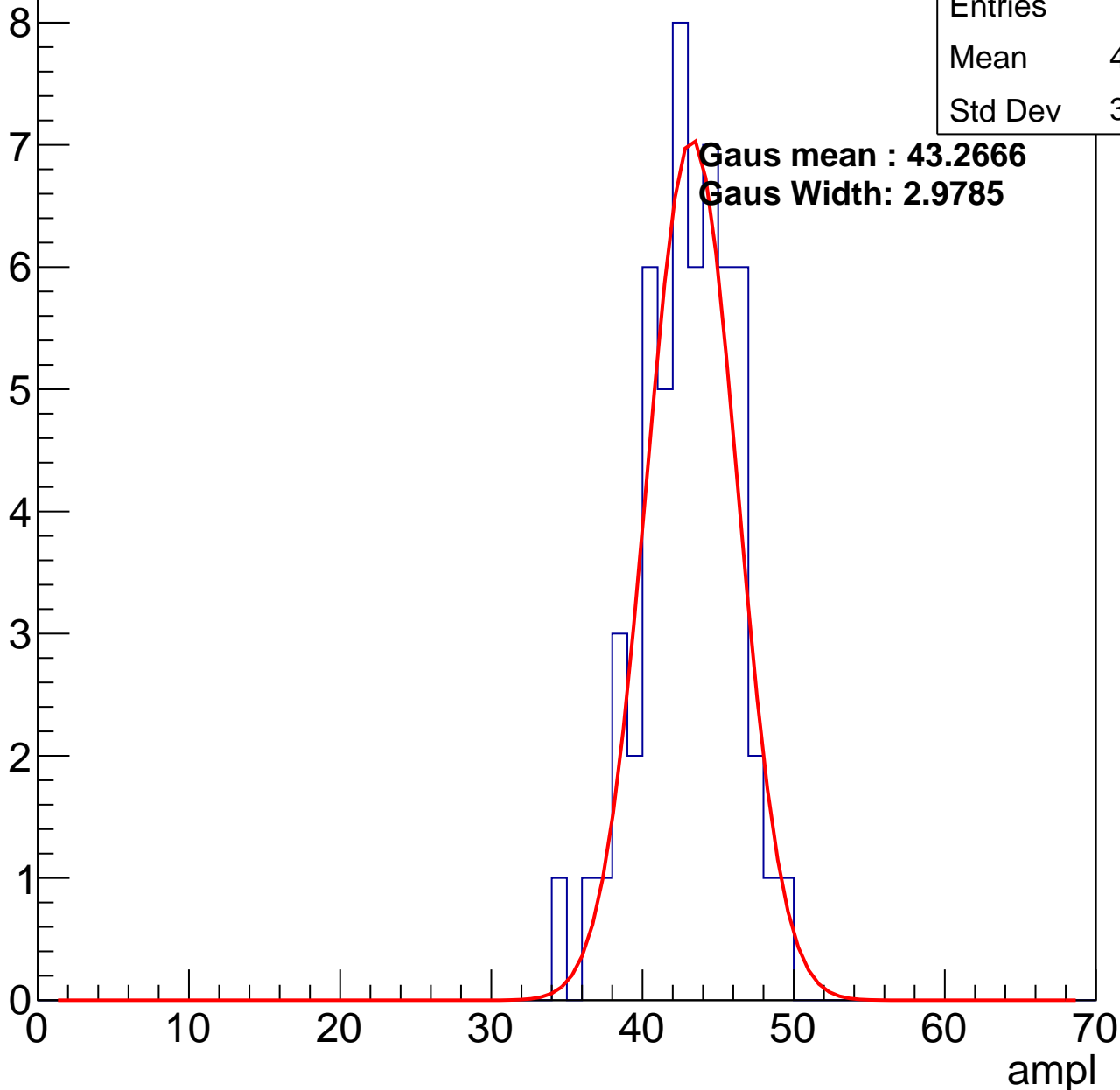
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.55
Std Dev	3.053

Gaus mean : 43.2666

Gaus Width: 2.9785



B1L101S, U6-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	50.2
Std Dev	3.676

Entry

10

8

6

4

2

0

0

10

20

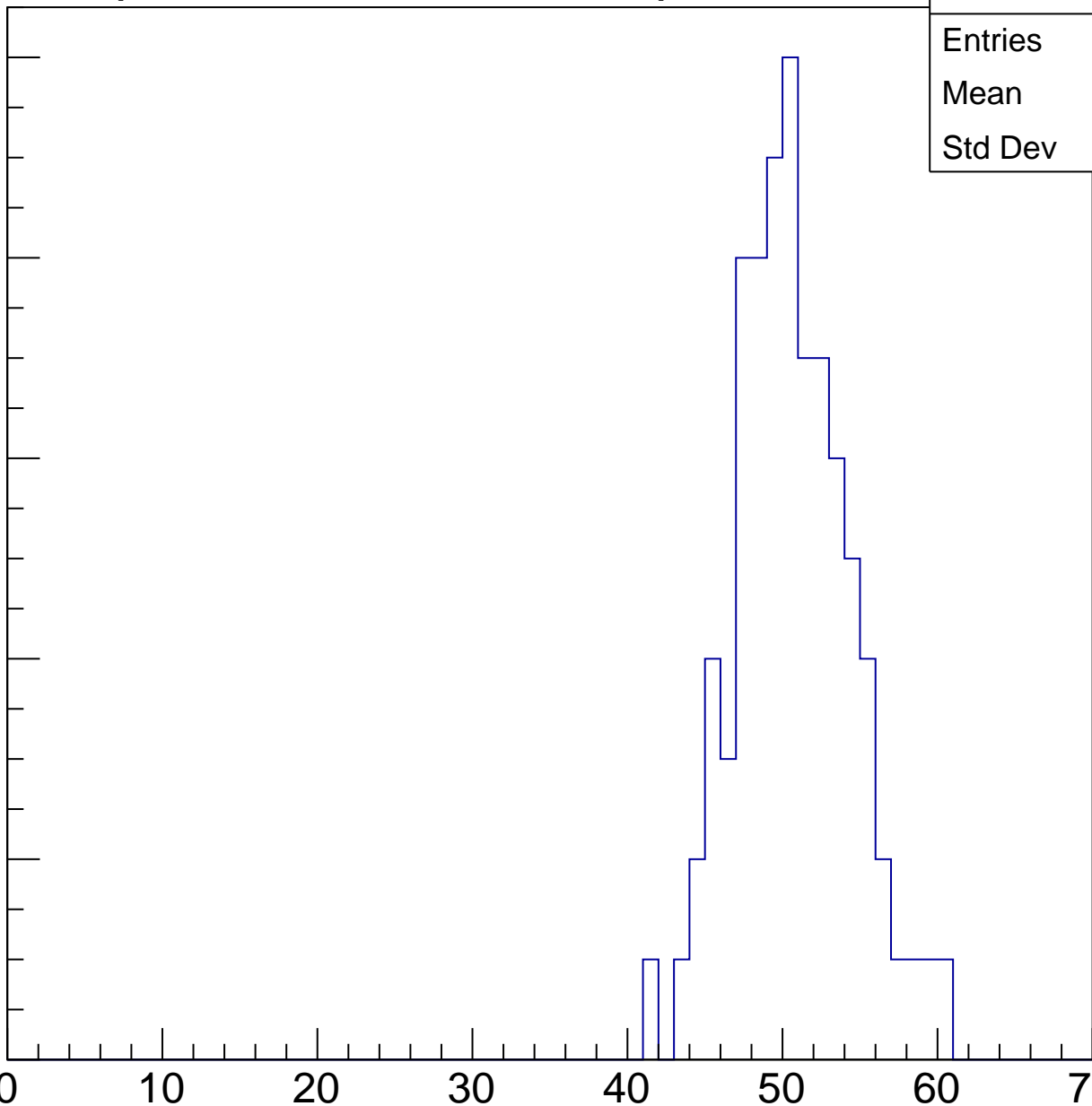
30

40

50

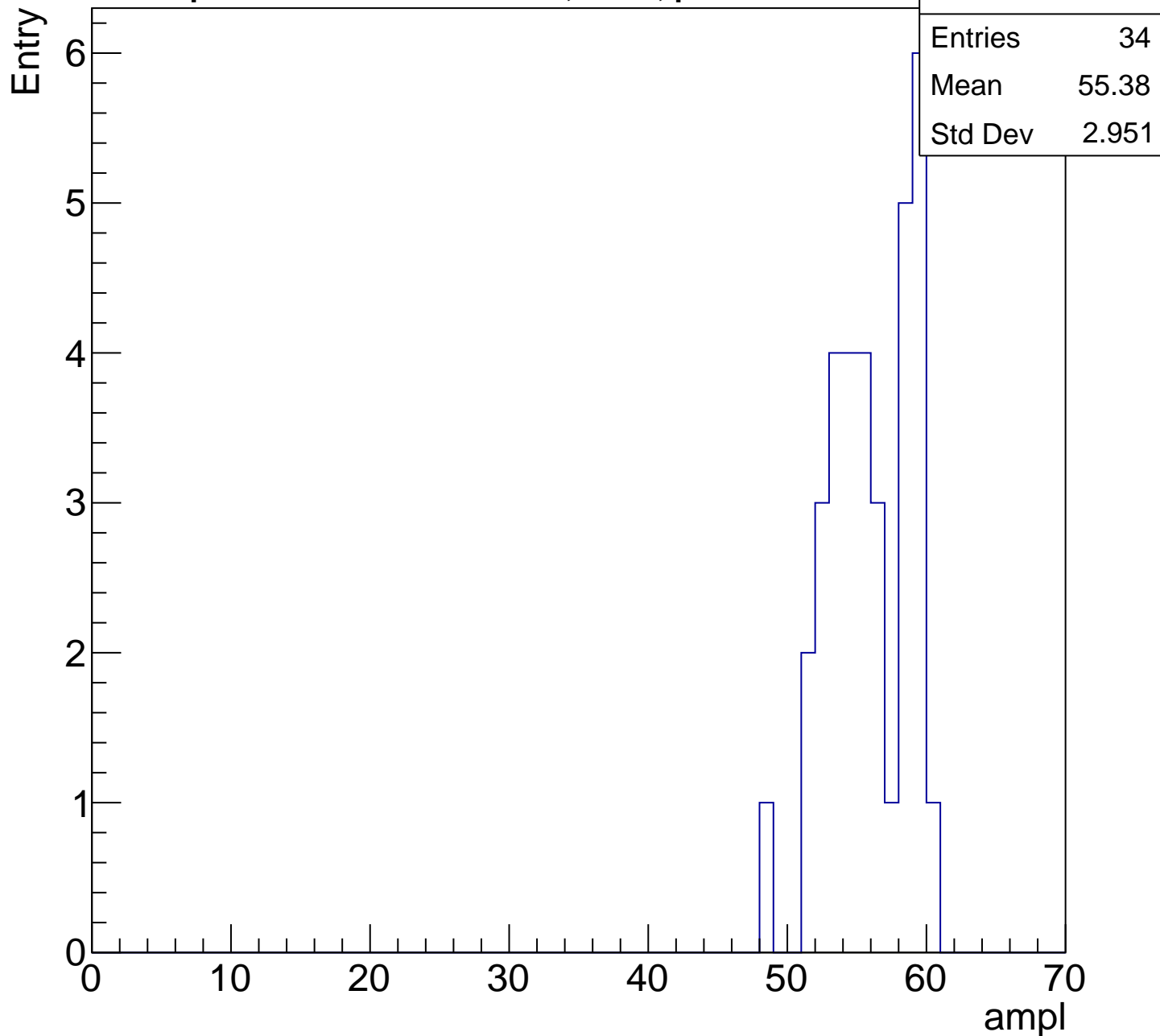
60

ampl



B1L101S, U6-ch121, adc4

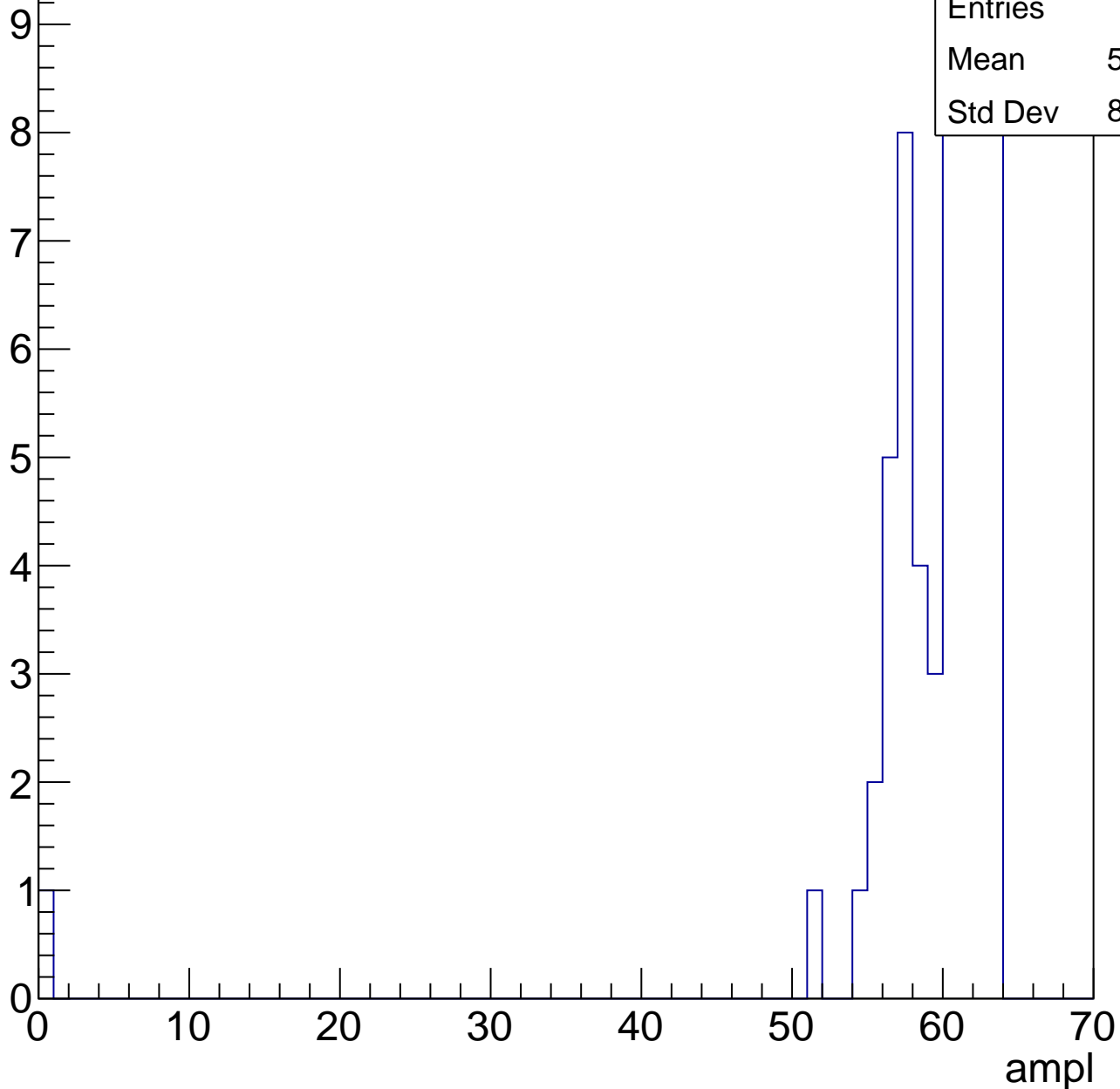
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U6-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch122, adc0

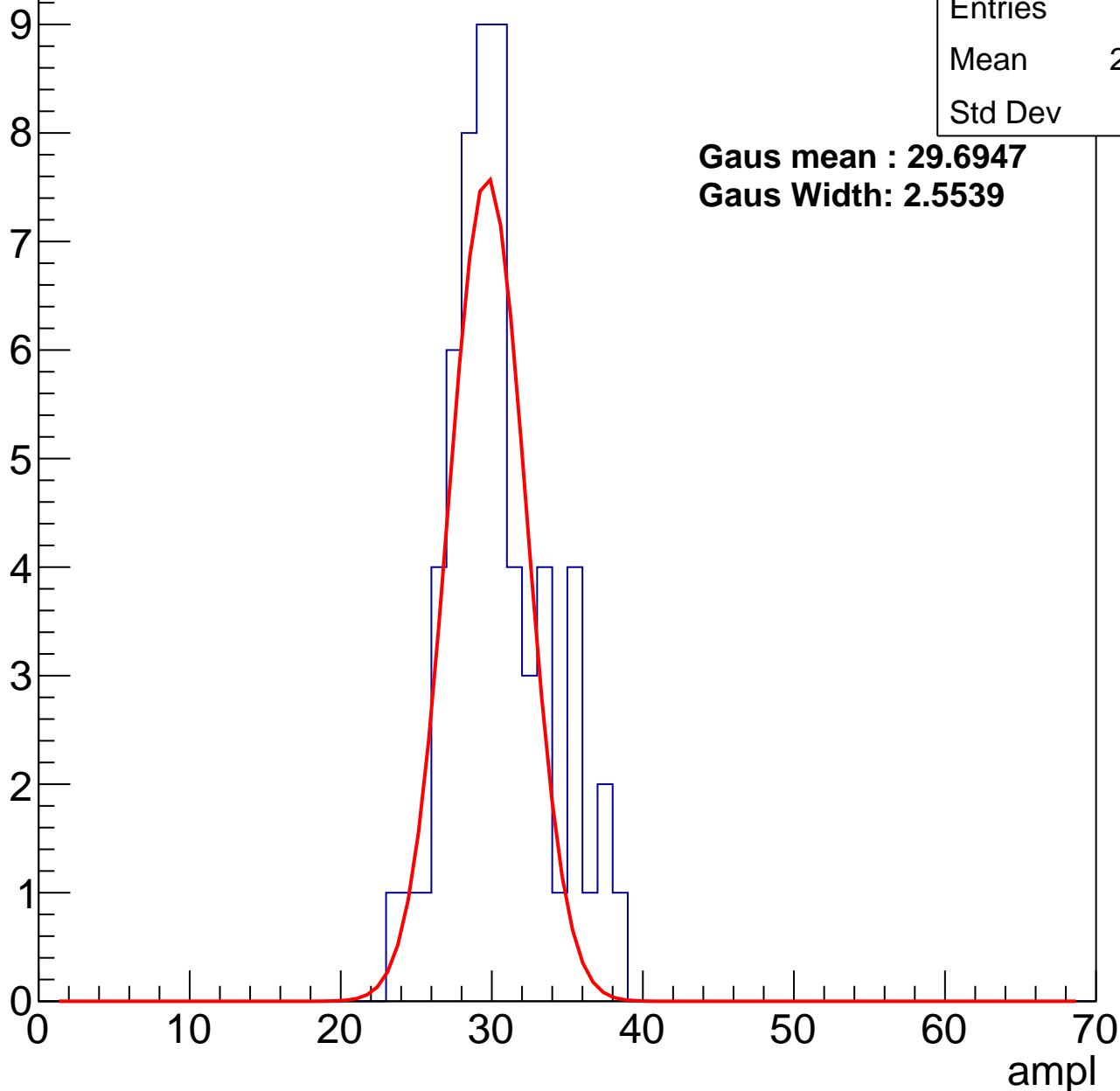
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.95
Std Dev	3.28

Gaus mean : 29.6947

Gaus Width: 2.5539



B1L101S, U6-ch122, adc1

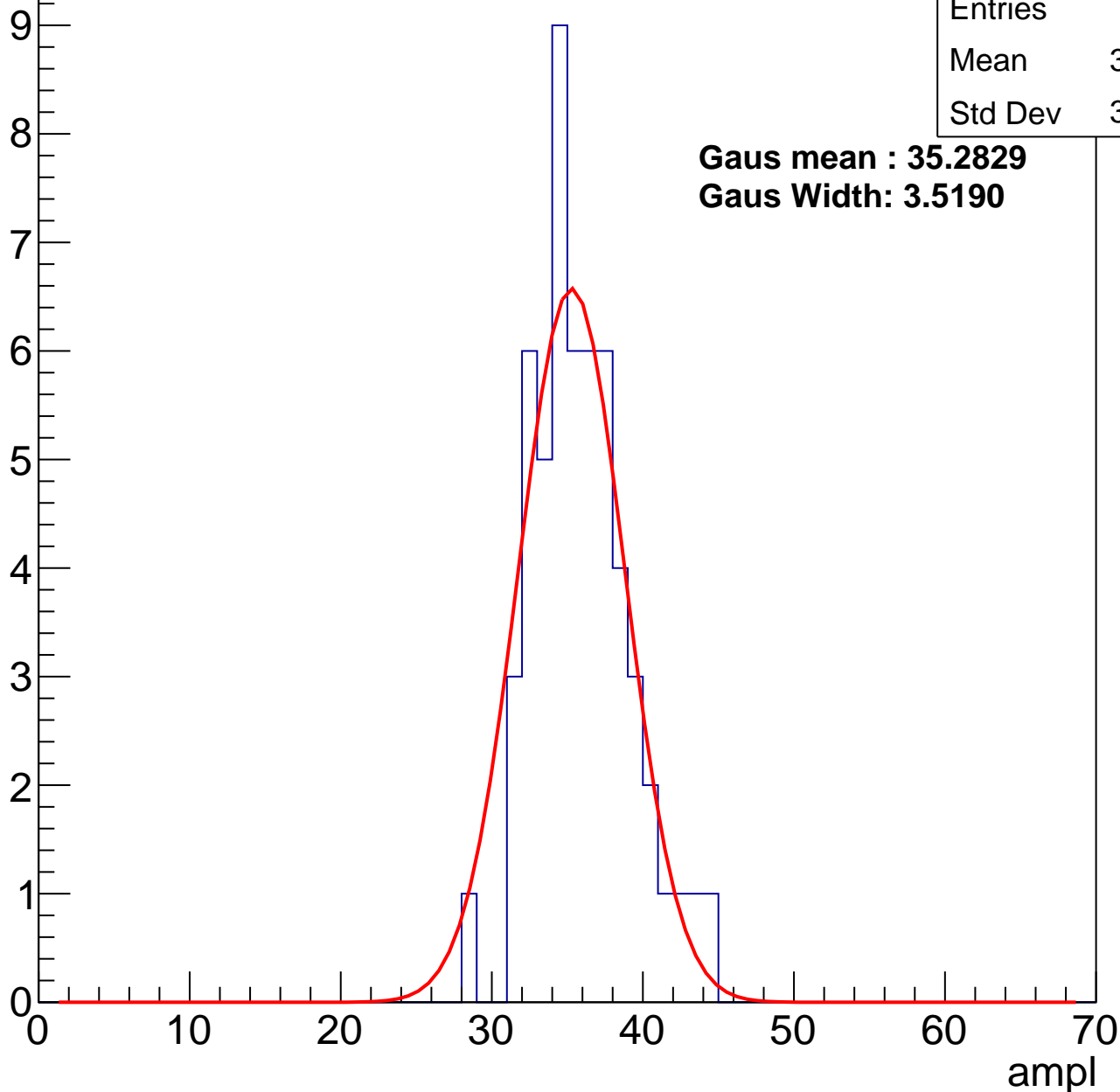
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.47
Std Dev	3.196

Gaus mean : 35.2829

Gaus Width: 3.5190



B1L101S, U6-ch122, adc2

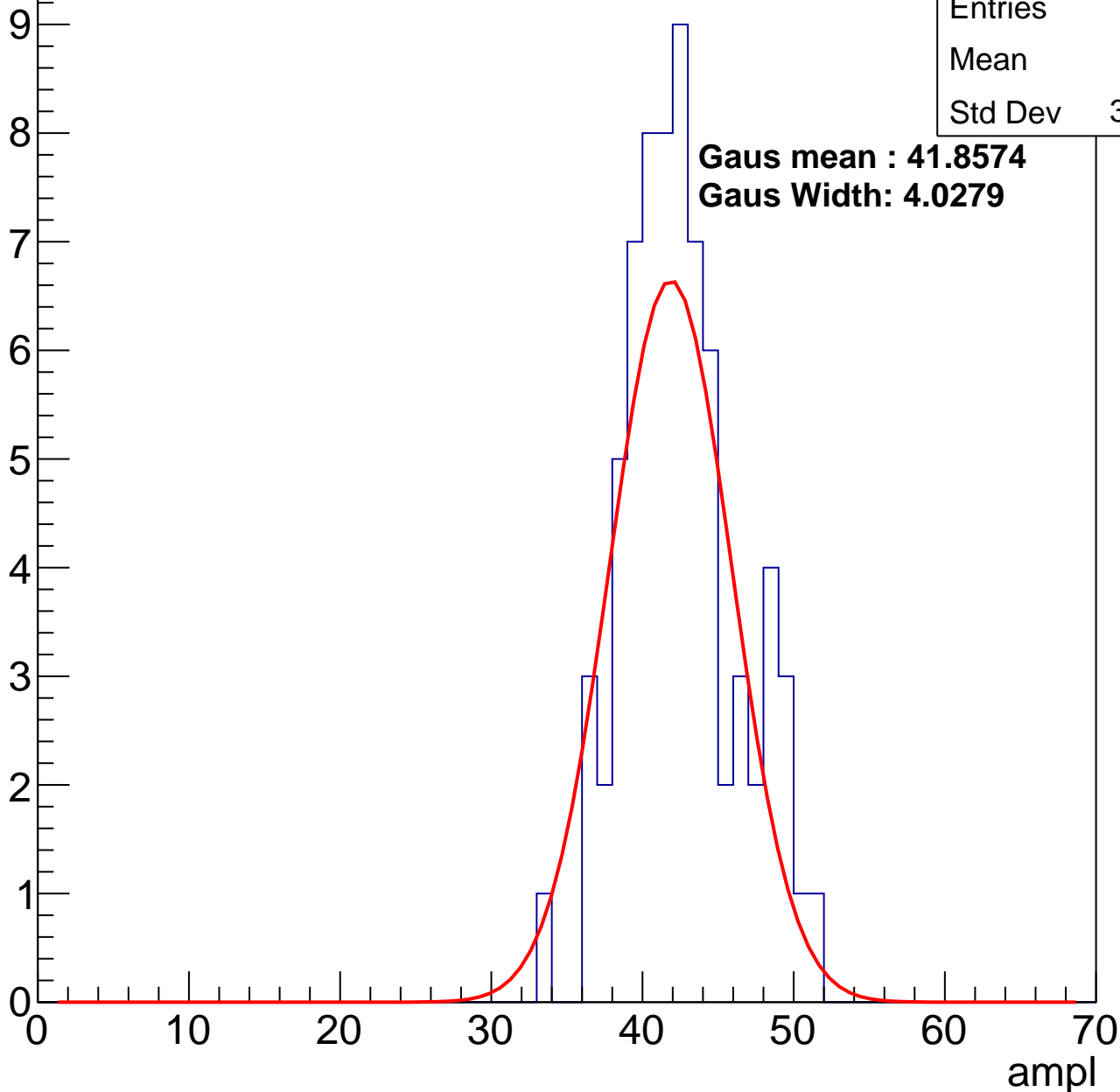
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.1
Std Dev	3.735

Gaus mean : 41.8574

Gaus Width: 4.0279

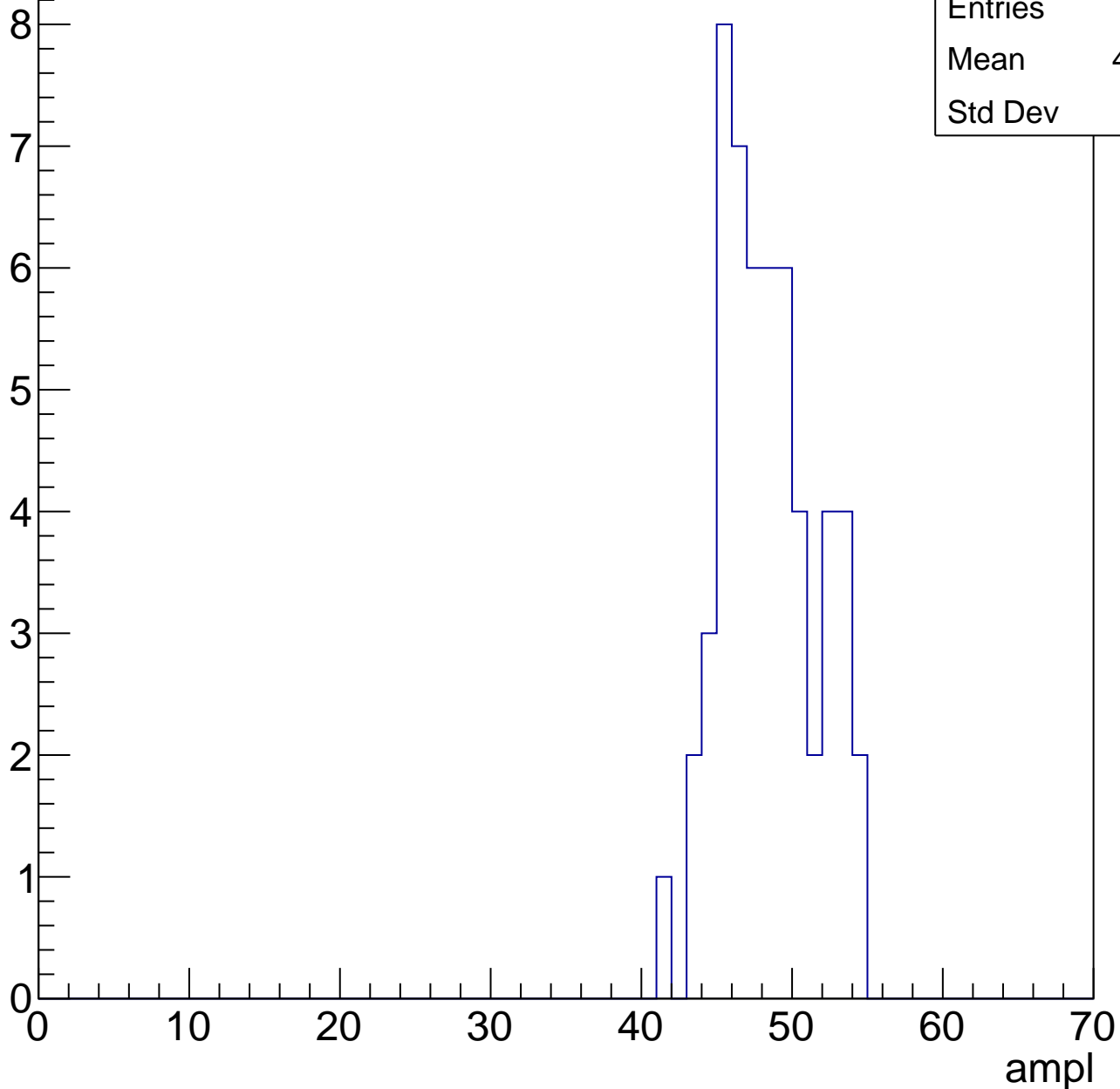


B1L101S, U6-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	47.91
Std Dev	3.1

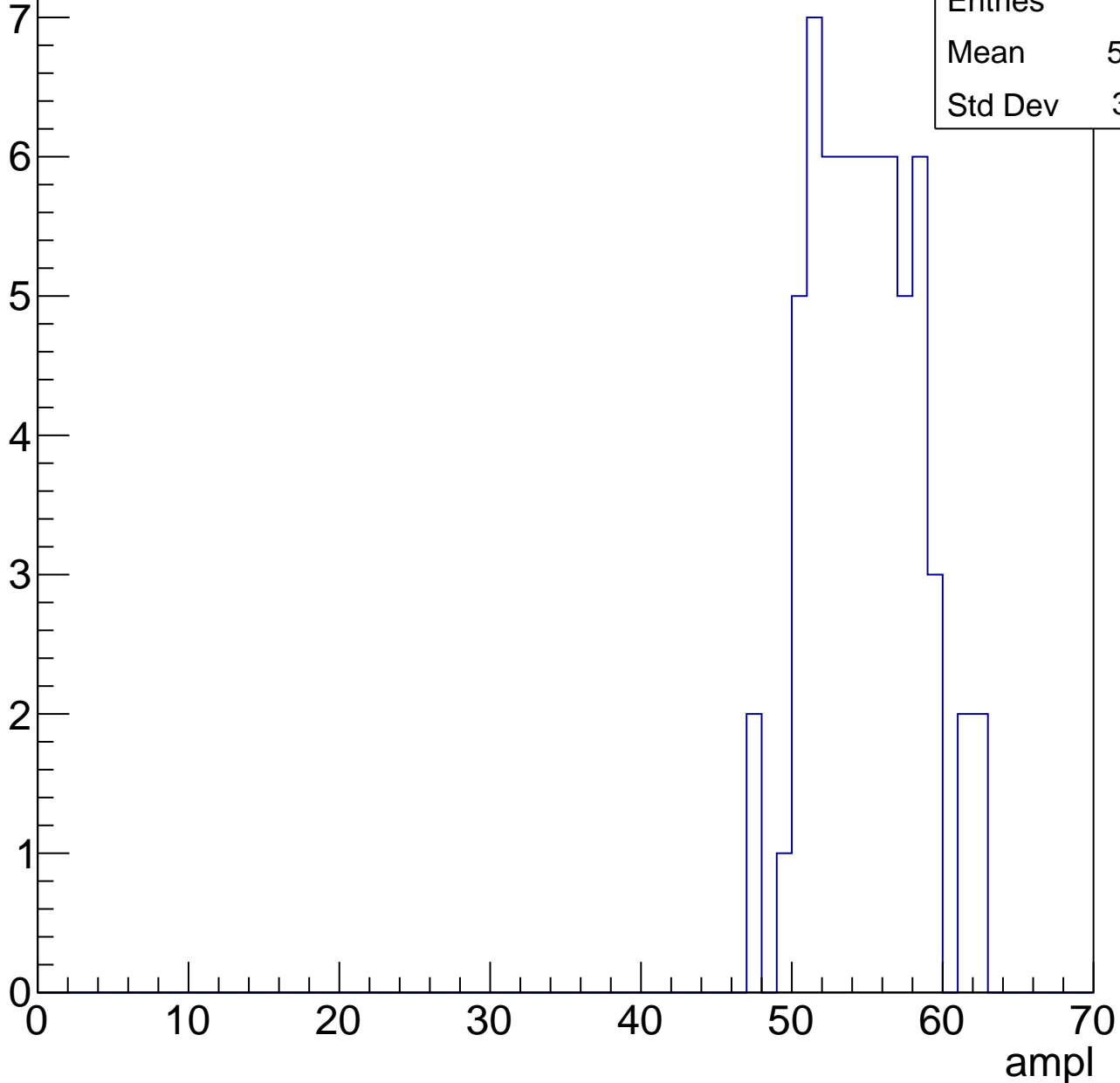


B1L101S, U6-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

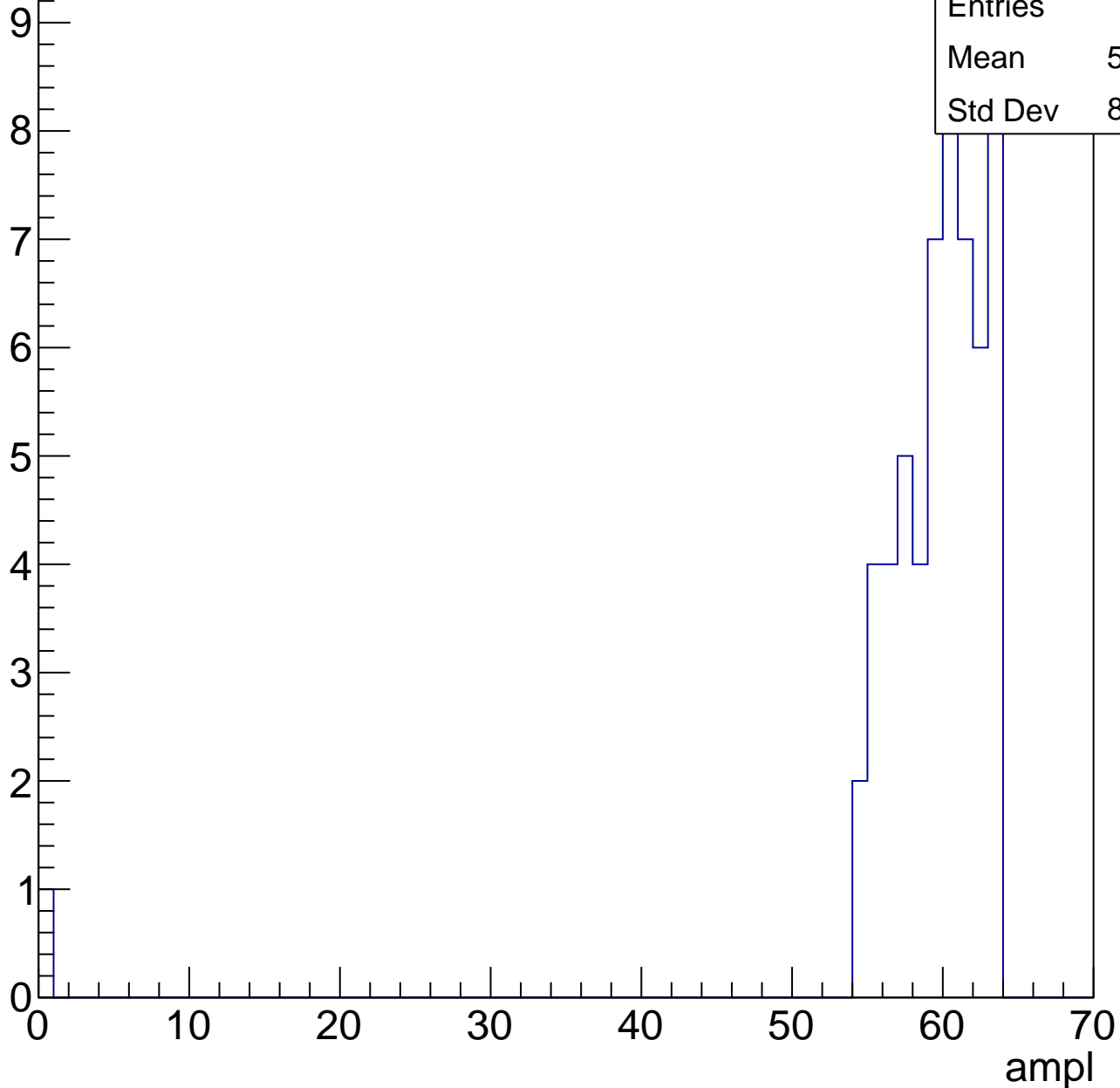
Entries	63
Mean	54.38
Std Dev	3.471



B1L101S, U6-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	57
Mean	58.33
Std Dev	8.217

B1L101S, U6-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

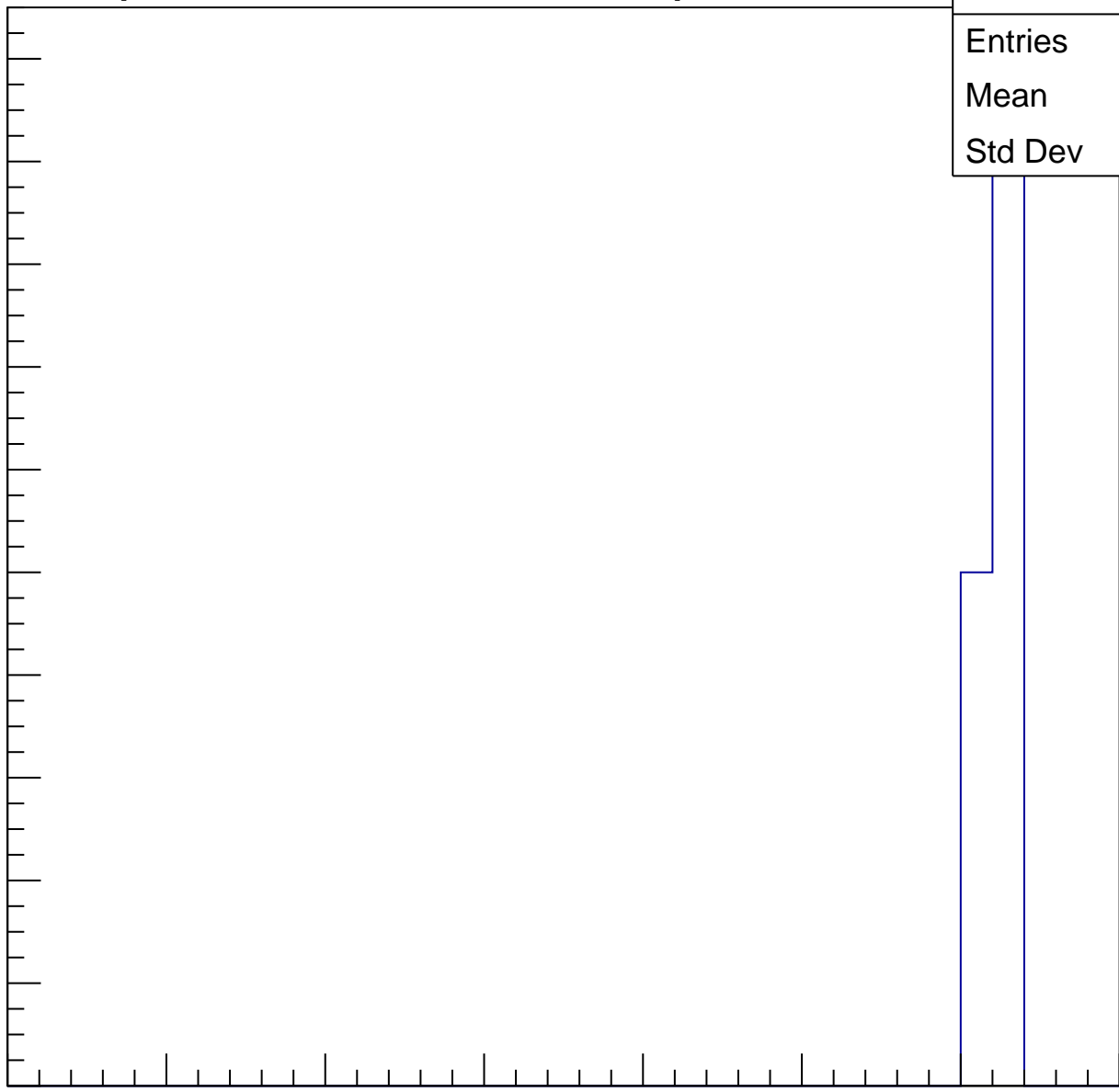
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U6-ch123, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	30.12
Std Dev	3.411

Gaus mean : 31.1881

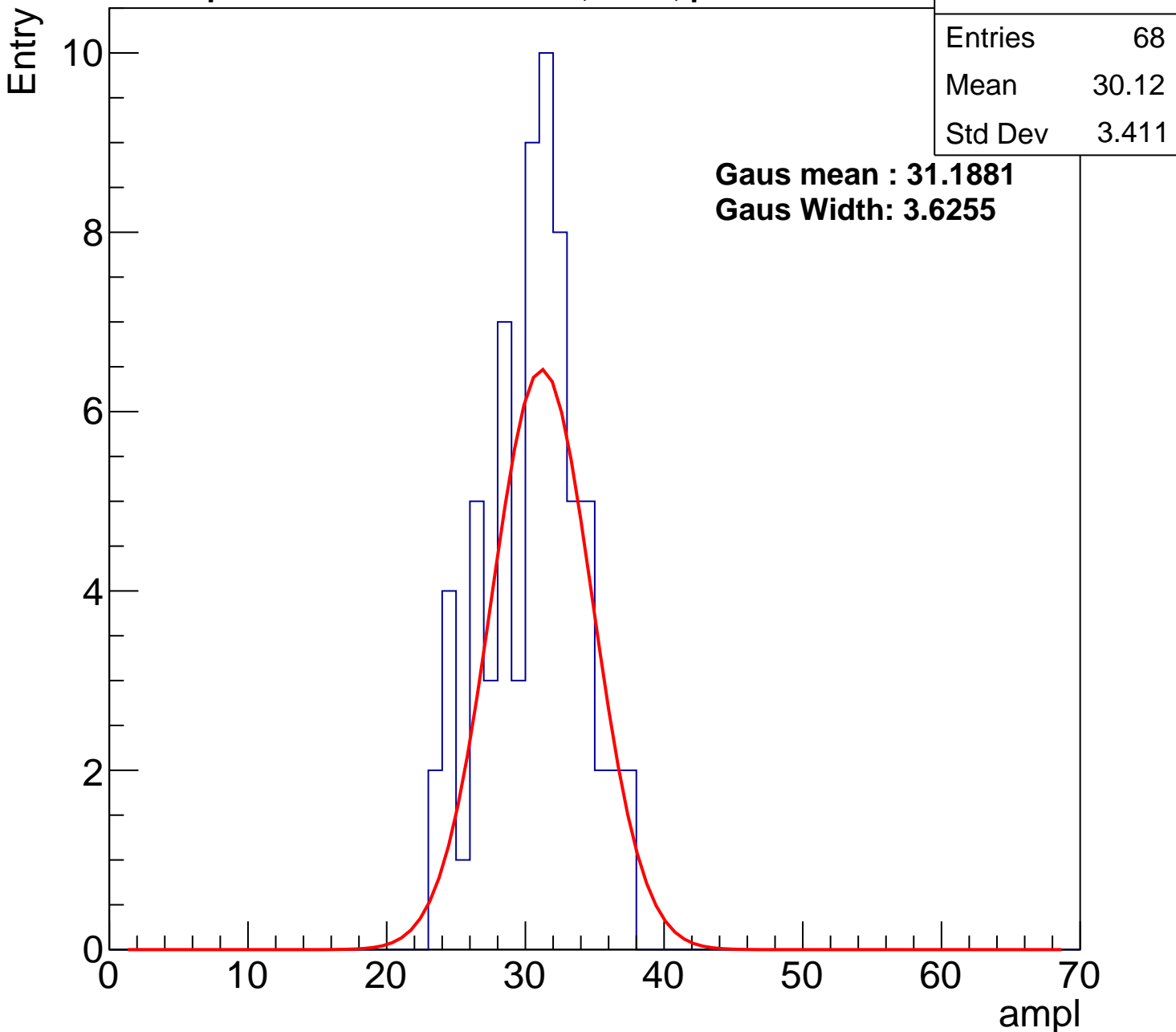
Gaus Width: 3.6255

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch123, adc1

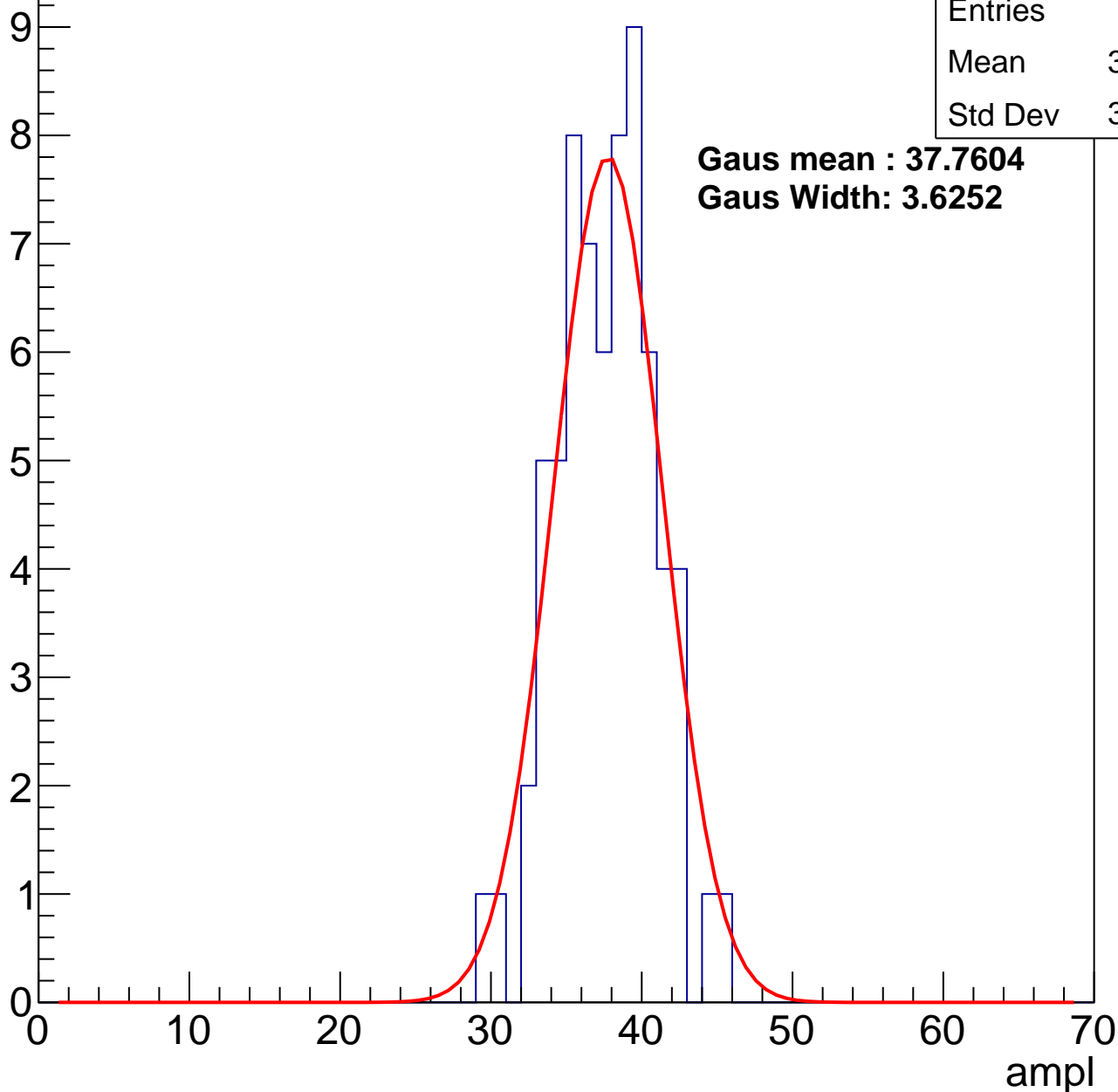
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.18
Std Dev	3.208

Gaus mean : 37.7604

Gaus Width: 3.6252



B1L101S, U6-ch123, adc2

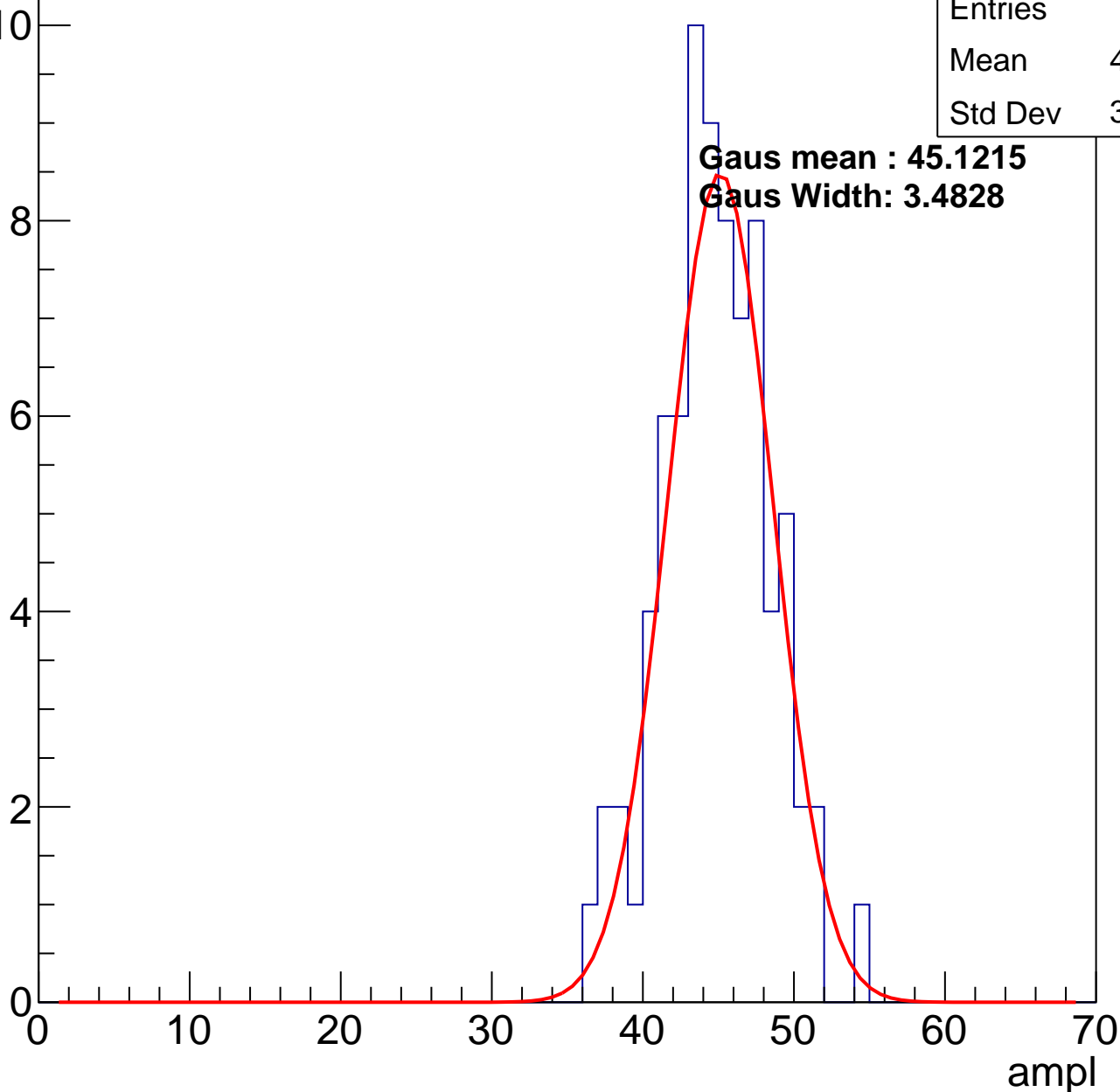
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.36
Std Dev	3.523

Gaus mean : 45.1215

Gaus Width: 3.4828

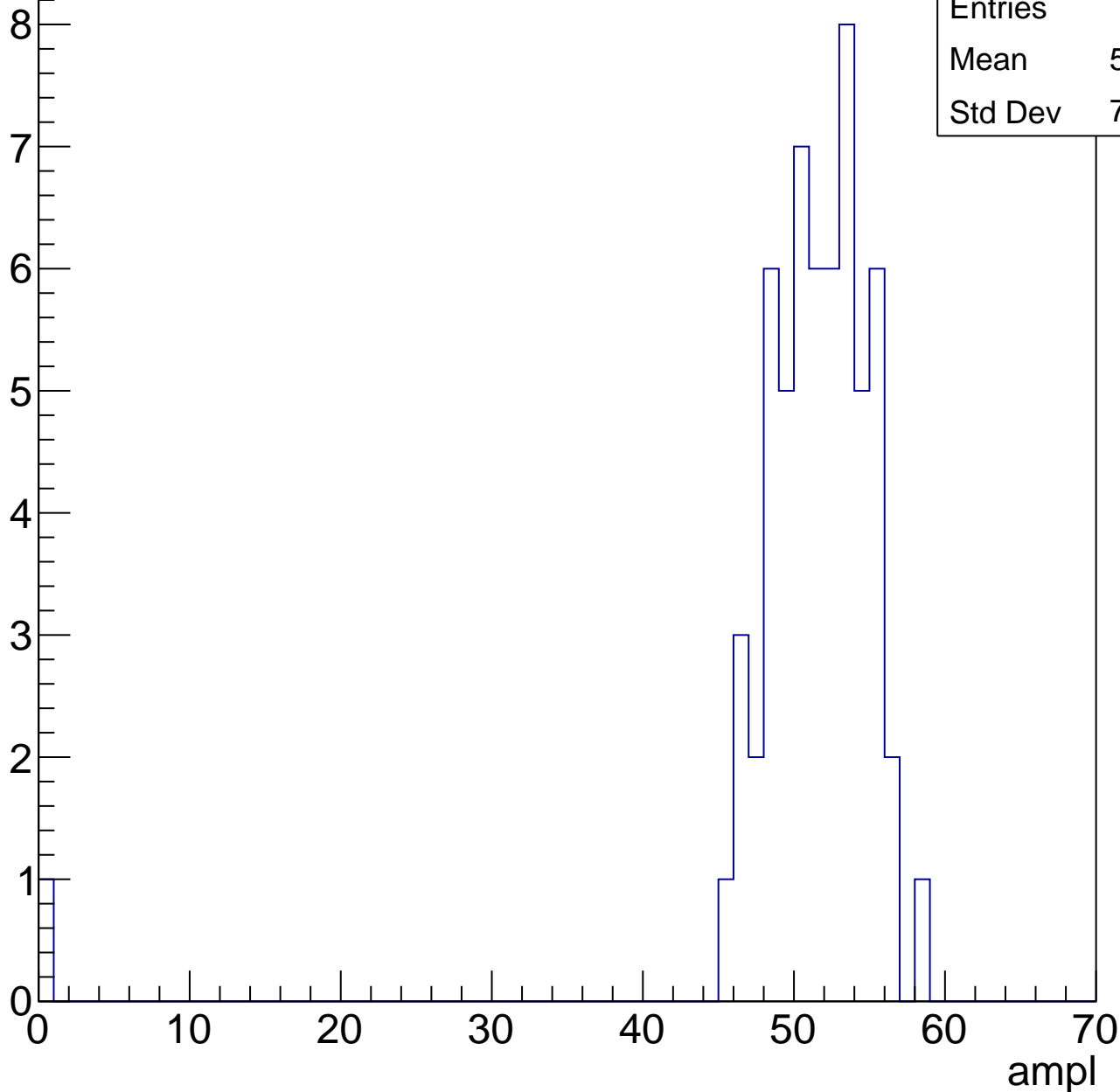


B1L101S, U6-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.37
Std Dev	7.225

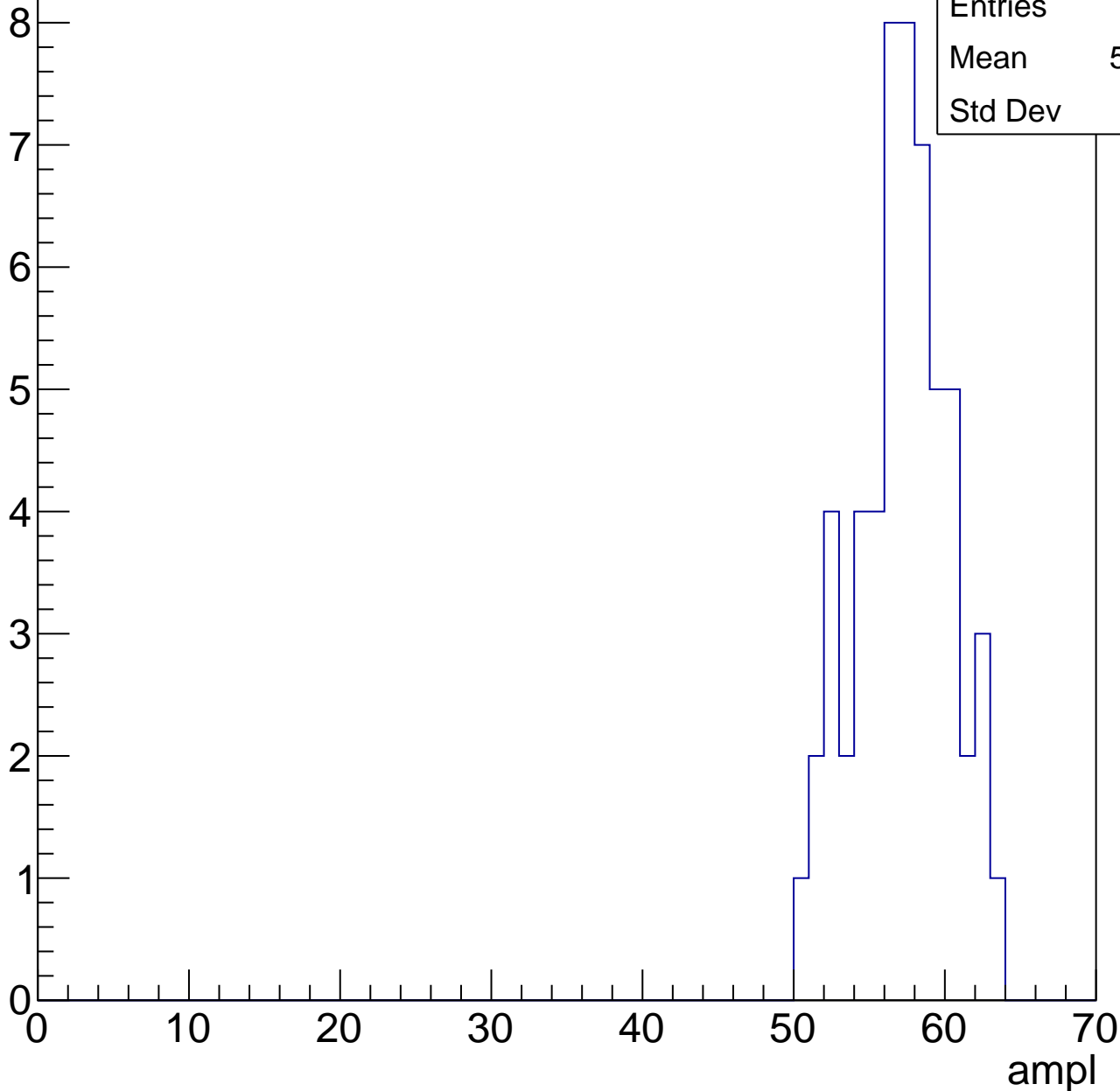


B1L101S, U6-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.75
Std Dev	3.06



B1L101S, U6-ch123, adc5

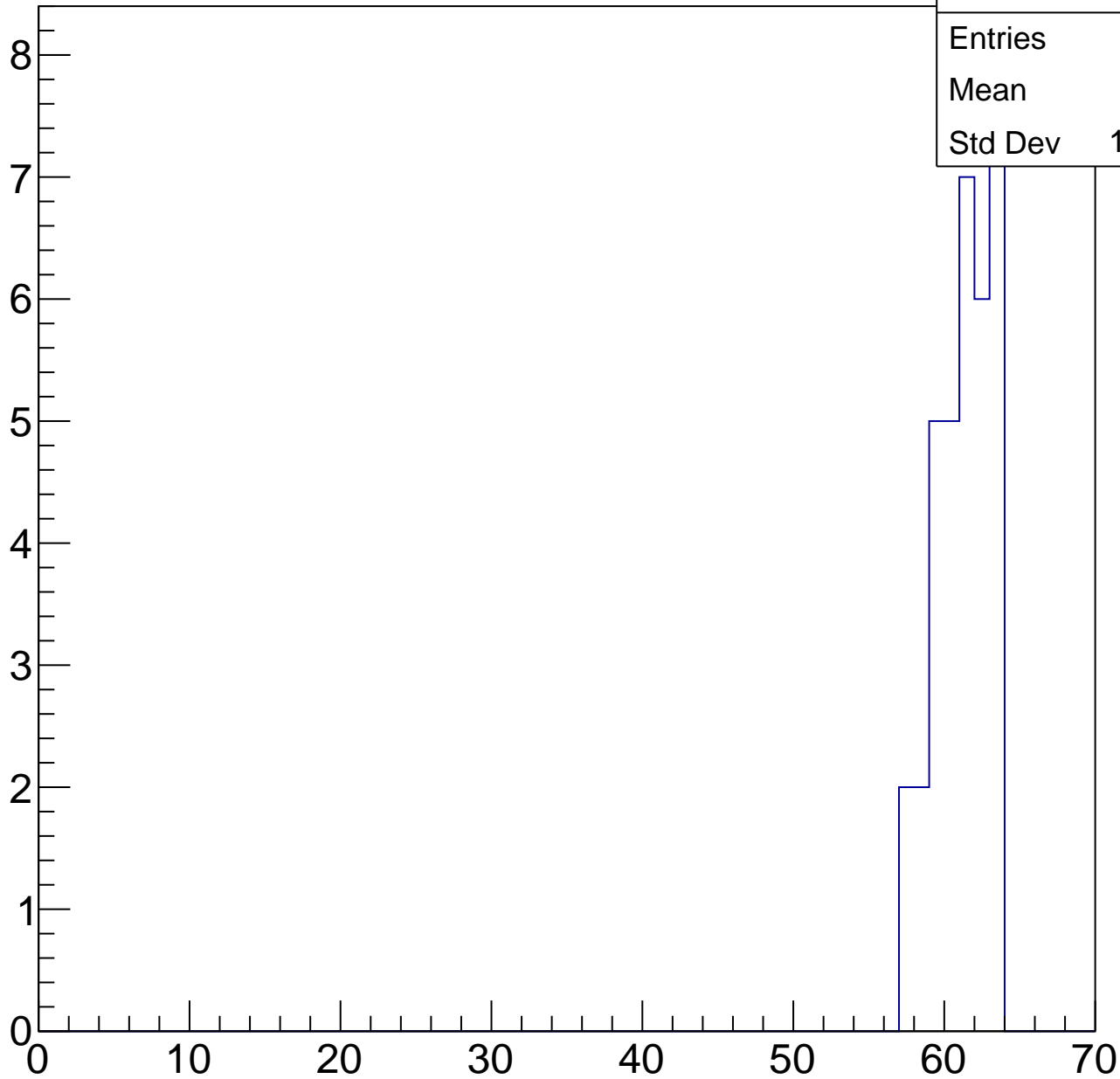
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.8
Std Dev	1.786

ampl



B1L101S, U6-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch124, adc0

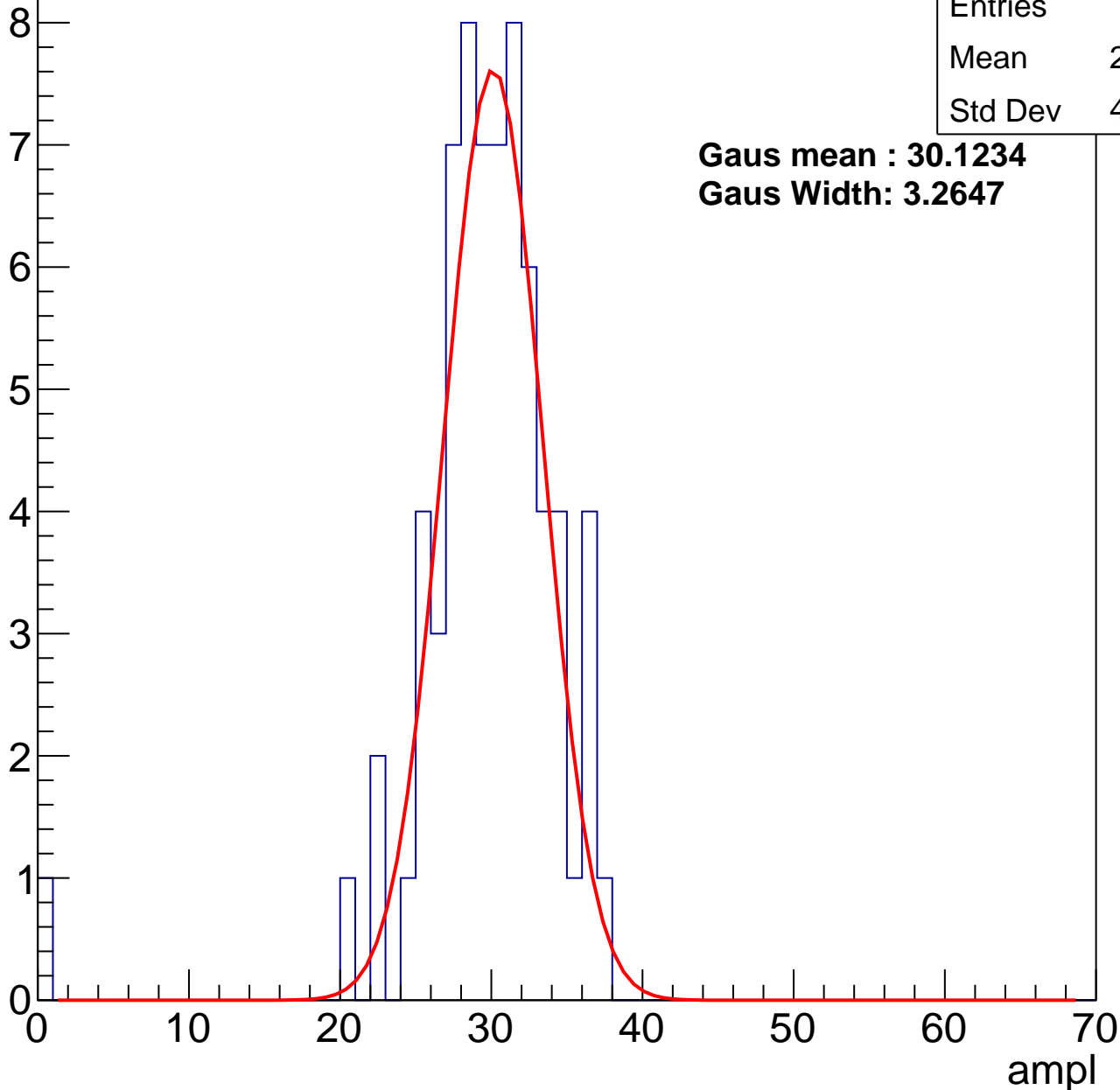
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.22
Std Dev	4.995

Gaus mean : 30.1234

Gaus Width: 3.2647



B1L101S, U6-ch124, adc1

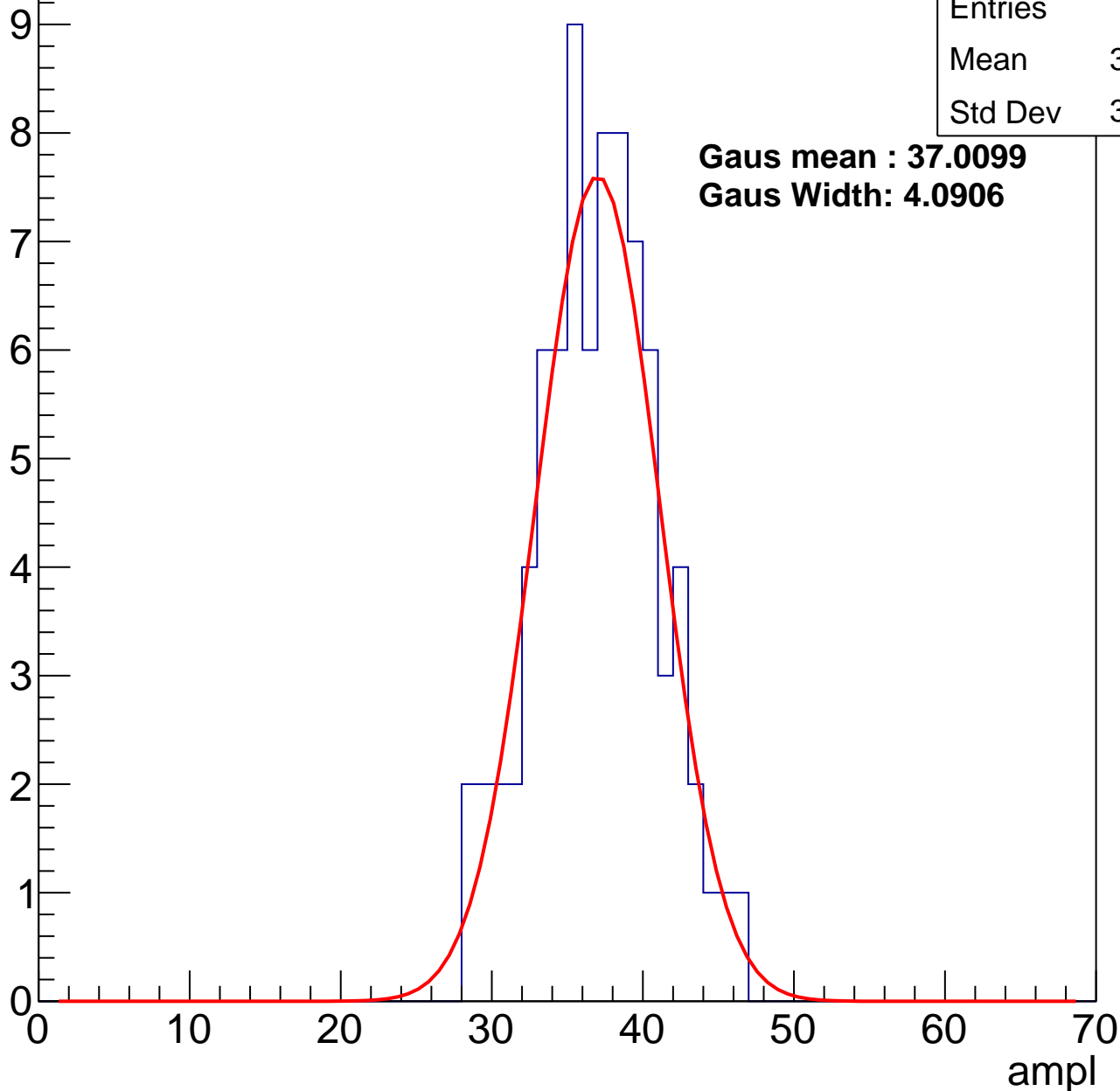
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.52
Std Dev	3.934

Gaus mean : 37.0099

Gaus Width: 4.0906



B1L101S, U6-ch124, adc2

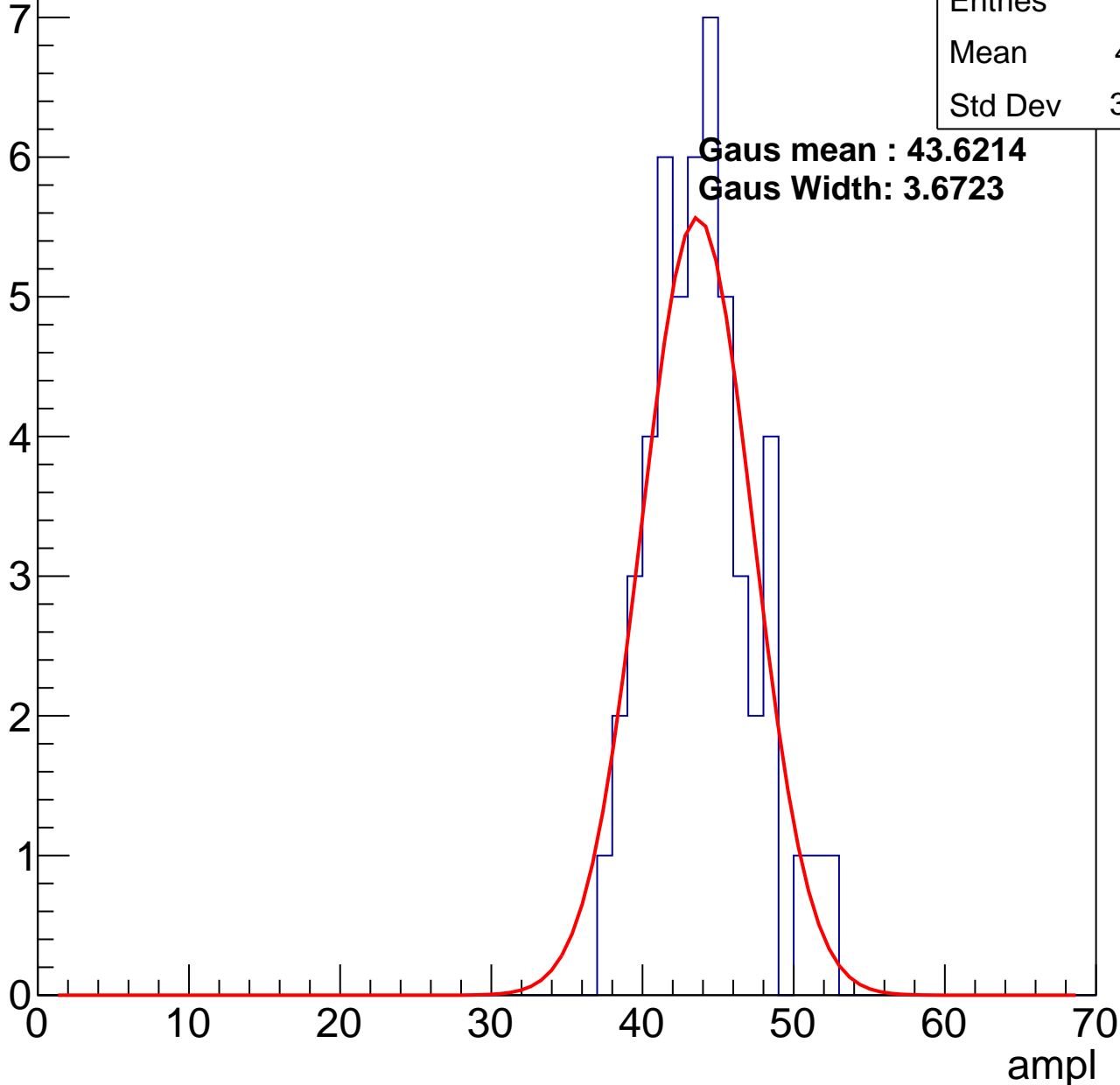
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	43.41
Std Dev	3.344

Gaus mean : 43.6214

Gaus Width: 3.6723

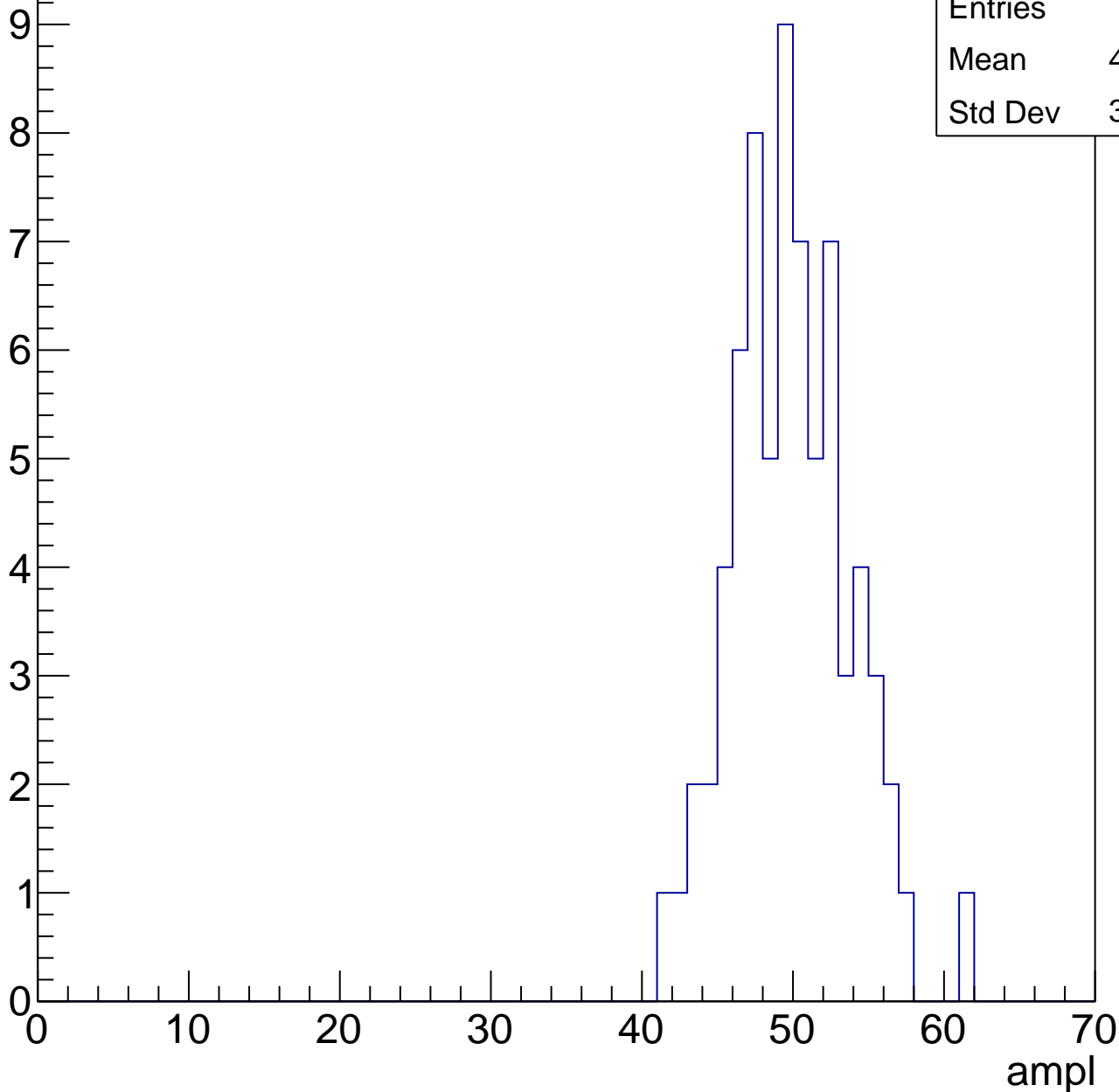


B1L101S, U6-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.42
Std Dev	3.808

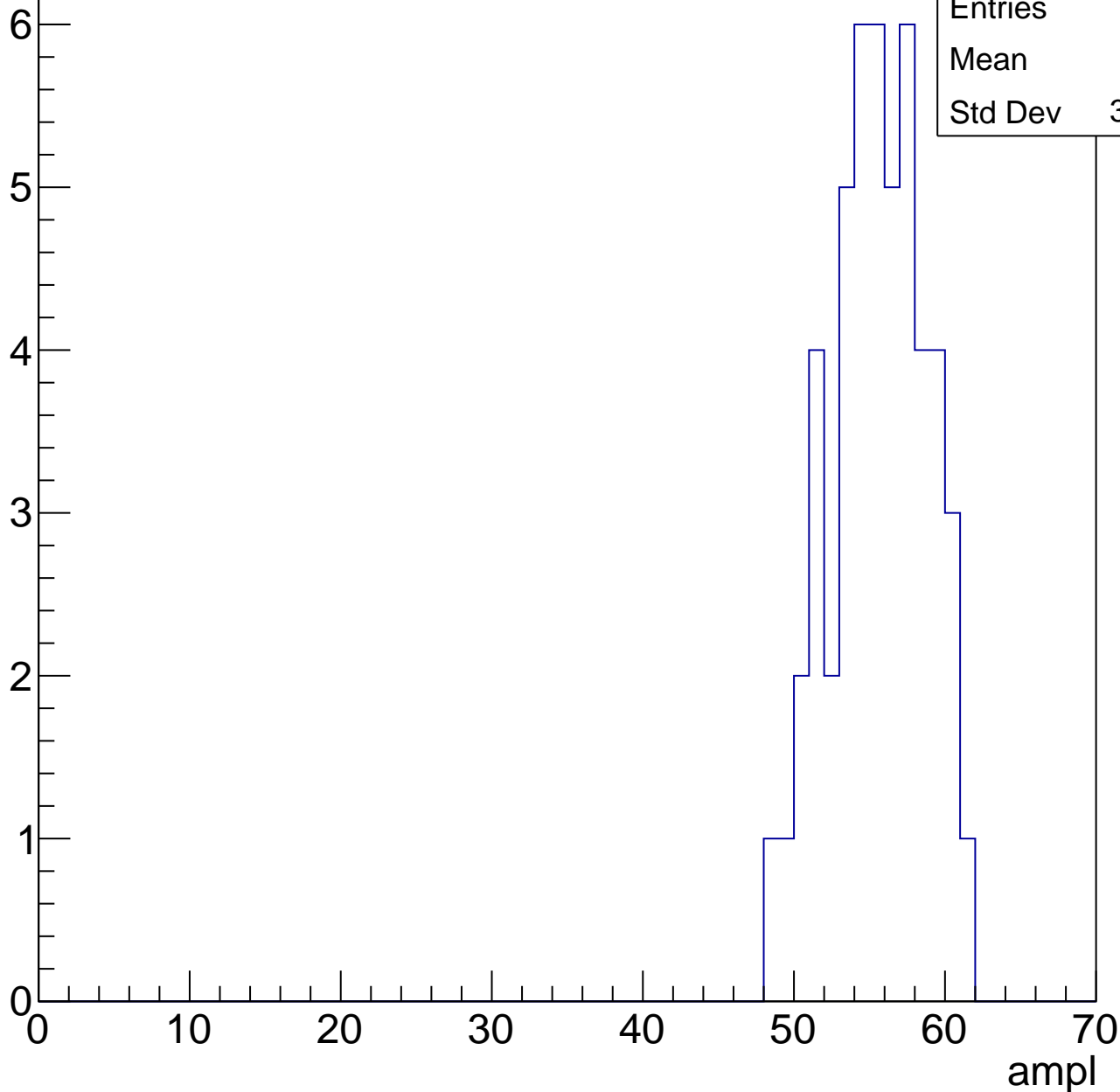


B1L101S, U6-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.1
Std Dev	3.106

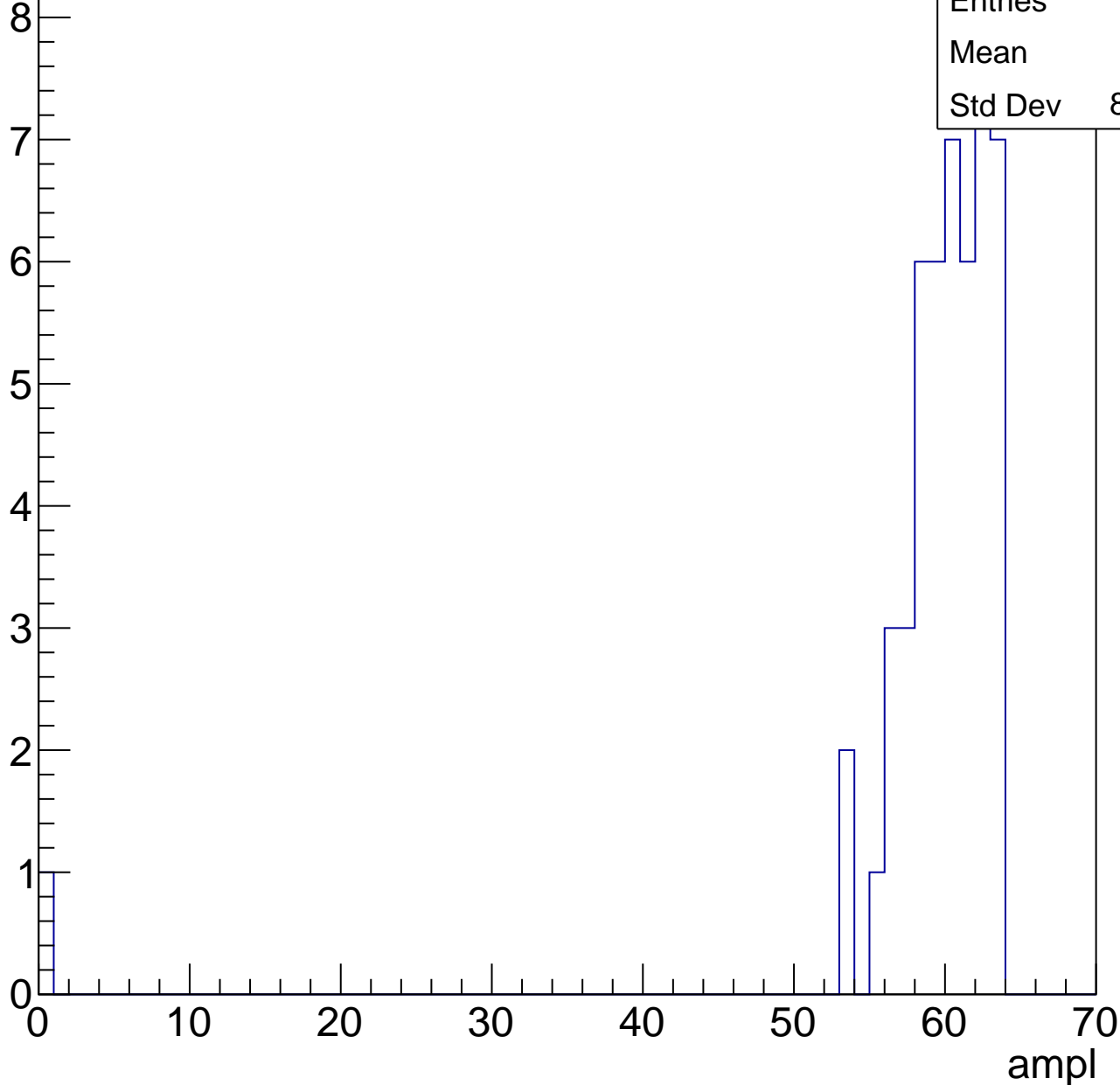


B1L101S, U6-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.5
Std Dev	8.737



B1L101S, U6-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U6-ch125, adc0

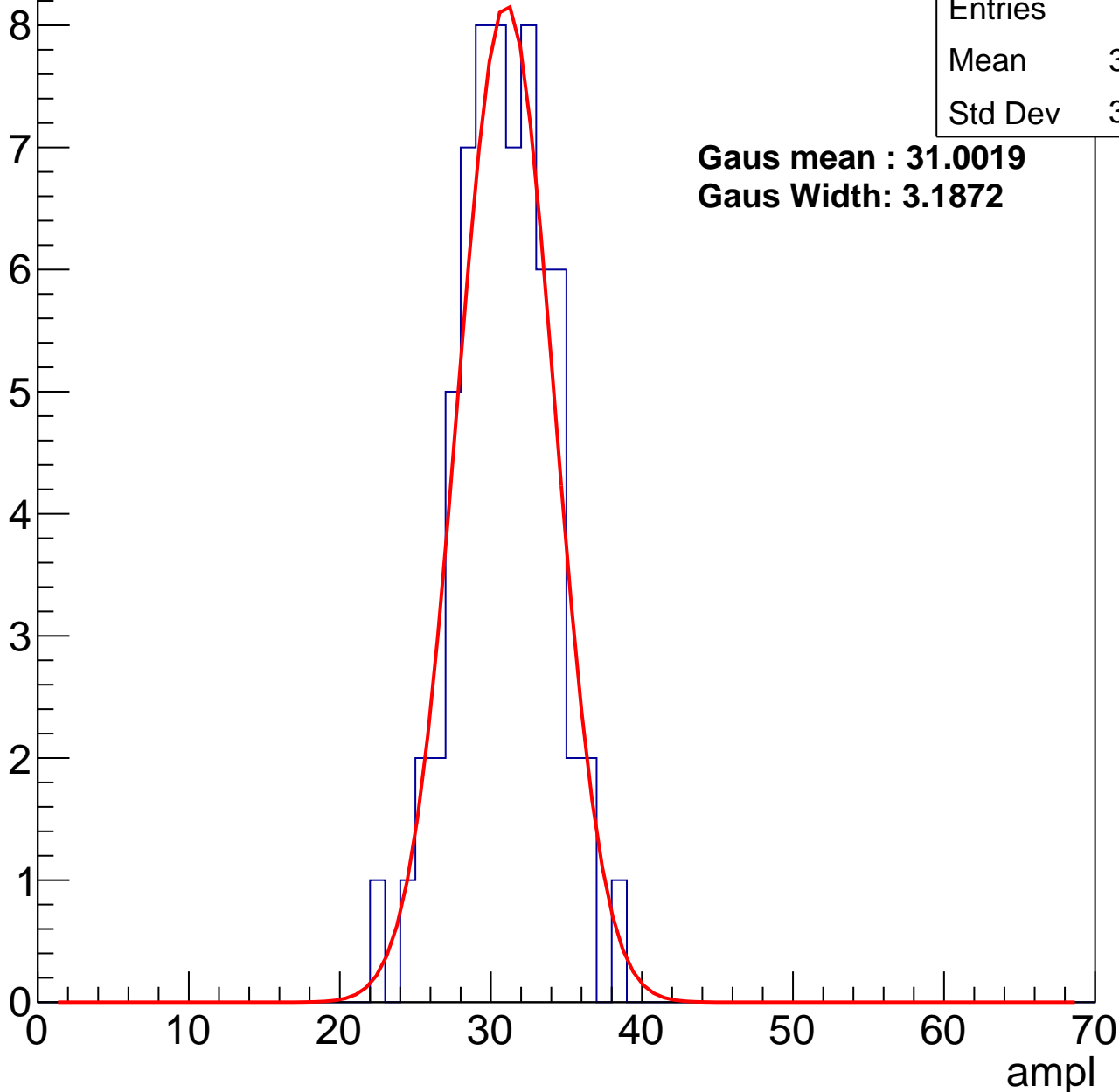
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.39
Std Dev	3.084

Gaus mean : 31.0019

Gaus Width: 3.1872



B1L101S, U6-ch125, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.46
Std Dev	3.528

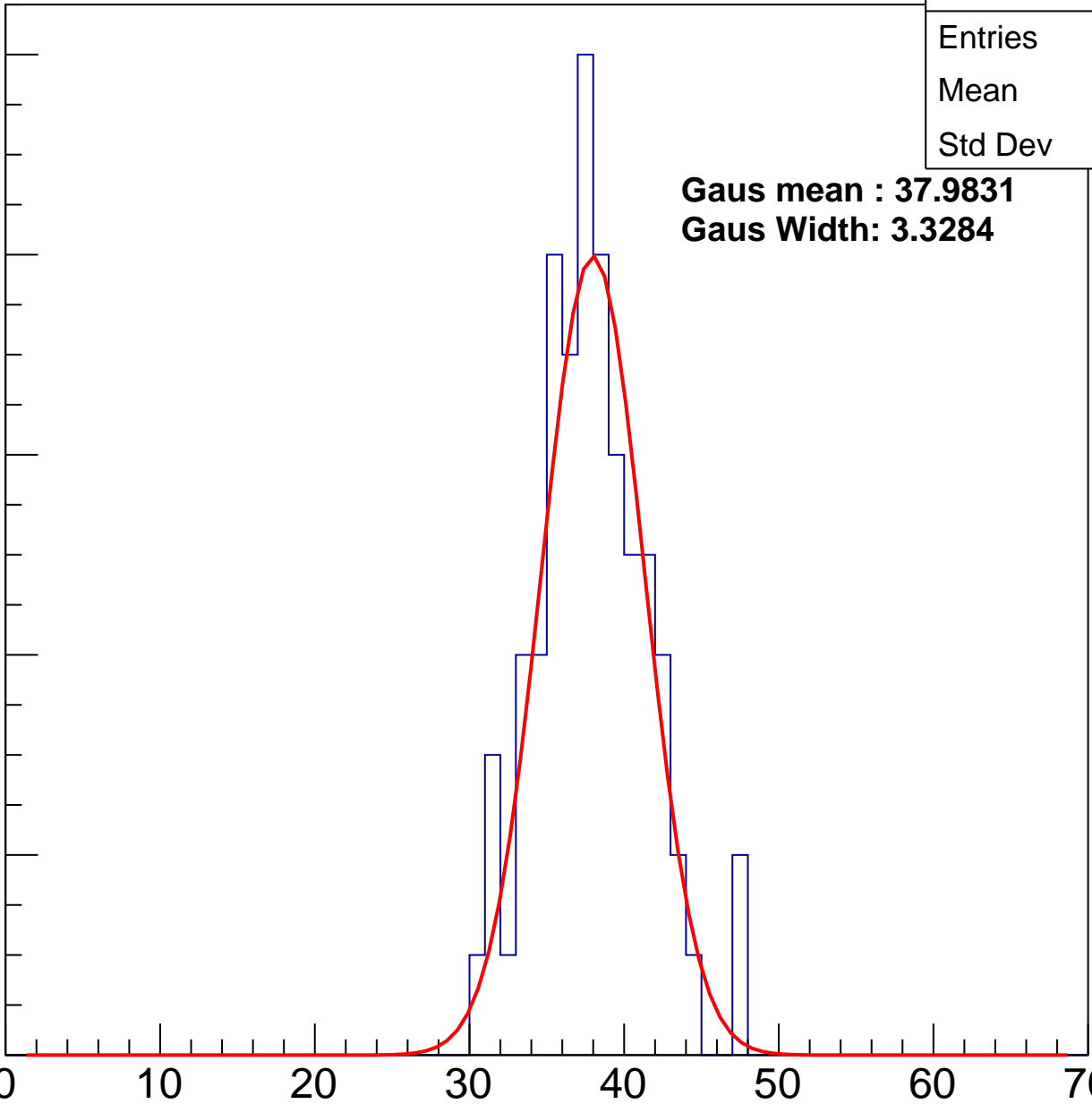
Gaus mean : 37.9831

Gaus Width: 3.3284

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch125, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	44.04
Std Dev	3.151

Gaus mean : 44.5485

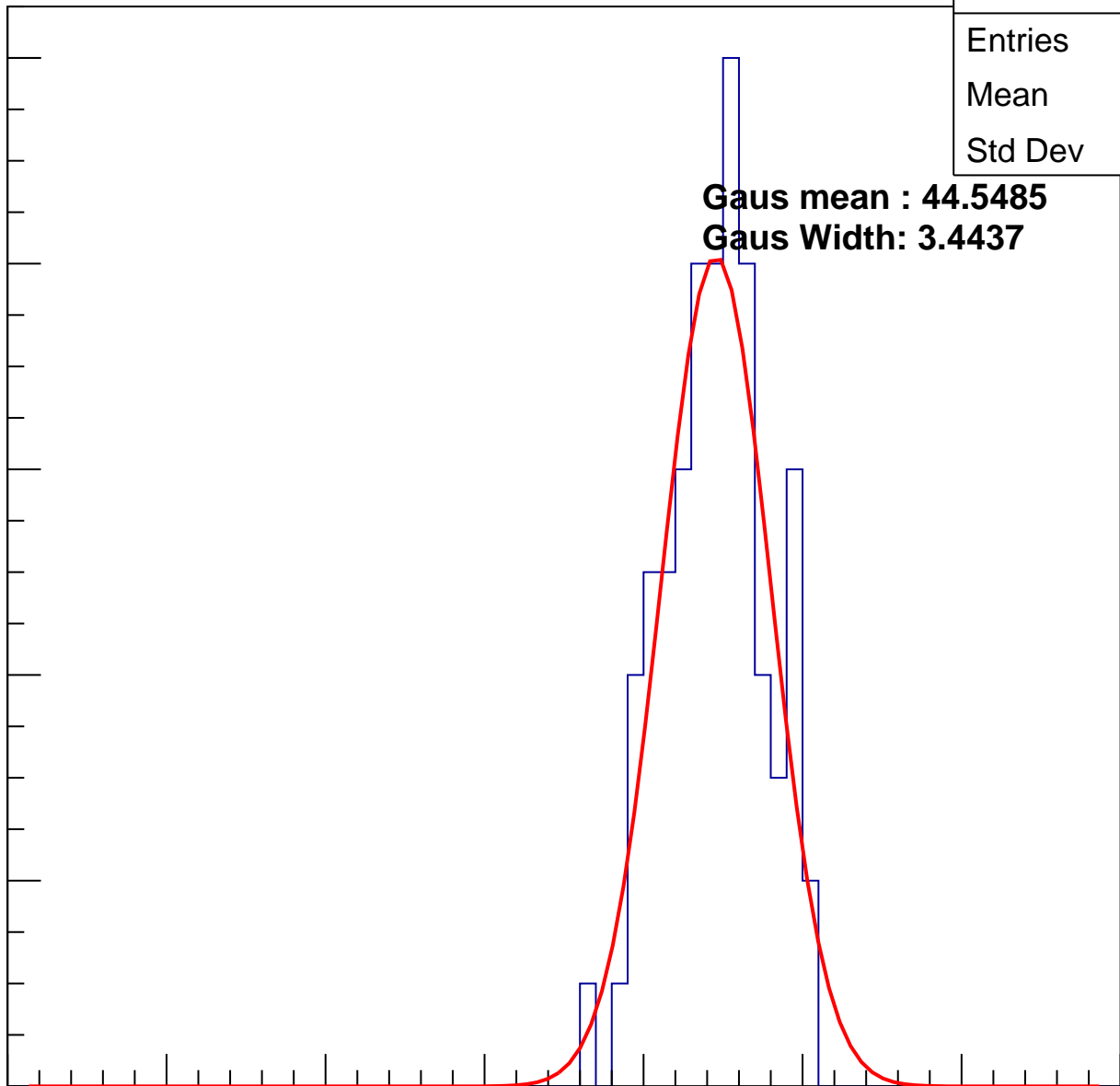
Gaus Width: 3.4437

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	51.78
Std Dev	3.485

Entry

10

8

6

4

2

0

0

10

20

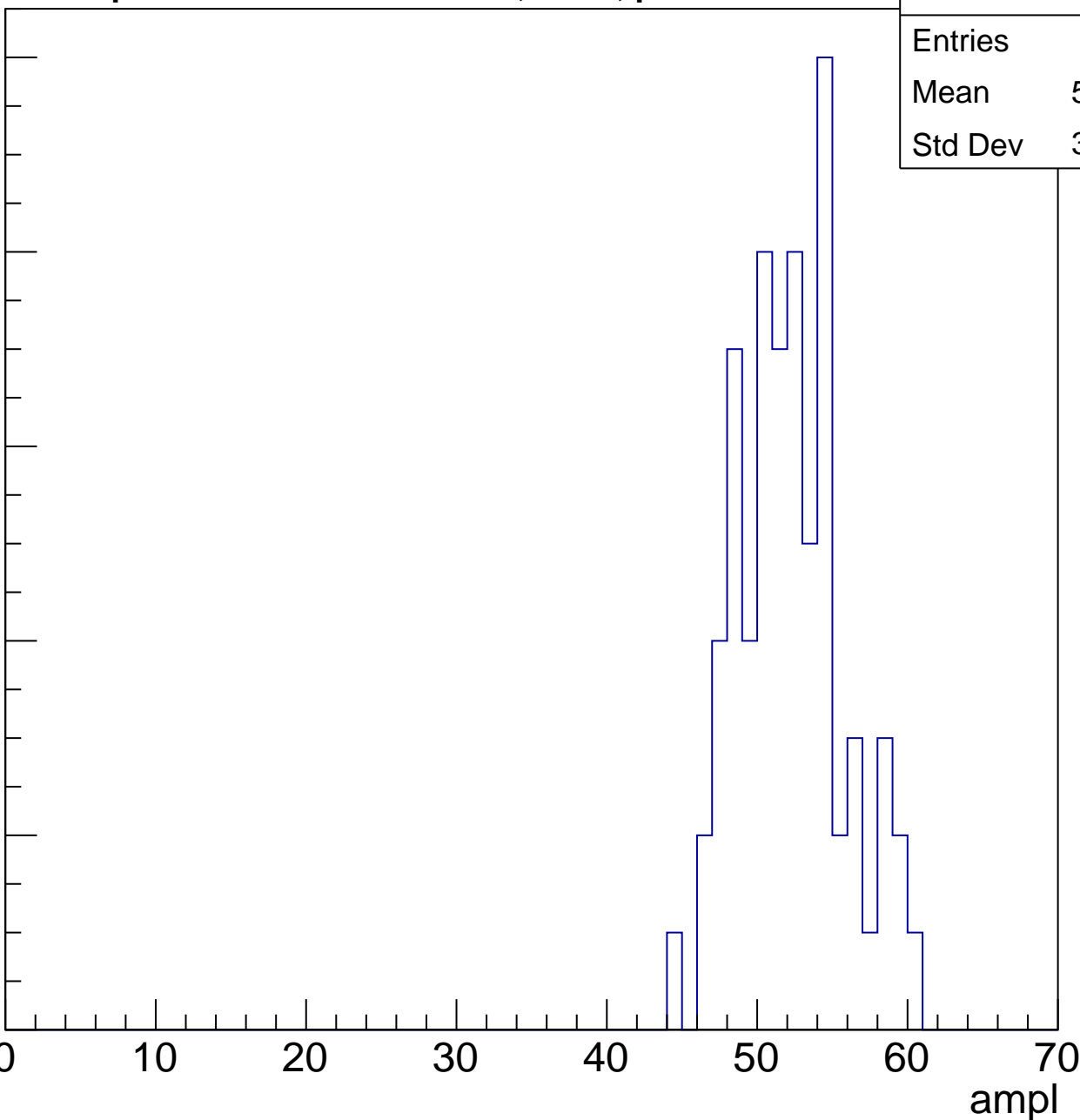
30

40

50

60

ampl

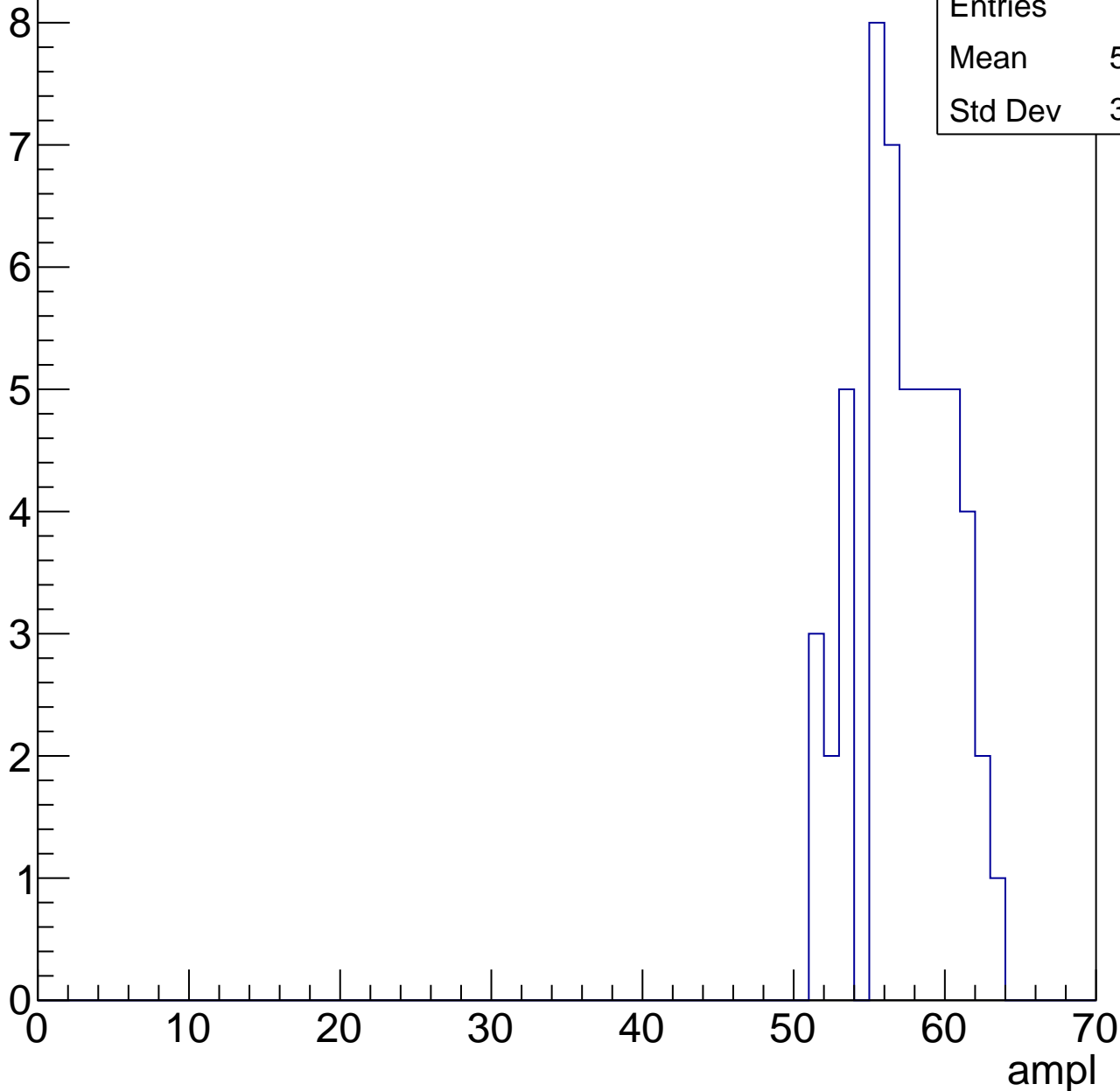


B1L101S, U6-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.83
Std Dev	3.087

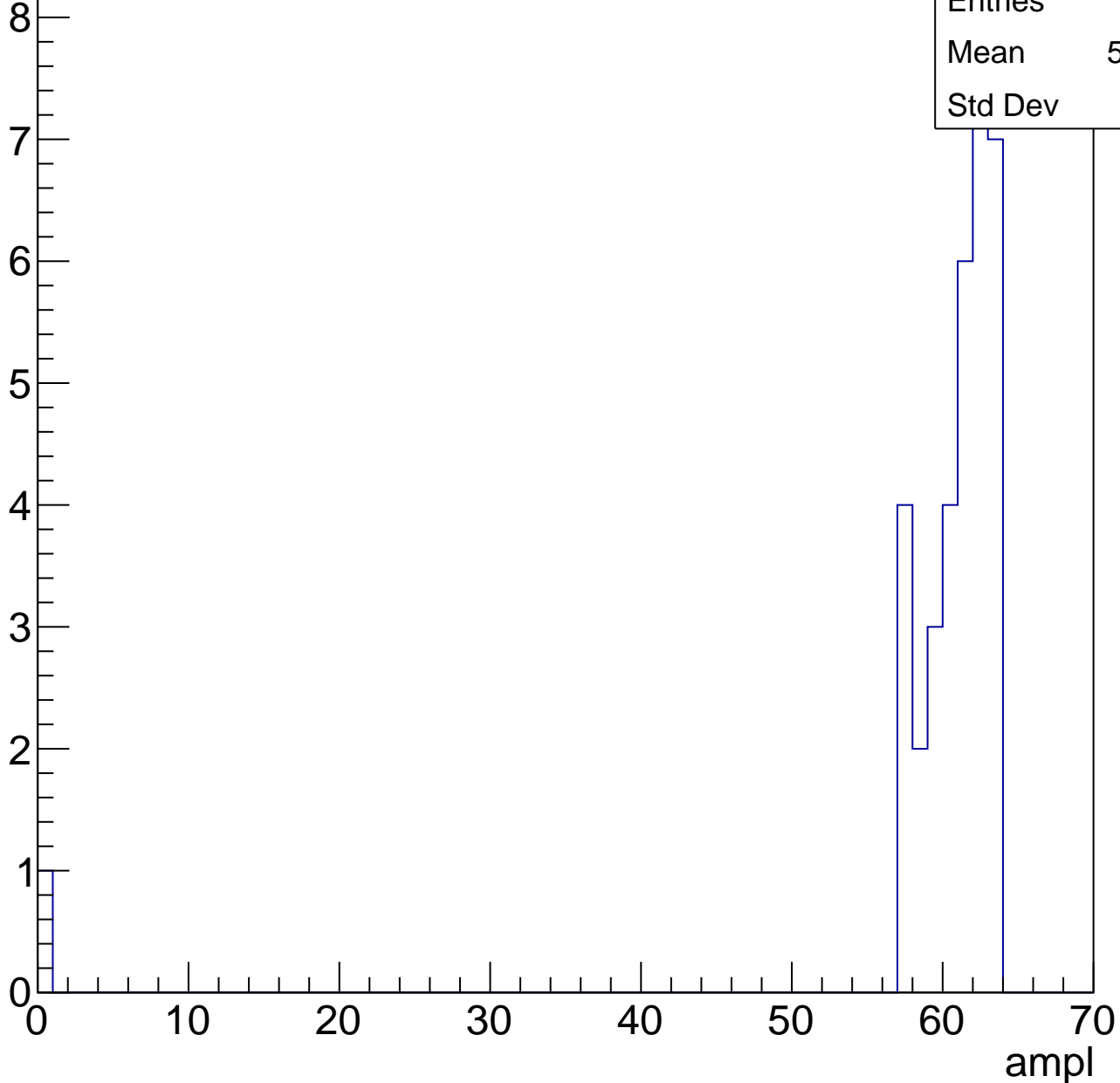


B1L101S, U6-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.97
Std Dev	10.3



B1L101S, U6-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U6-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U6-ch126, adc0

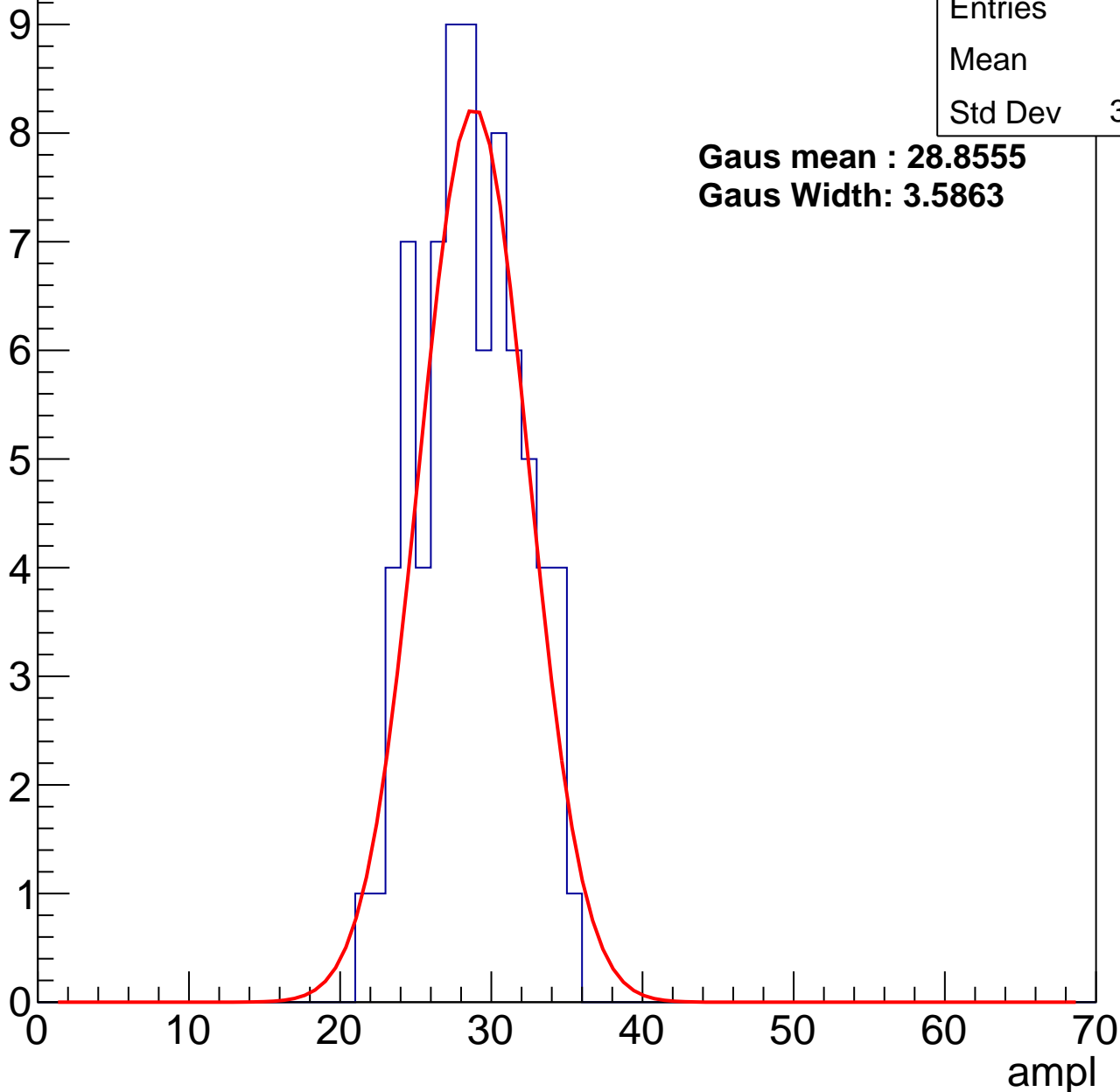
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.2
Std Dev	3.309

Gaus mean : 28.8555

Gaus Width: 3.5863



B1L101S, U6-ch126, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	35.67
Std Dev	3.632

Gaus mean : 36.6221

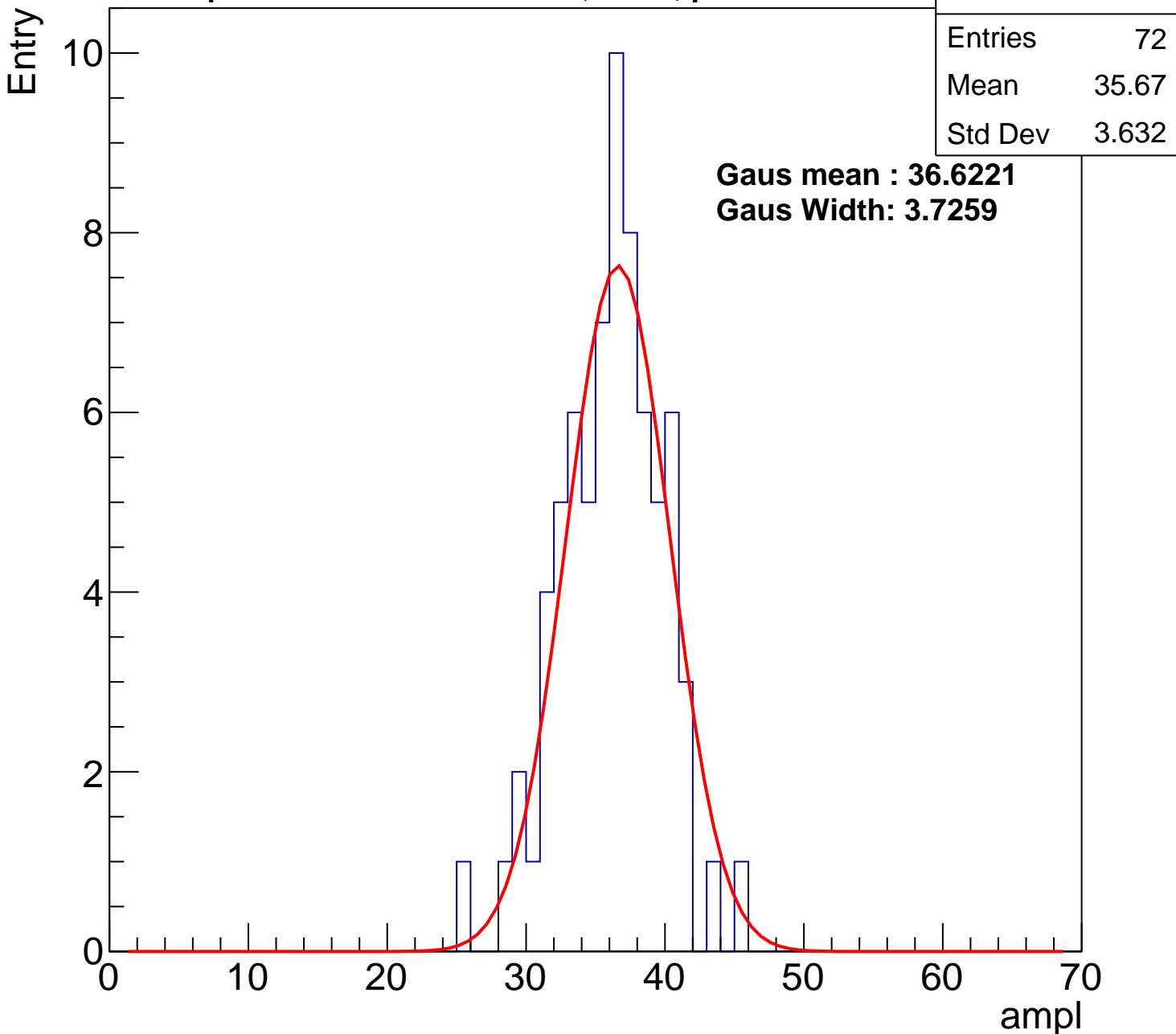
Gaus Width: 3.7259

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U6-ch126, adc2

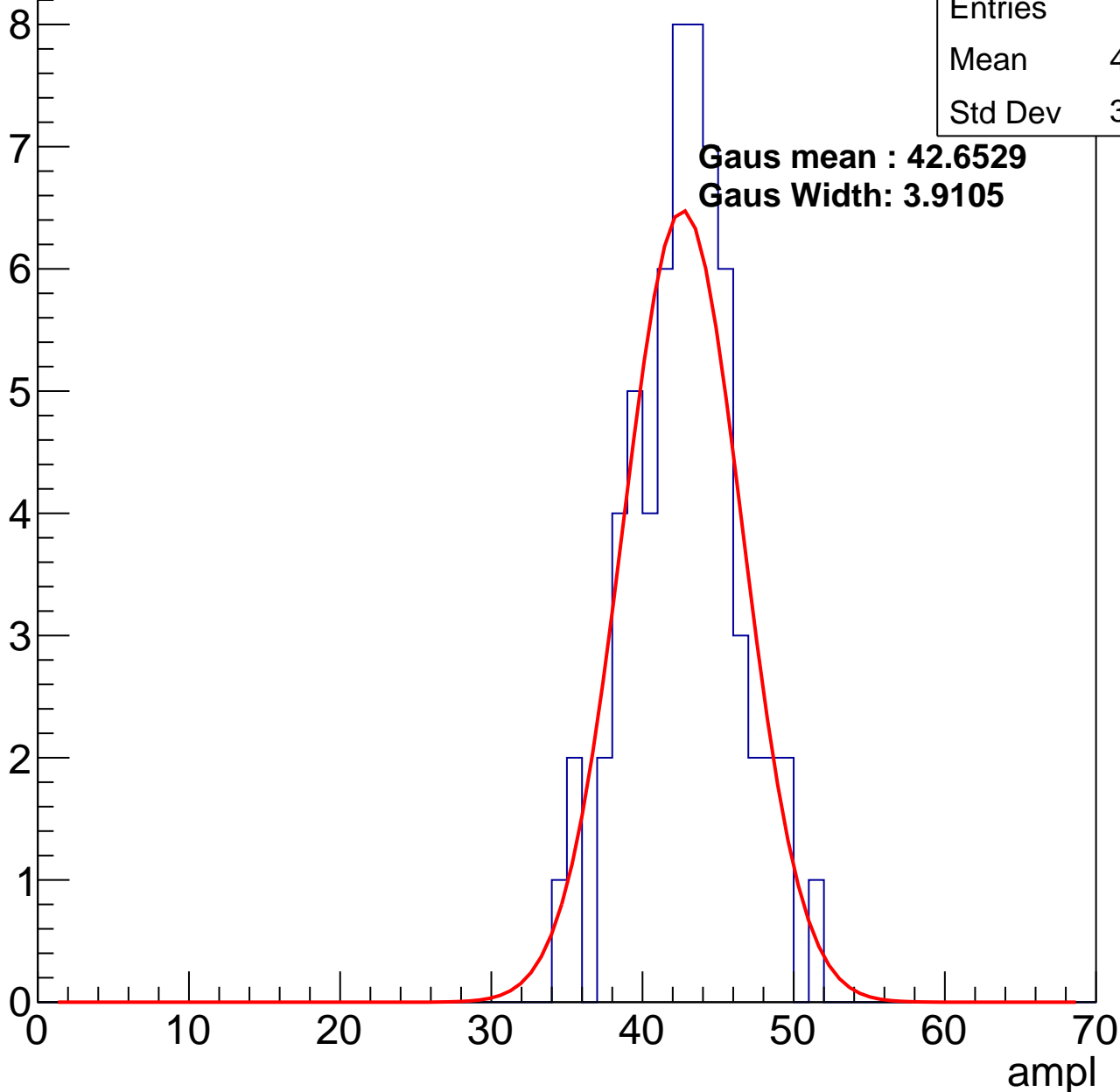
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	42.32
Std Dev	3.509

Gaus mean : 42.6529

Gaus Width: 3.9105

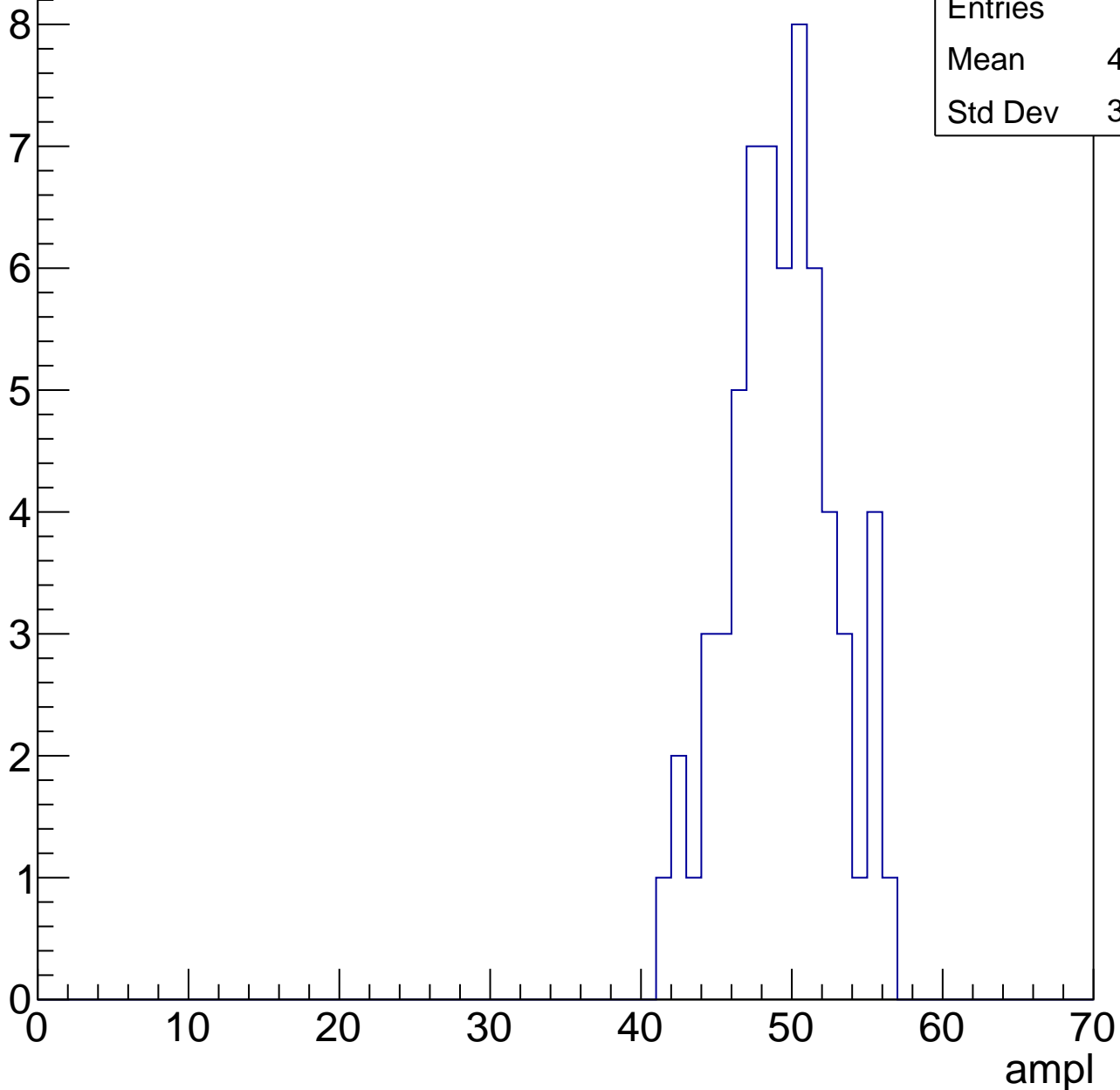


B1L101S, U6-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	48.82
Std Dev	3.439

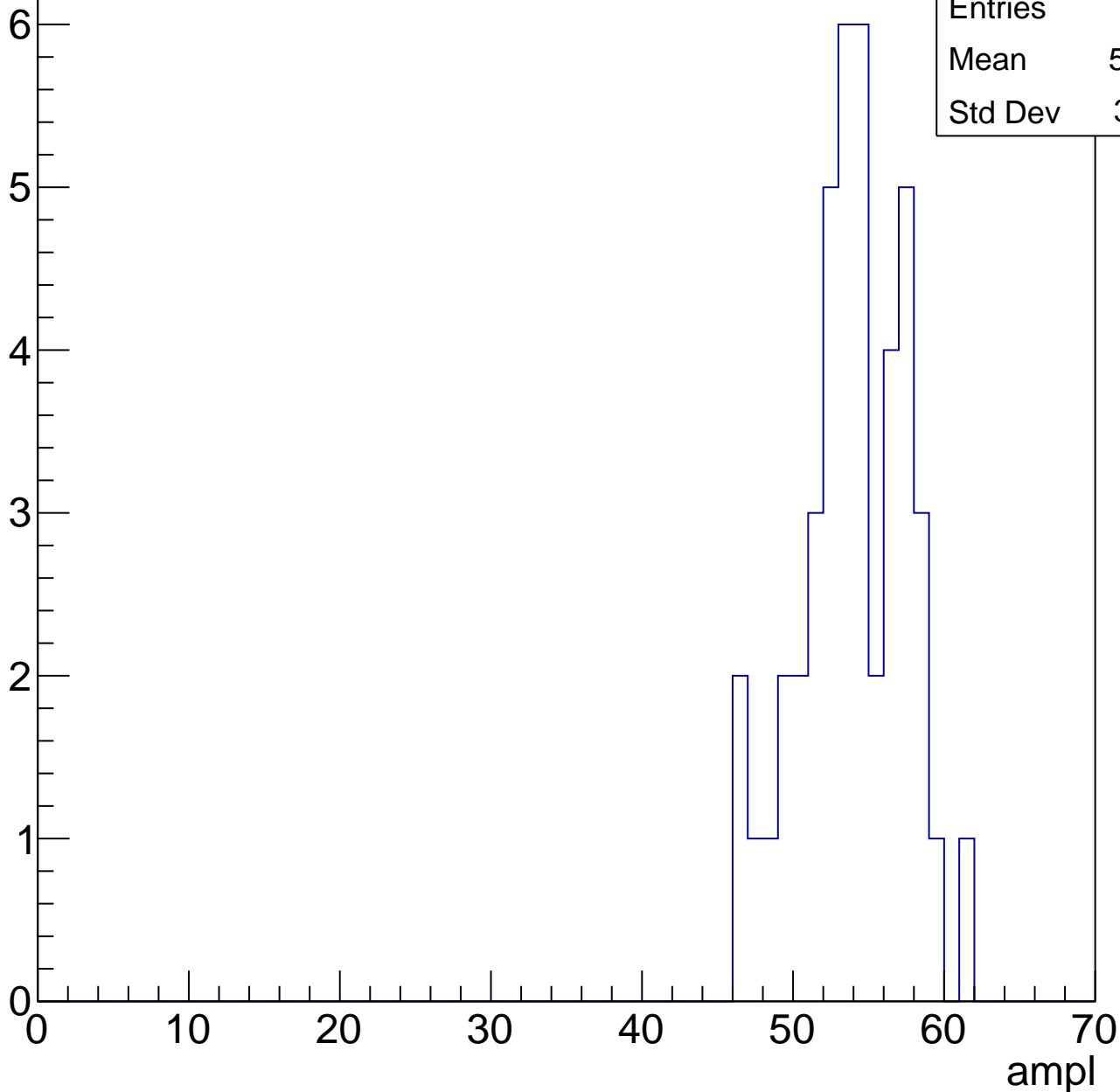


B1L101S, U6-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	53.48
Std Dev	3.441



B1L101S, U6-ch126, adc5

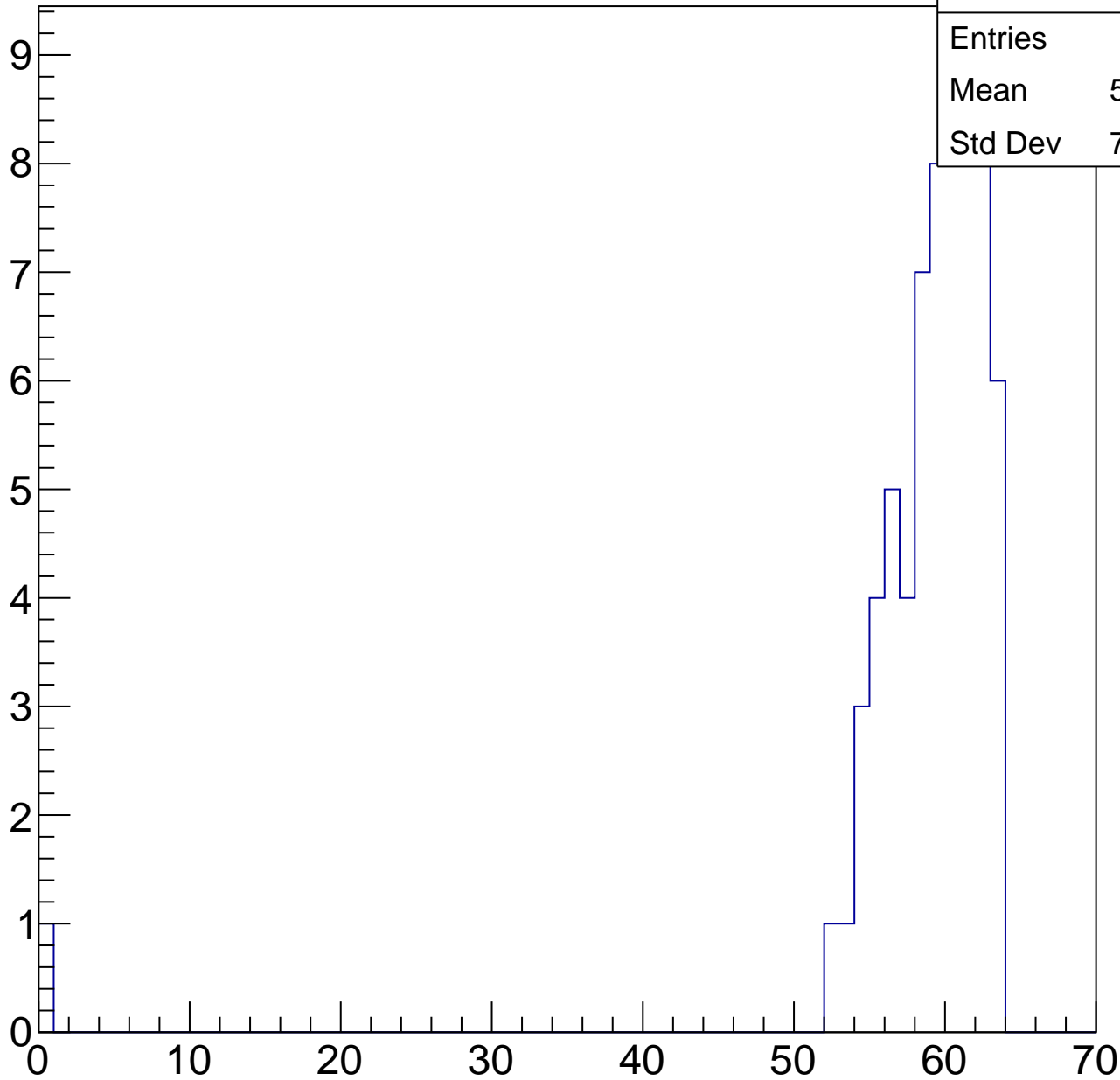
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	58.12
Std Dev	7.722

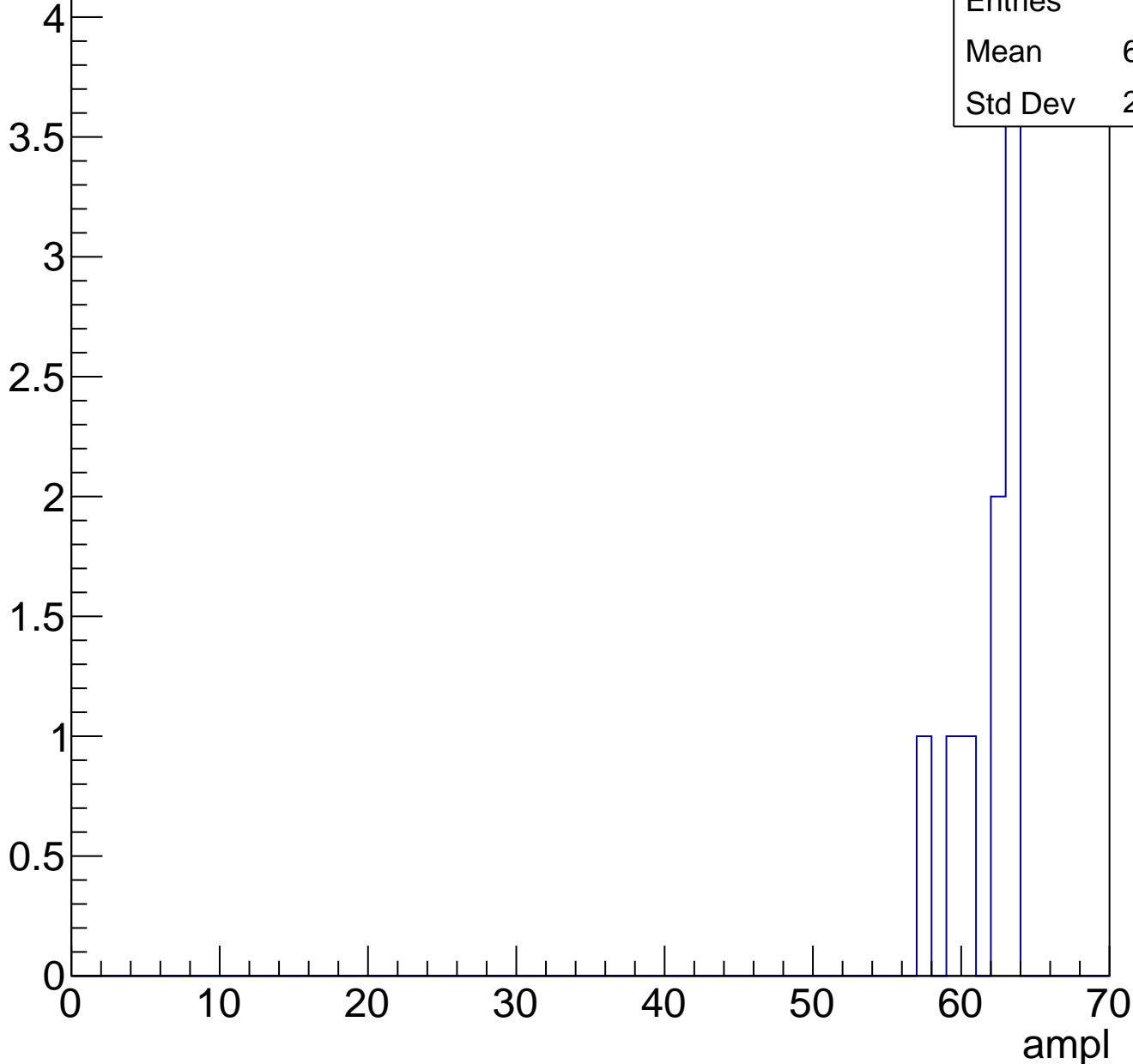
ampl



B1L101S, U6-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch127, adc0

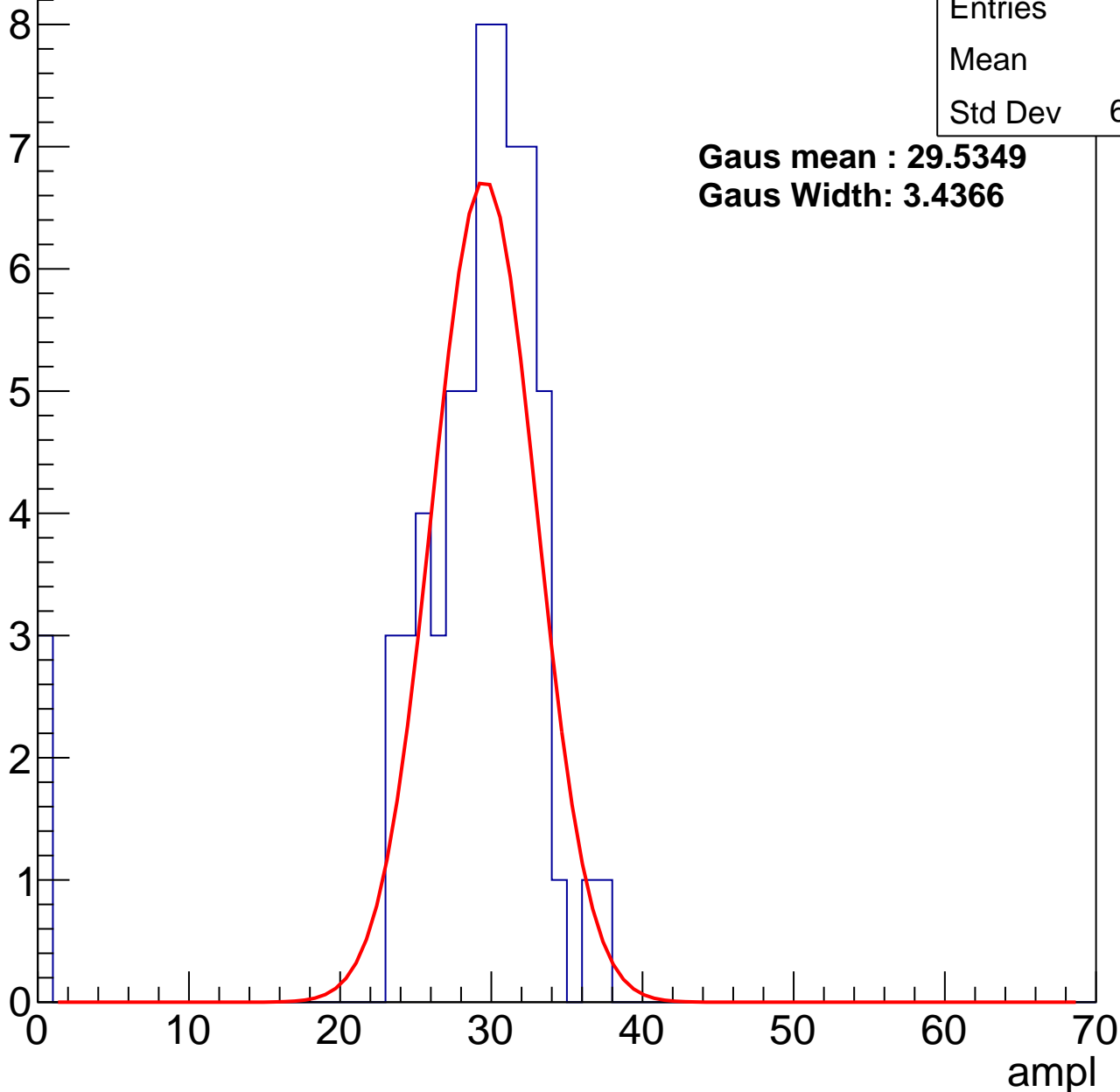
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.8
Std Dev	6.899

Gaus mean : 29.5349

Gaus Width: 3.4366



B1L101S, U6-ch127, adc1

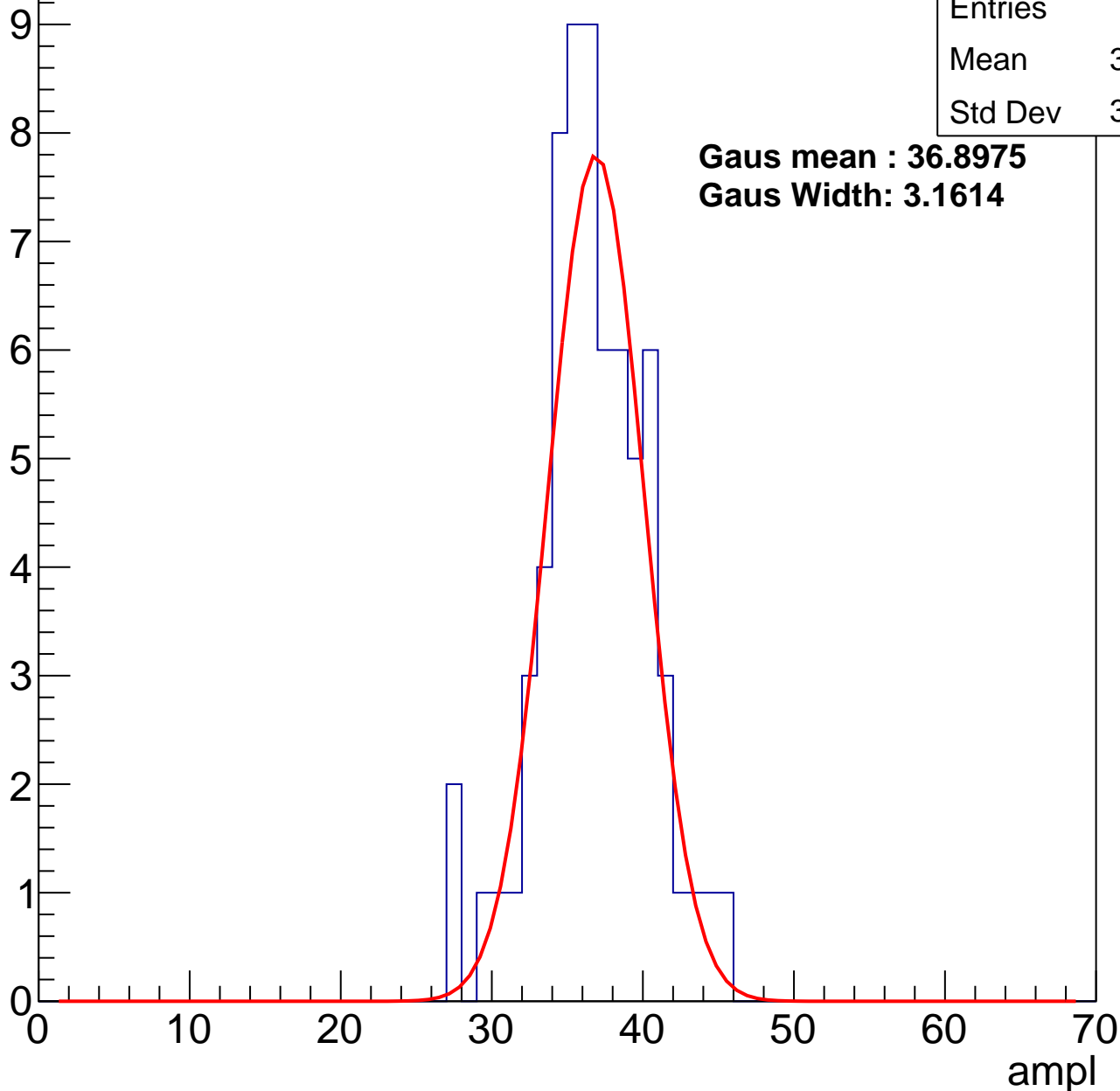
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.25
Std Dev	3.574

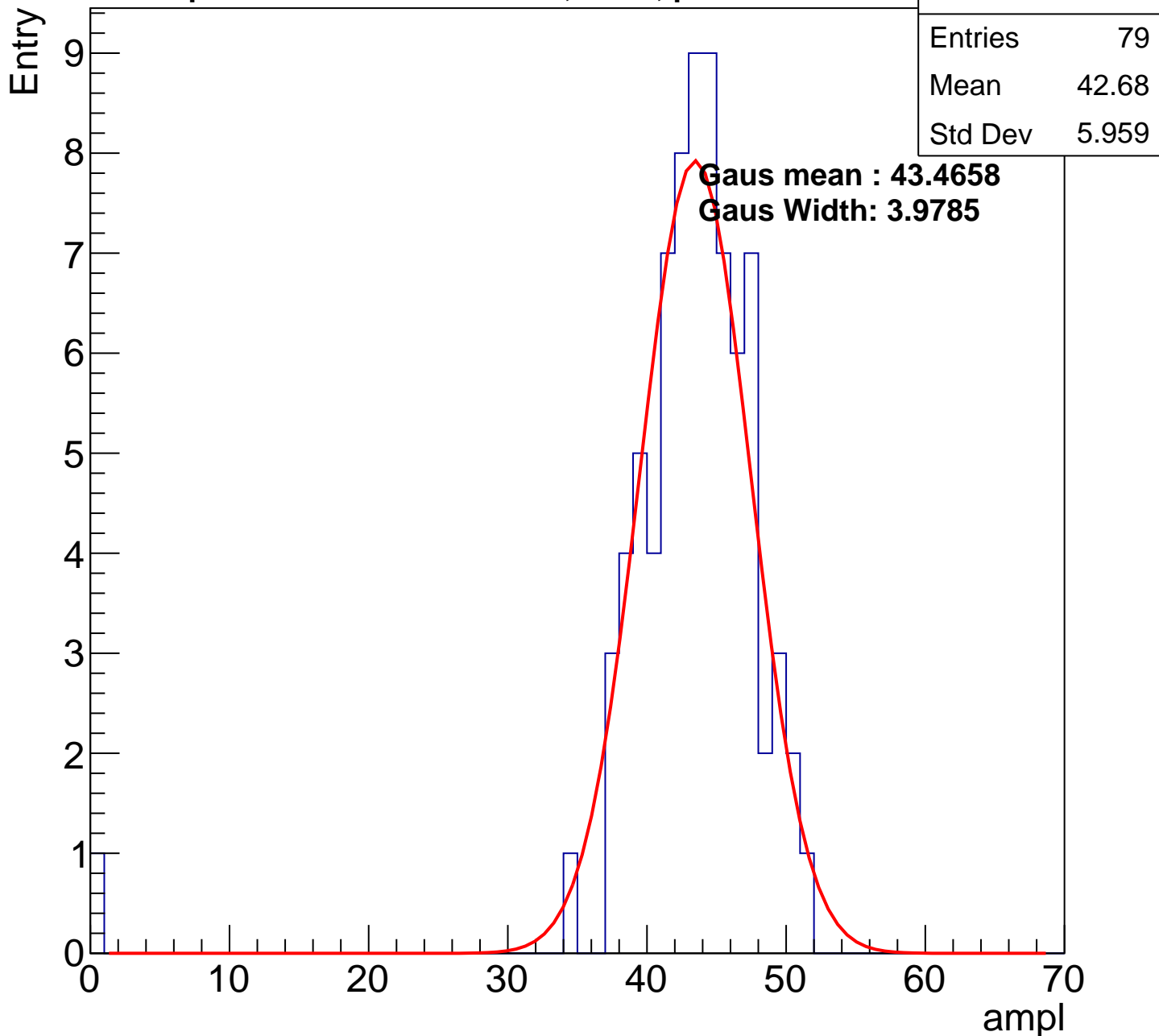
Gaus mean : 36.8975

Gaus Width: 3.1614



B1L101S, U6-ch127, adc2

calib_packv5_042523_0143.root, FC#0, port D2

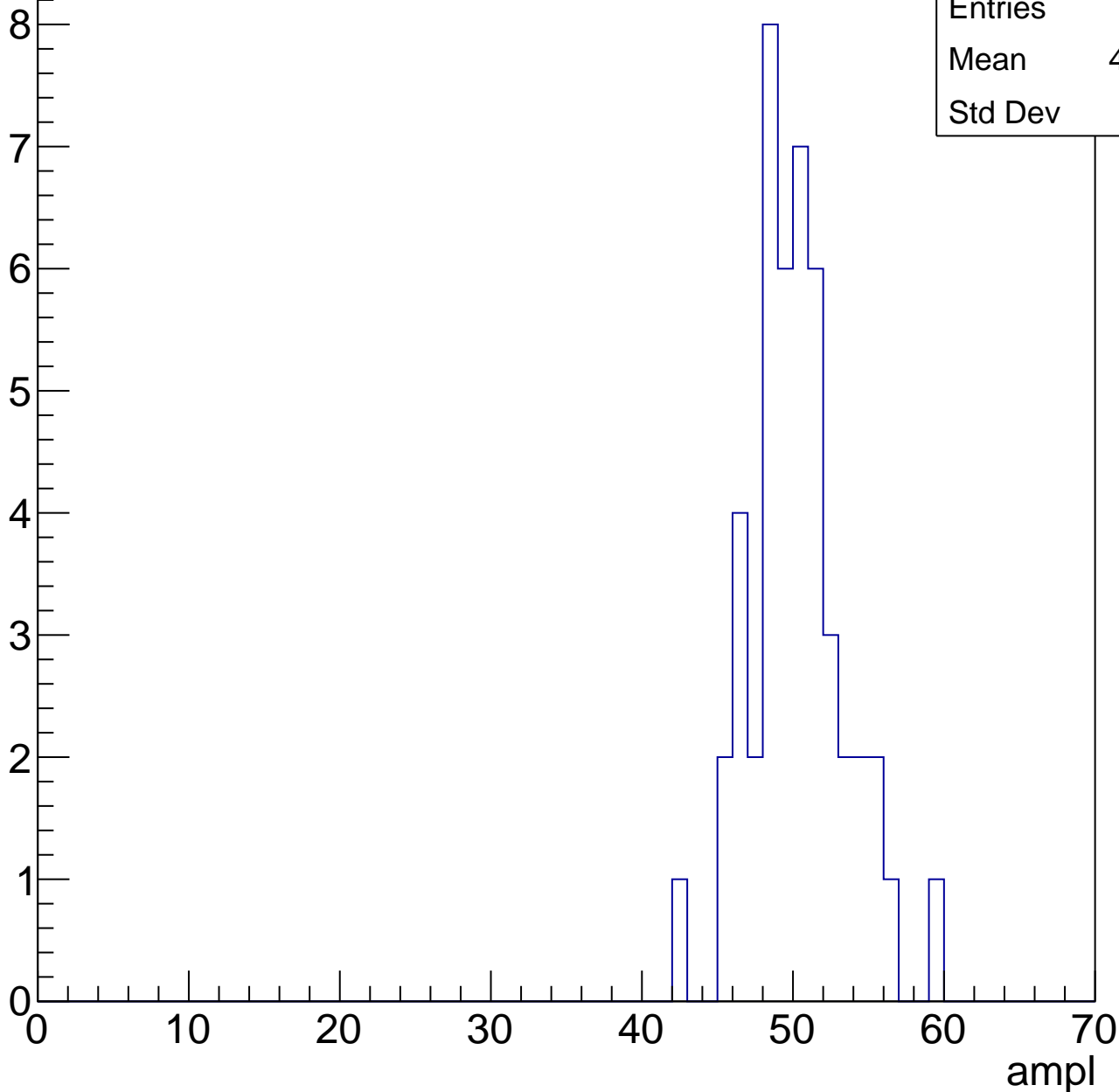


B1L101S, U6-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	49.77
Std Dev	3.15

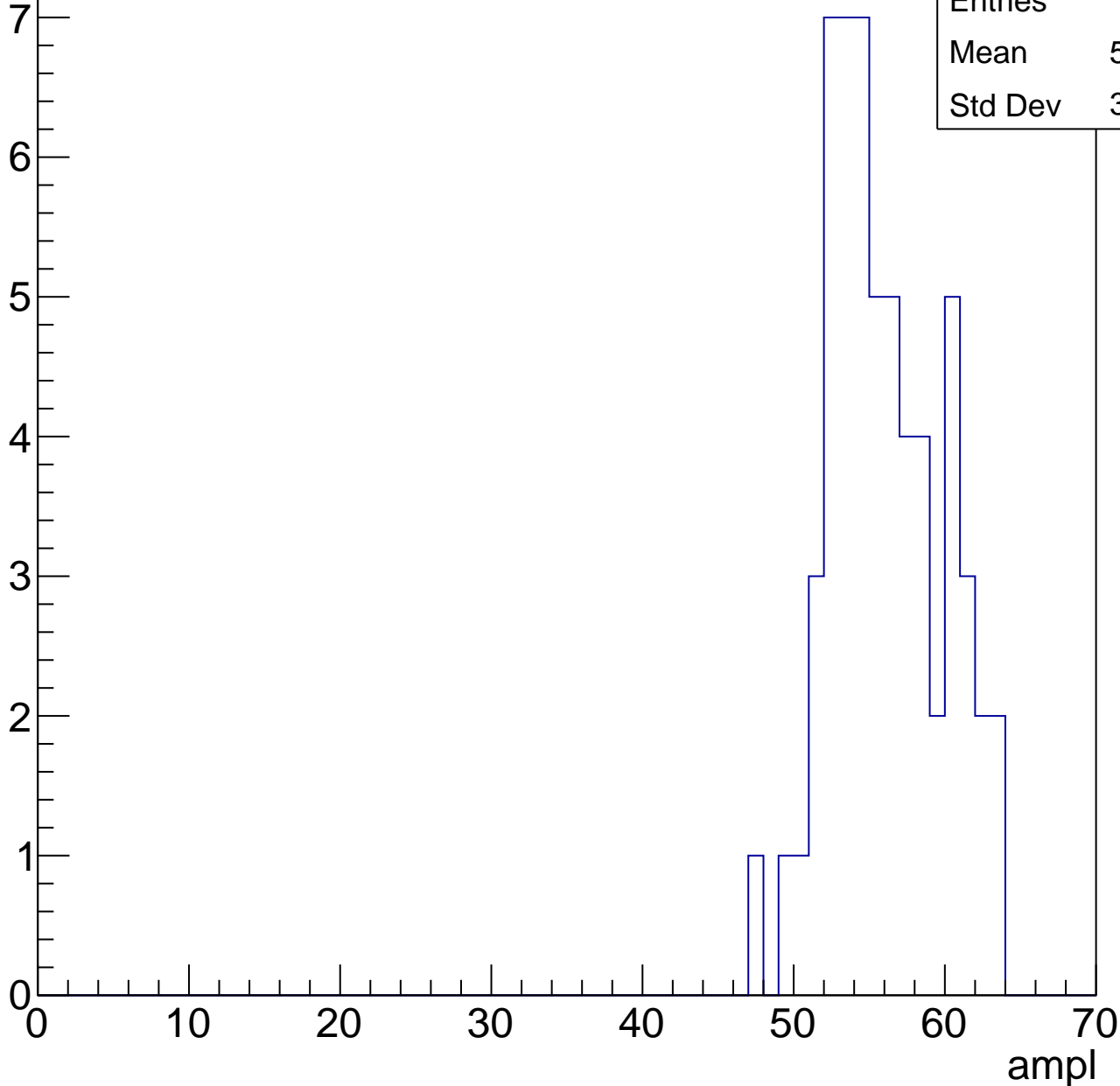


B1L101S, U6-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.56
Std Dev	3.684

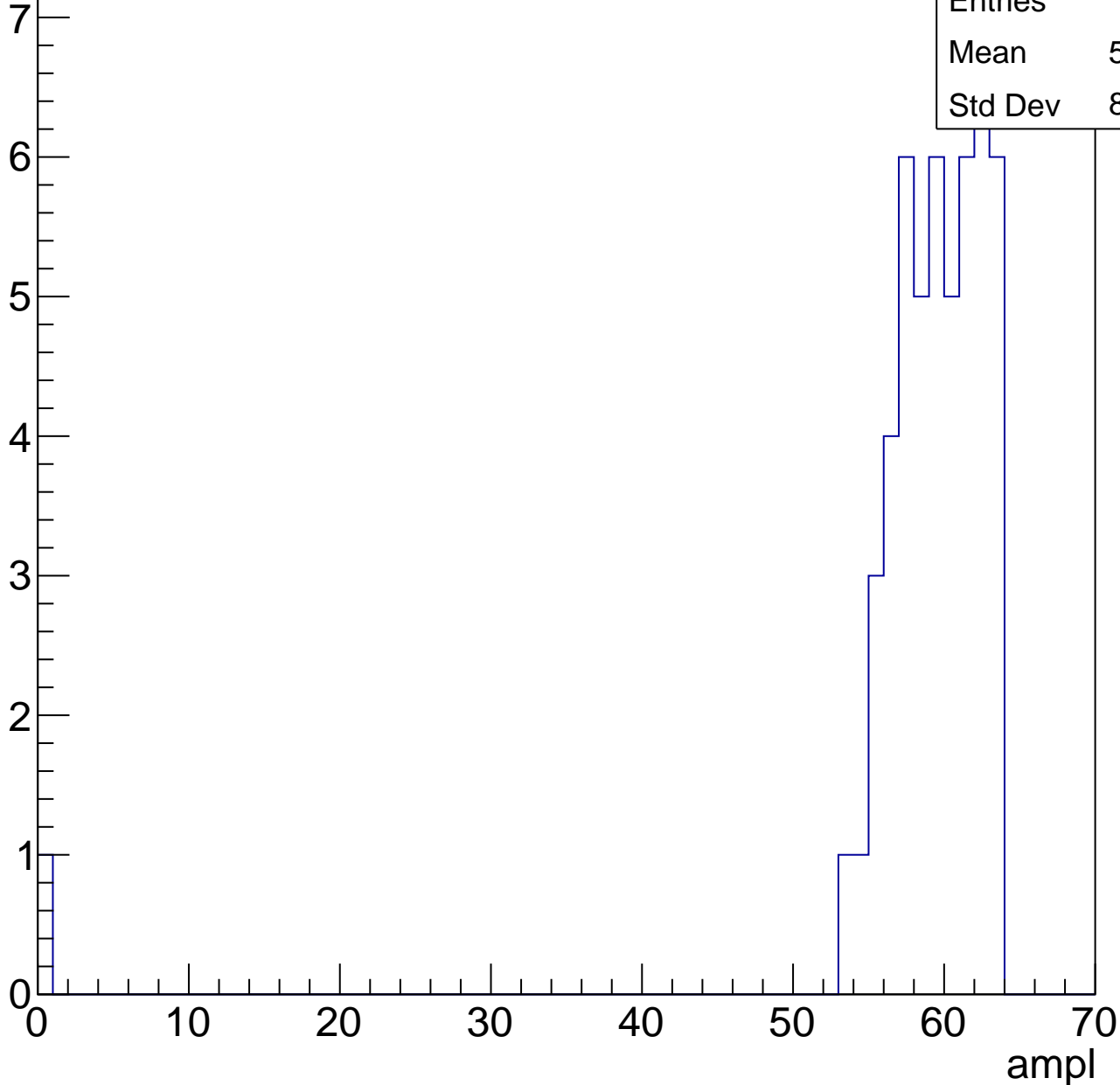


B1L101S, U6-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

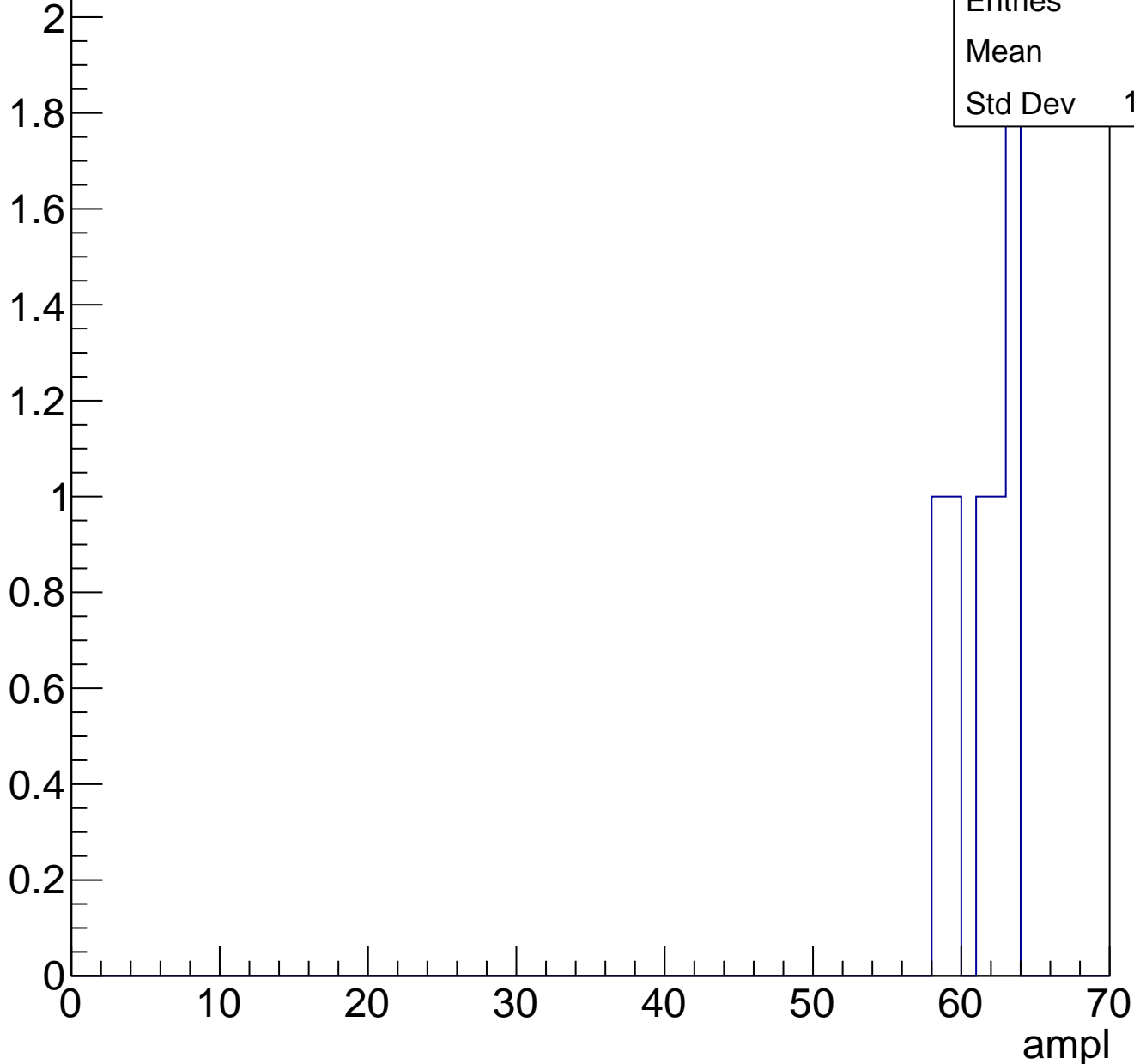
Entries	51
Mean	58.04
Std Dev	8.627



B1L101S, U6-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U6-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L101S, U6-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

