

B0L001S, U23-ch0, adc0

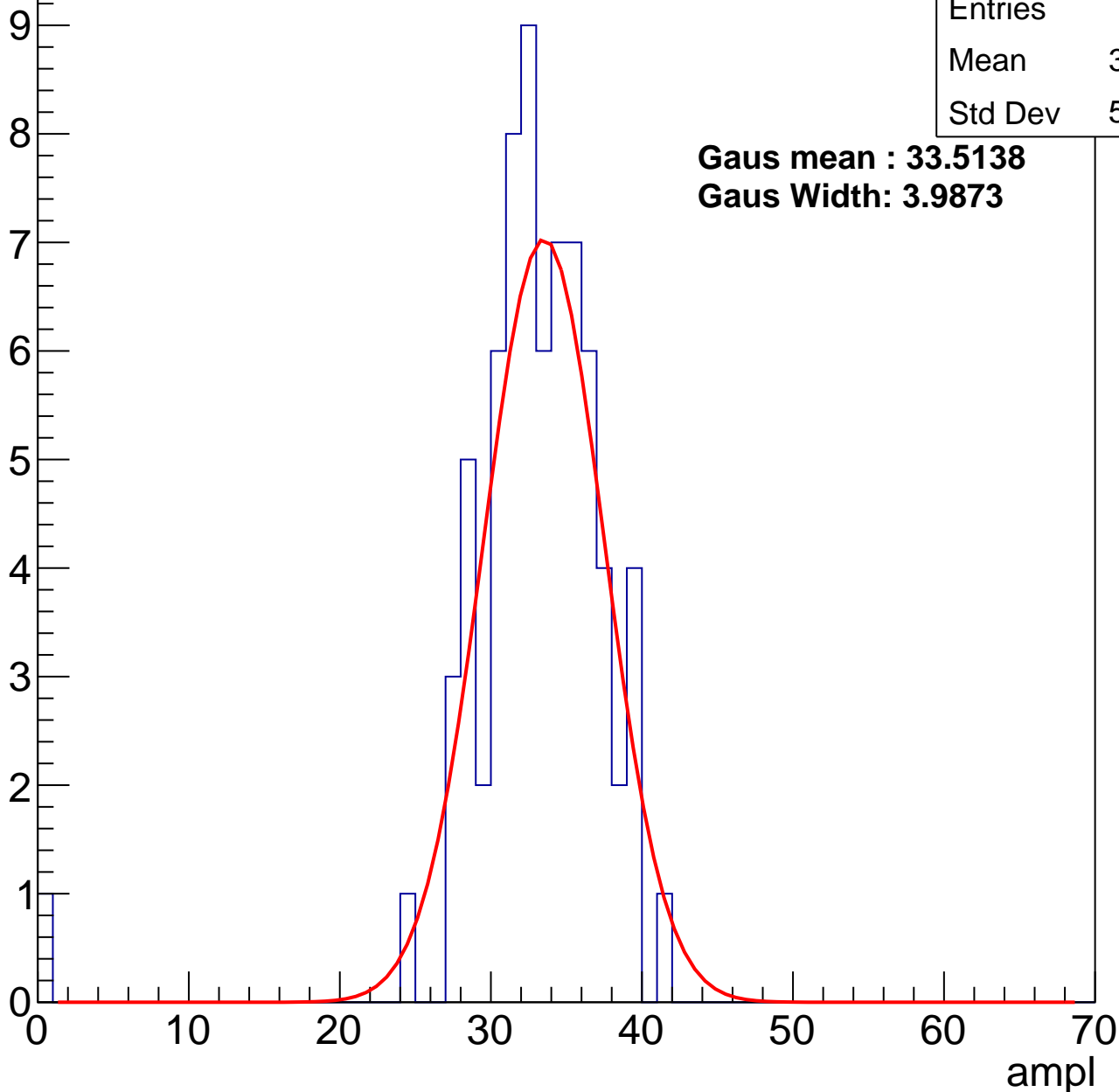
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	32.46
Std Dev	5.164

Gaus mean : 33.5138

Gaus Width: 3.9873



B0L001S, U23-ch0, adc1

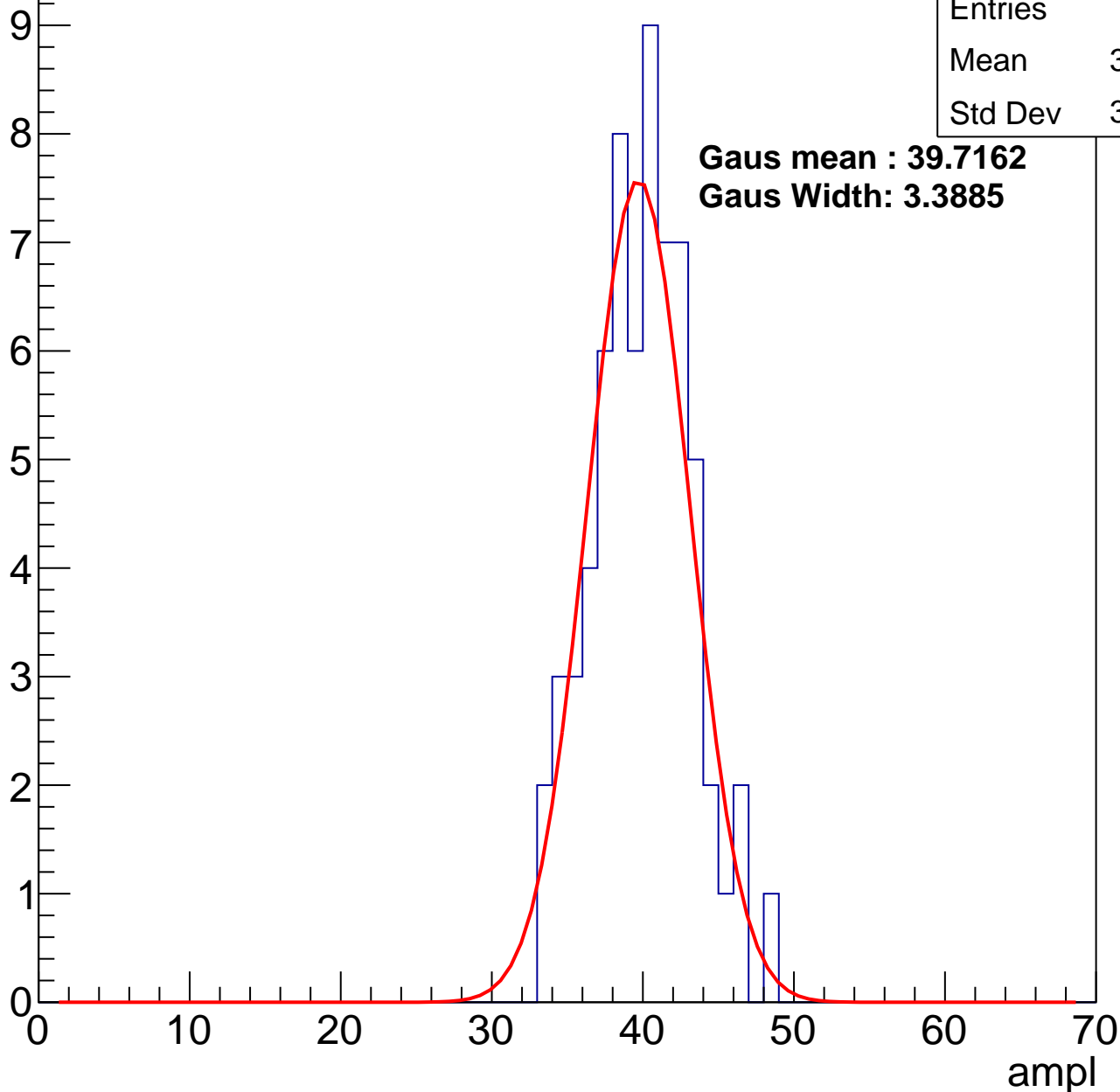
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	39.48
Std Dev	3.239

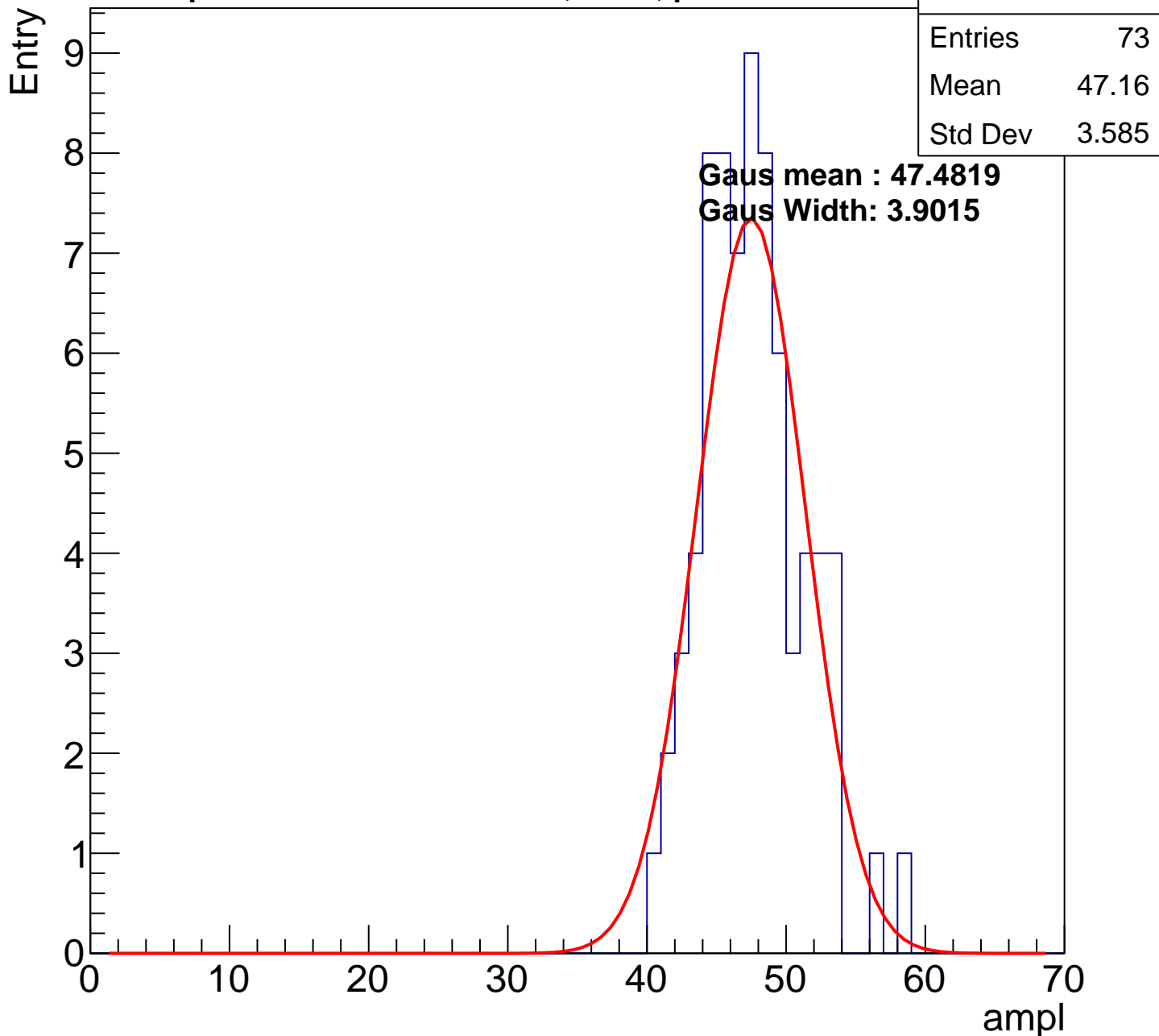
Gaus mean : 39.7162

Gaus Width: 3.3885



B0L001S, U23-ch0, adc2

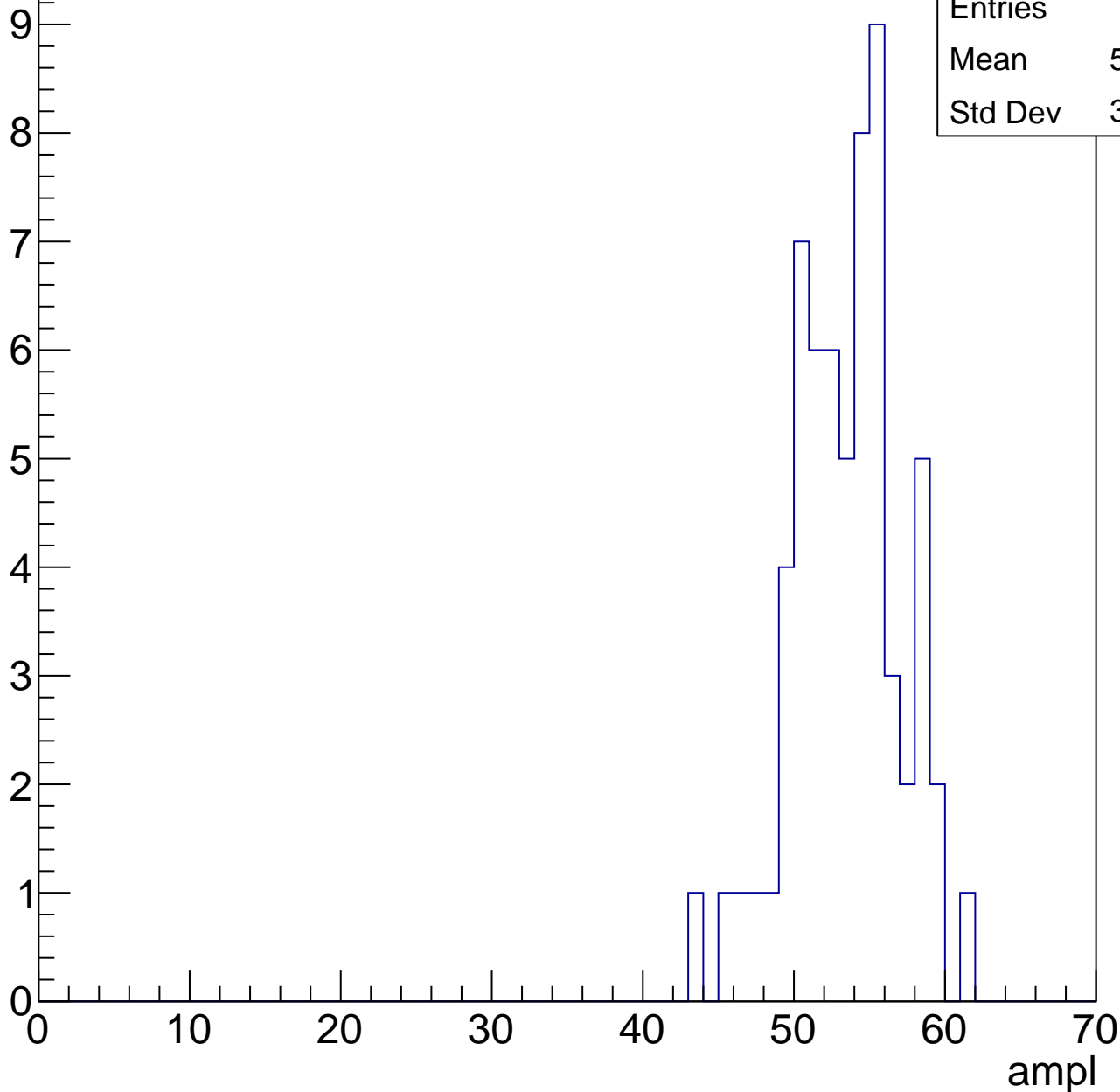
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U23-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

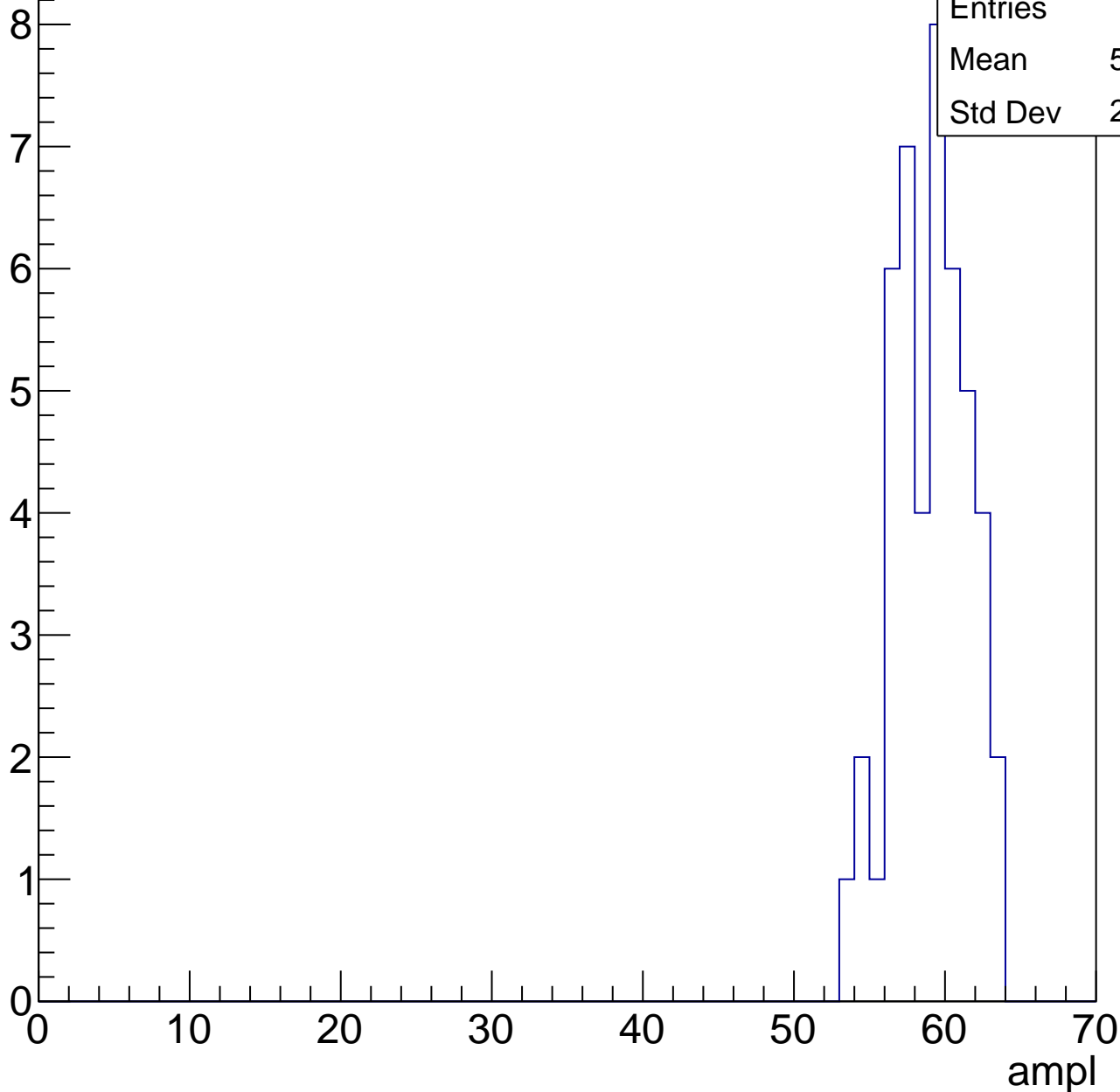


B0L001S, U23-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.57
Std Dev	2.437

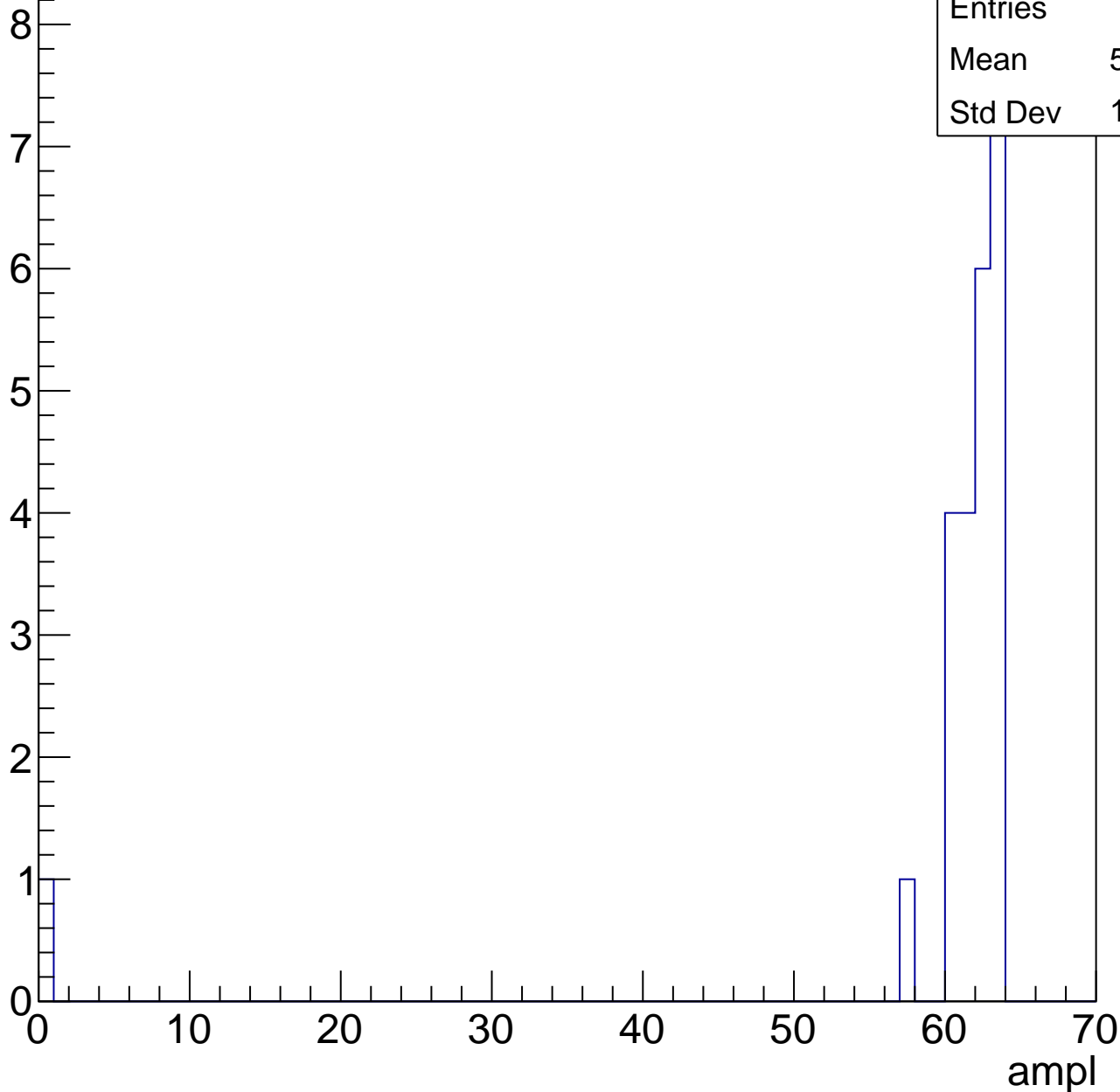


B0L001S, U23-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	59.04
Std Dev	12.39



B0L001S, U23-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch1, adc0

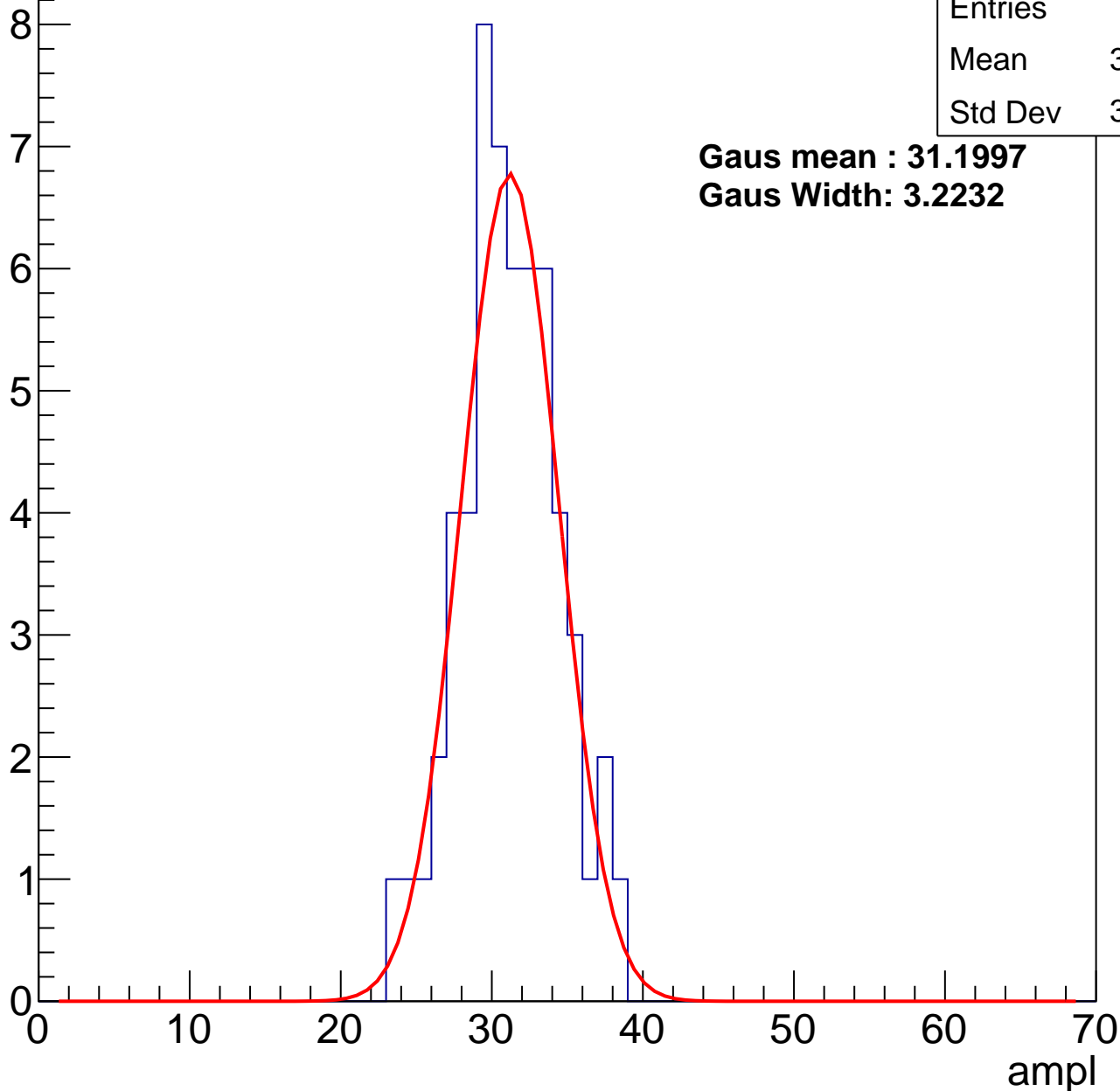
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.72
Std Dev	3.227

Gaus mean : 31.1997

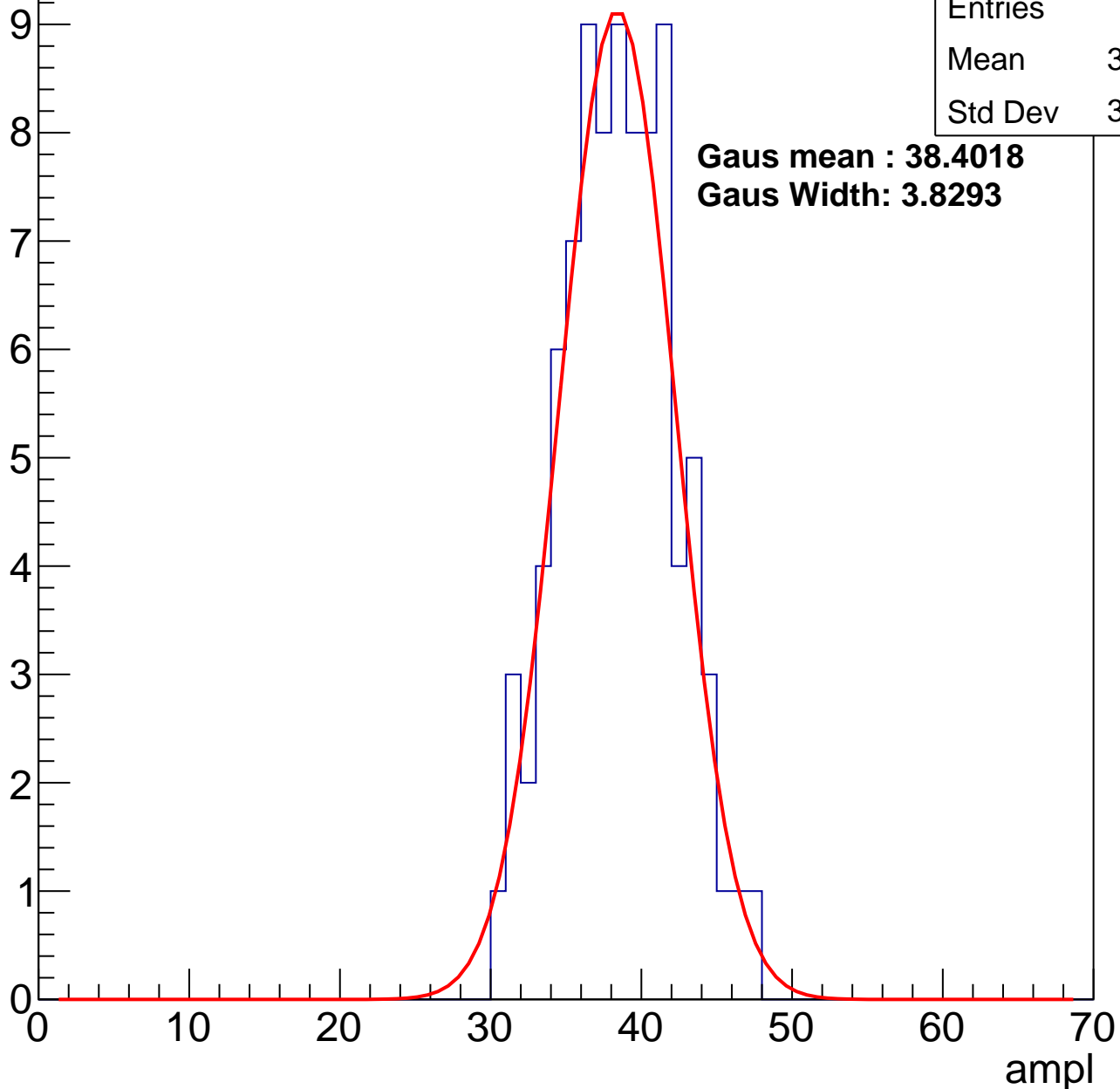
Gaus Width: 3.2232



B0L001S, U23-ch1, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch1, adc2

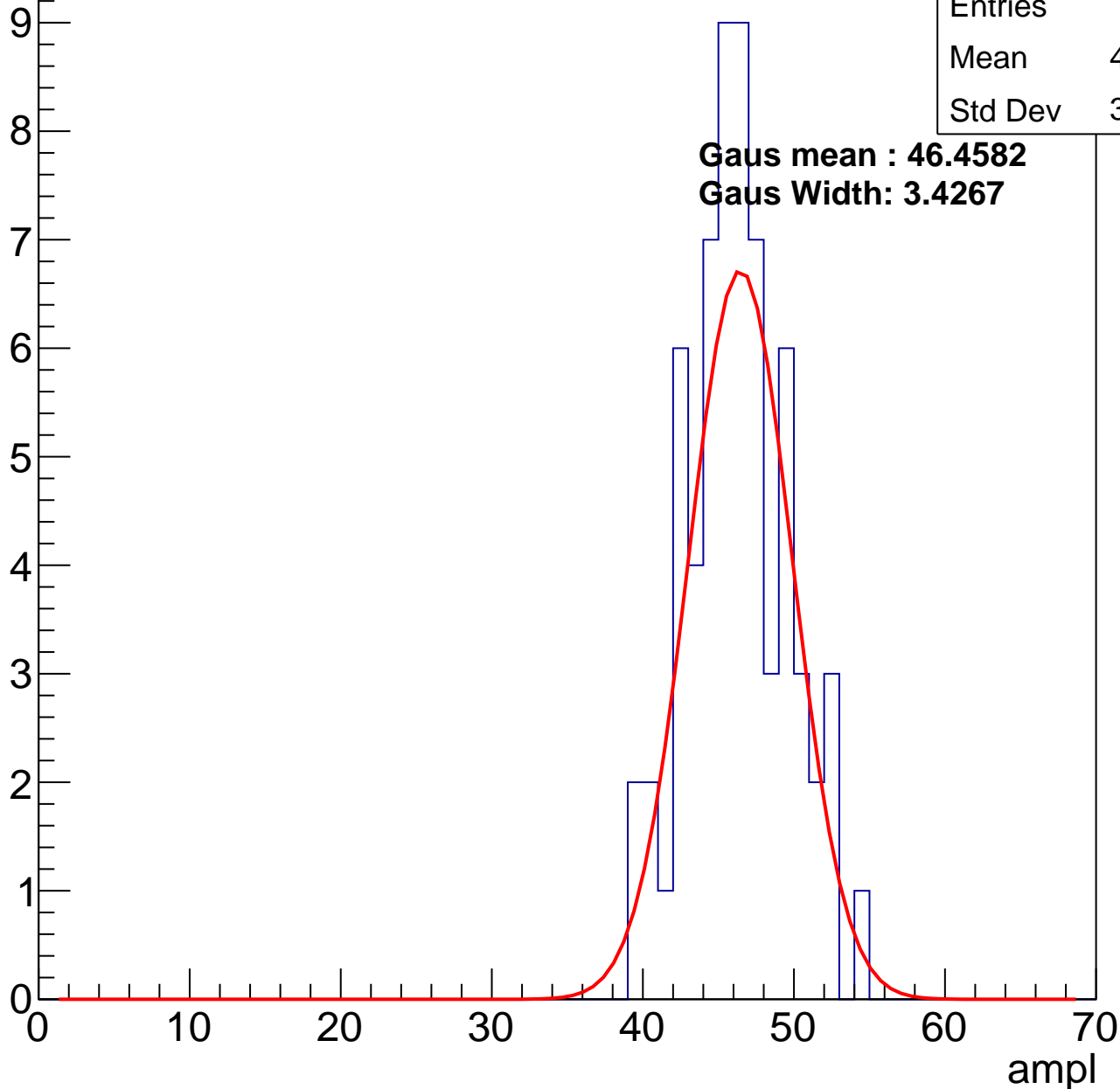
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.83
Std Dev	3.294

Gaus mean : 46.4582

Gaus Width: 3.4267

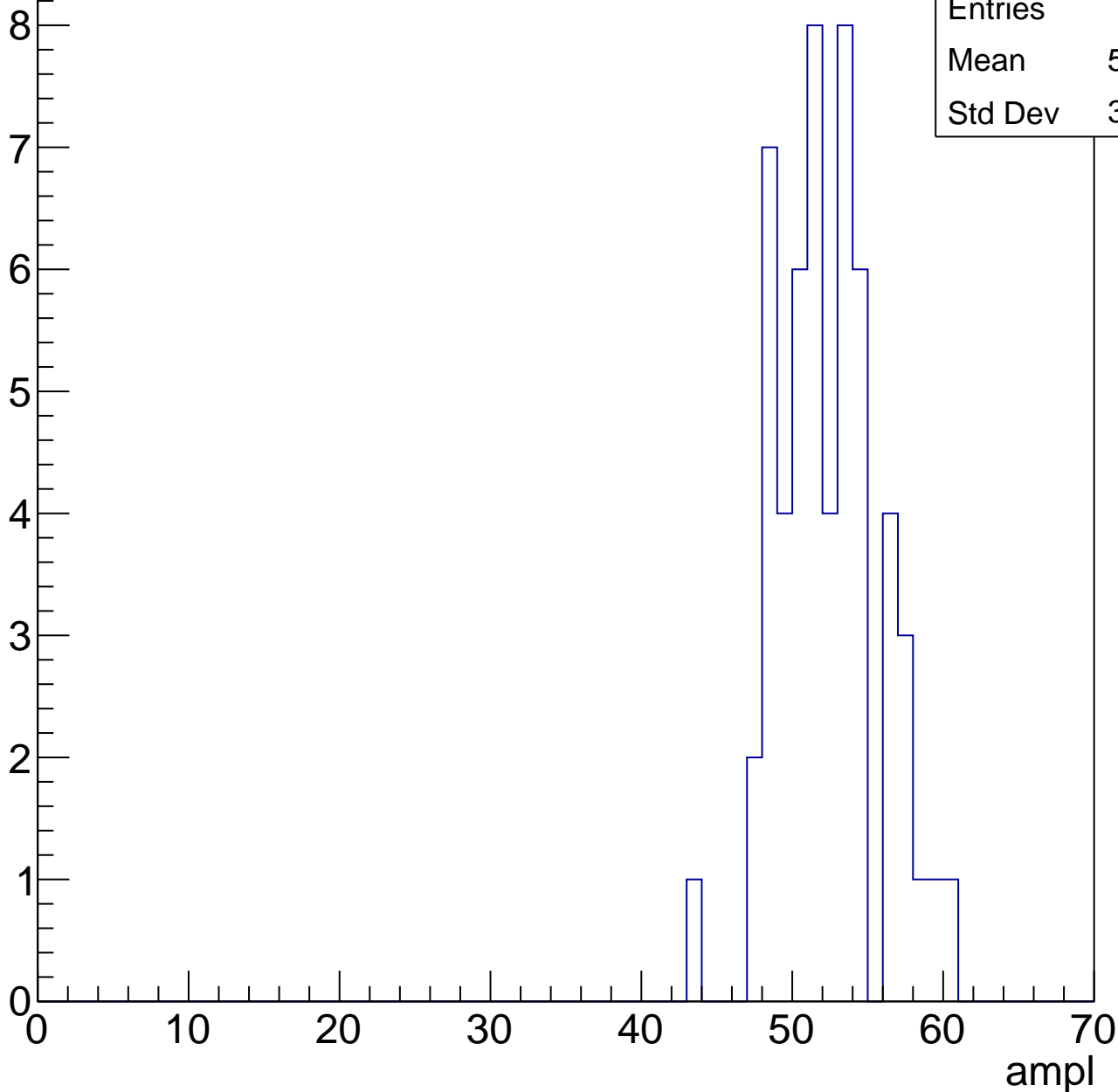


B0L001S, U23-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	51.88
Std Dev	3.354

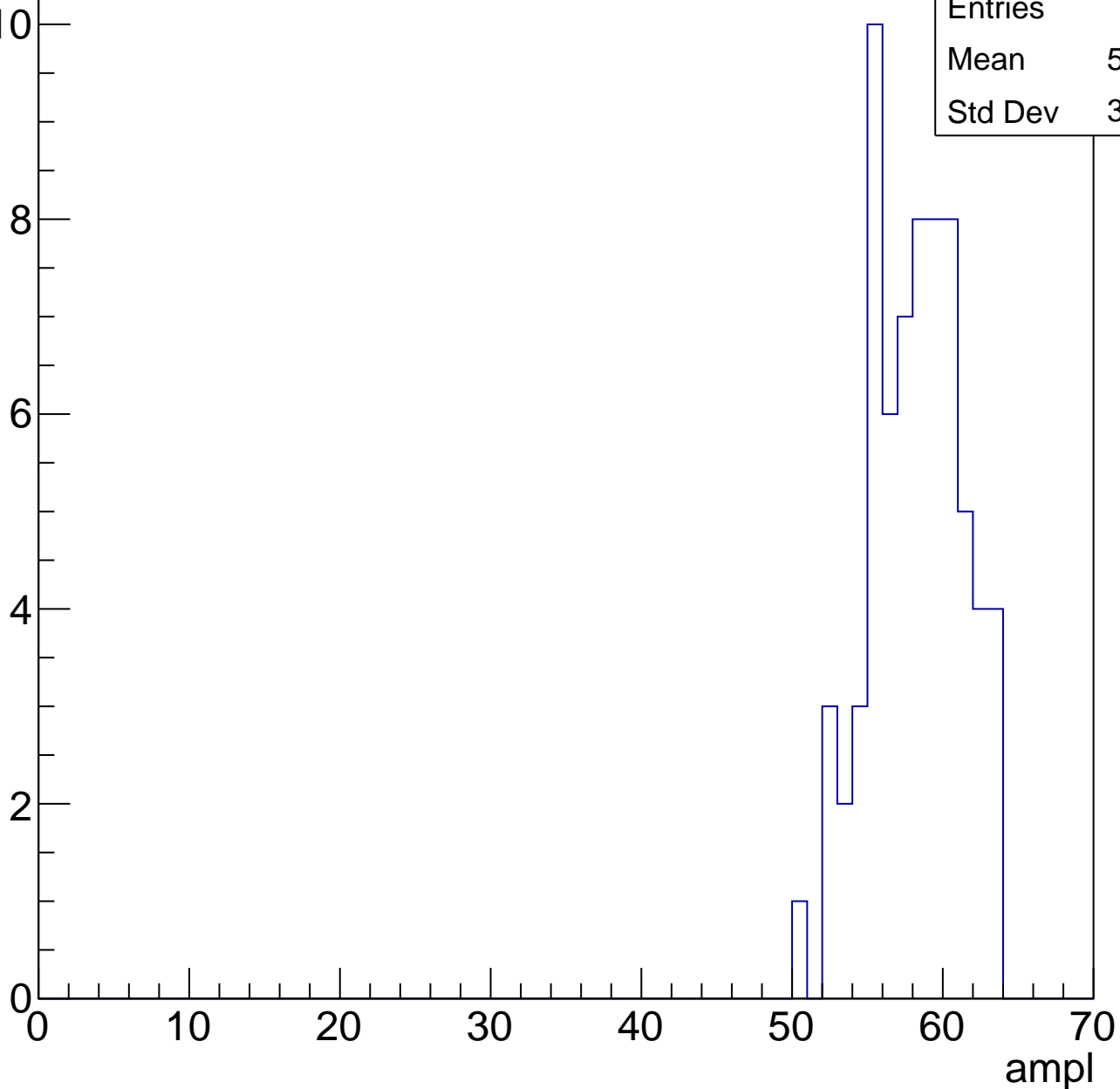


B0L001S, U23-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	57.68
Std Dev	3.029

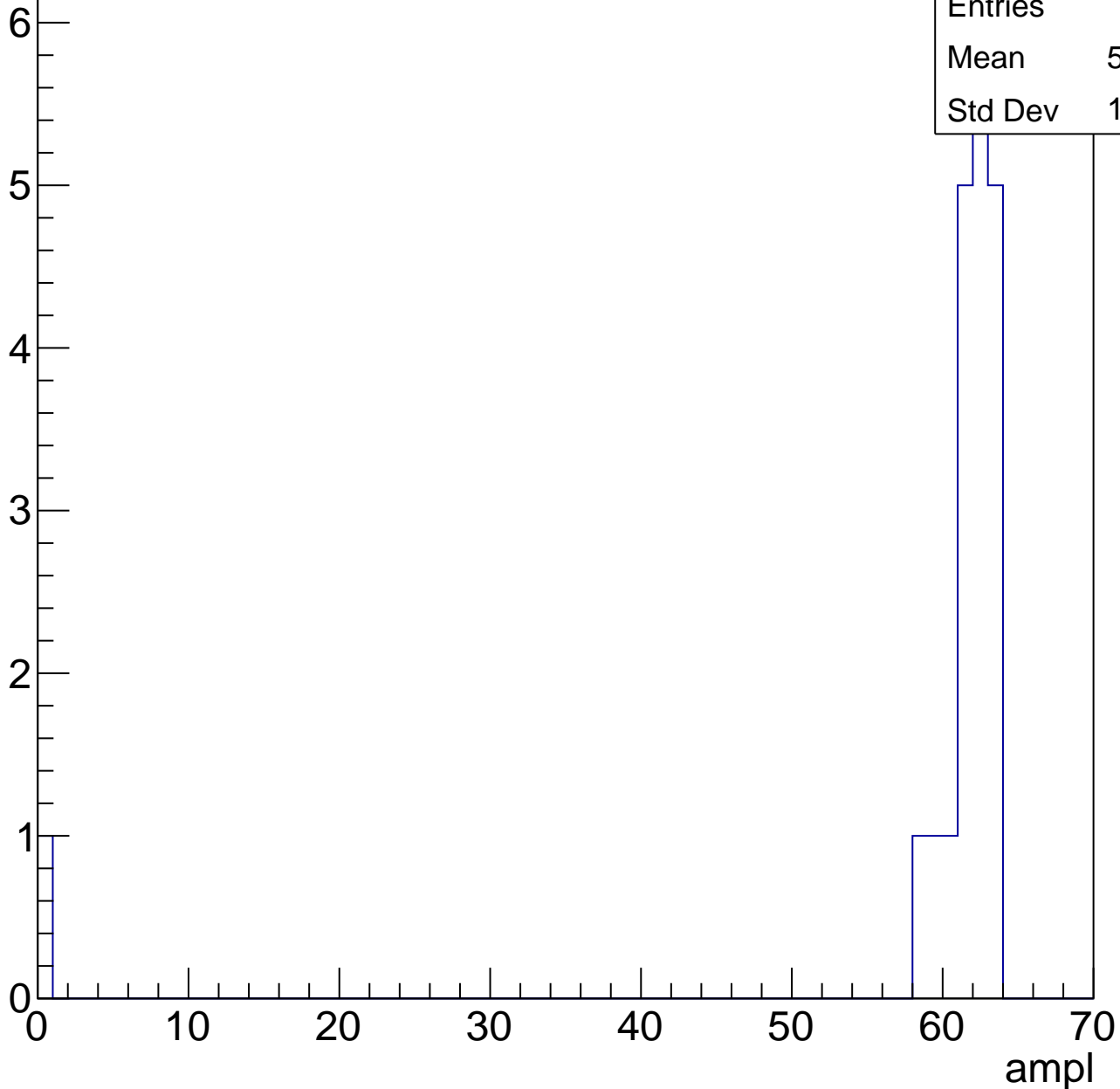


B0L001S, U23-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

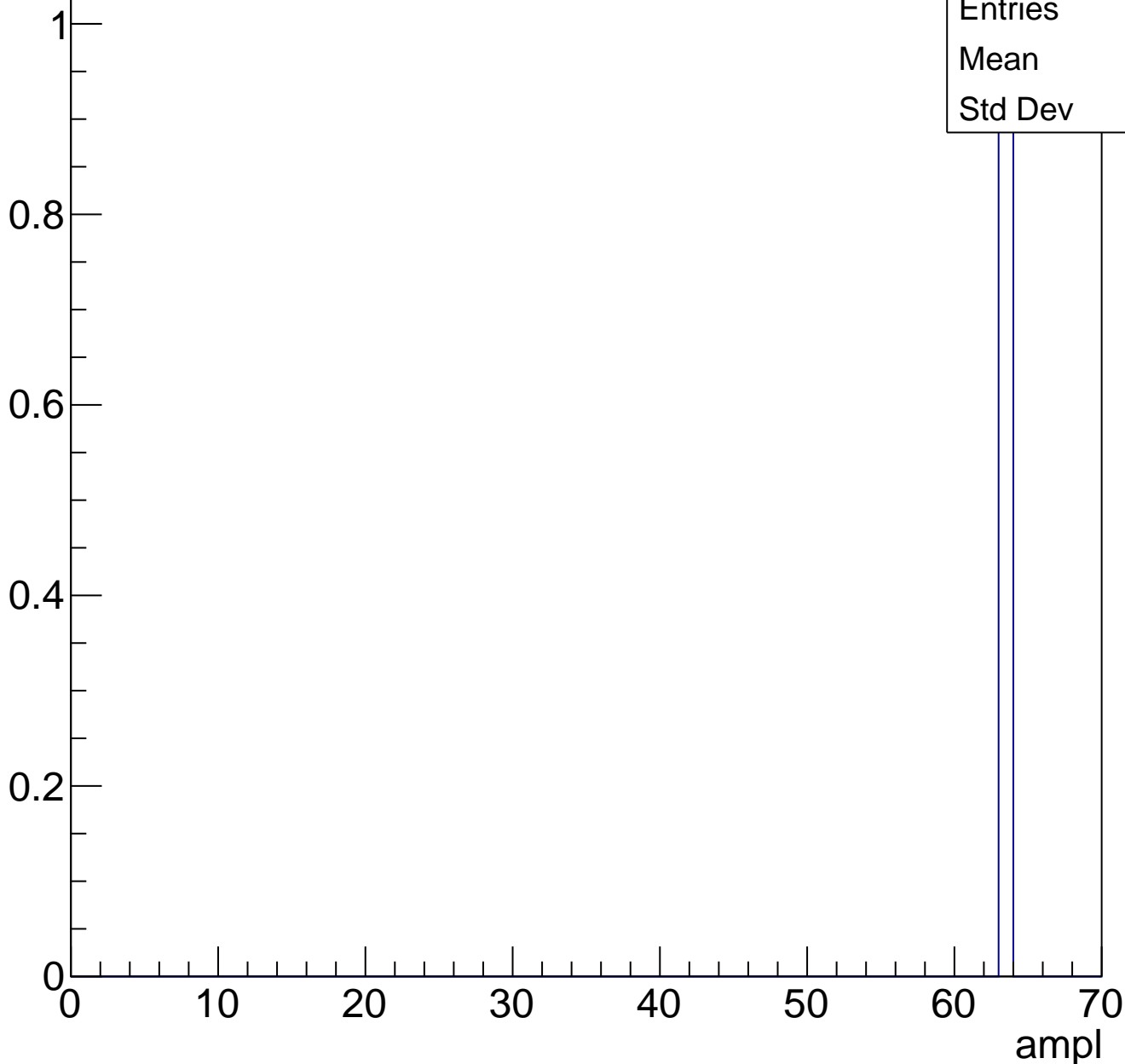
Entries	20
Mean	58.45
Std Dev	13.47



B0L001S, U23-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch2, adc0

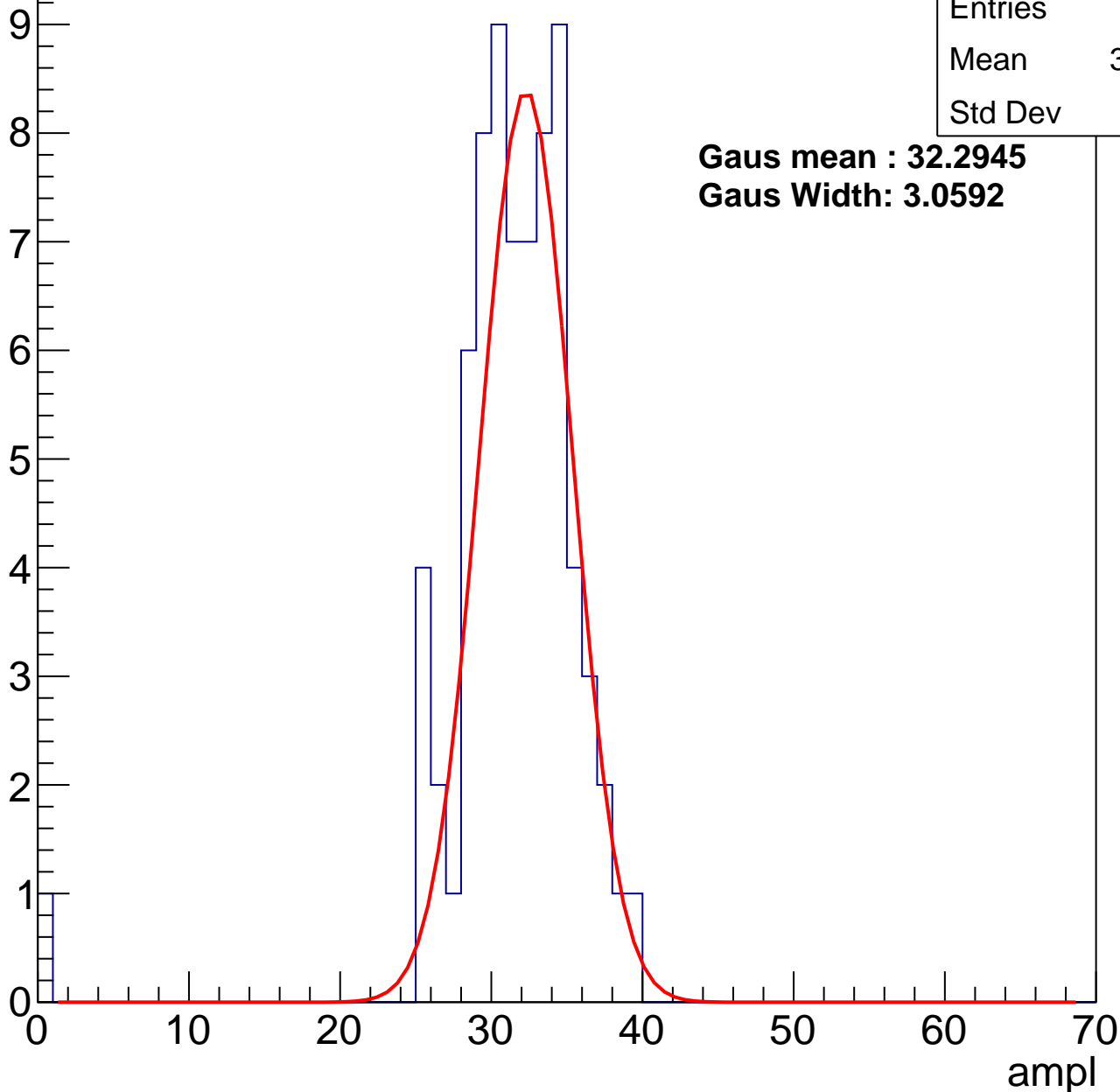
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.95
Std Dev	4.84

Gaus mean : 32.2945

Gaus Width: 3.0592



B0L001S, U23-ch2, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 61

Mean 38.21

Std Dev 3.275

Gaus mean : 39.1218

Gaus Width: 3.1557

0

0

10

20

30

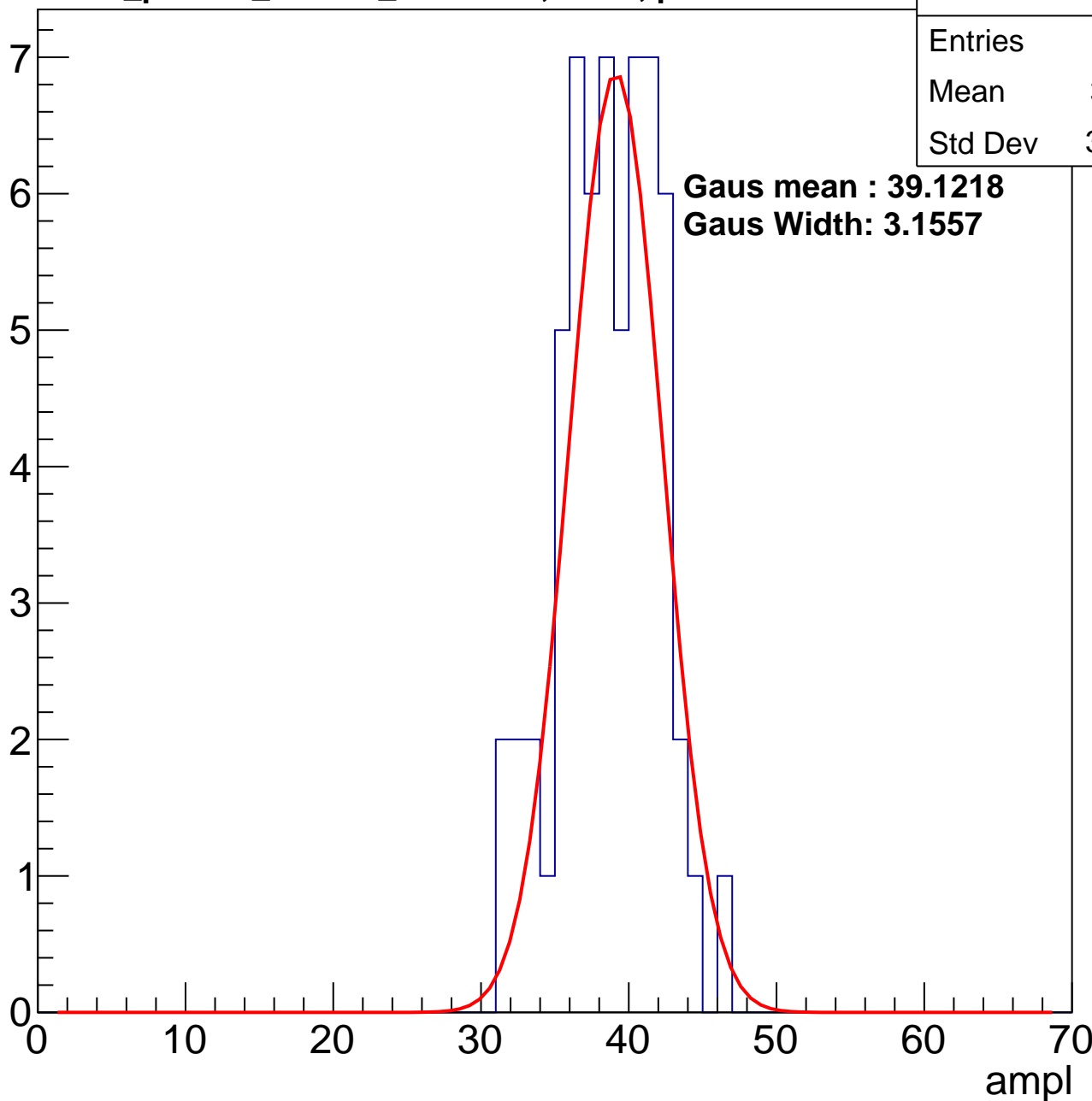
40

50

60

70

ampl



B0L001S, U23-ch2, adc2

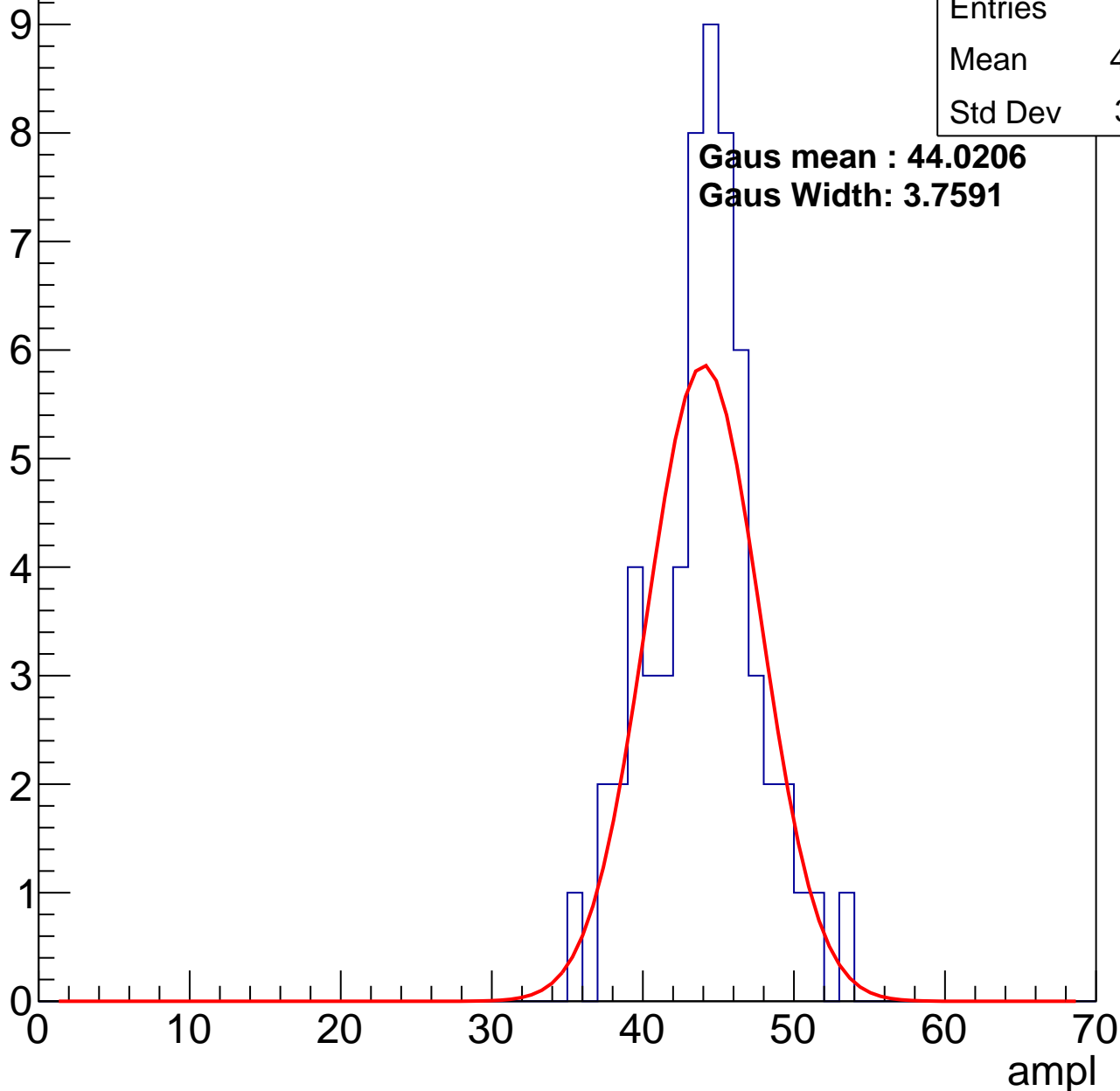
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.62
Std Dev	3.531

Gaus mean : 44.0206

Gaus Width: 3.7591

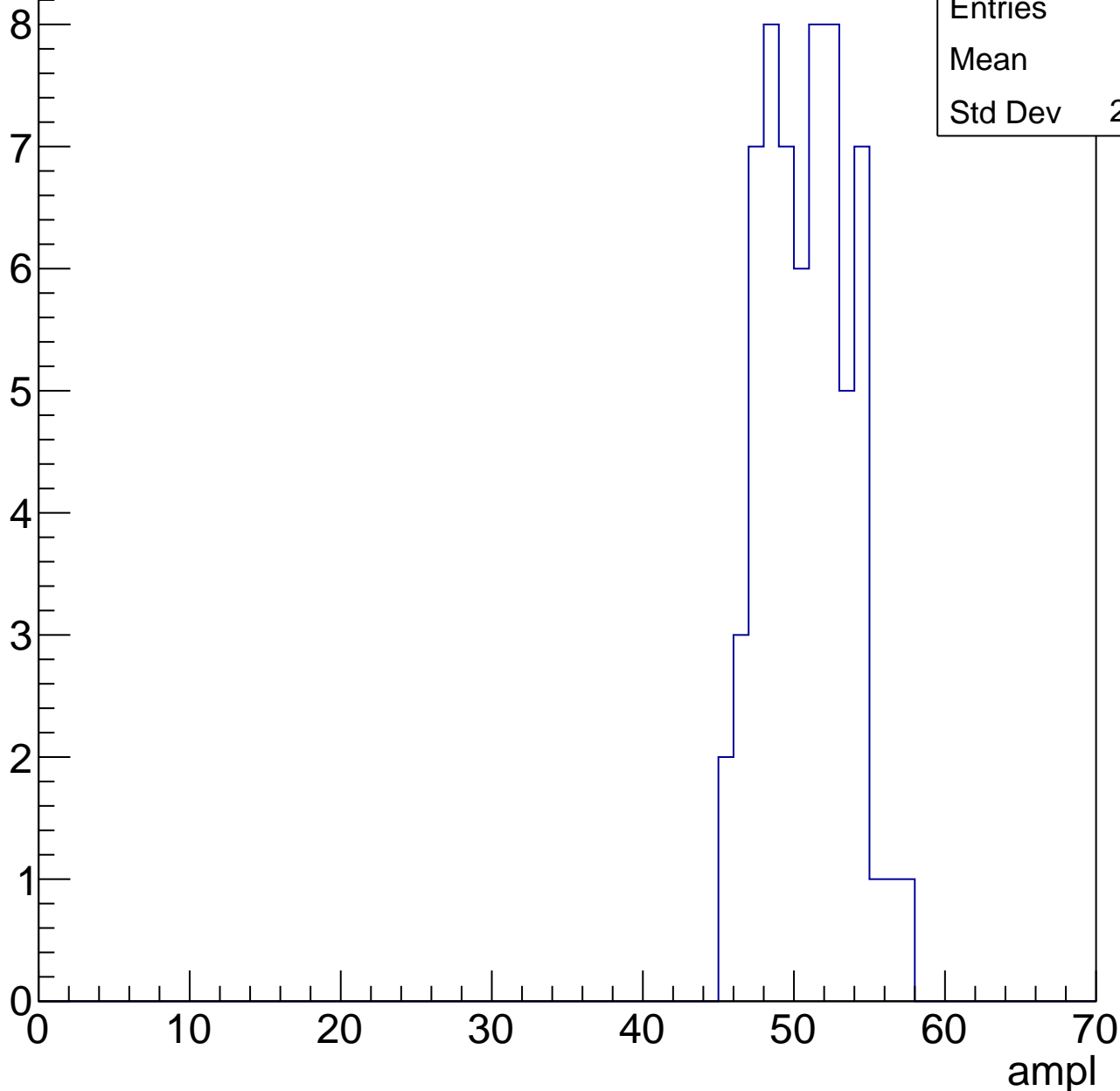


B0L001S, U23-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.3
Std Dev	2.799

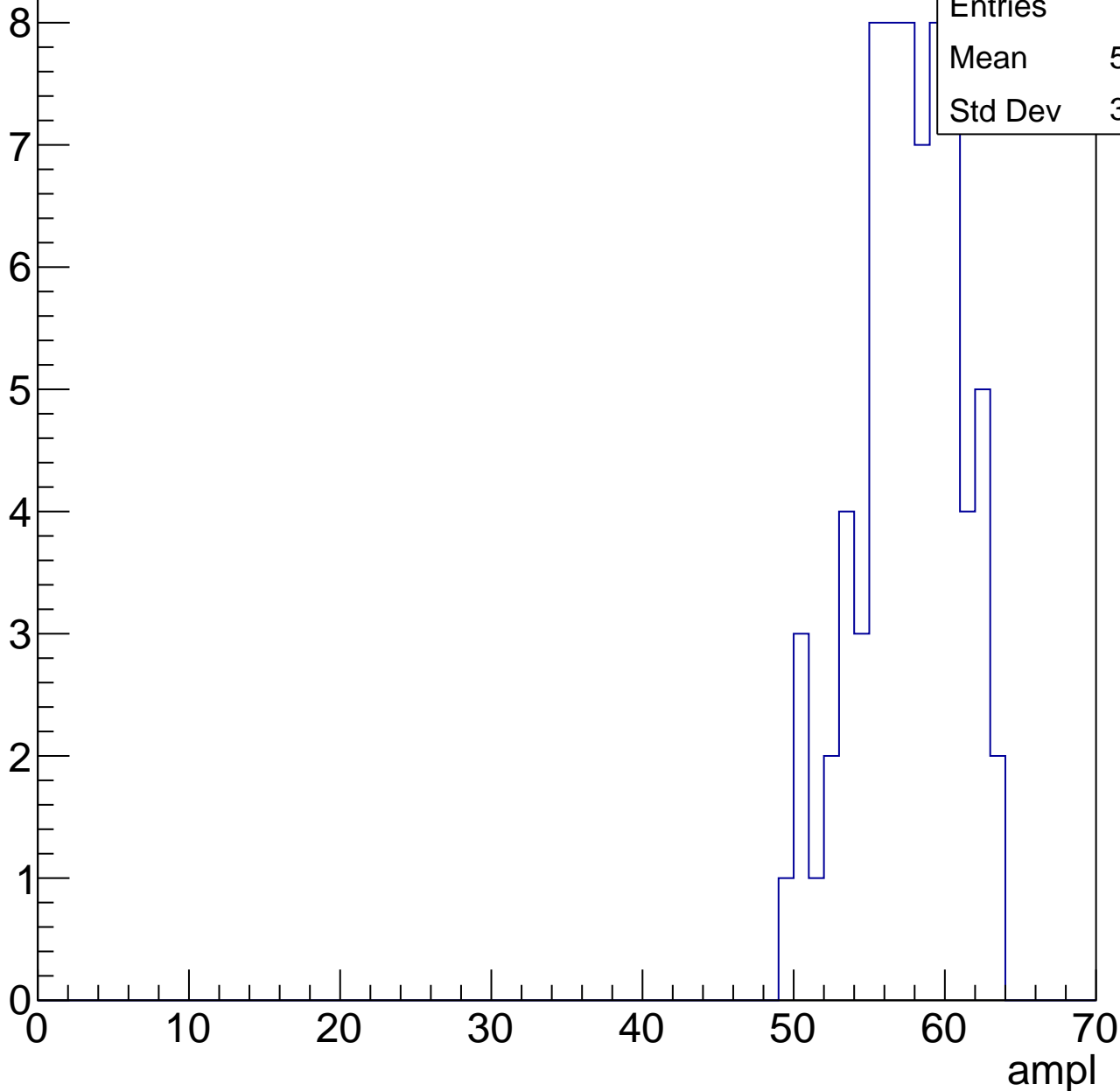


B0L001S, U23-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	57.08
Std Dev	3.332

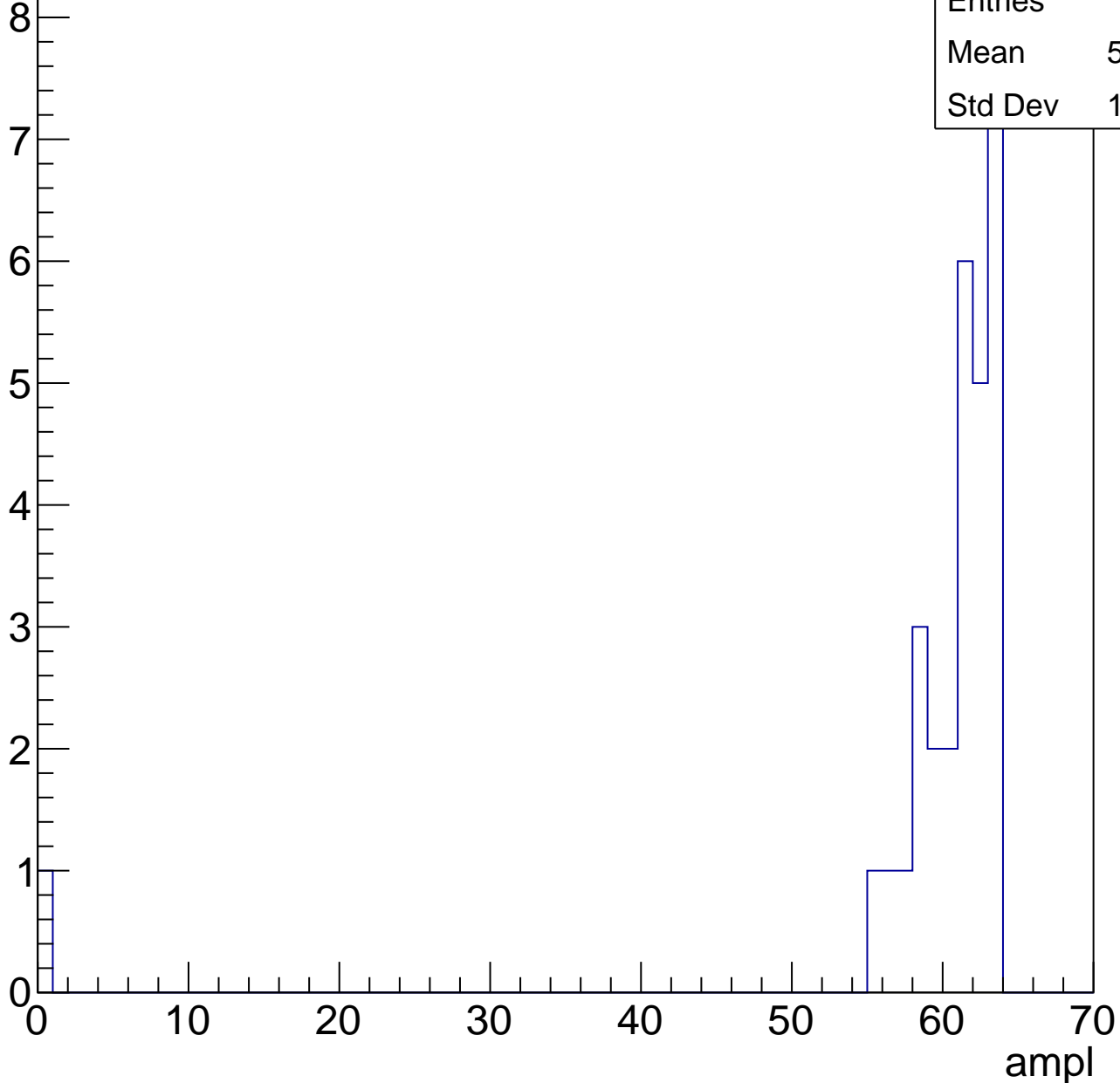


B0L001S, U23-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.67
Std Dev	11.12



B0L001S, U23-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

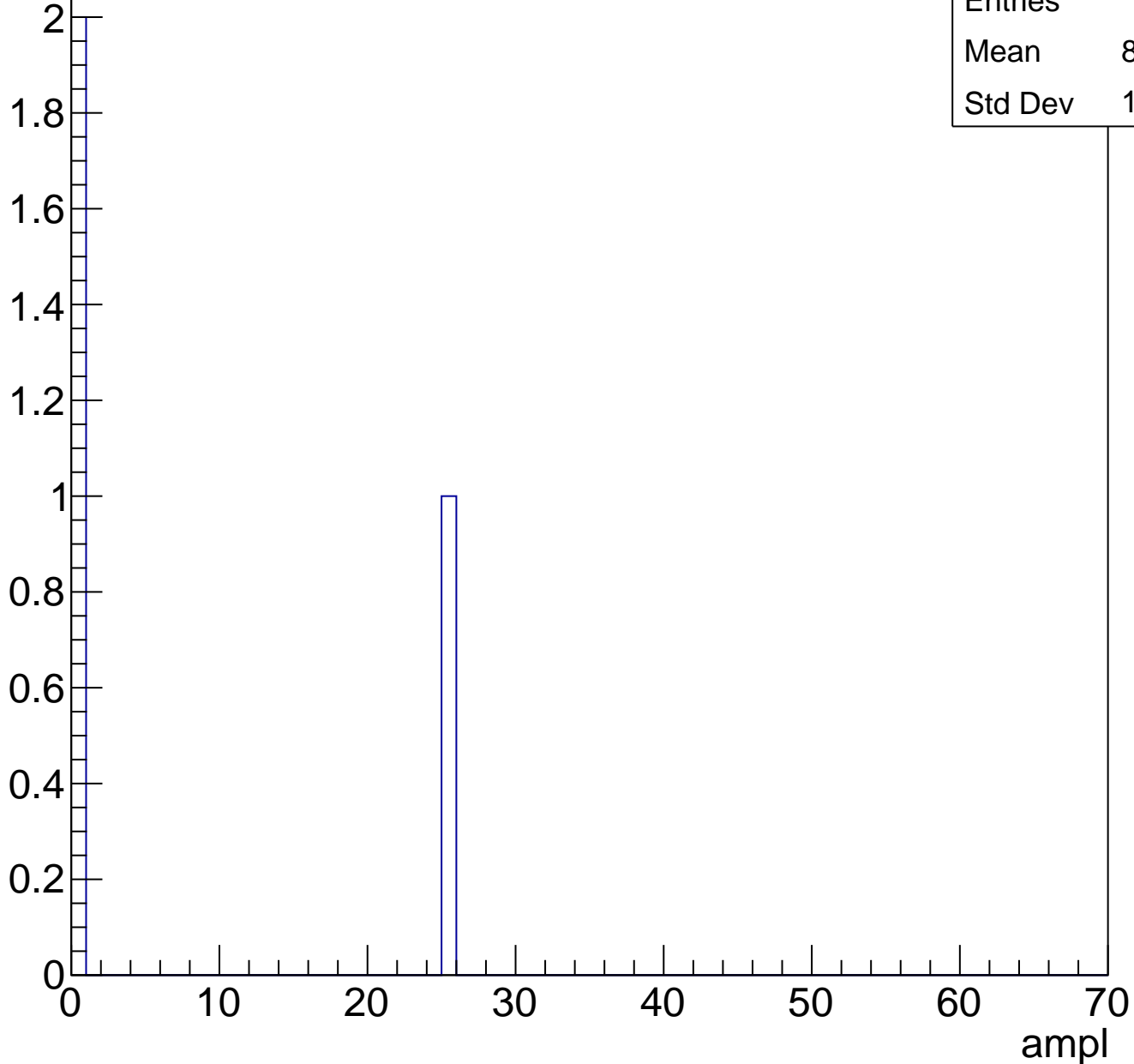


Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L001S, U23-ch3, adc0

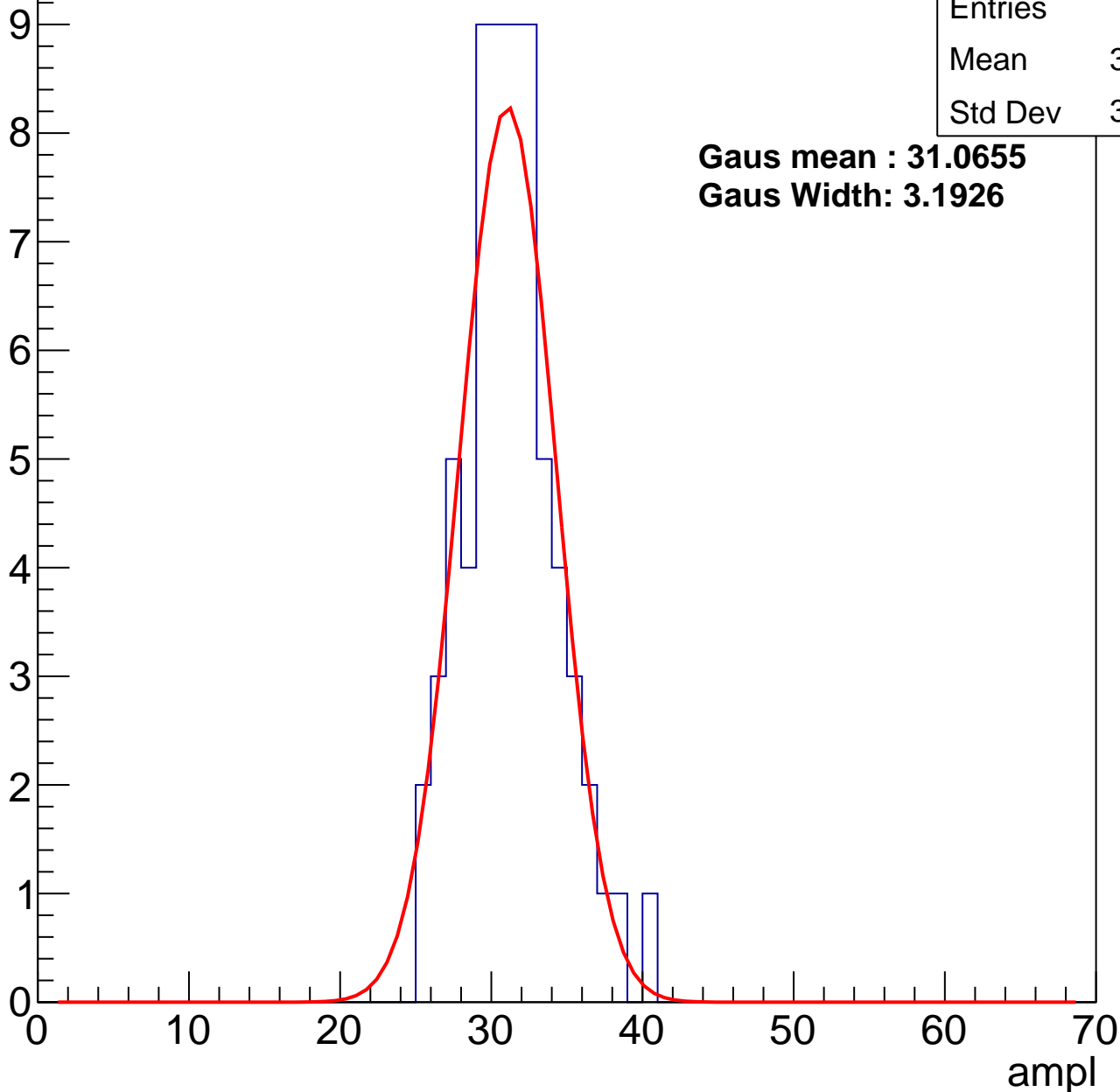
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.84
Std Dev	3.079

Gaus mean : 31.0655

Gaus Width: 3.1926



B0L001S, U23-ch3, adc1

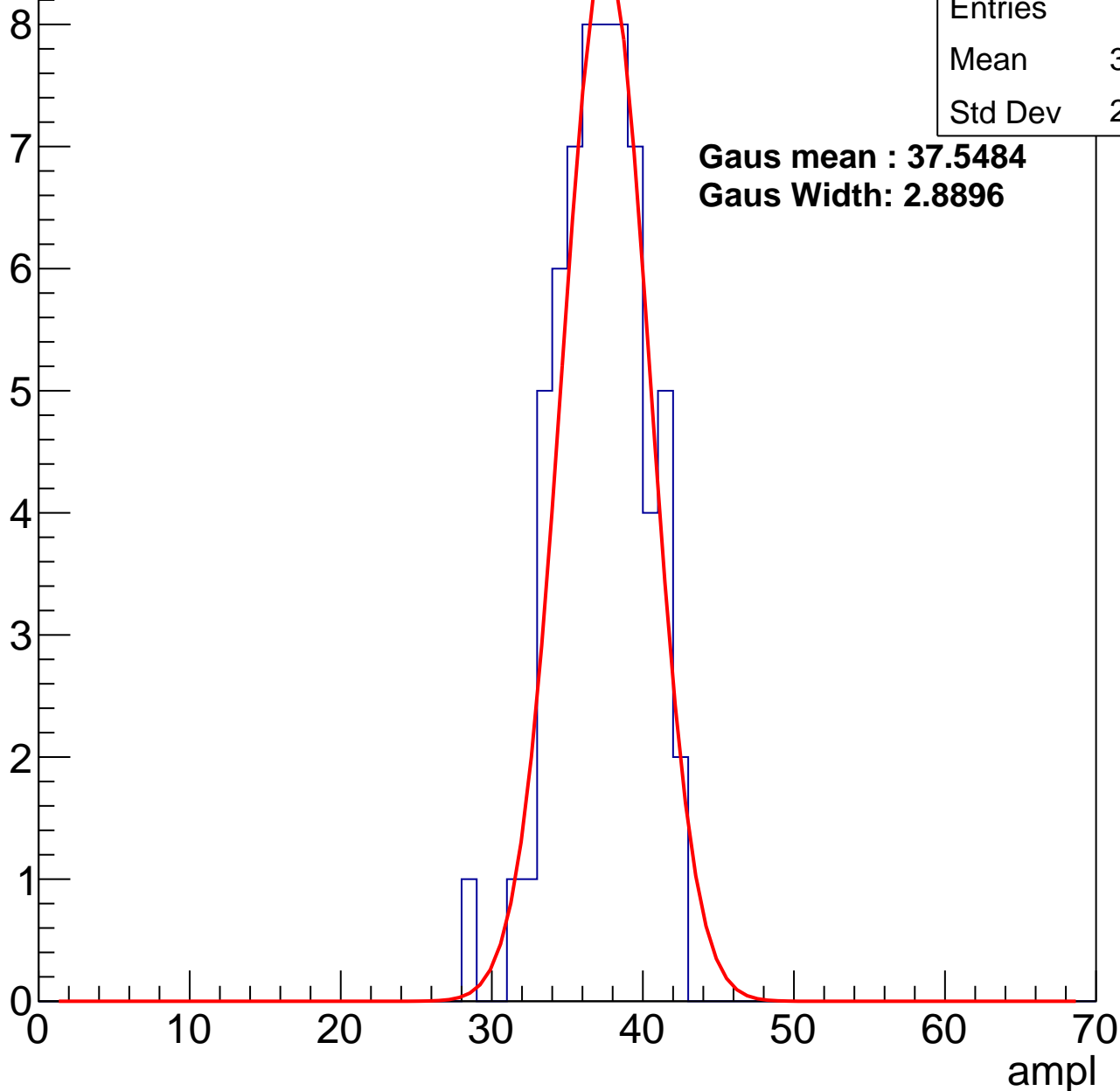
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	36.75
Std Dev	2.845

Gaus mean : 37.5484

Gaus Width: 2.8896



B0L001S, U23-ch3, adc2

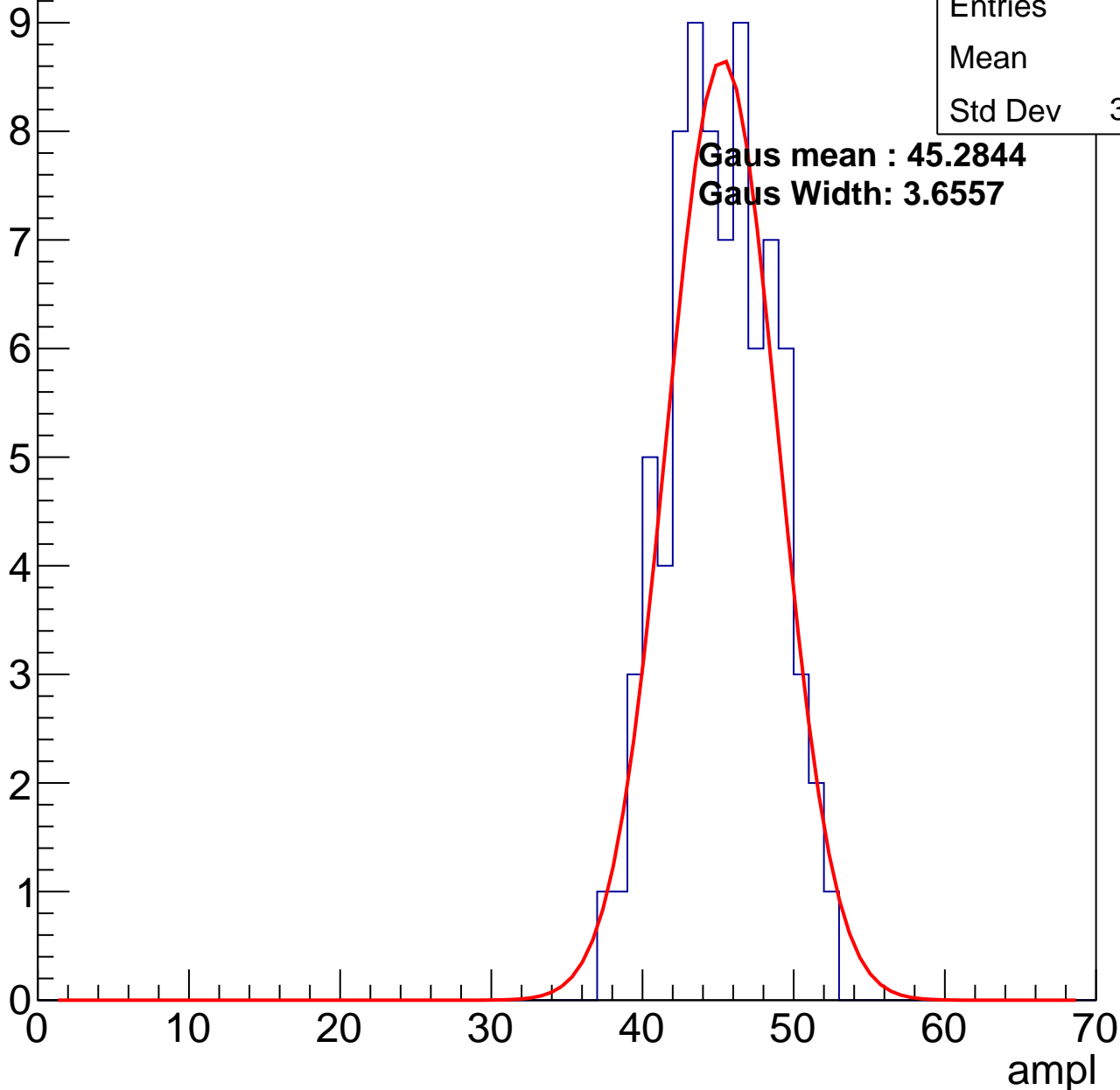
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	44.7
Std Dev	3.367

Gaus mean : 45.2844

Gaus Width: 3.6557

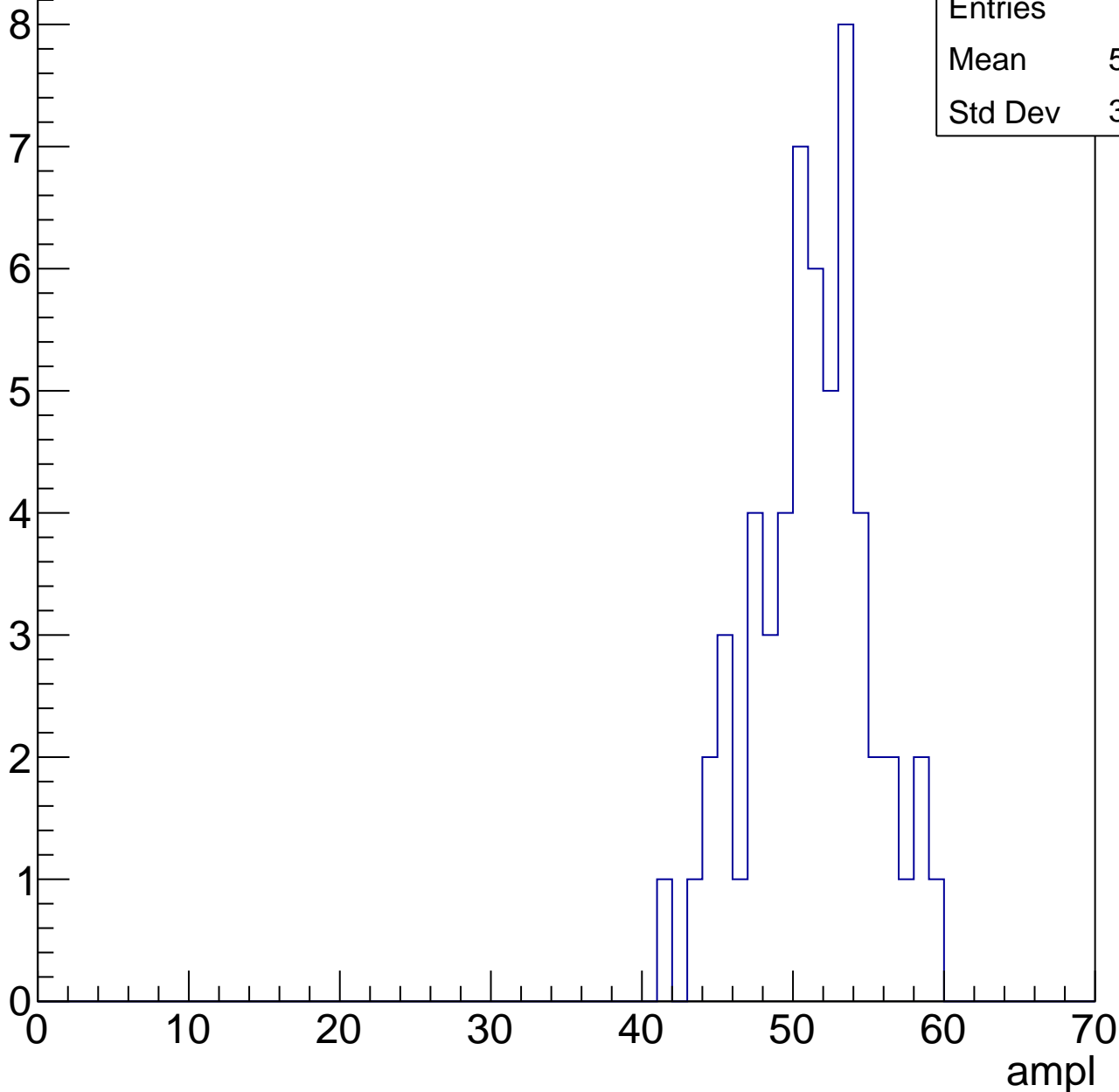


B0L001S, U23-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

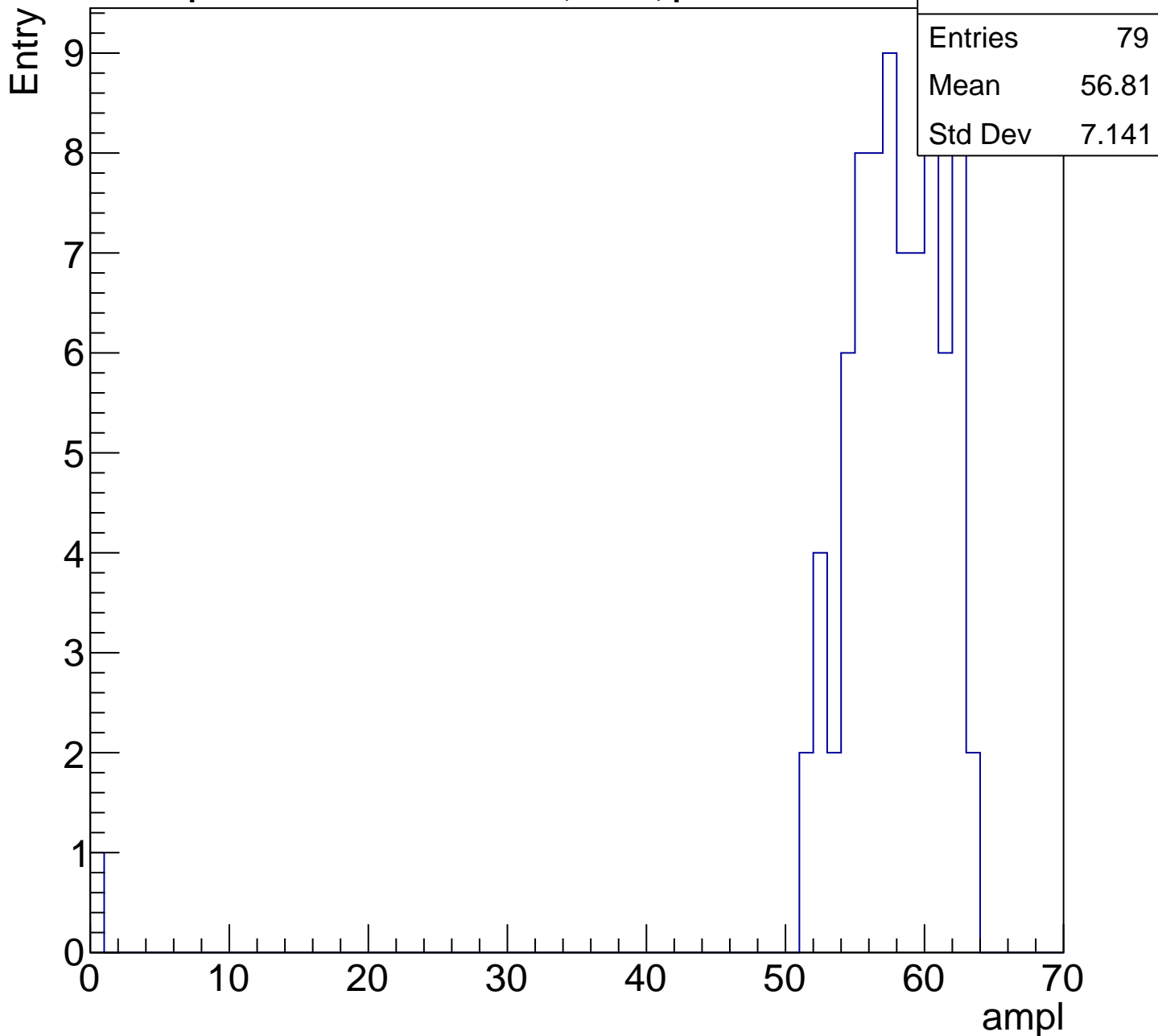
Entry

Entries	57
Mean	50.72
Std Dev	3.879



B0L001S, U23-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

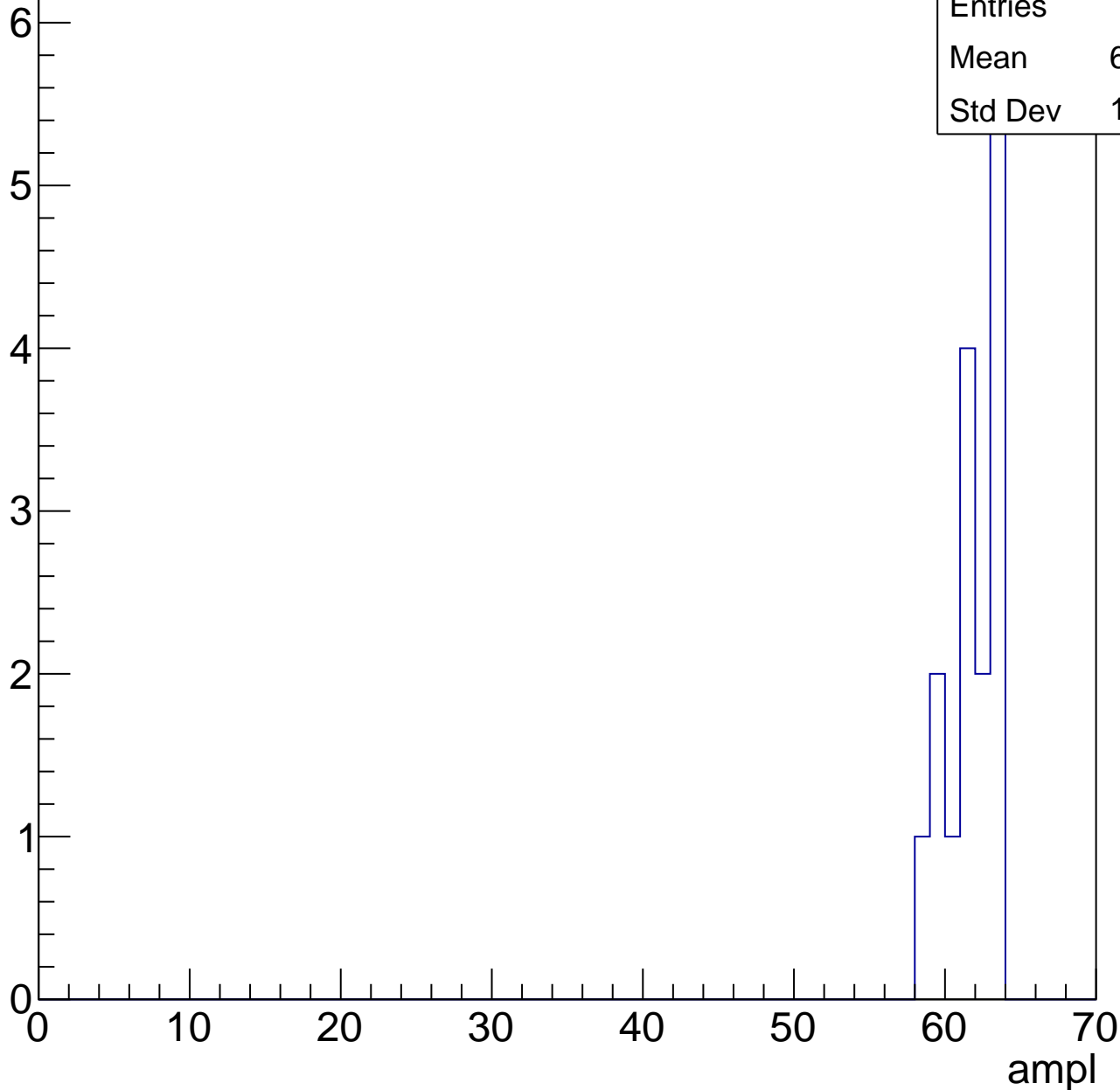


B0L001S, U23-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	61.38
Std Dev	1.615



B0L001S, U23-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L001S, U23-ch4, adc0

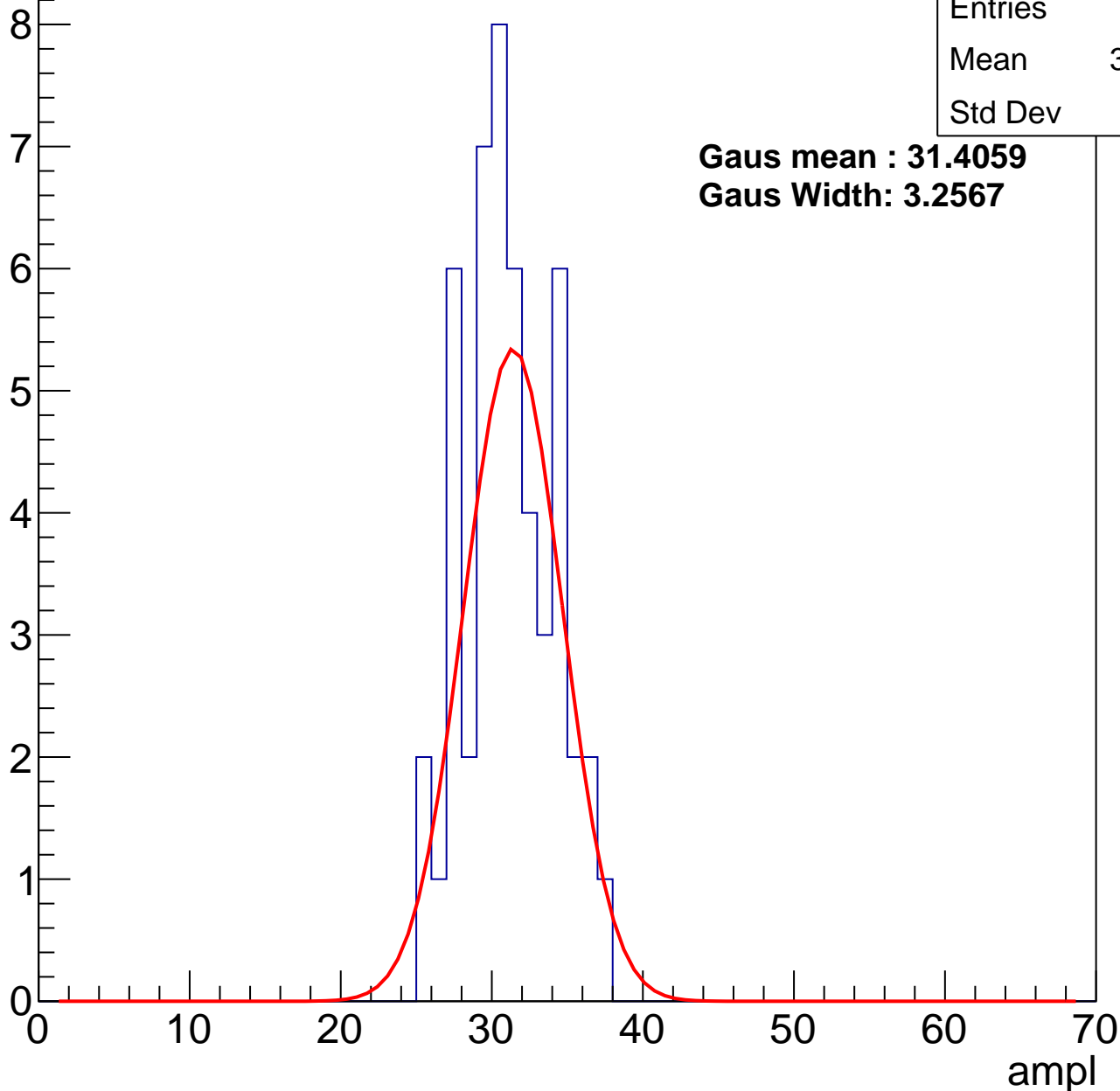
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	30.66
Std Dev	2.93

Gaus mean : 31.4059

Gaus Width: 3.2567



B0L001S, U23-ch4, adc1

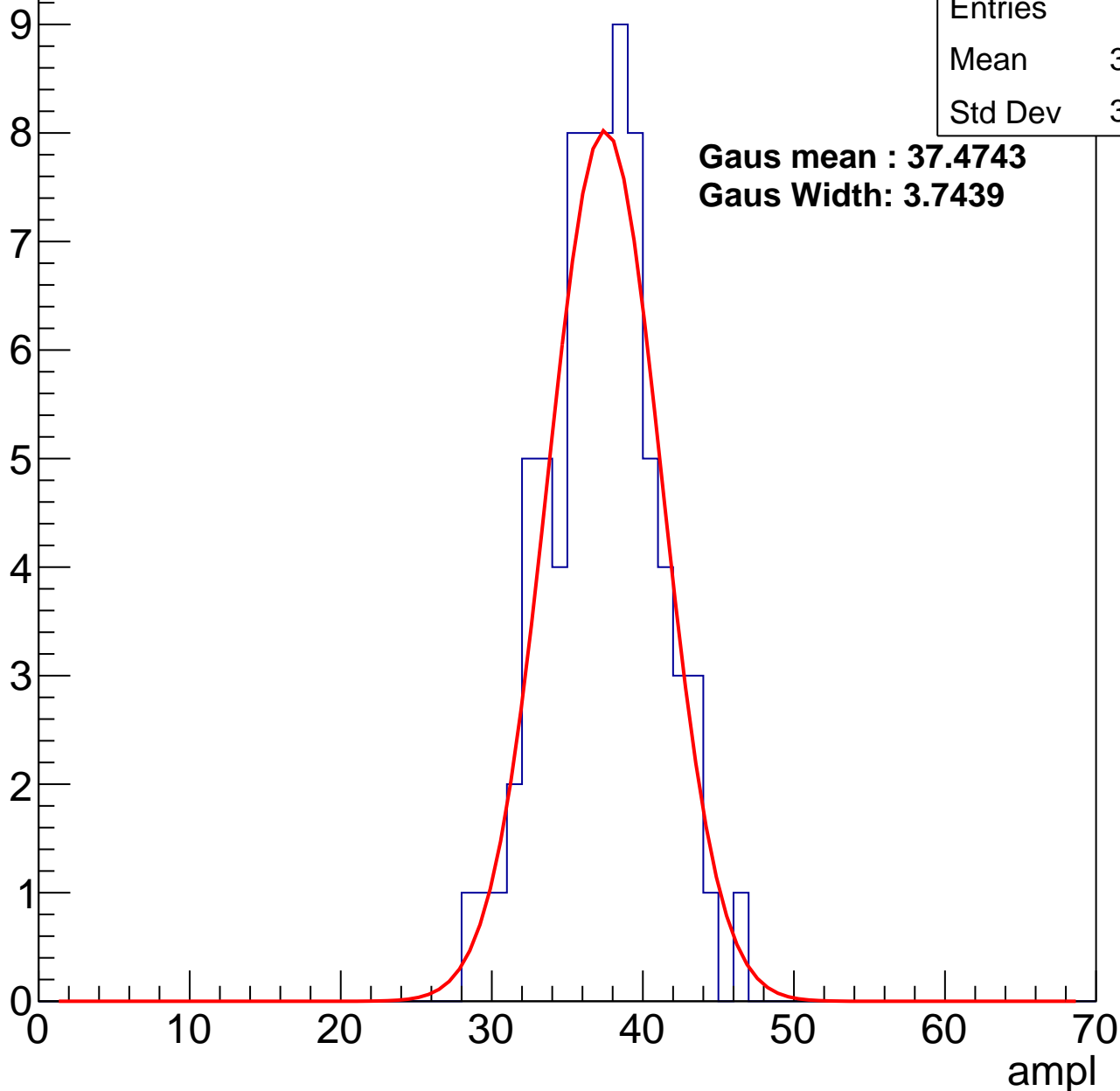
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.84
Std Dev	3.618

Gaus mean : 37.4743

Gaus Width: 3.7439



B0L001S, U23-ch4, adc2

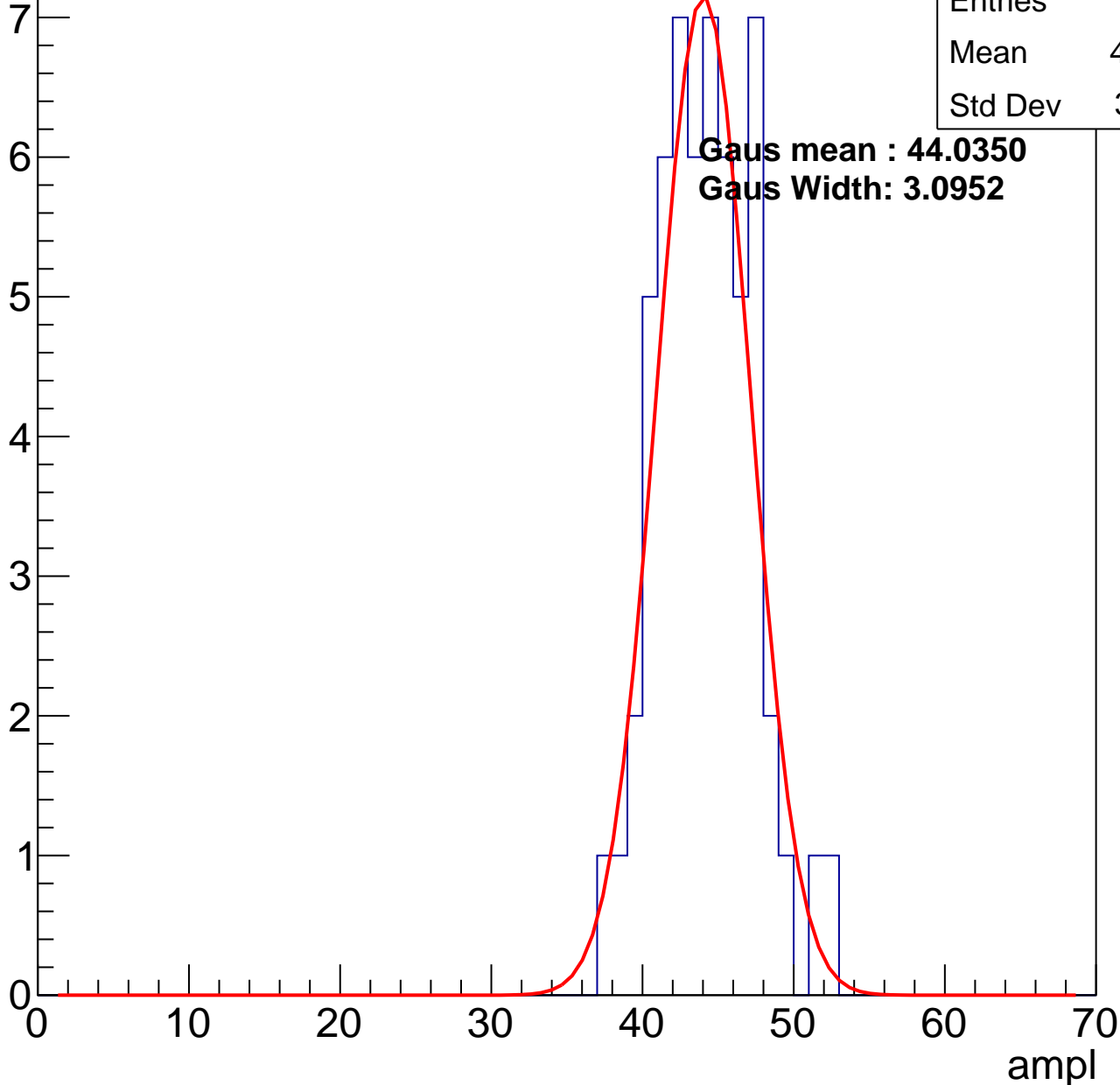
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.72
Std Dev	3.101

Gaus mean : 44.0350

Gaus Width: 3.0952

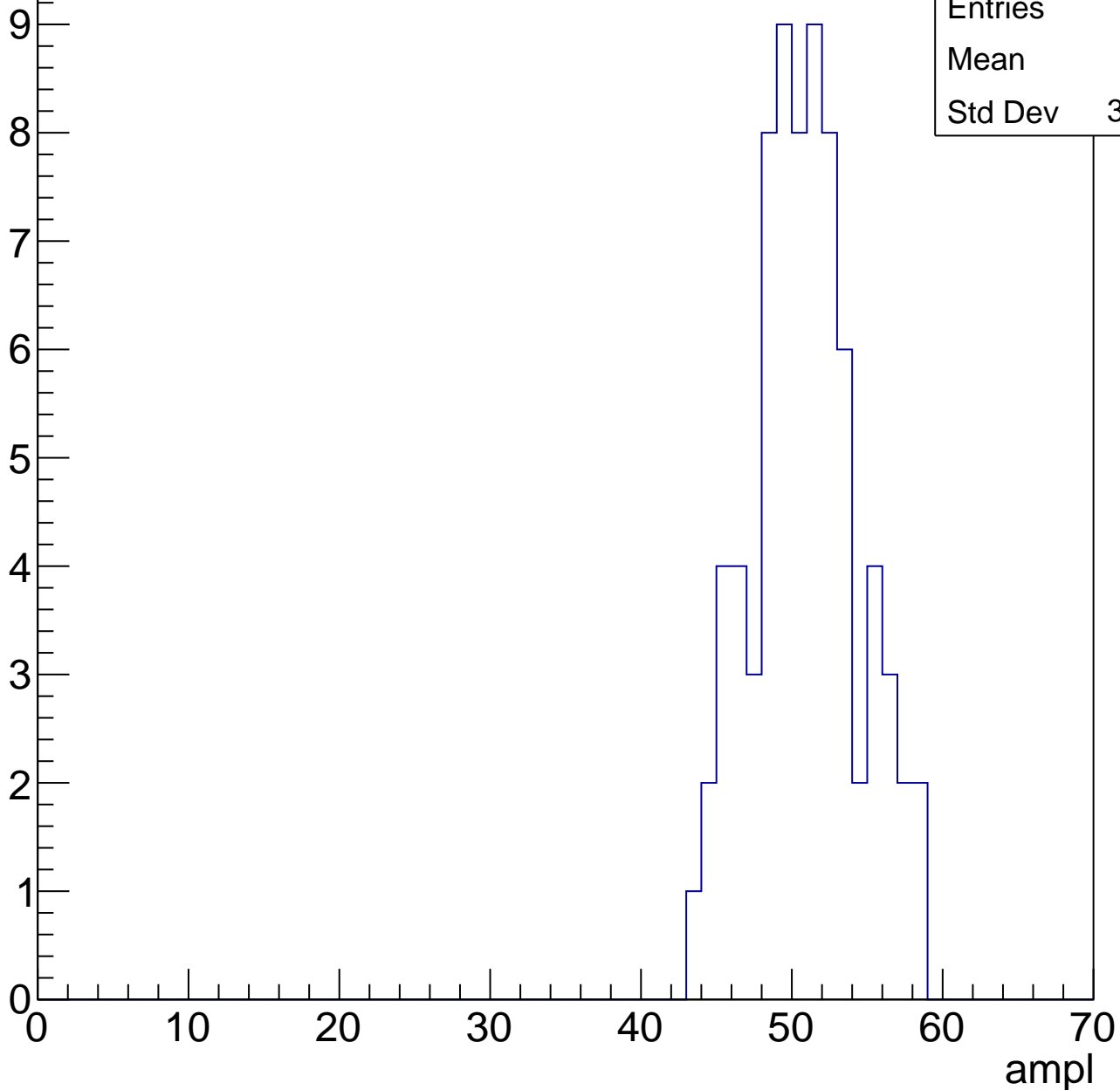


B0L001S, U23-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.4
Std Dev	3.468

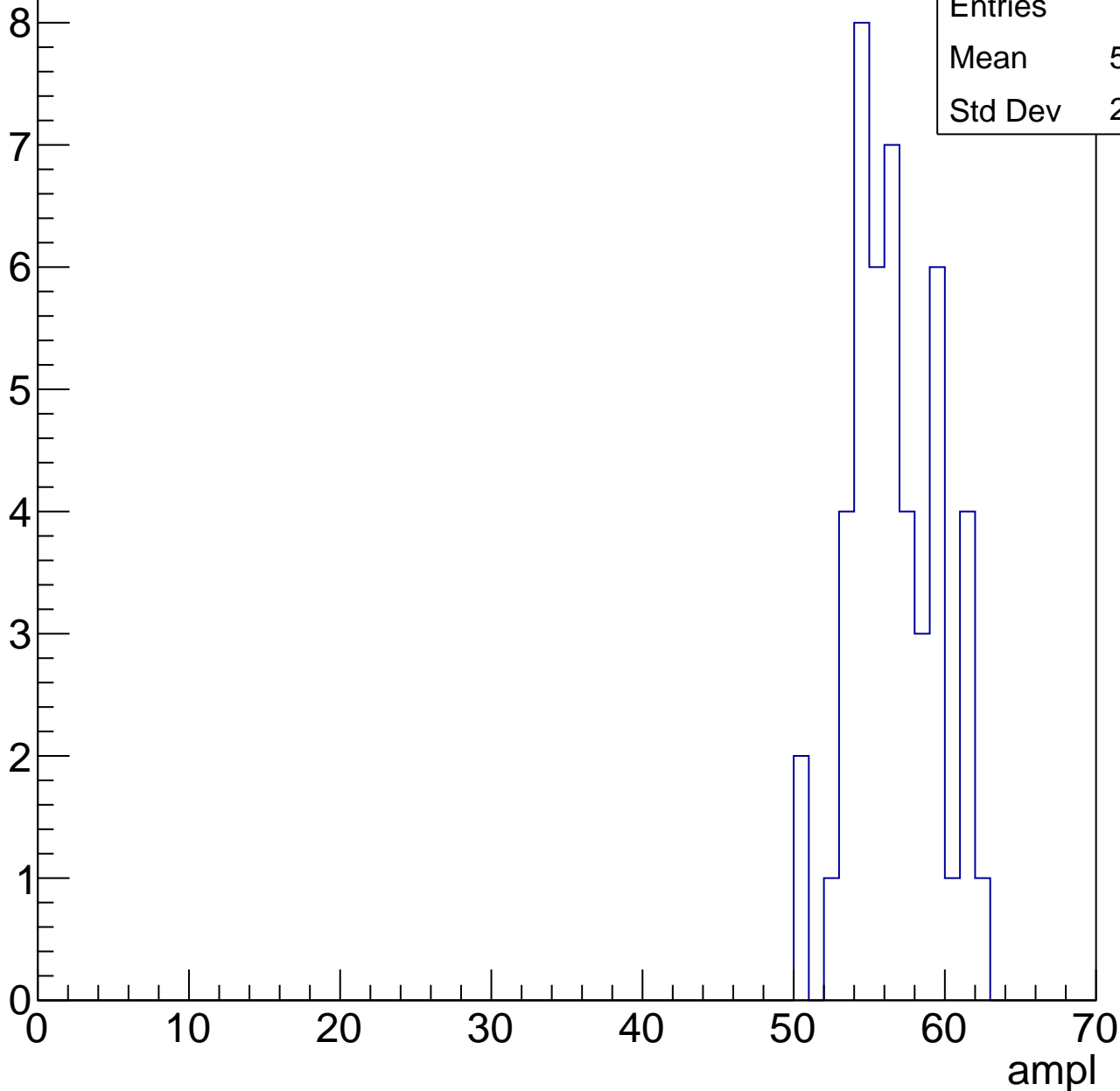


B0L001S, U23-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

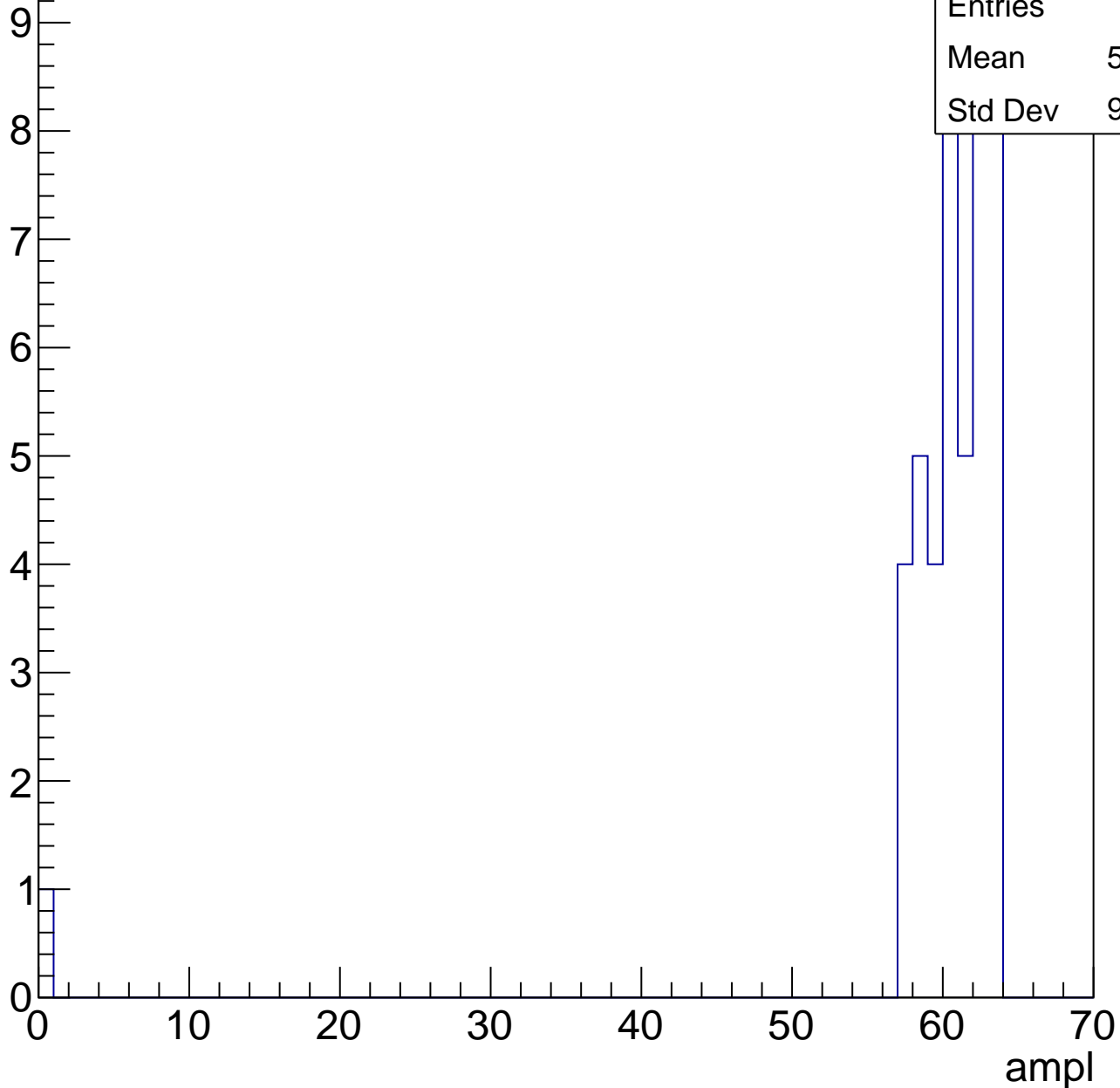
Entries	47
Mean	56.17
Std Dev	2.853



B0L001S, U23-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

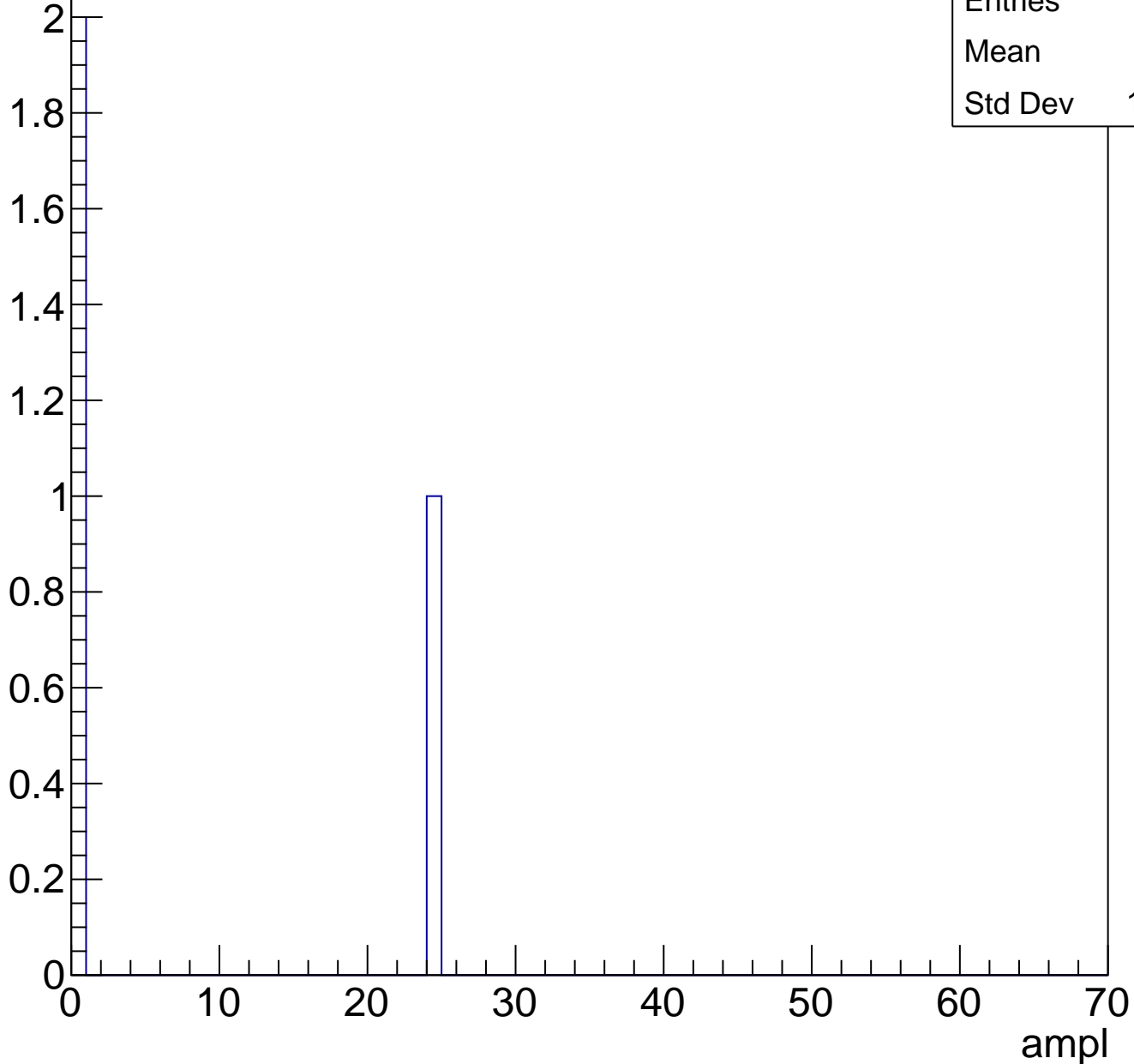
Entry



B0L001S, U23-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch5, adc0

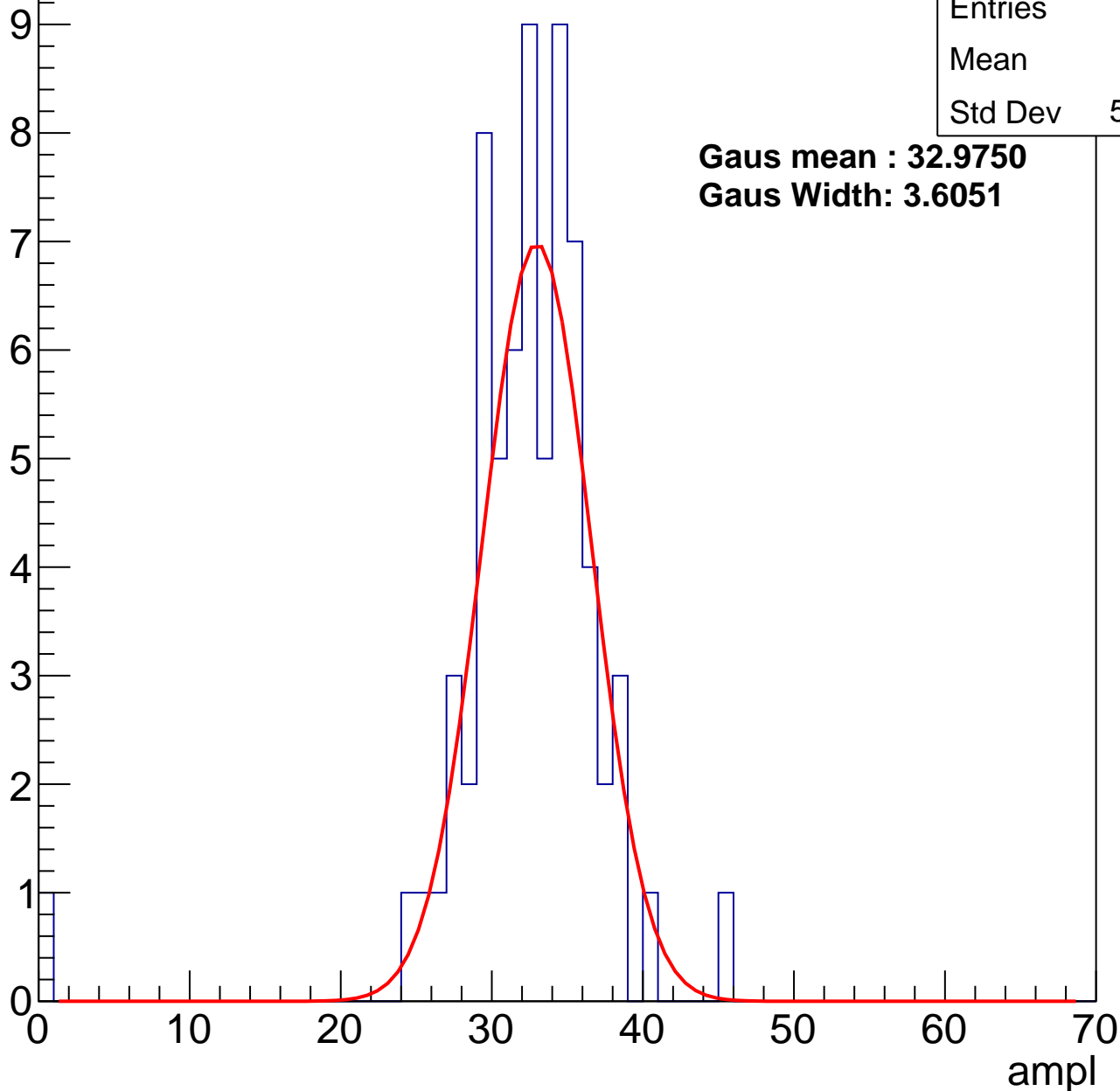
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.9
Std Dev	5.297

Gaus mean : 32.9750

Gaus Width: 3.6051



B0L001S, U23-ch5, adc1

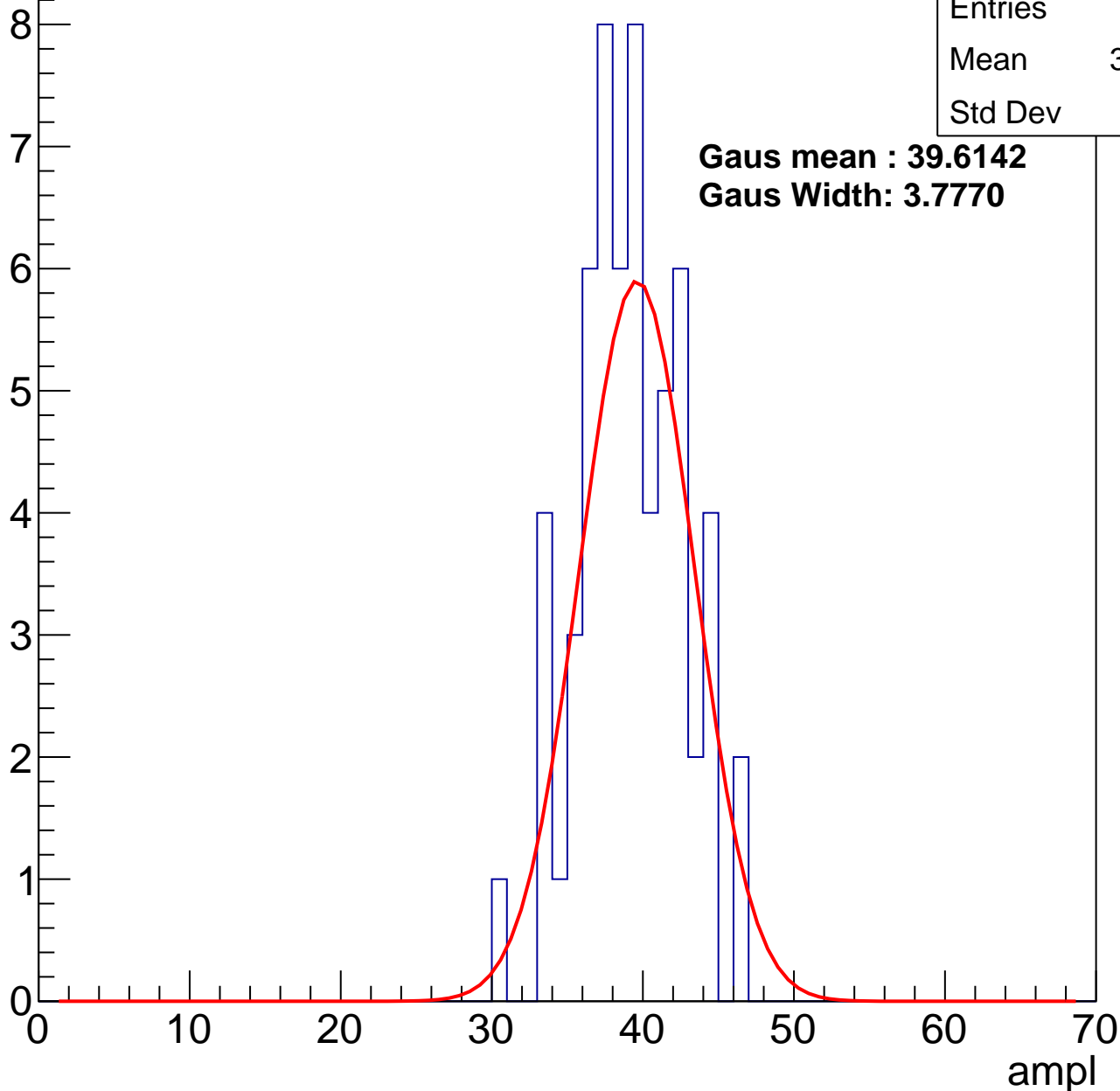
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.73
Std Dev	3.4

Gaus mean : 39.6142

Gaus Width: 3.7770



B0L001S, U23-ch5, adc2

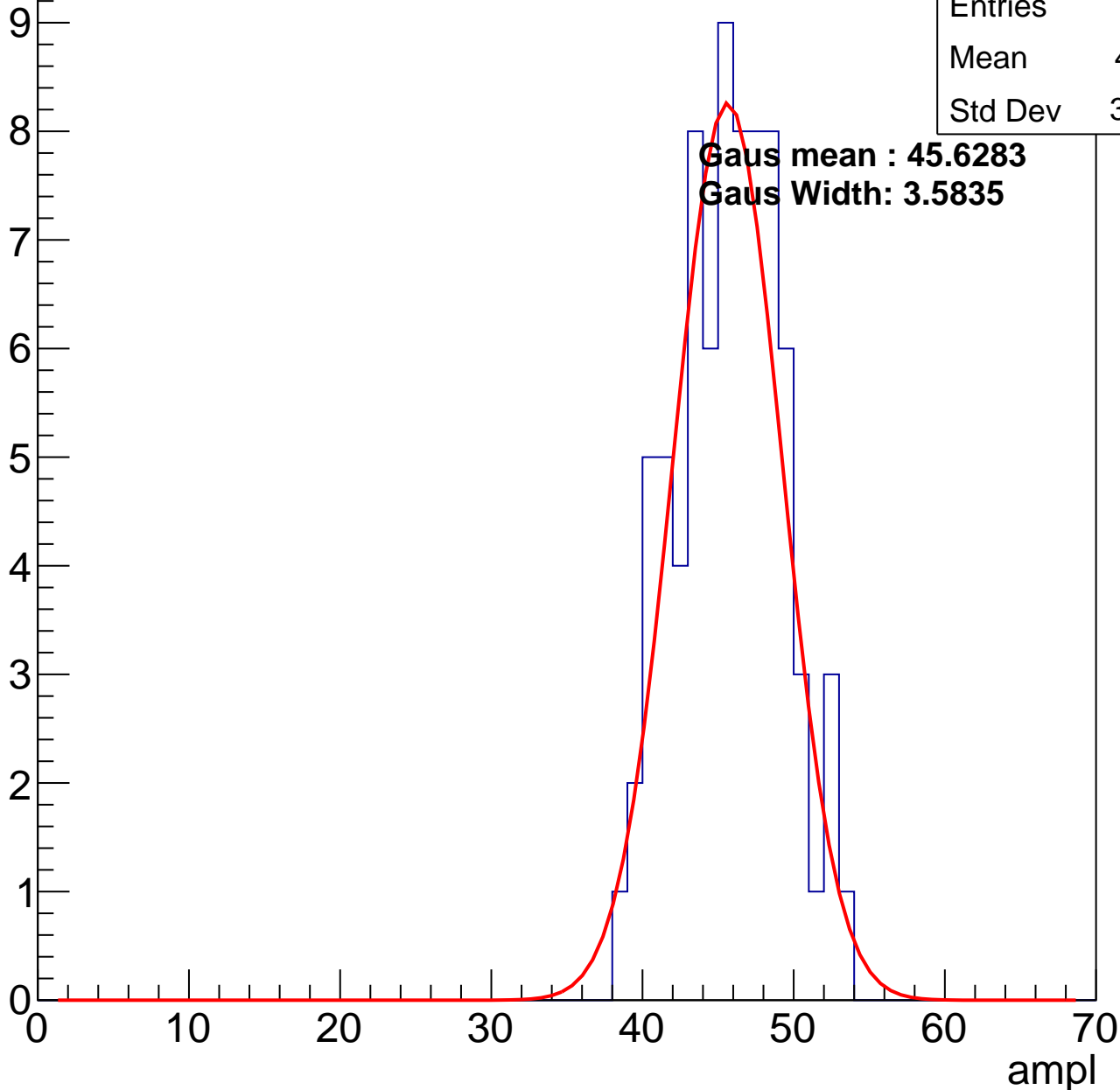
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	45.31
Std Dev	3.439

Gaus mean : 45.6283

Gaus Width: 3.5835

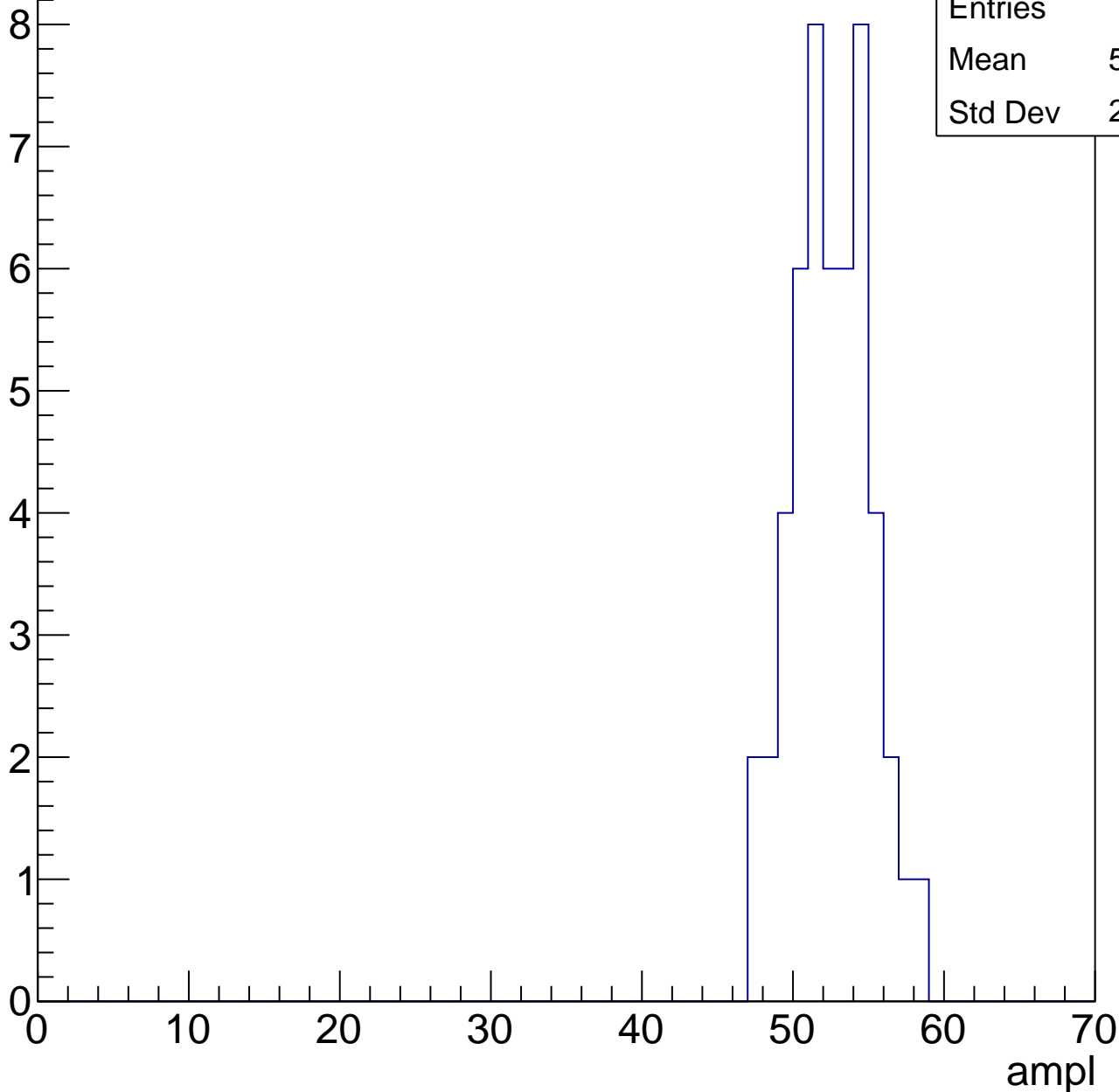


B0L001S, U23-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

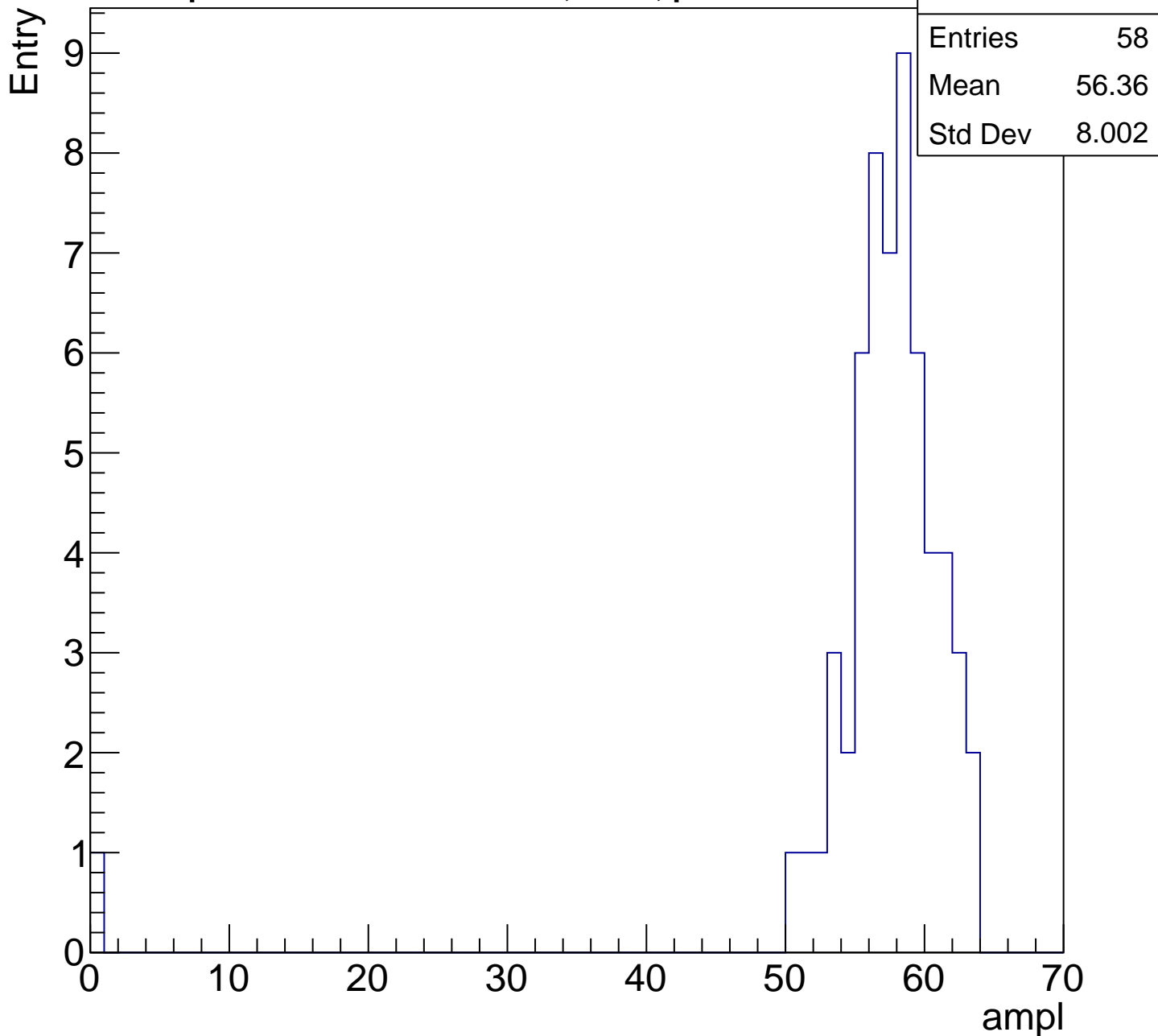
Entry

Entries	50
Mean	52.06
Std Dev	2.517



B0L001S, U23-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

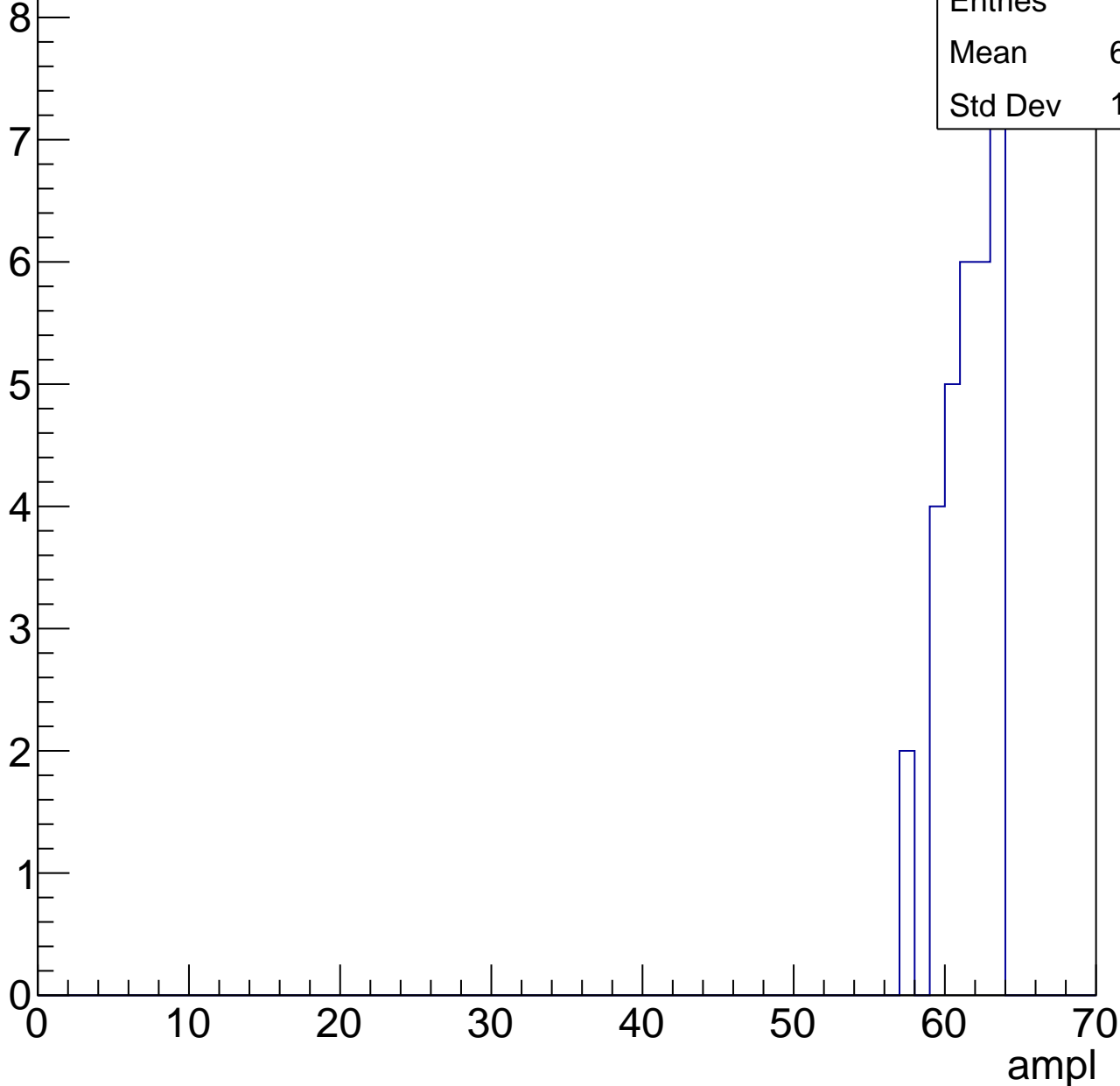


B0L001S, U23-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	61.03
Std Dev	1.713



B0L001S, U23-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch6, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	30.1
Std Dev	4.715

Gaus mean : 31.0703

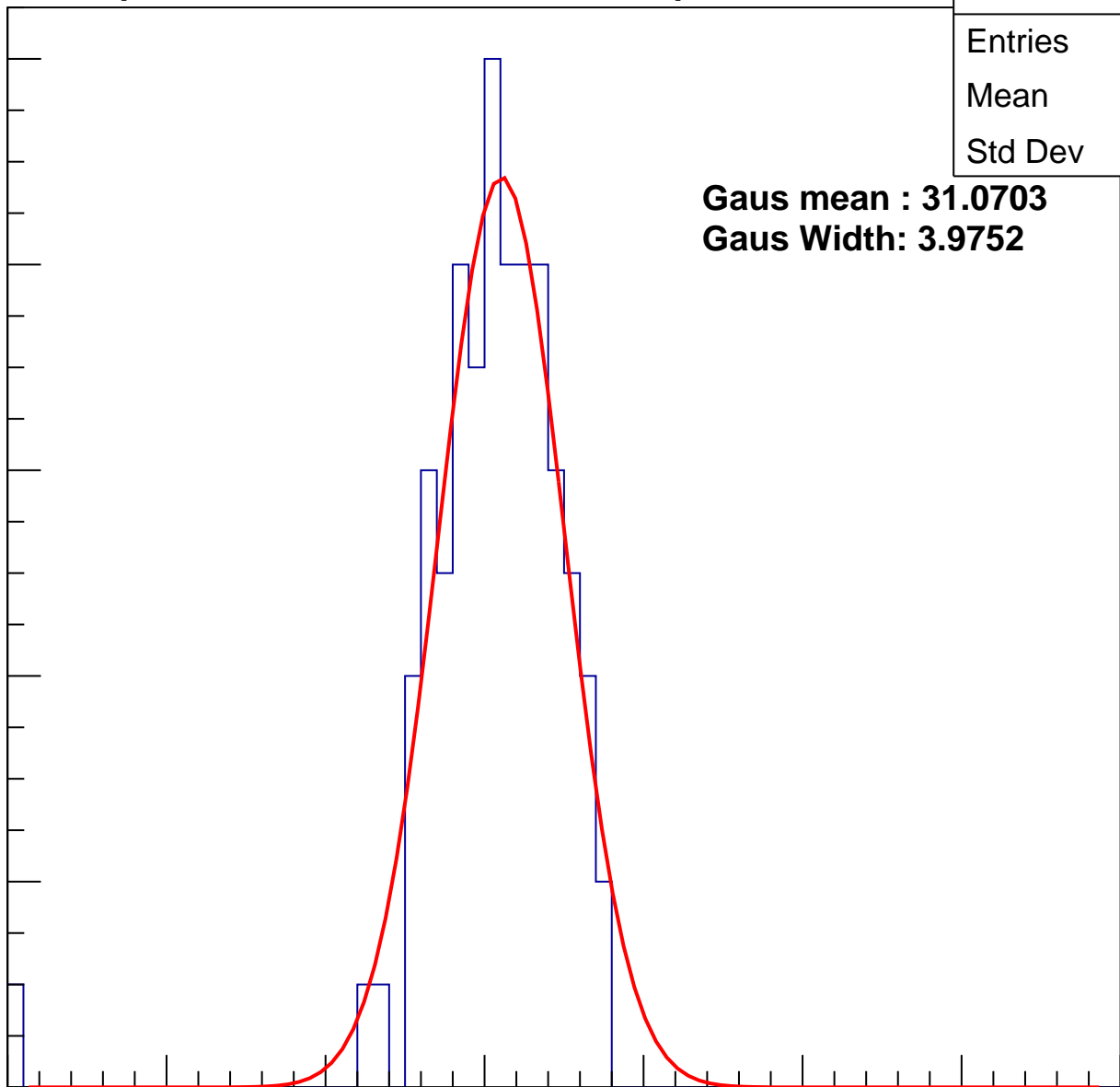
Gaus Width: 3.9752

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch6, adc1

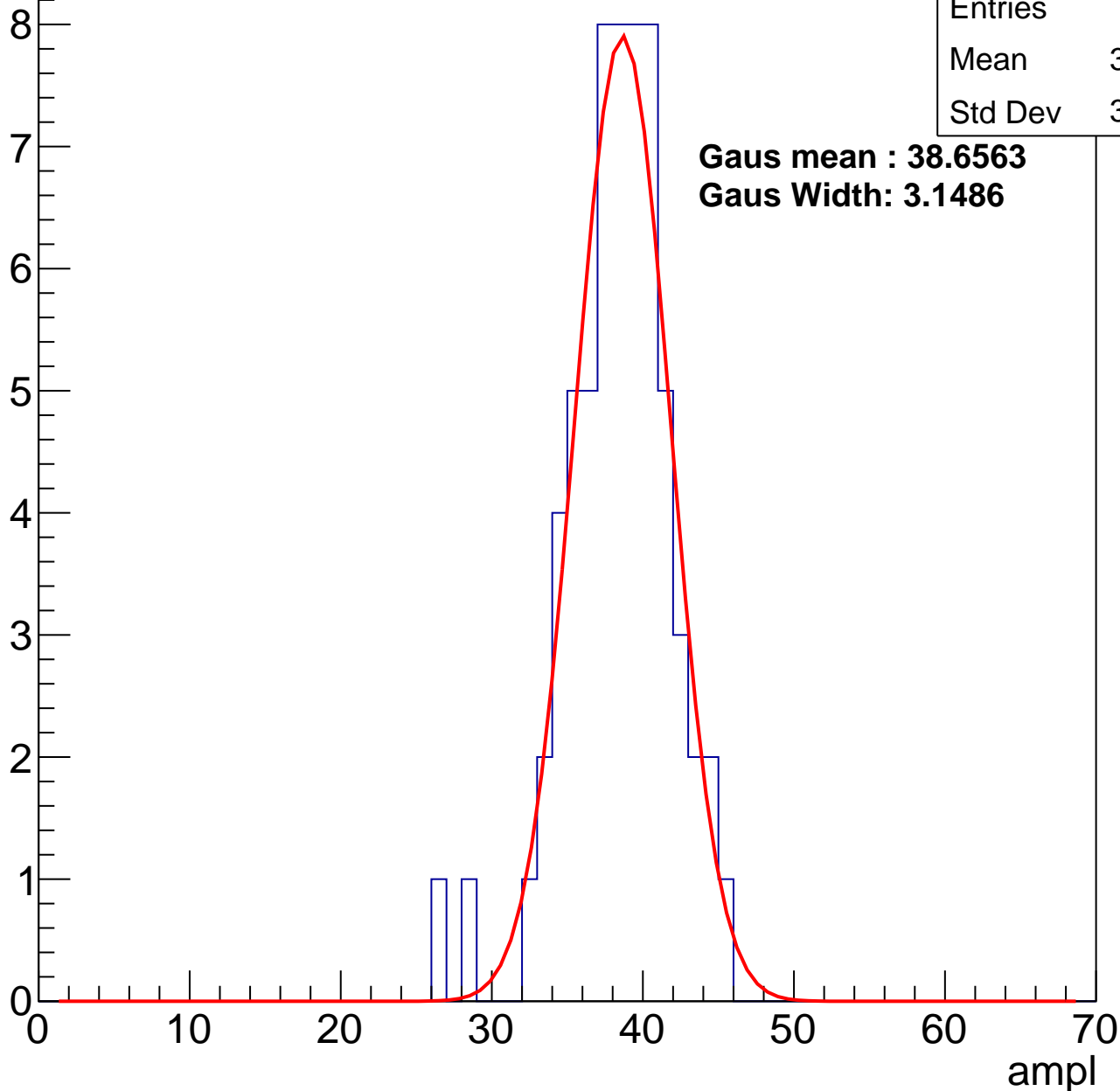
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.89
Std Dev	3.469

Gaus mean : 38.6563

Gaus Width: 3.1486



B0L001S, U23-ch6, adc2

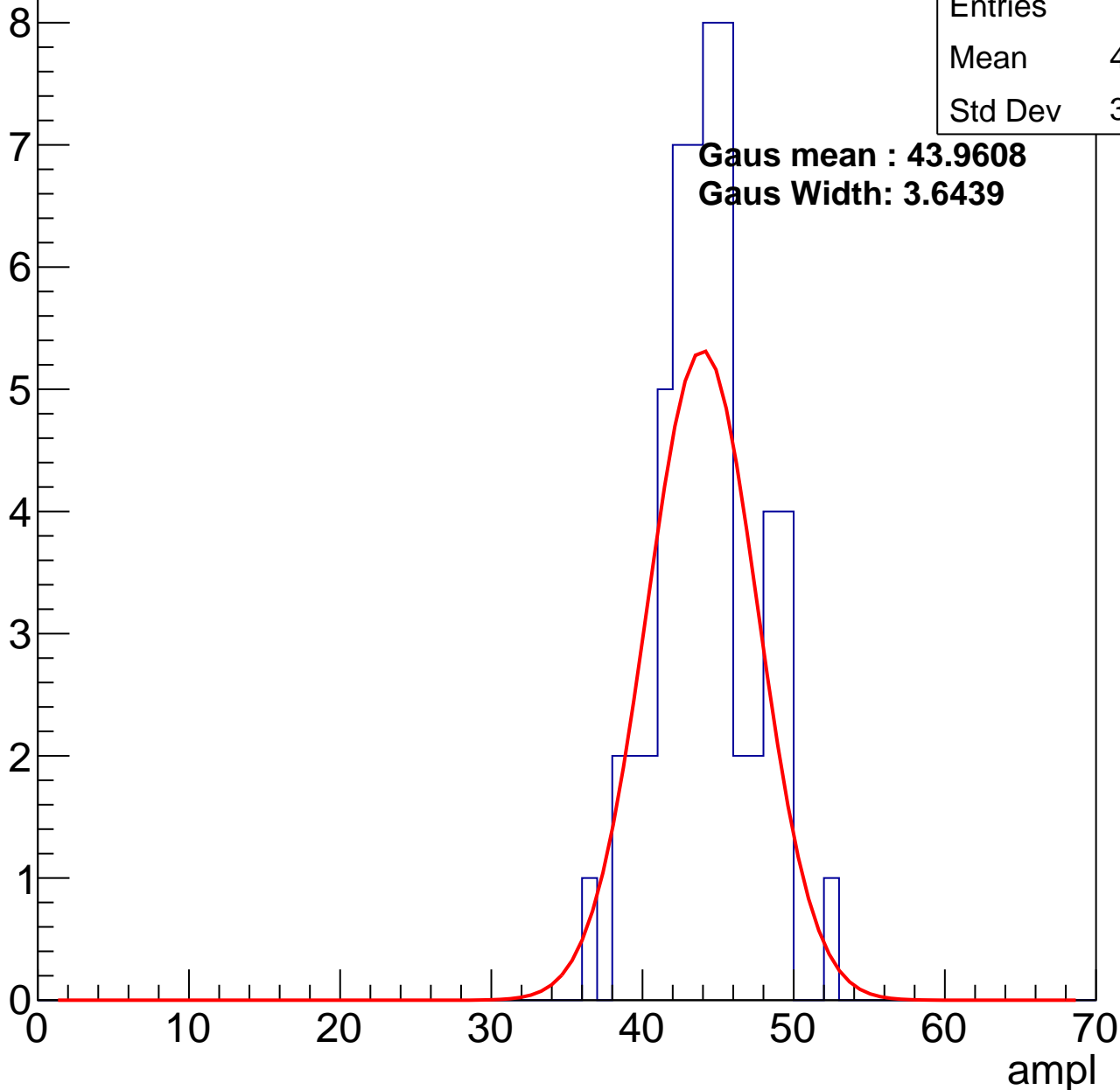
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.78
Std Dev	3.183

Gaus mean : 43.9608

Gaus Width: 3.6439

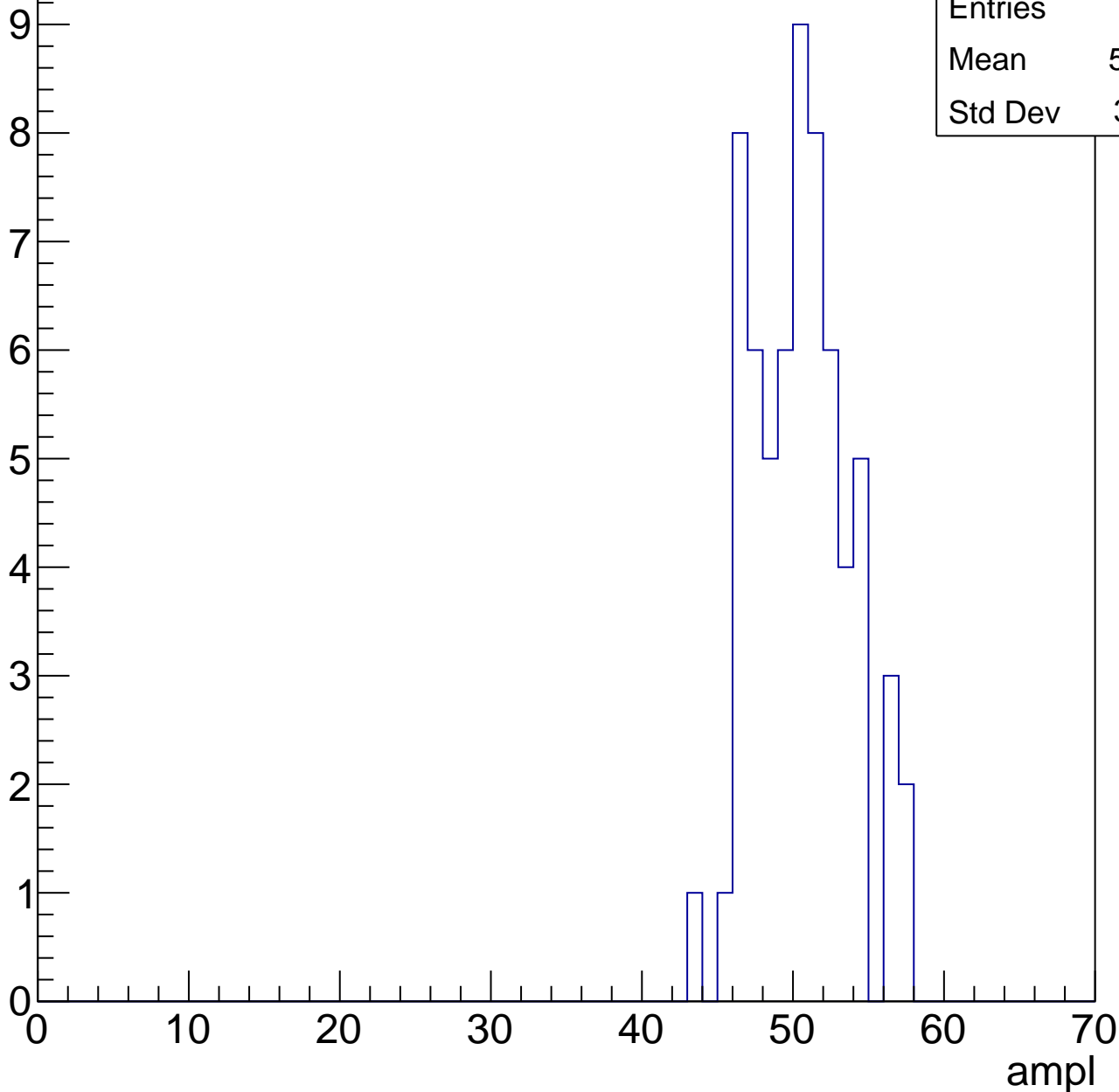


B0L001S, U23-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.09
Std Dev	3.151



B0L001S, U23-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

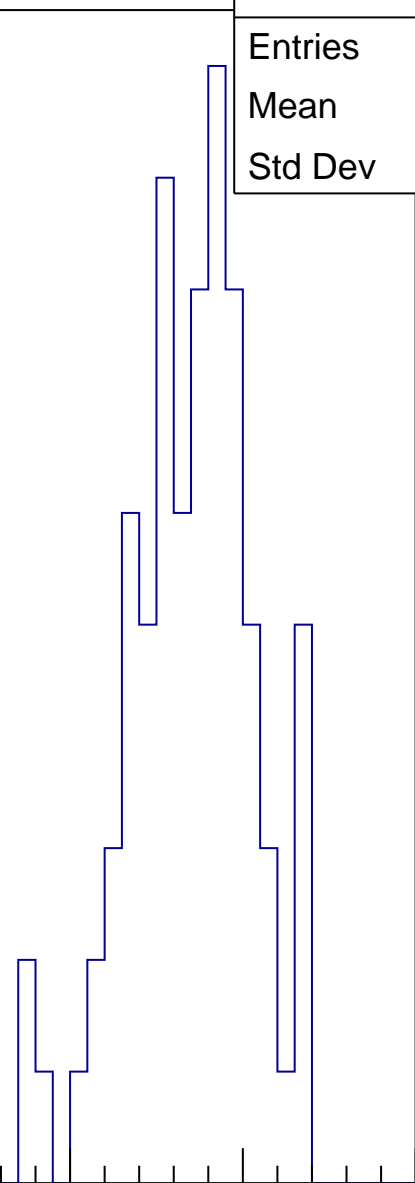
Entries	75
Mean	56.49
Std Dev	3.616

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

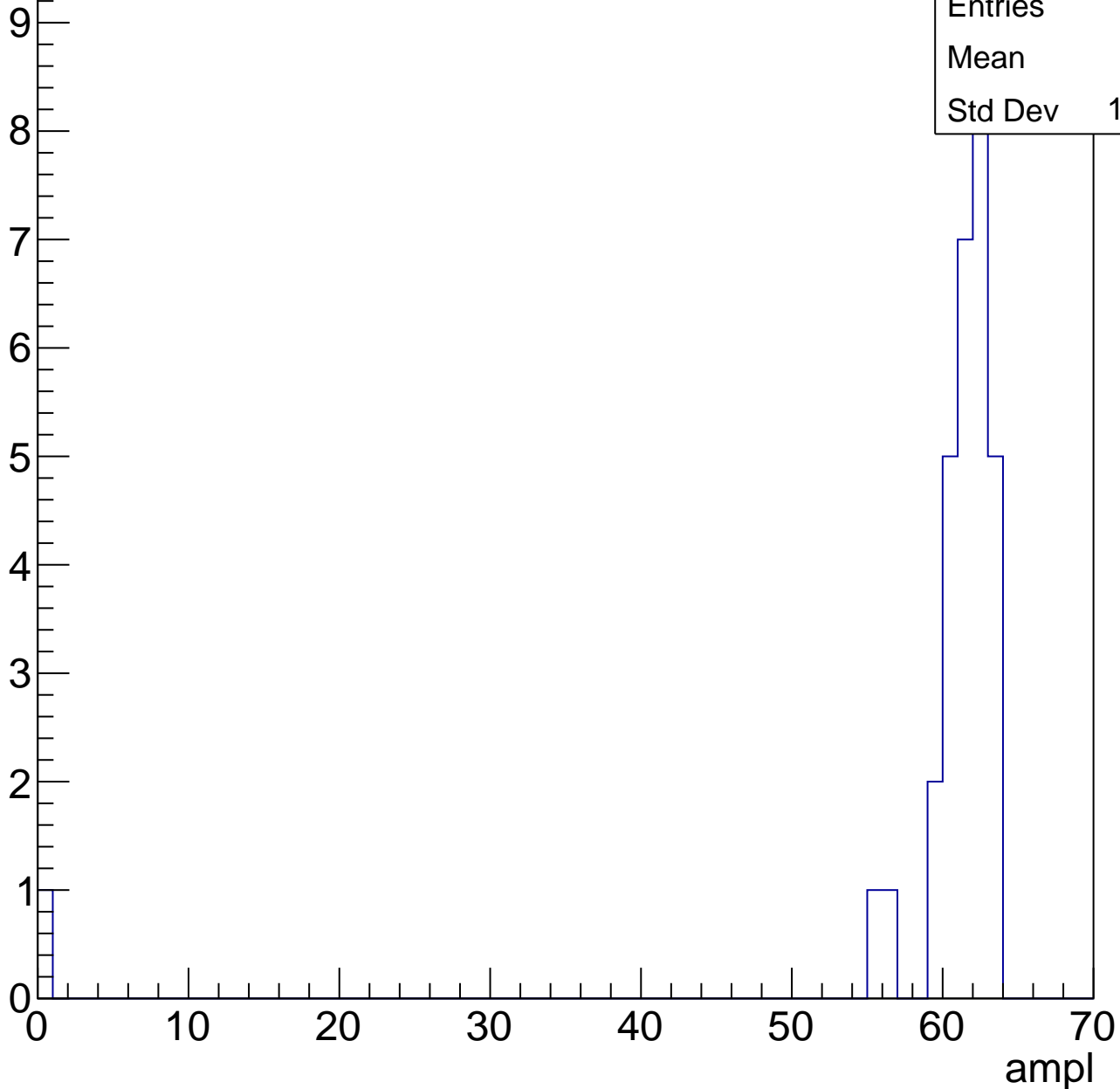


B0L001S, U23-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59
Std Dev	10.92



B0L001S, U23-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch7, adc0

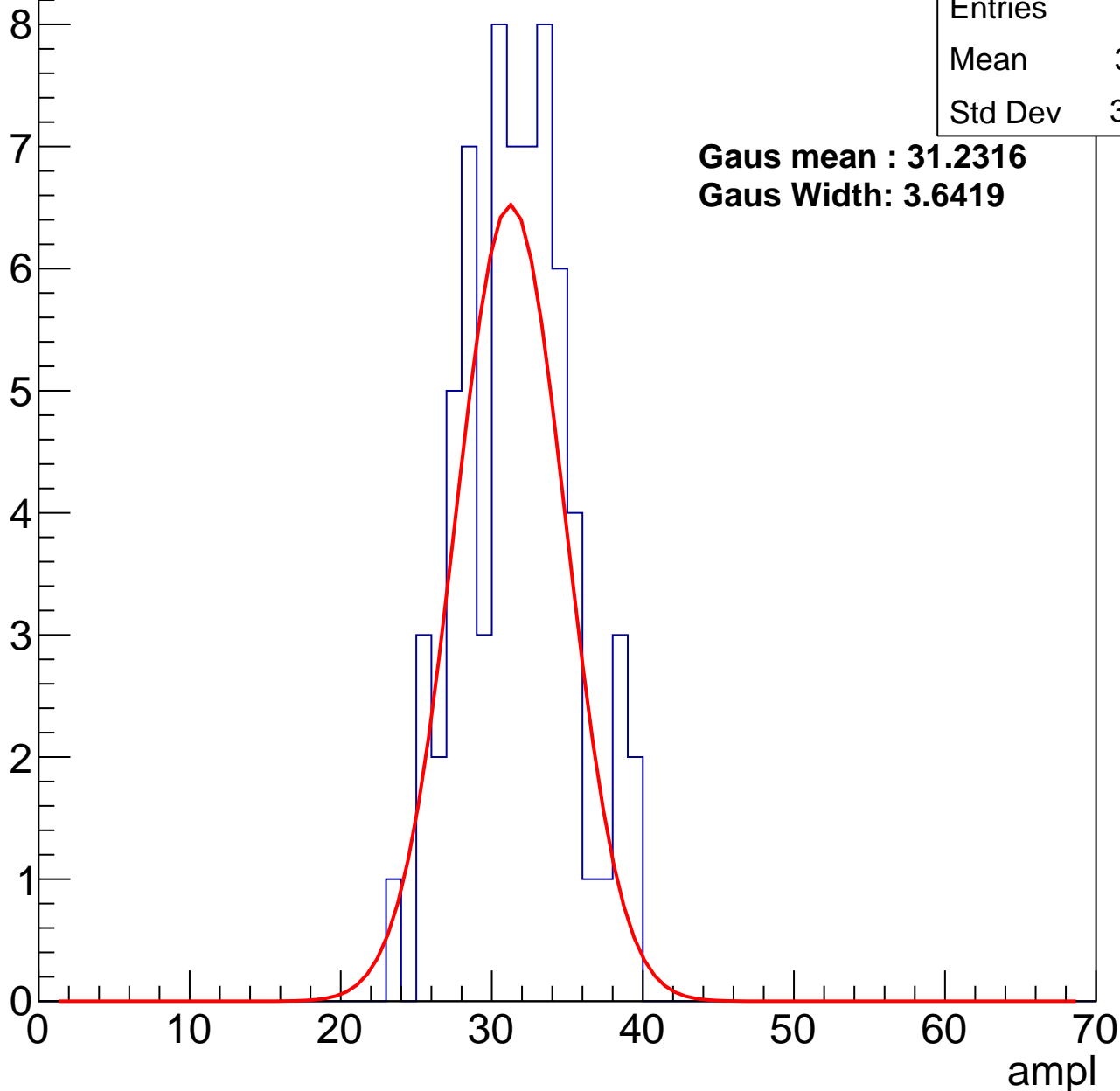
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.21
Std Dev	3.587

Gaus mean : 31.2316

Gaus Width: 3.6419



B0L001S, U23-ch7, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	38.06
Std Dev	2.971

Gaus mean : 38.5308

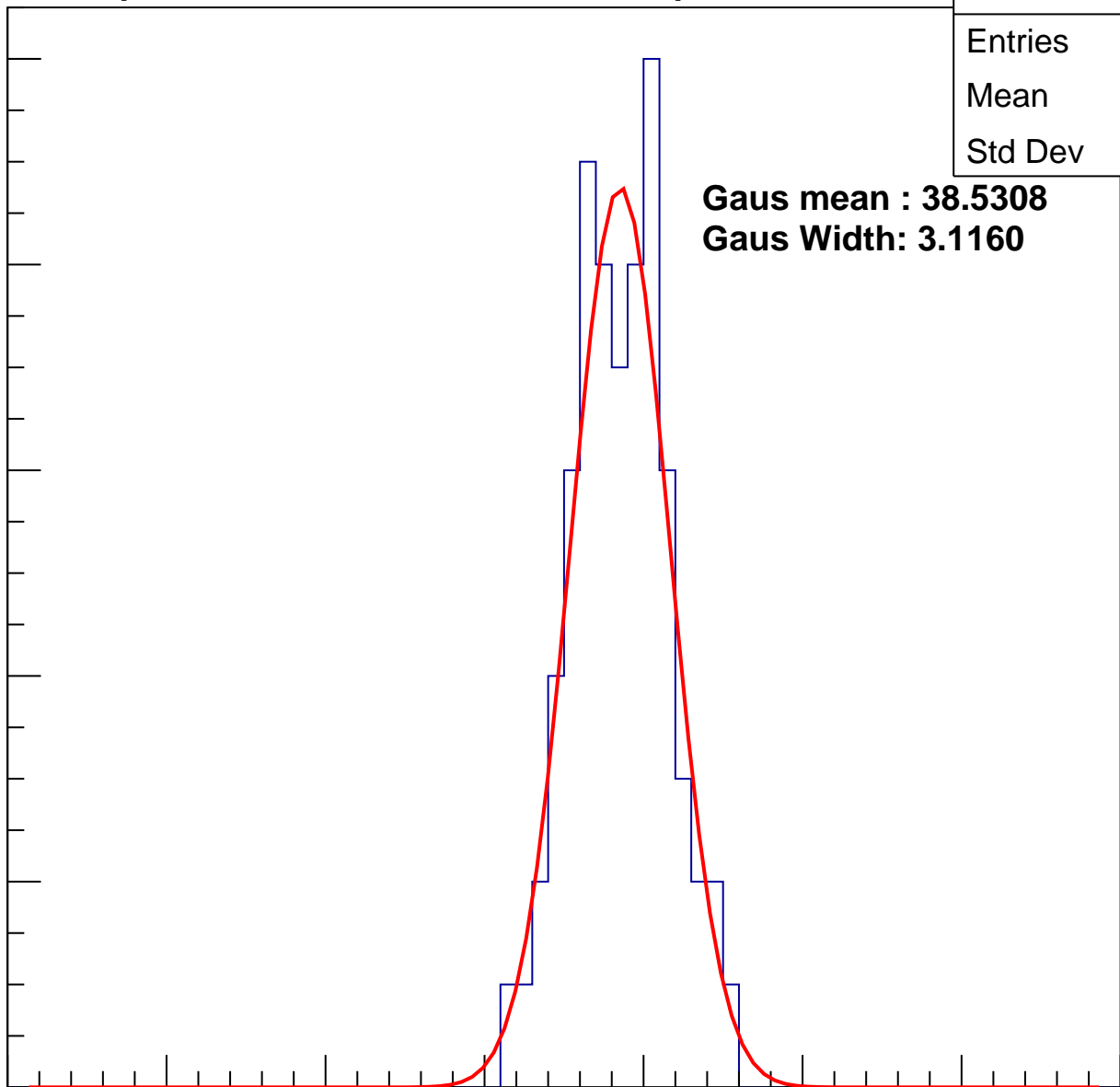
Gaus Width: 3.1160

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch7, adc2

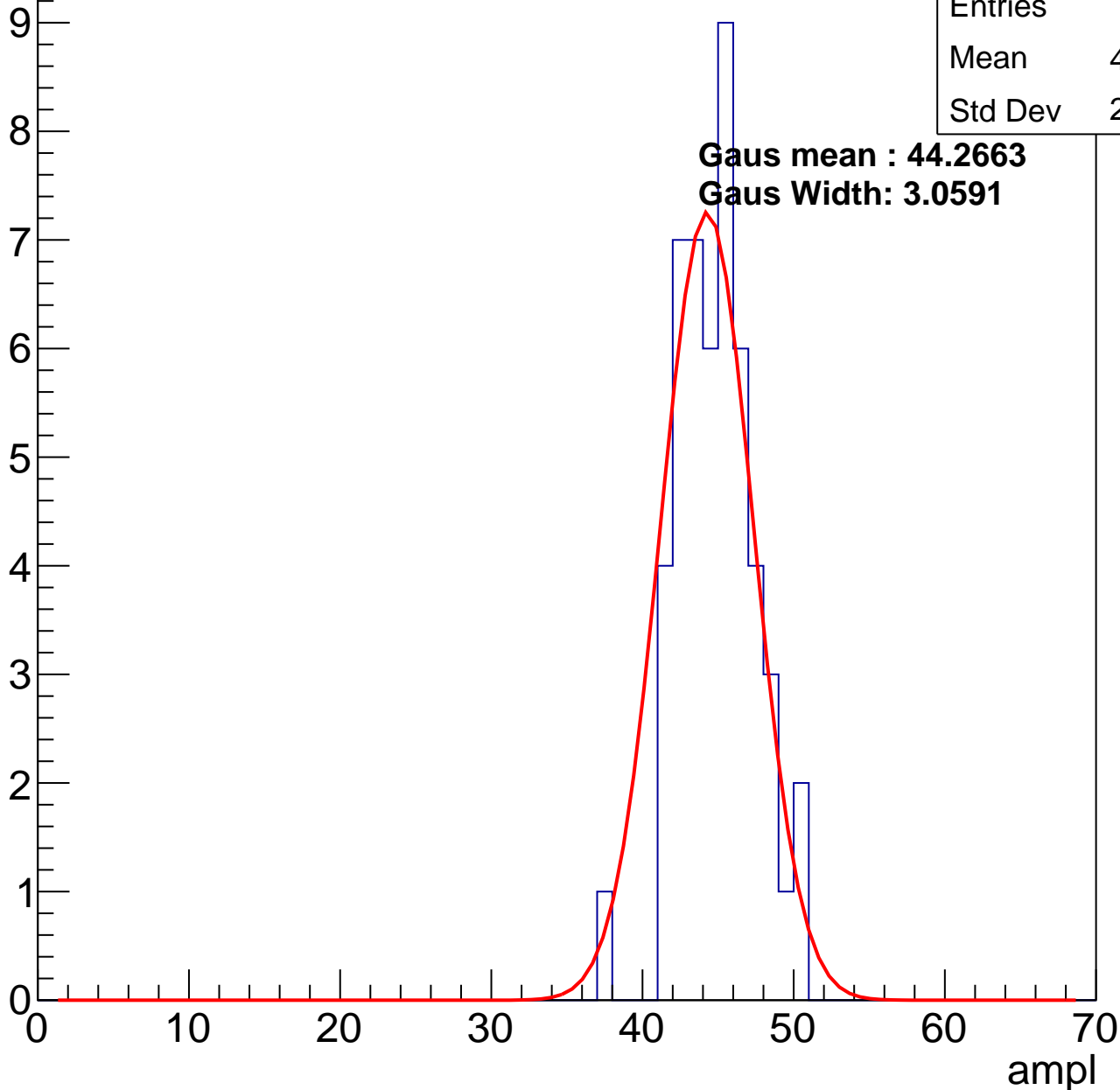
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	44.44
Std Dev	2.547

Gaus mean : 44.2663

Gaus Width: 3.0591

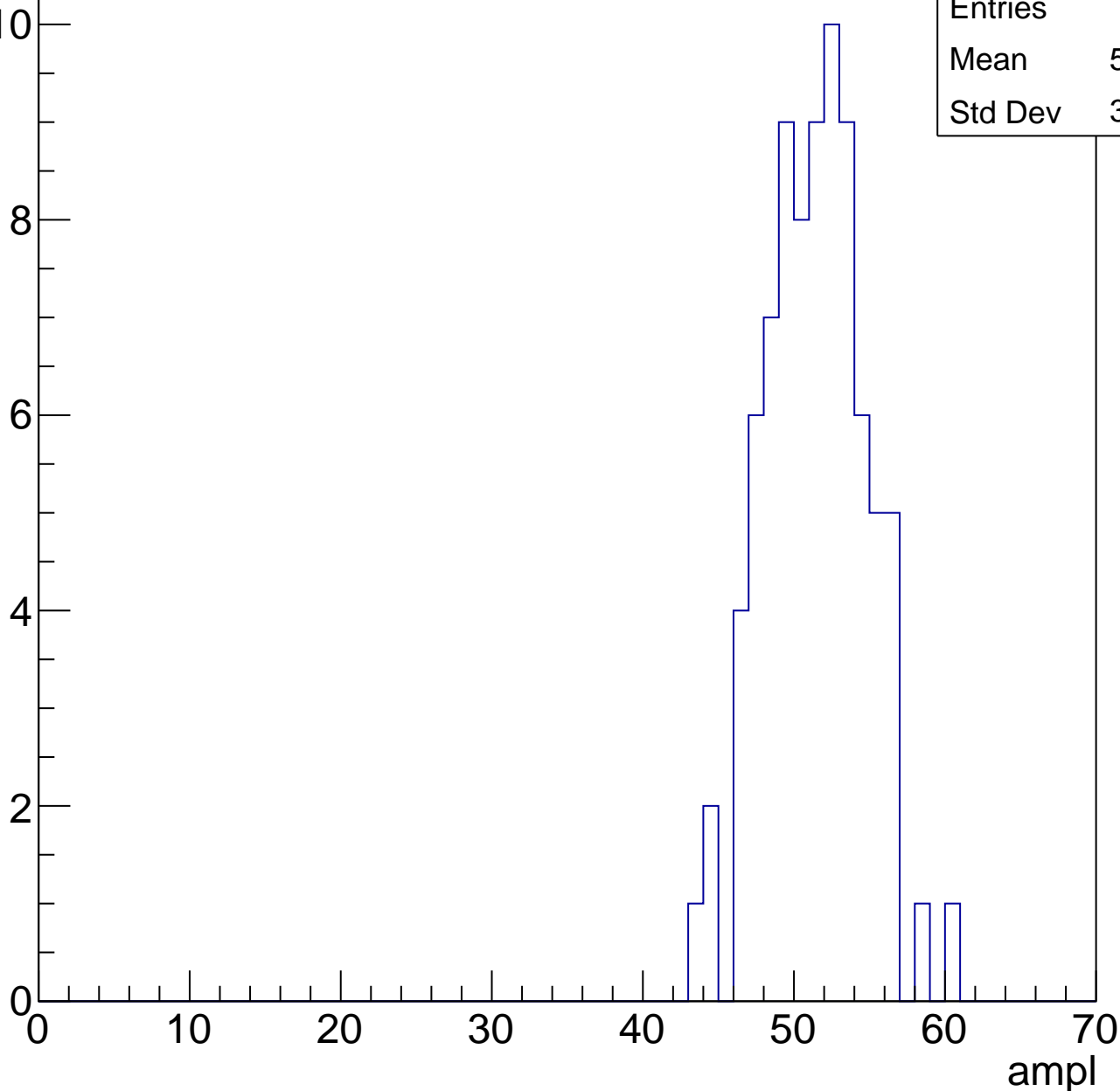


B0L001S, U23-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	50.93
Std Dev	3.292

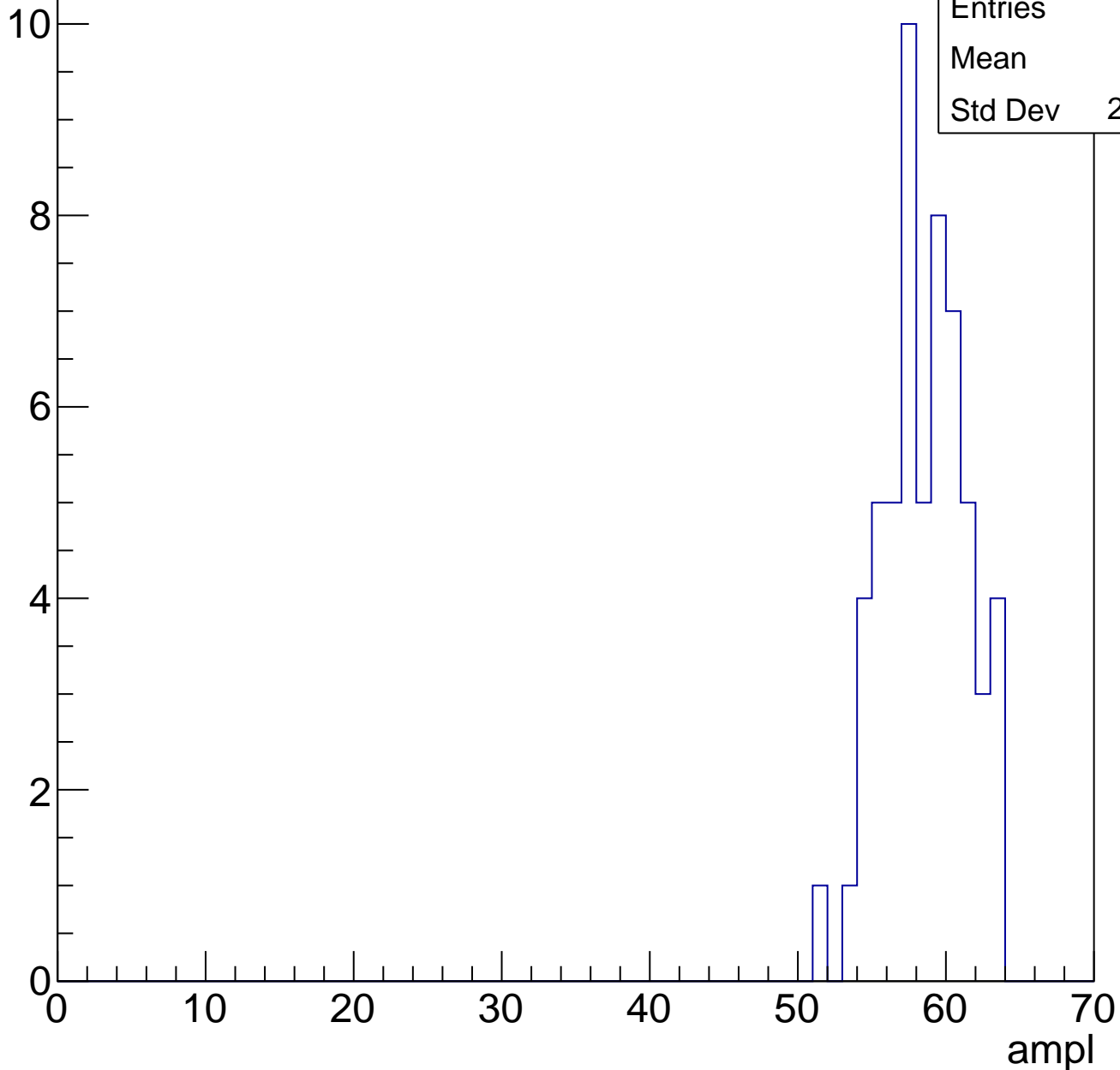


B0L001S, U23-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	58.1
Std Dev	2.759

Entry

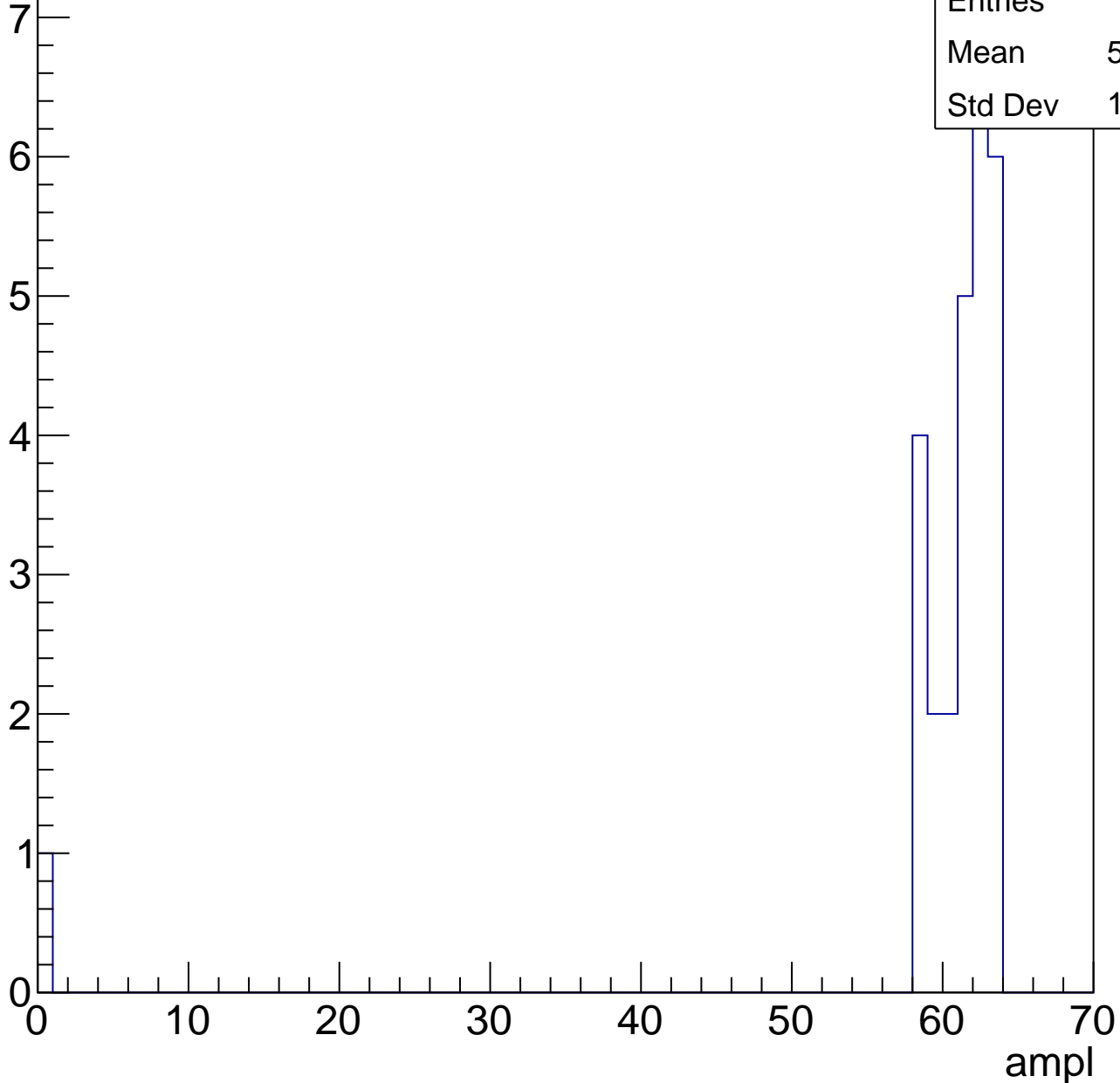


B0L001S, U23-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.78
Std Dev	11.65



B0L001S, U23-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch8, adc0

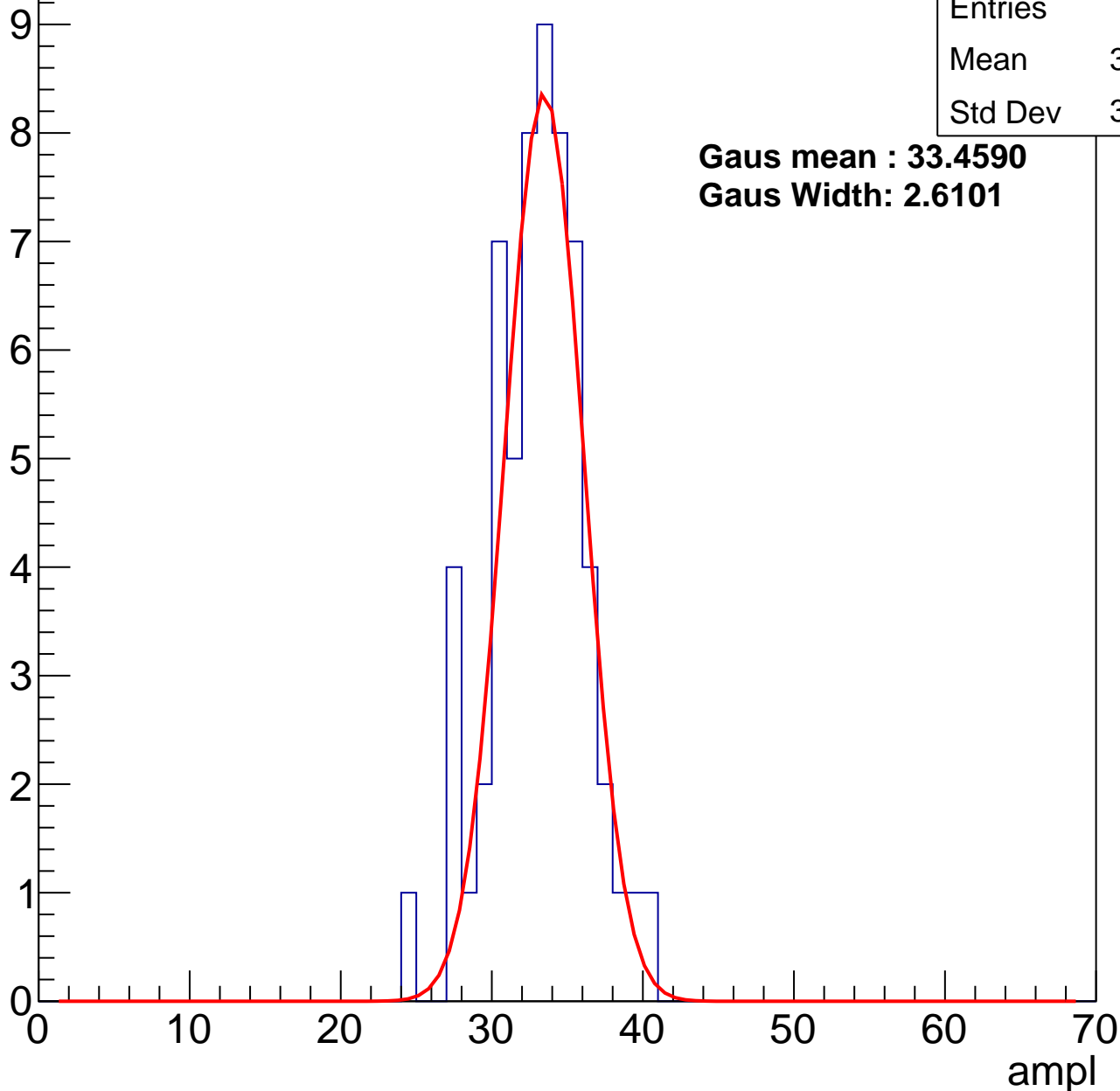
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	32.59
Std Dev	3.075

Gaus mean : 33.4590

Gaus Width: 2.6101



B0L001S, U23-ch8, adc1

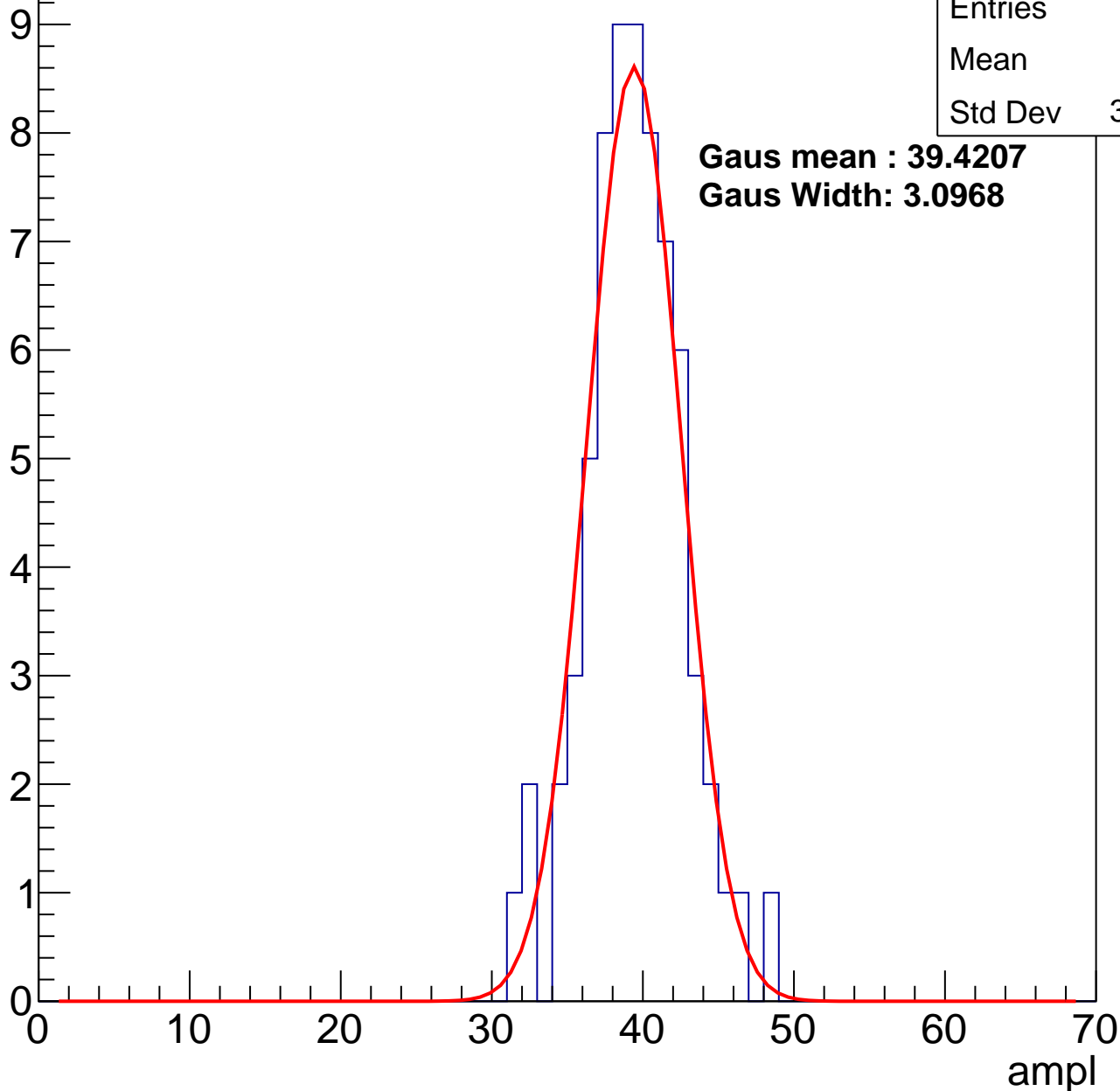
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	39
Std Dev	3.208

Gaus mean : 39.4207

Gaus Width: 3.0968



B0L001S, U23-ch8, adc2

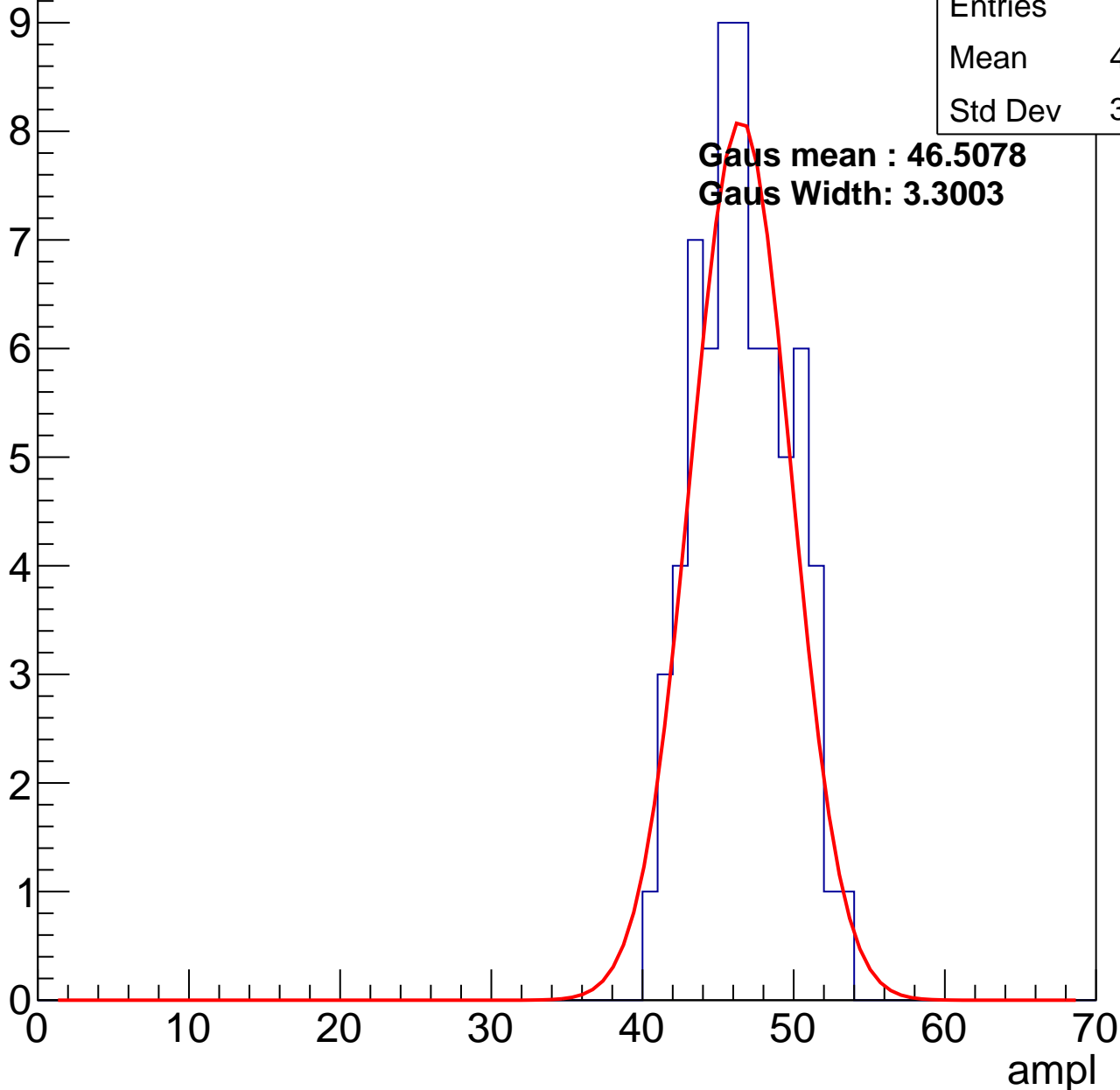
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	46.16
Std Dev	3.032

Gaus mean : 46.5078

Gaus Width: 3.3003

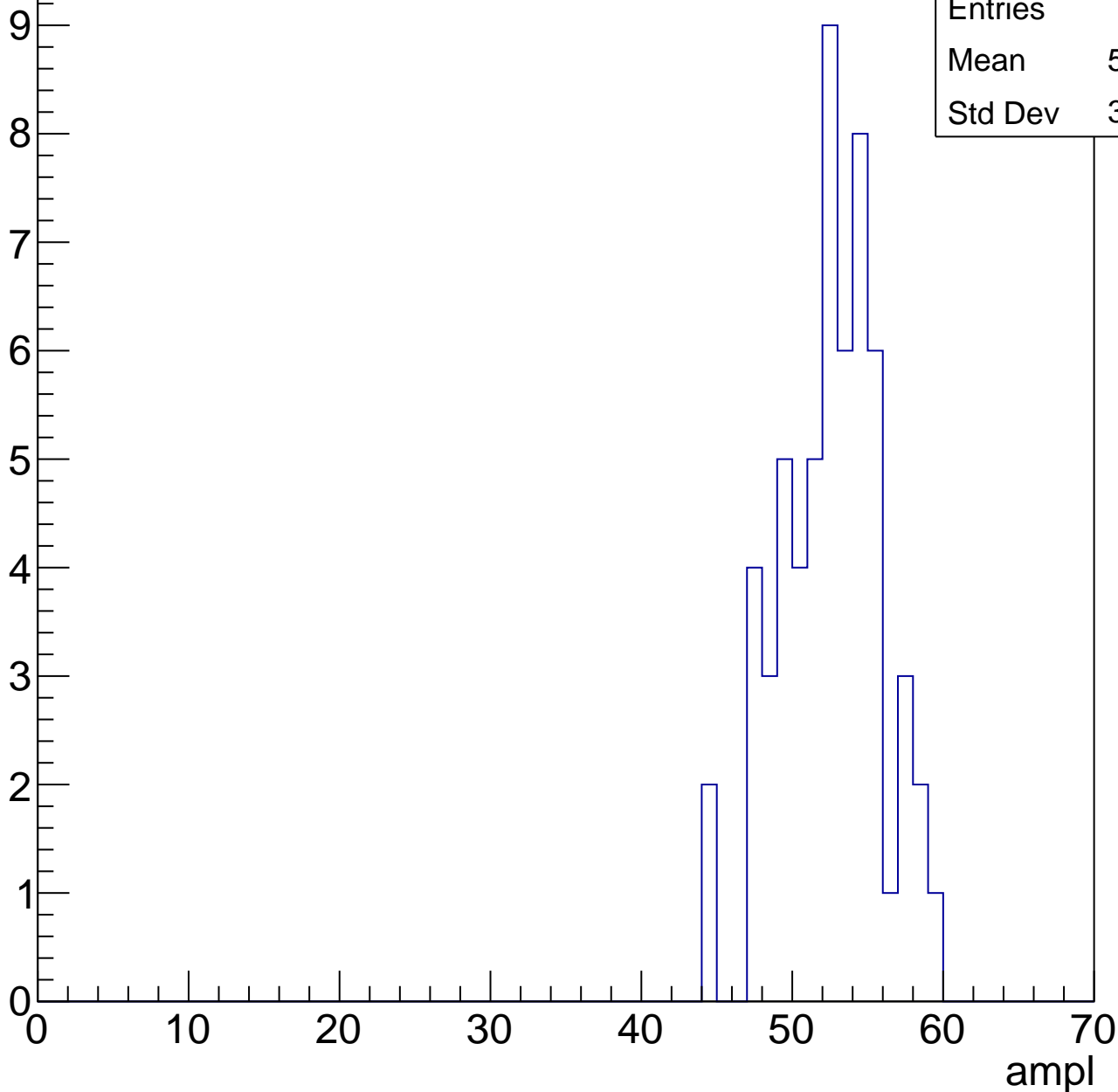


B0L001S, U23-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

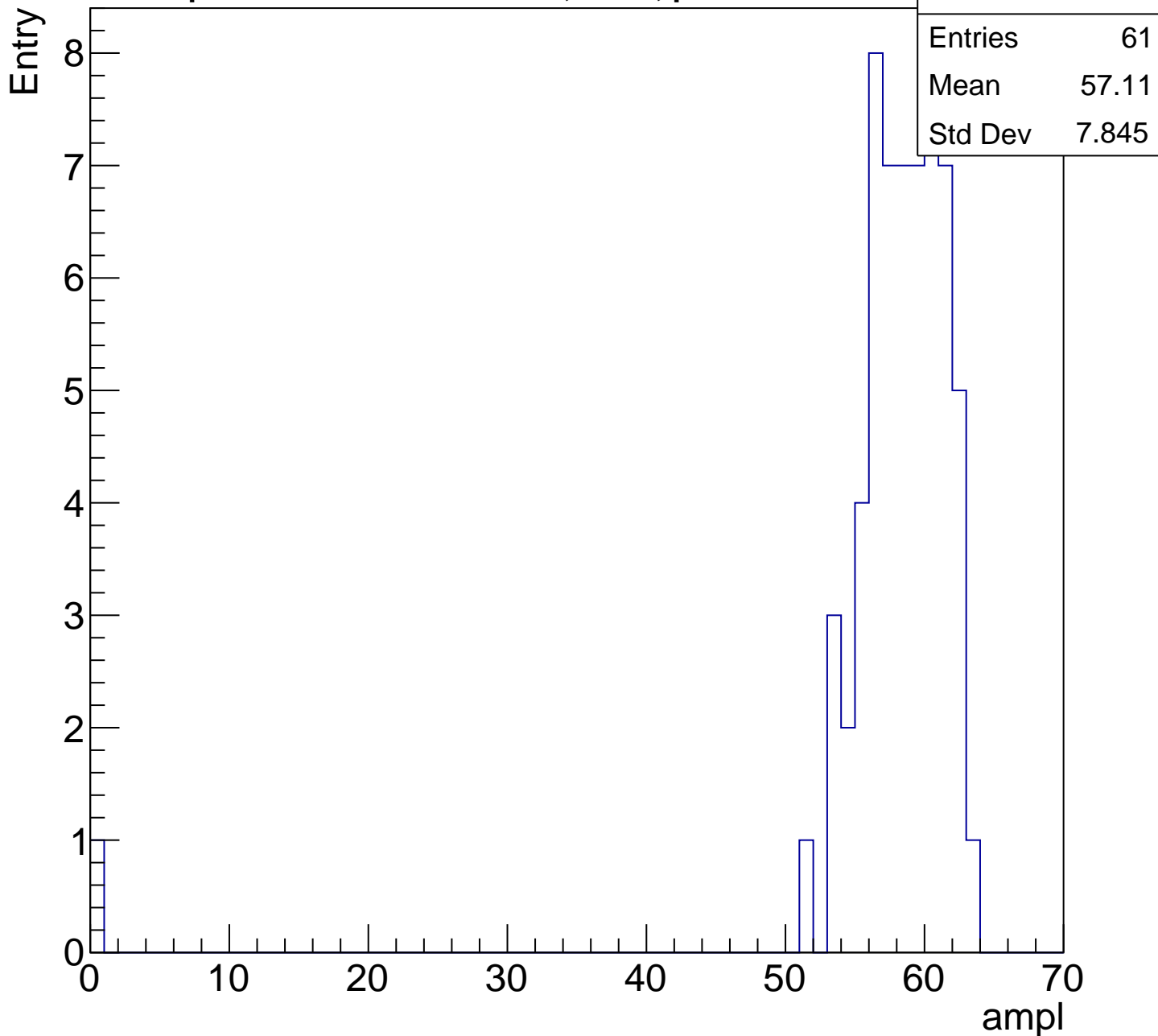
Entry

Entries	59
Mean	52.03
Std Dev	3.309



B0L001S, U23-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

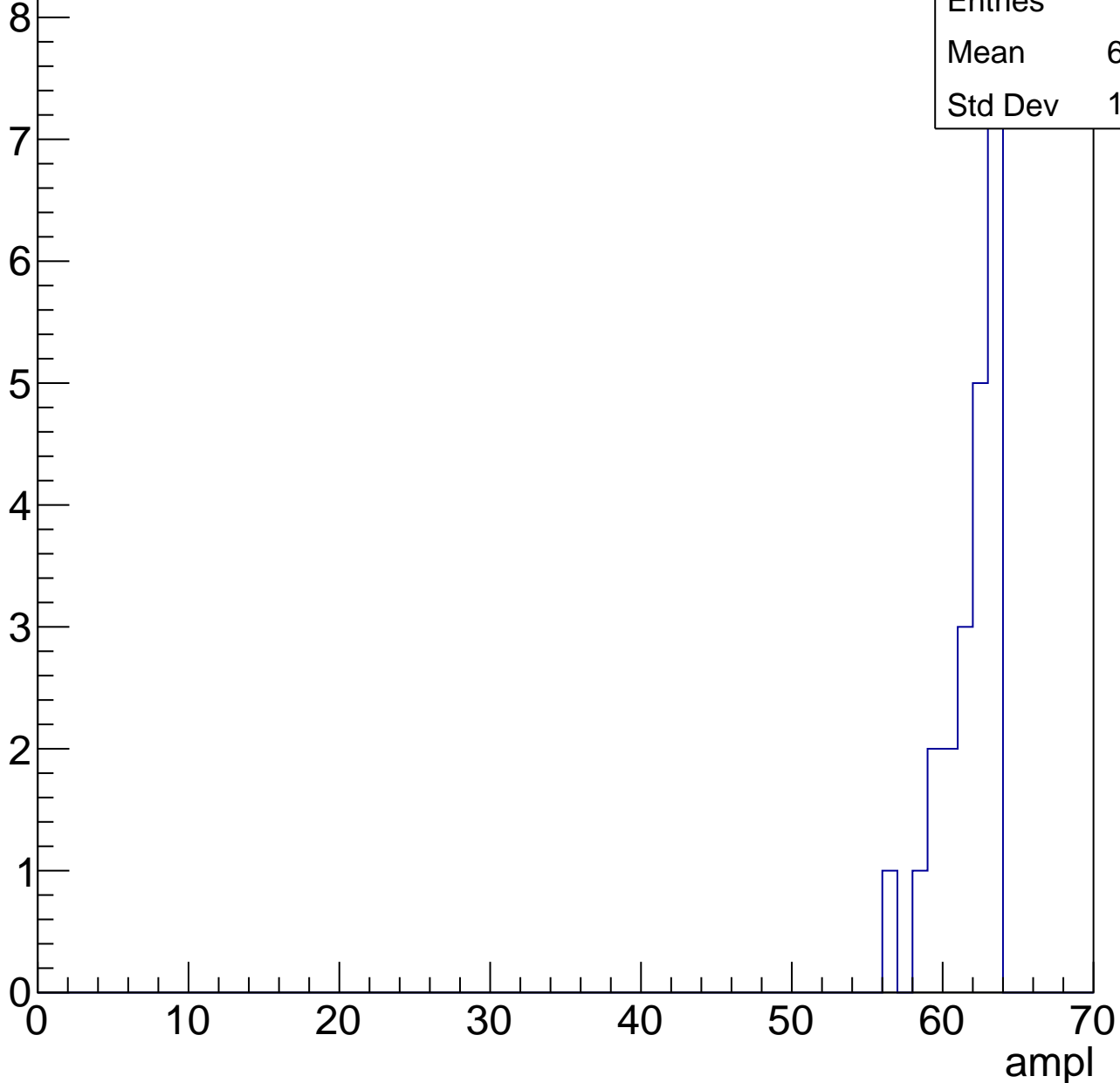


B0L001S, U23-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	61.32
Std Dev	1.892



B0L001S, U23-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L001S, U23-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch9, adc0

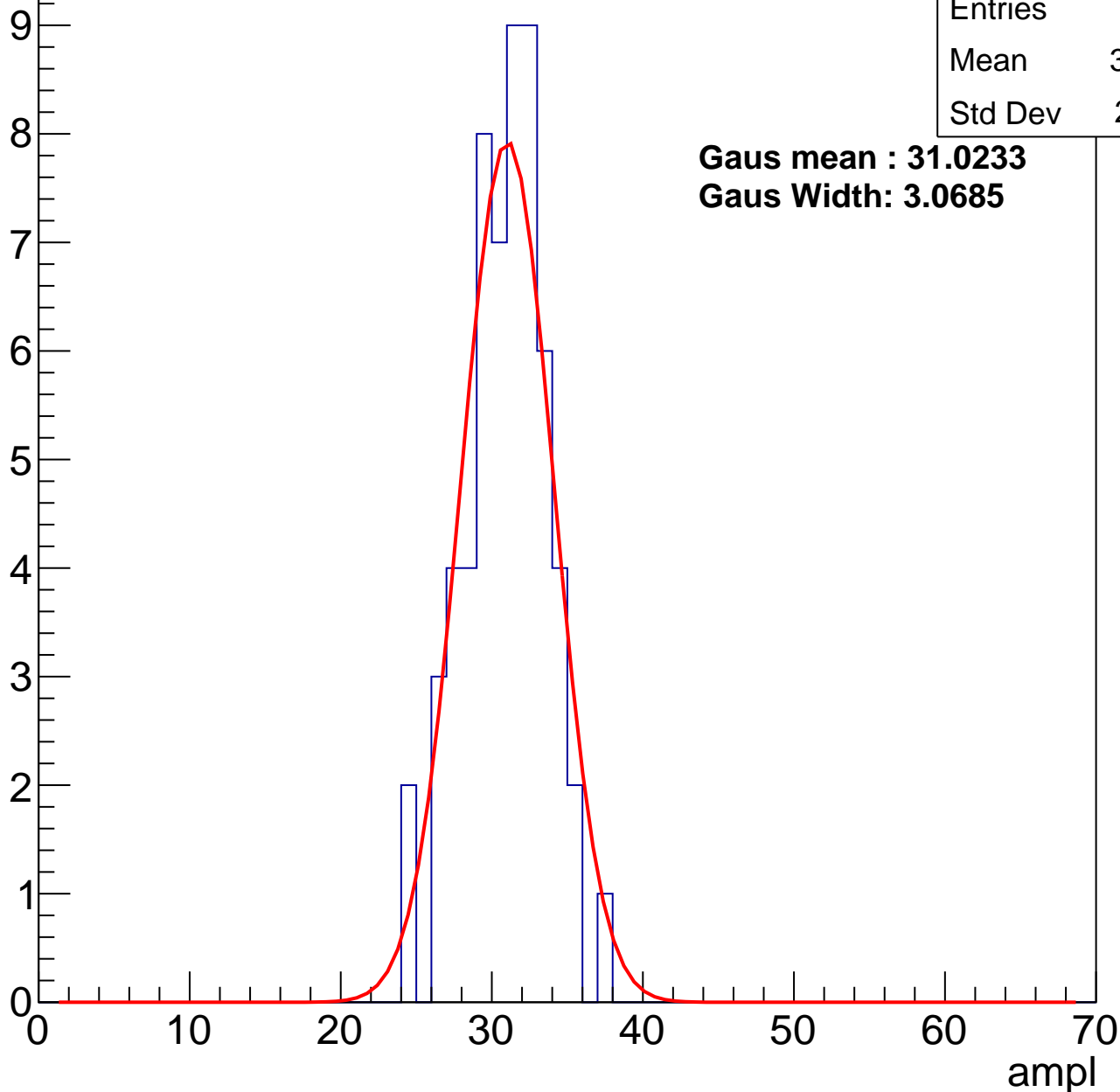
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	30.44
Std Dev	2.701

Gaus mean : 31.0233

Gaus Width: 3.0685



B0L001S, U23-ch9, adc1

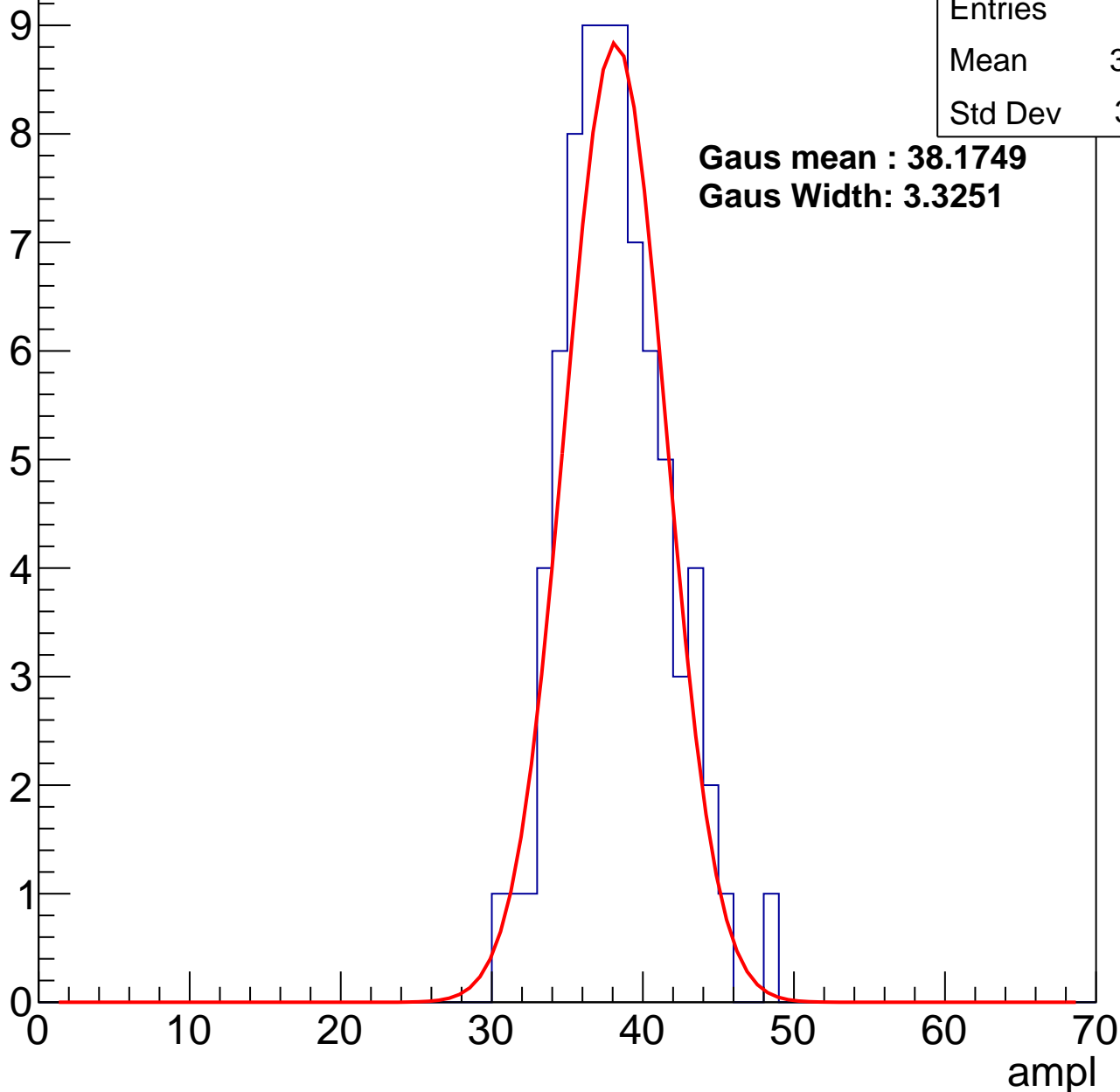
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.73
Std Dev	3.421

Gaus mean : 38.1749

Gaus Width: 3.3251



B0L001S, U23-ch9, adc2

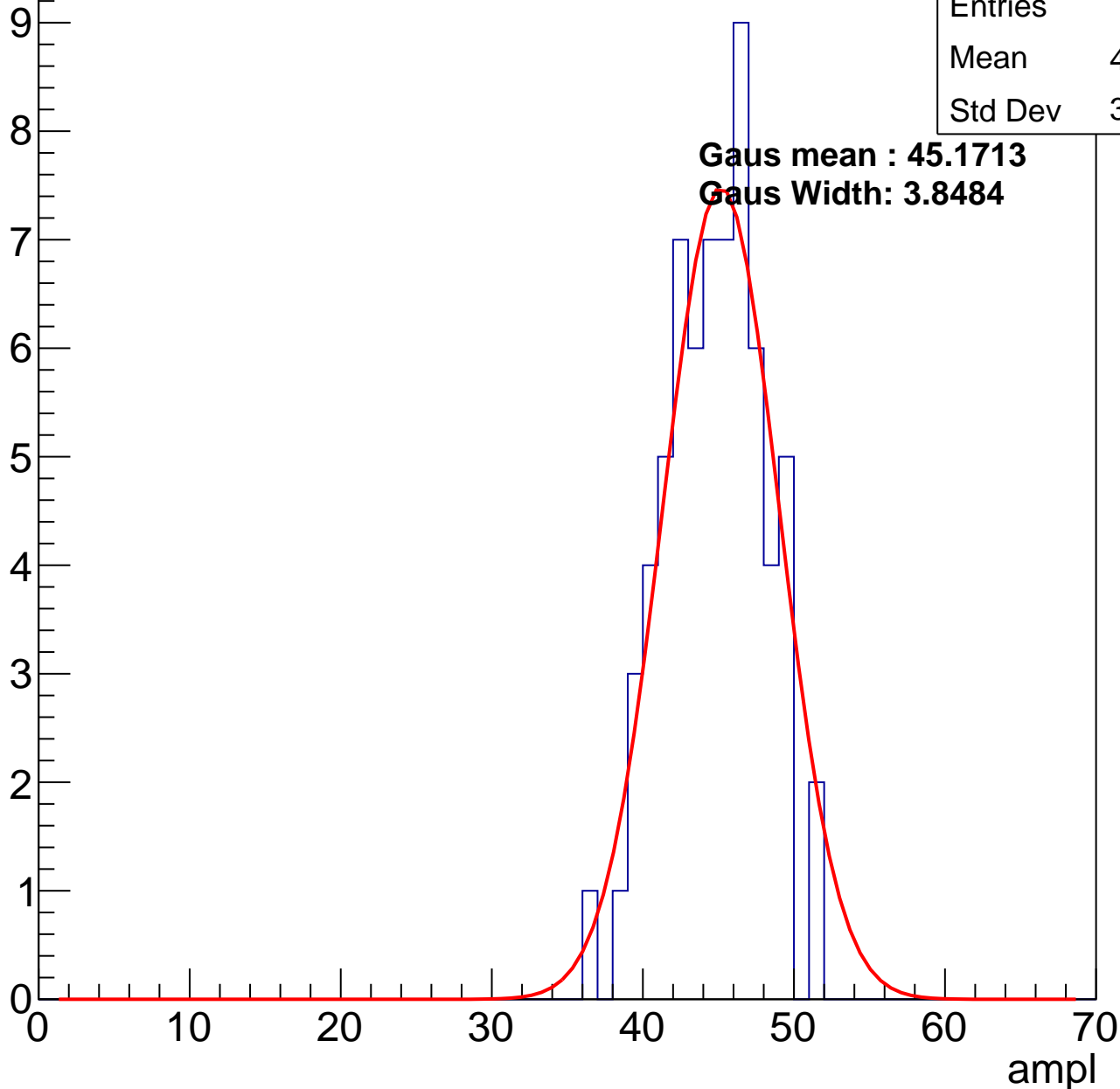
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.27
Std Dev	3.226

Gaus mean : 45.1713

Gaus Width: 3.8484

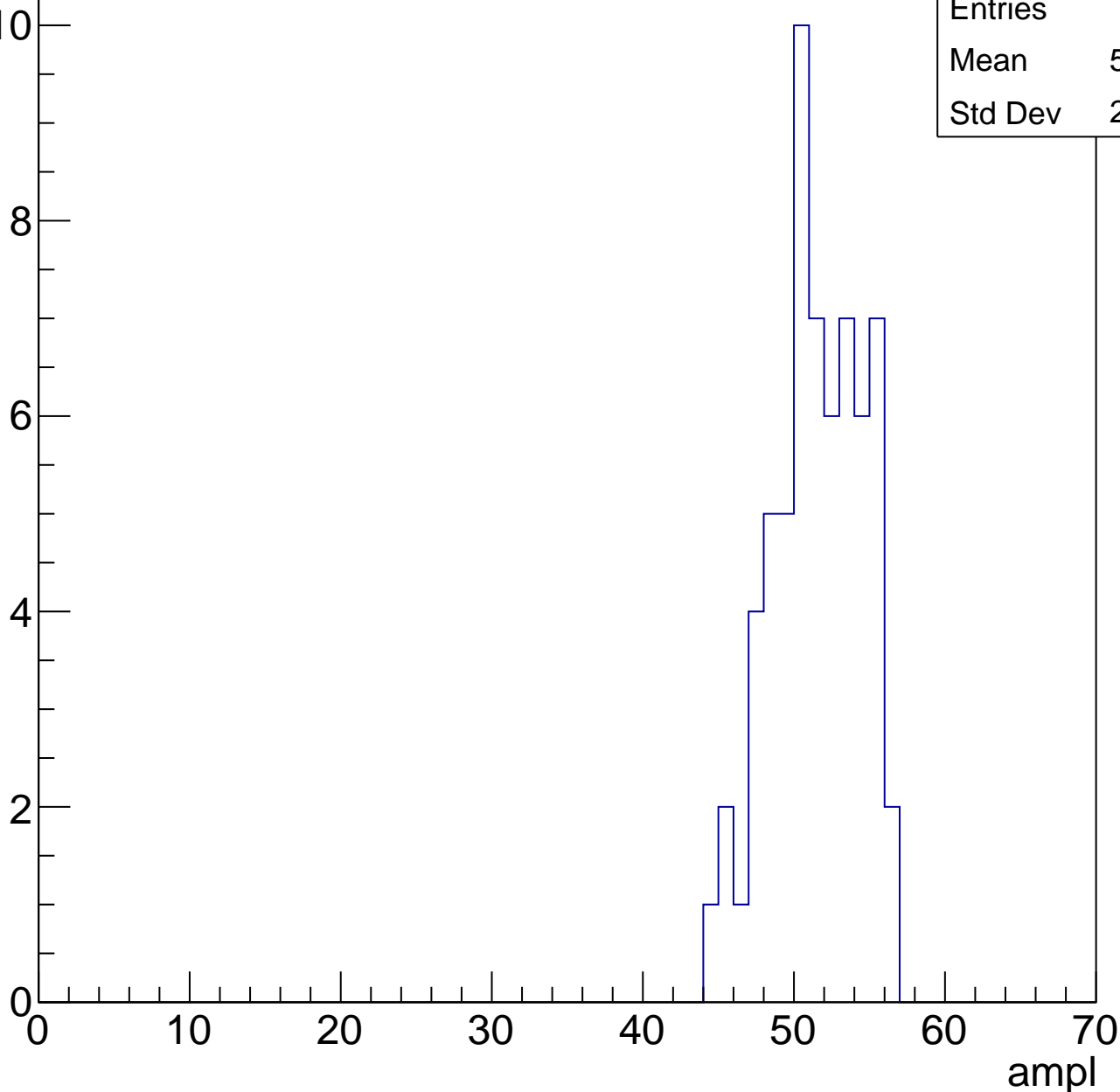


B0L001S, U23-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

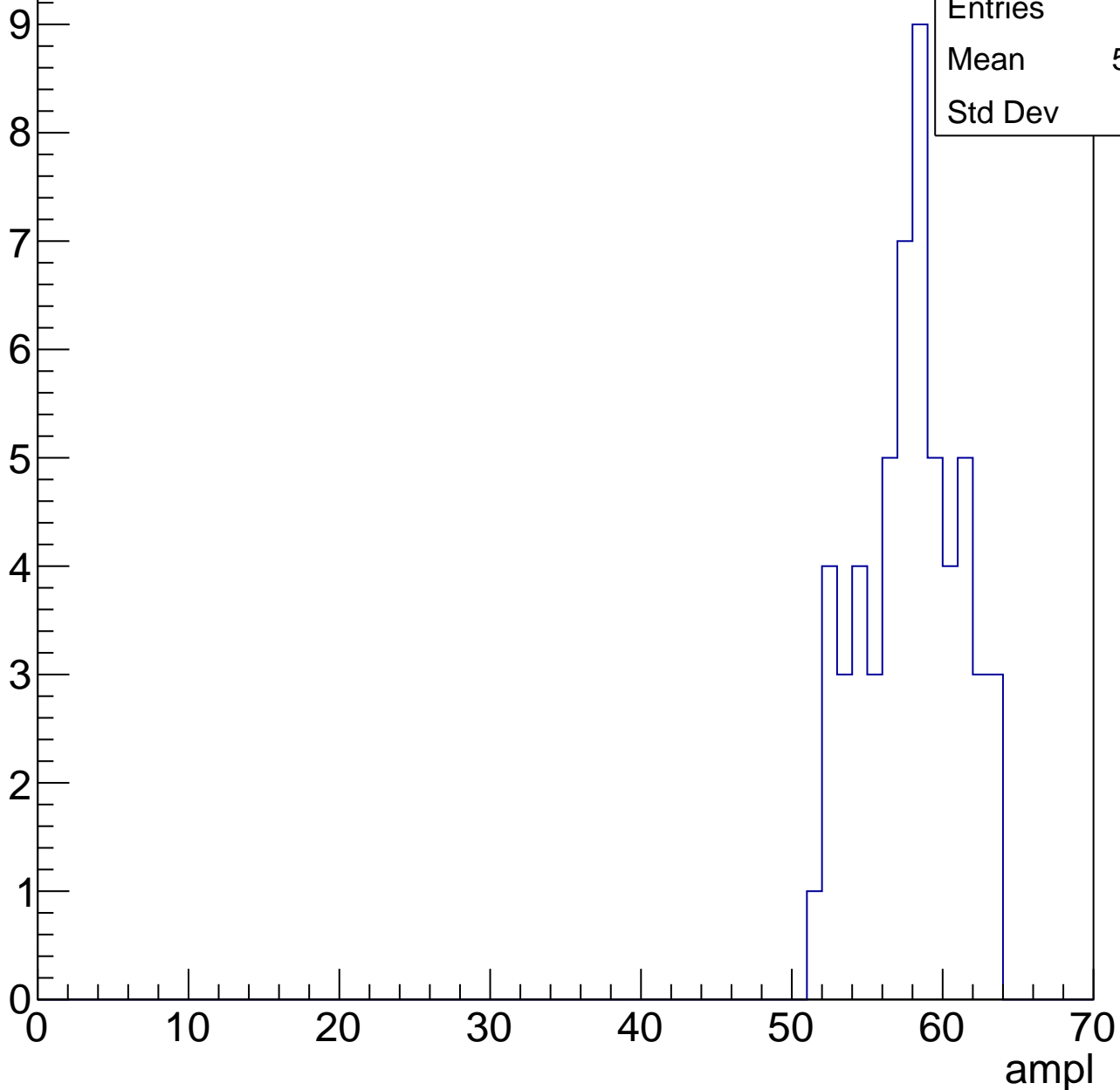
Entries	63
Mean	51.02
Std Dev	2.914



B0L001S, U23-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

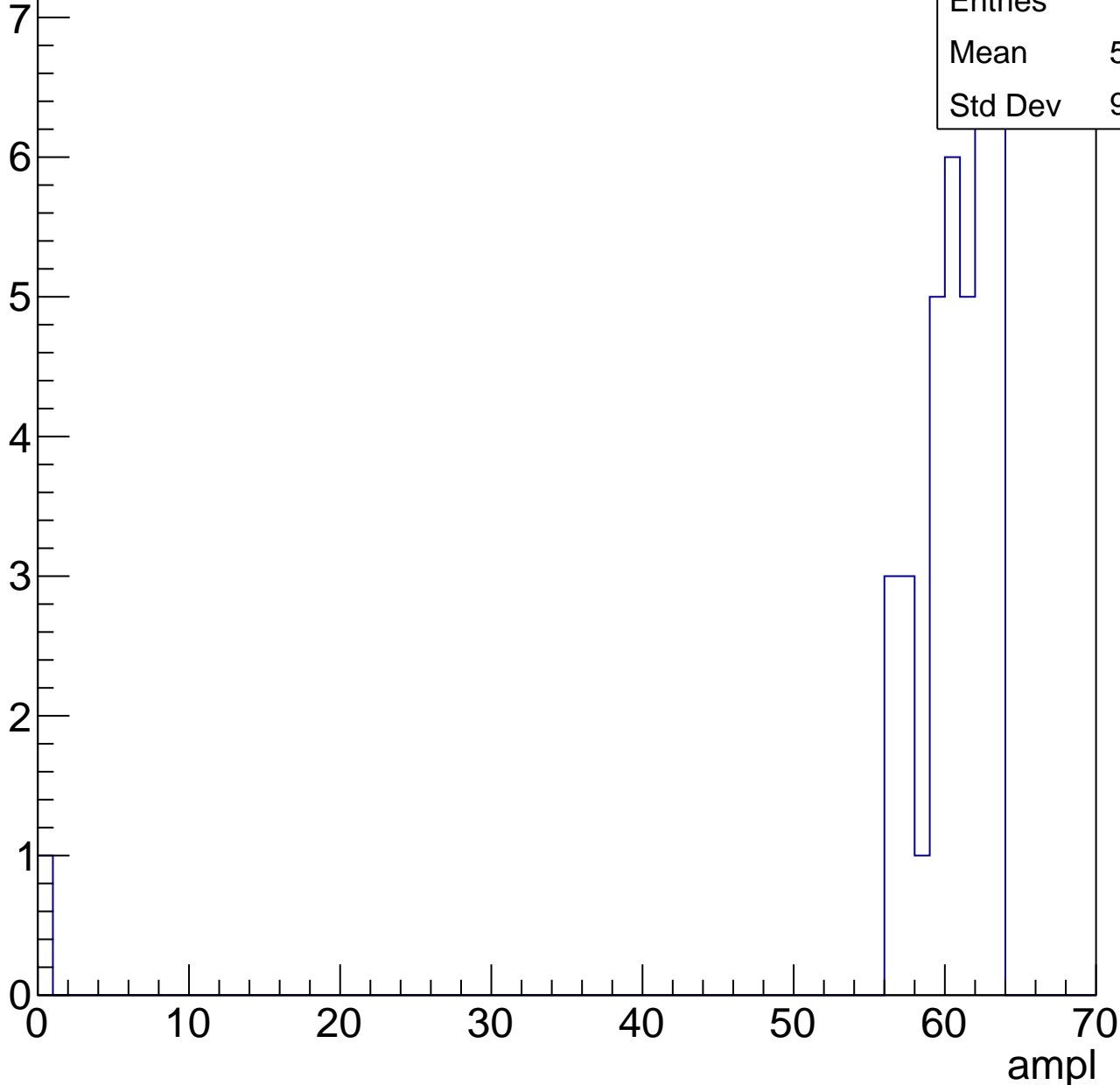


B0L001S, U23-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.74
Std Dev	9.893



B0L001S, U23-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch10, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	30.94
Std Dev	3.292

Gaus mean : 31.7225

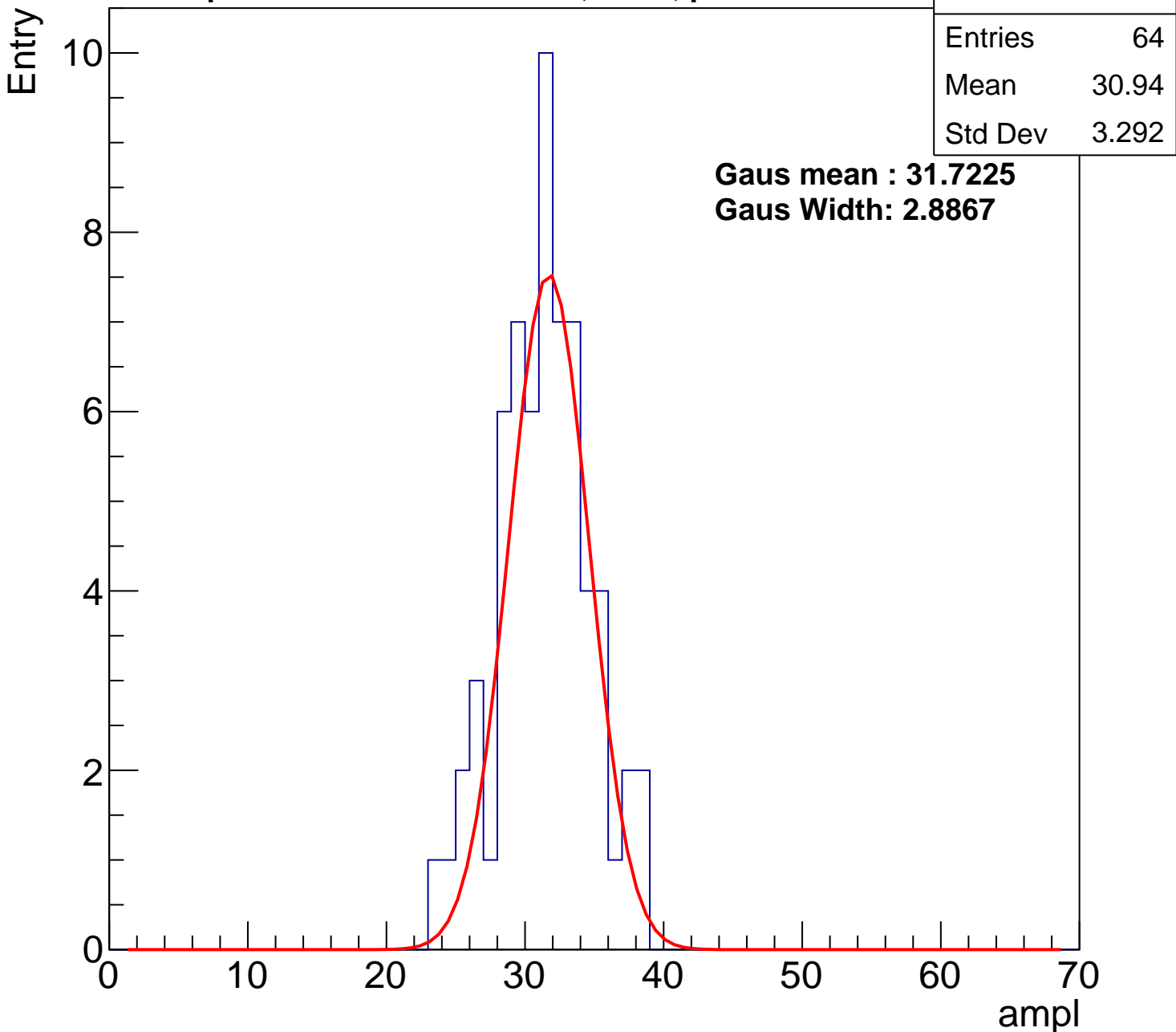
Gaus Width: 2.8867

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch10, adc1

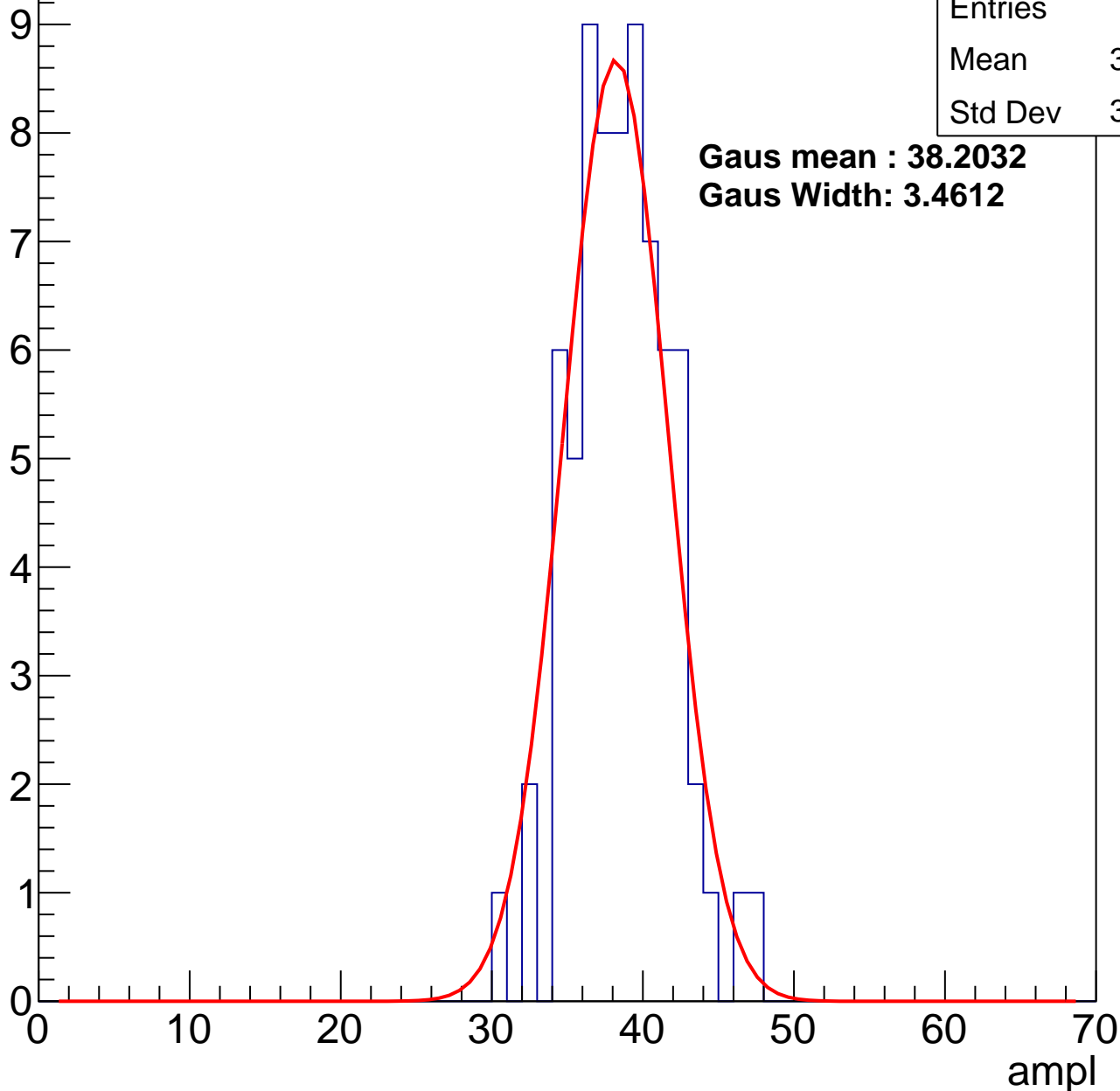
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.18
Std Dev	3.199

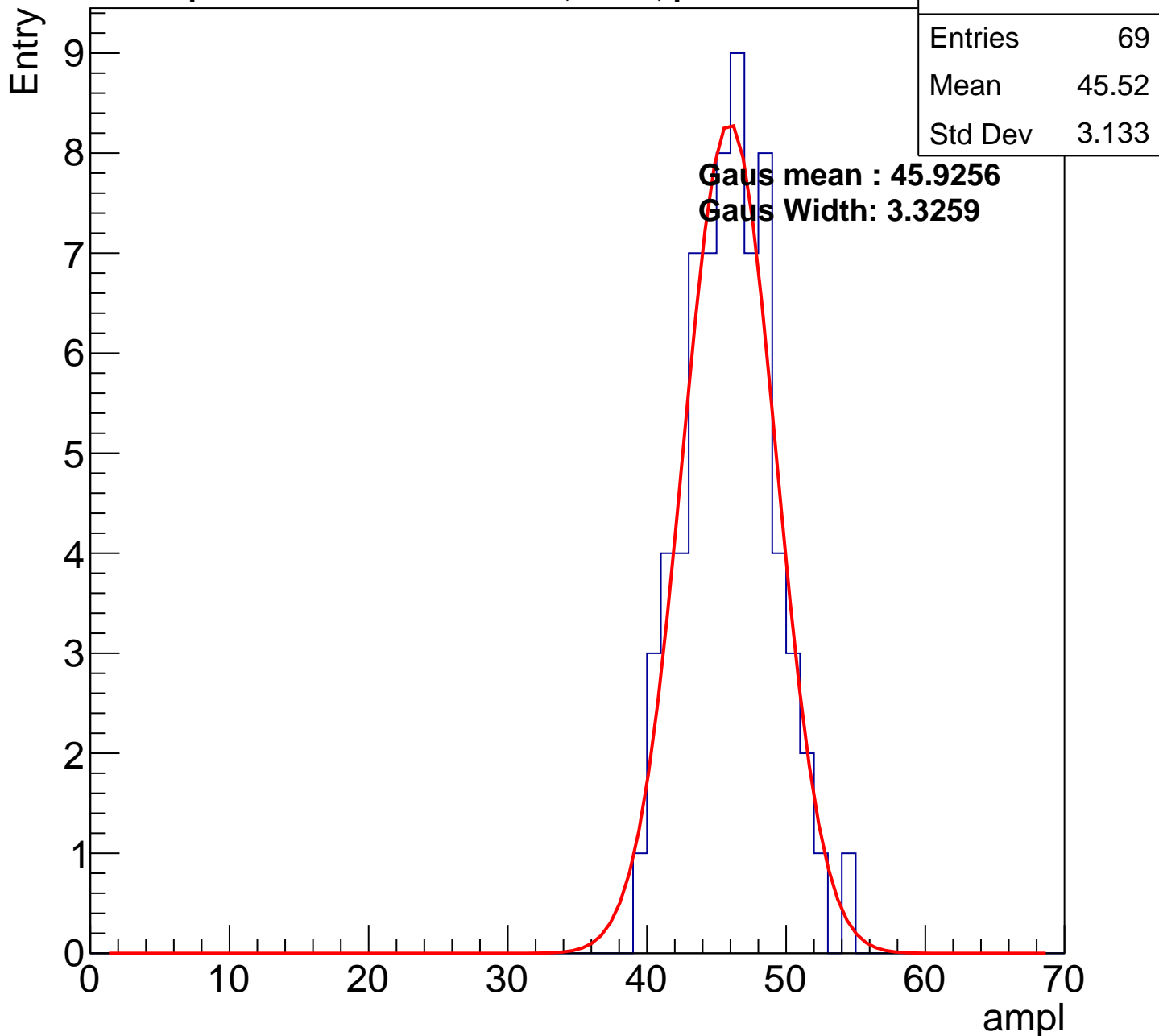
Gaus mean : 38.2032

Gaus Width: 3.4612



B0L001S, U23-ch10, adc2

calib_packv5_042523_0143.root, FC#9, port A1

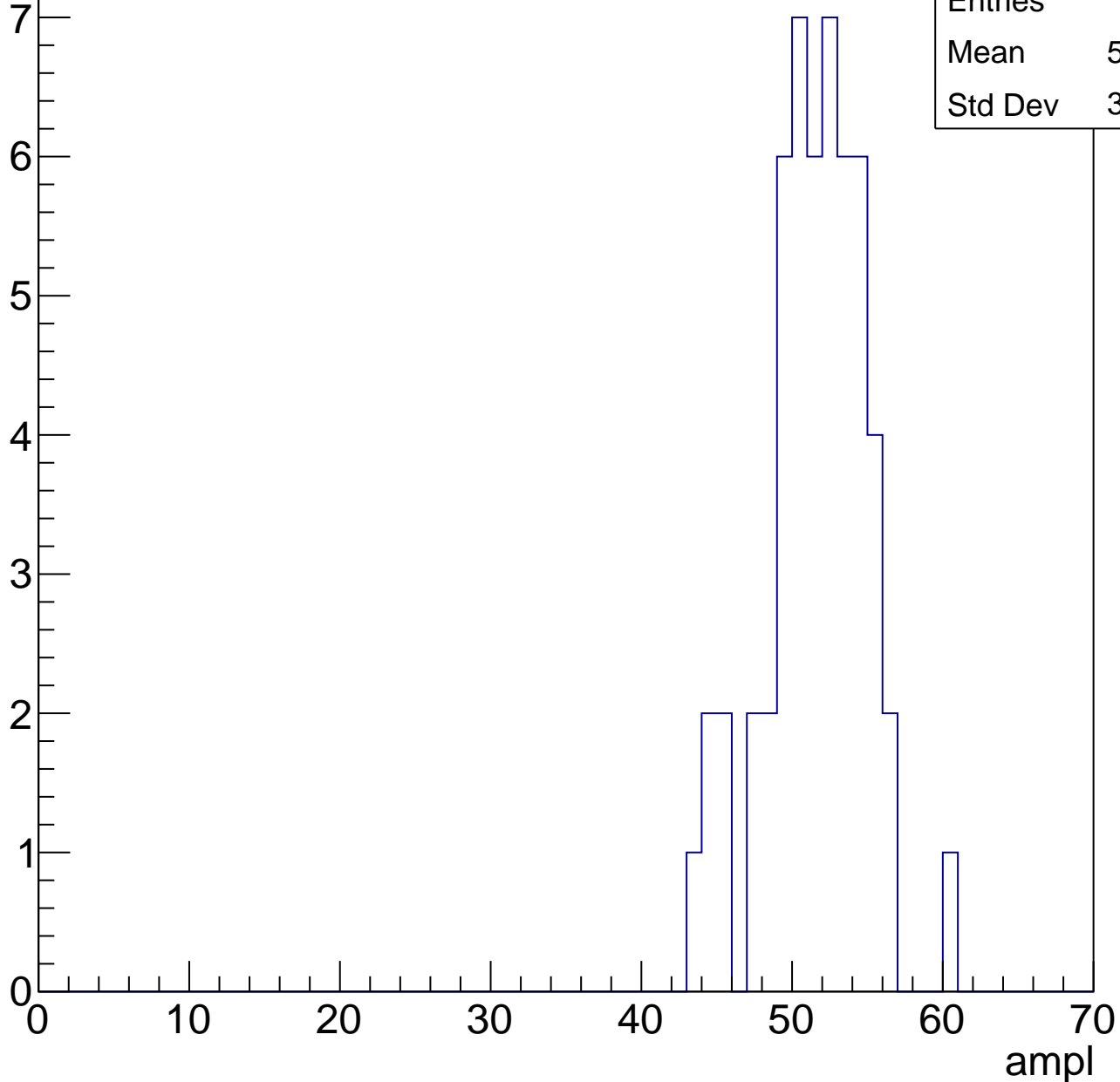


B0L001S, U23-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

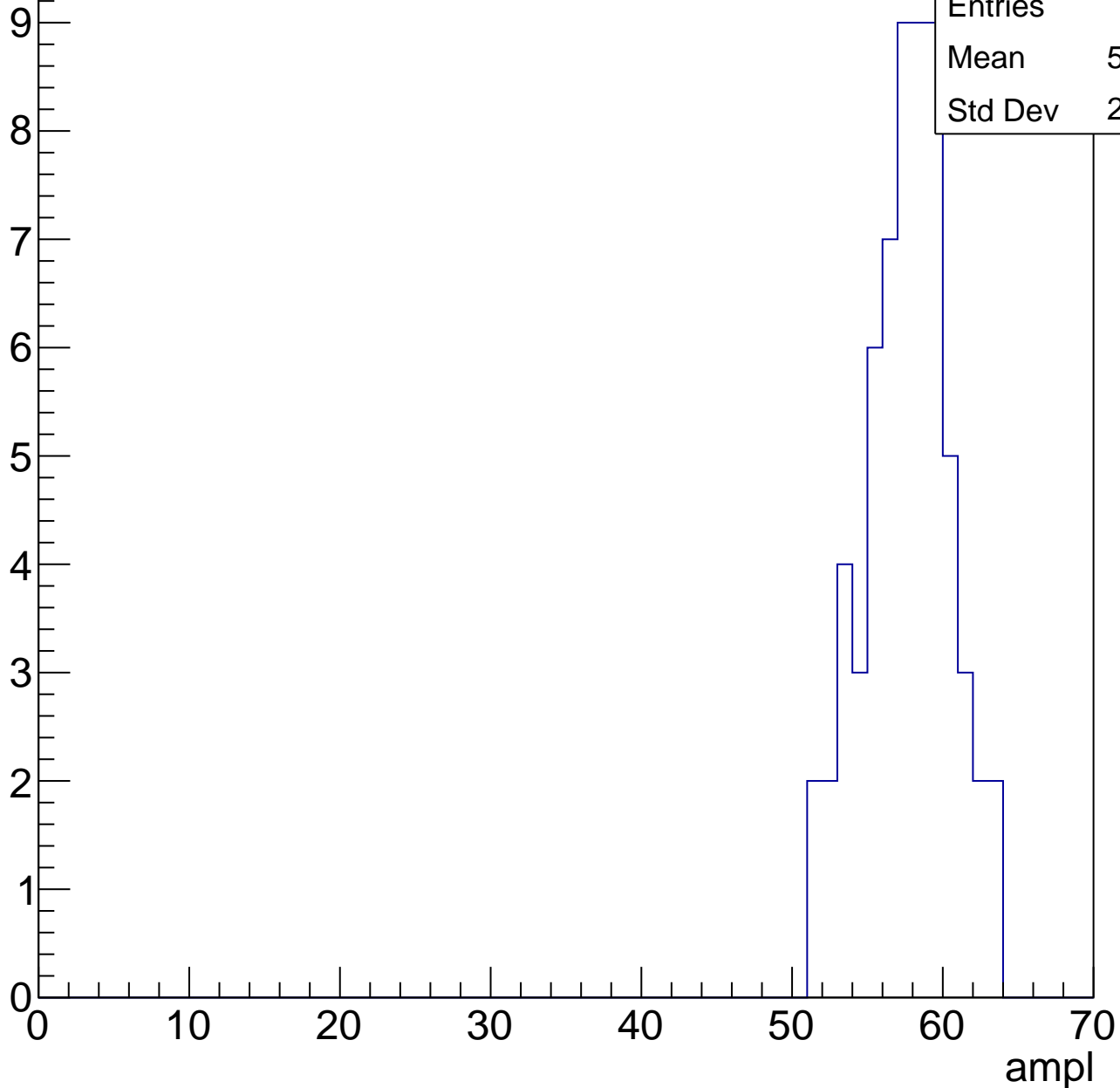
Entries	54
Mean	51.09
Std Dev	3.318



B0L001S, U23-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

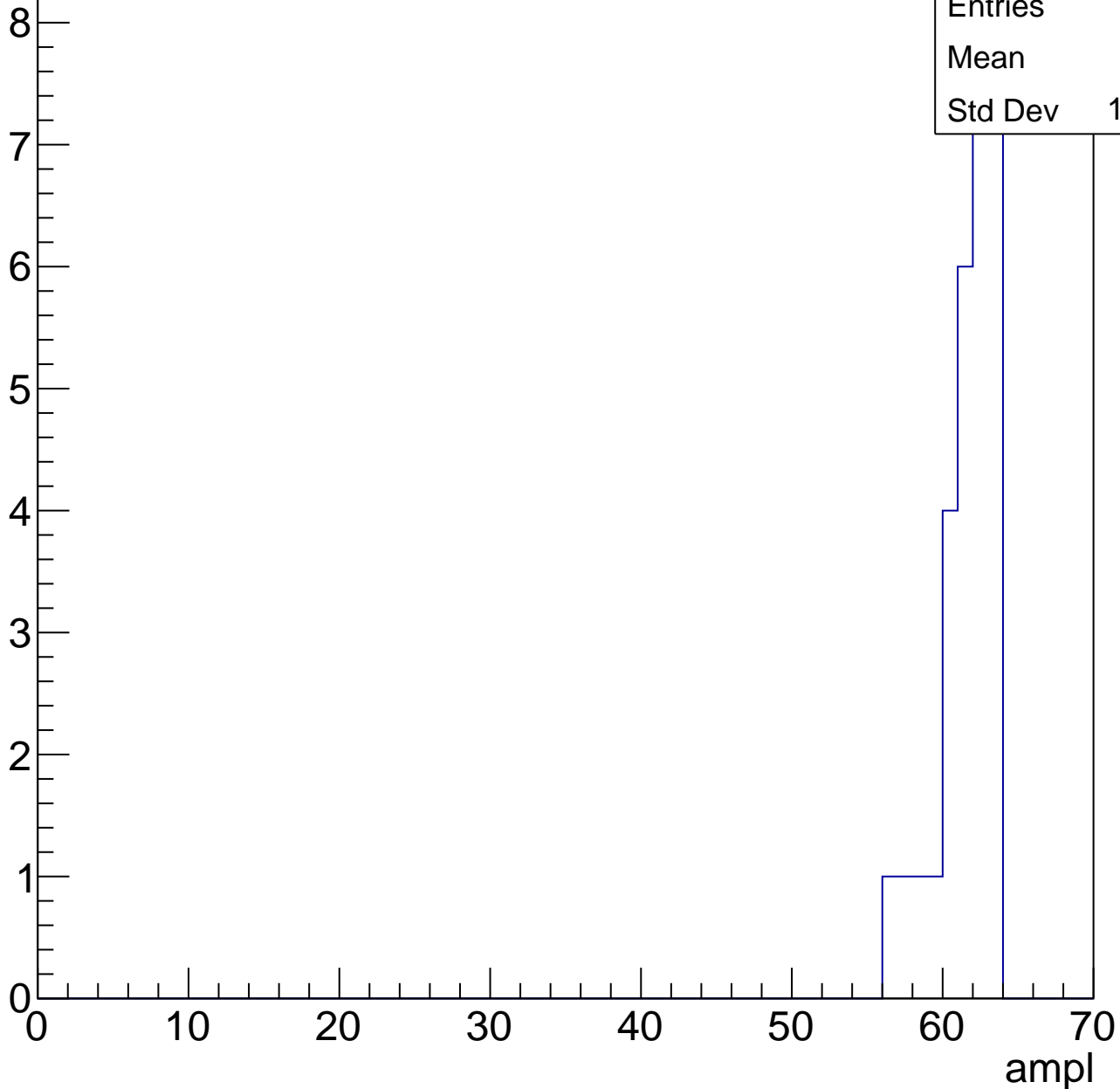


B0L001S, U23-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

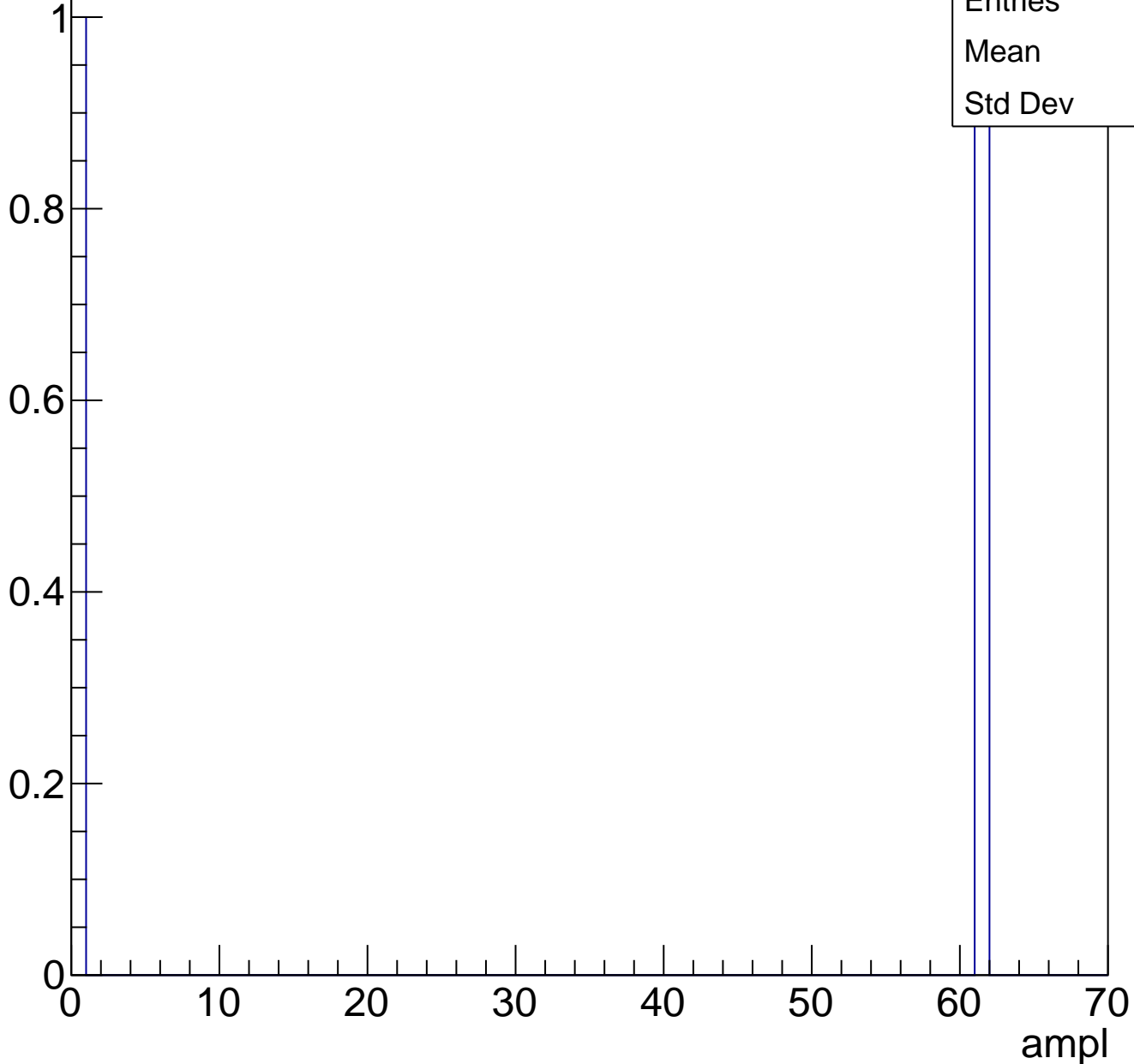
Entries	30
Mean	61.2
Std Dev	1.796



B0L001S, U23-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch11, adc0

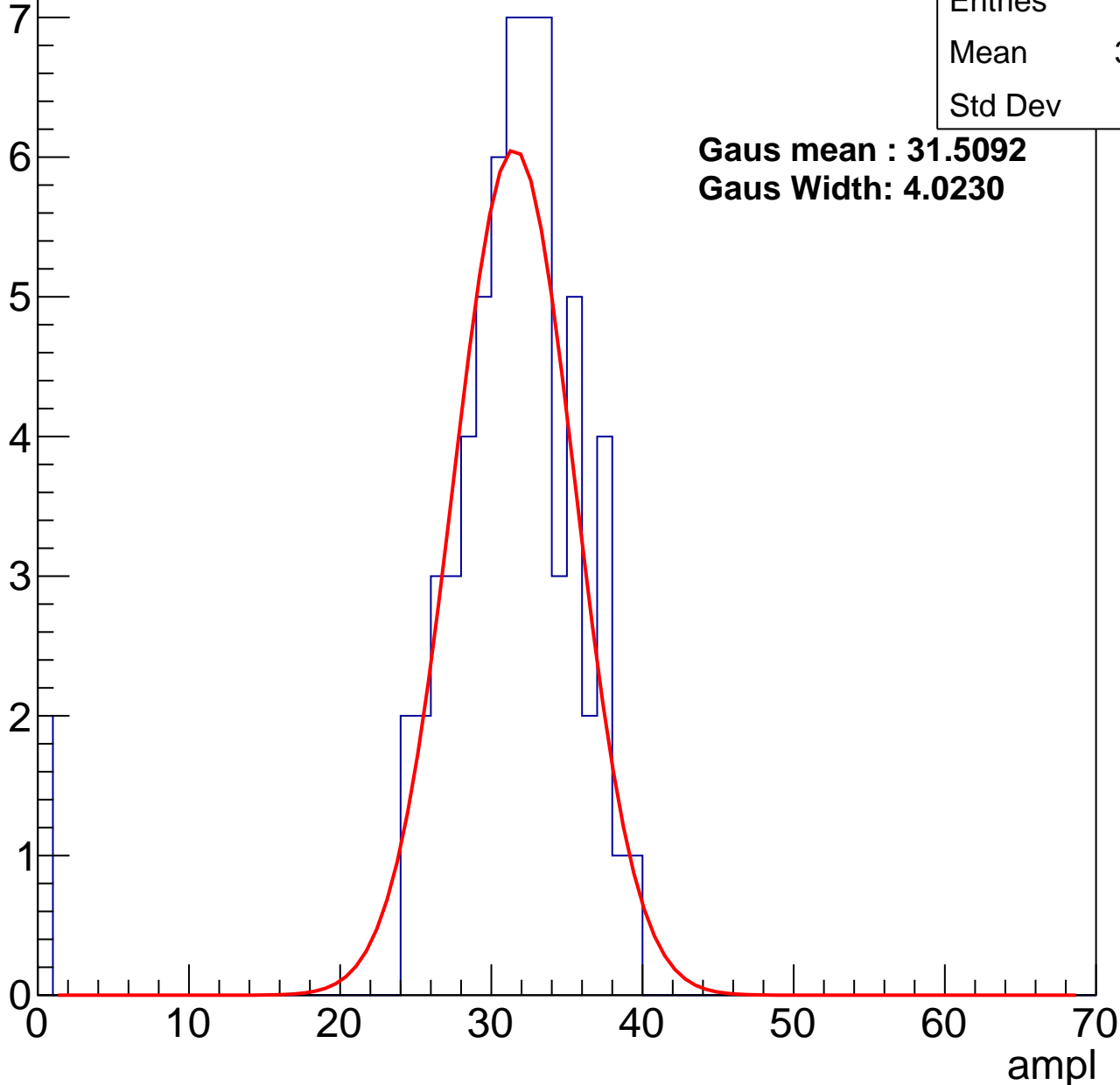
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.31
Std Dev	6.49

Gaus mean : 31.5092

Gaus Width: 4.0230



B0L001S, U23-ch11, adc1

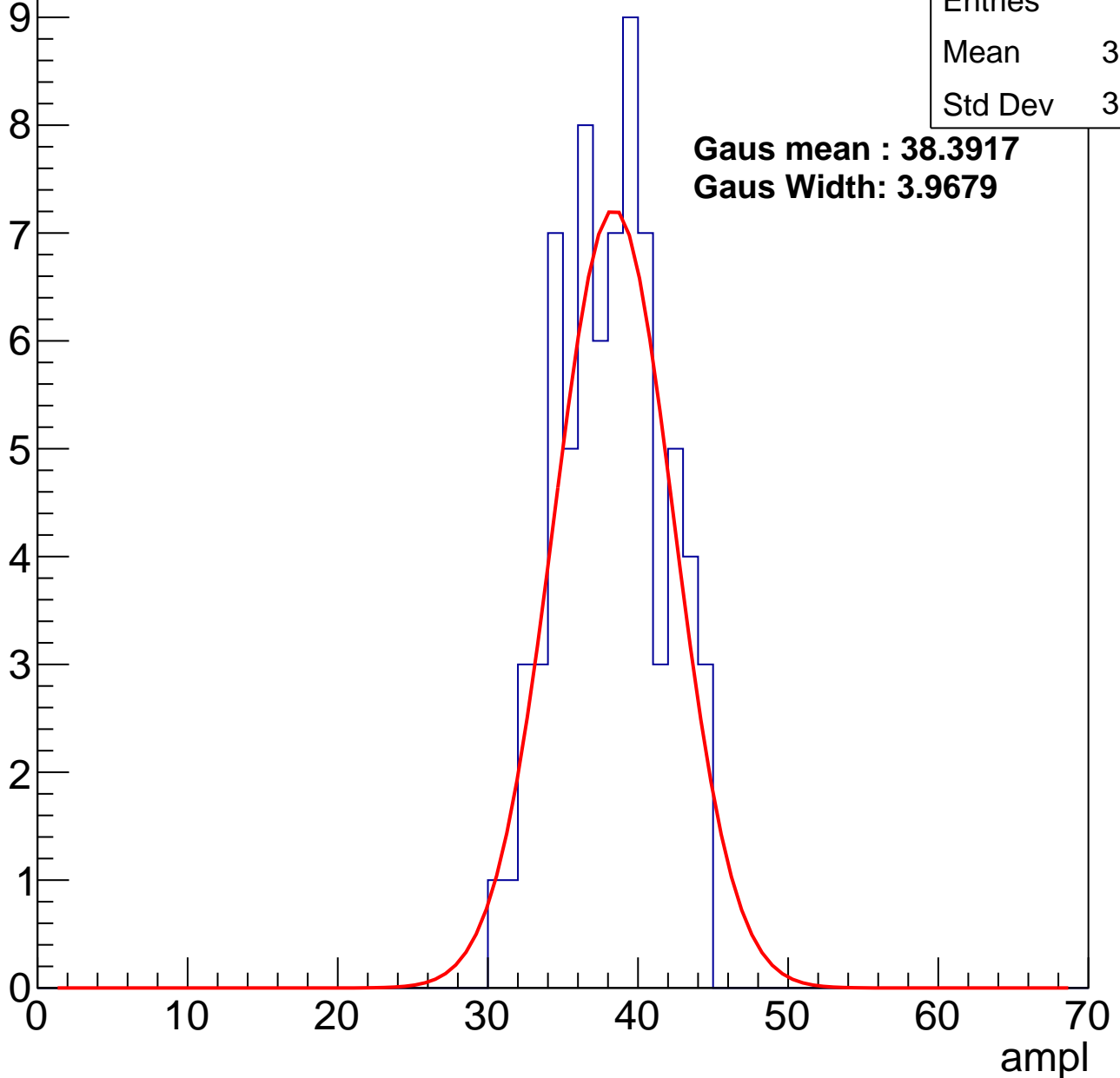
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.68
Std Dev	3.407

Gaus mean : 38.3917

Gaus Width: 3.9679



B0L001S, U23-ch11, adc2

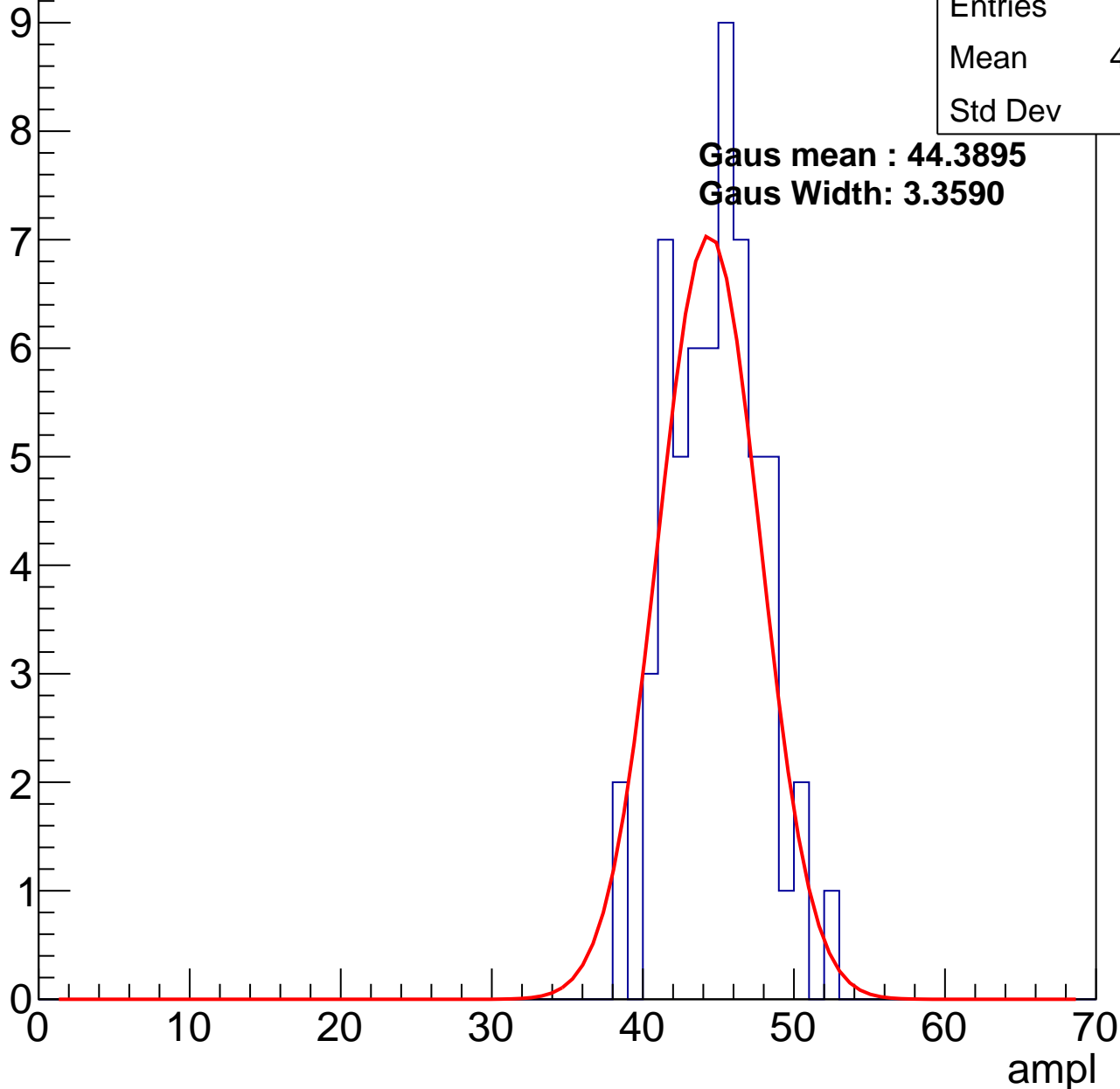
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.37
Std Dev	2.98

Gaus mean : 44.3895

Gaus Width: 3.3590

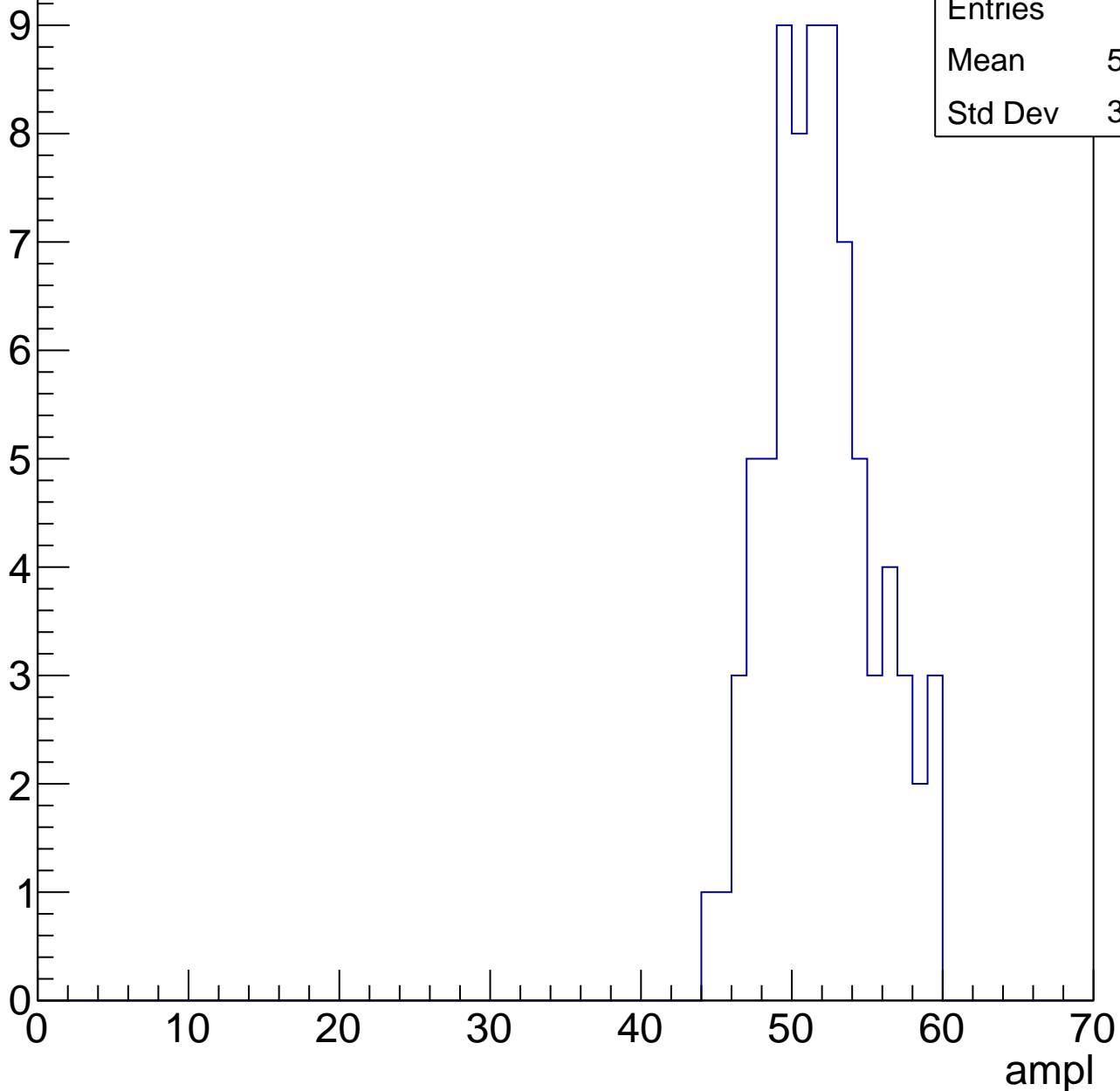


B0L001S, U23-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	51.48
Std Dev	3.493

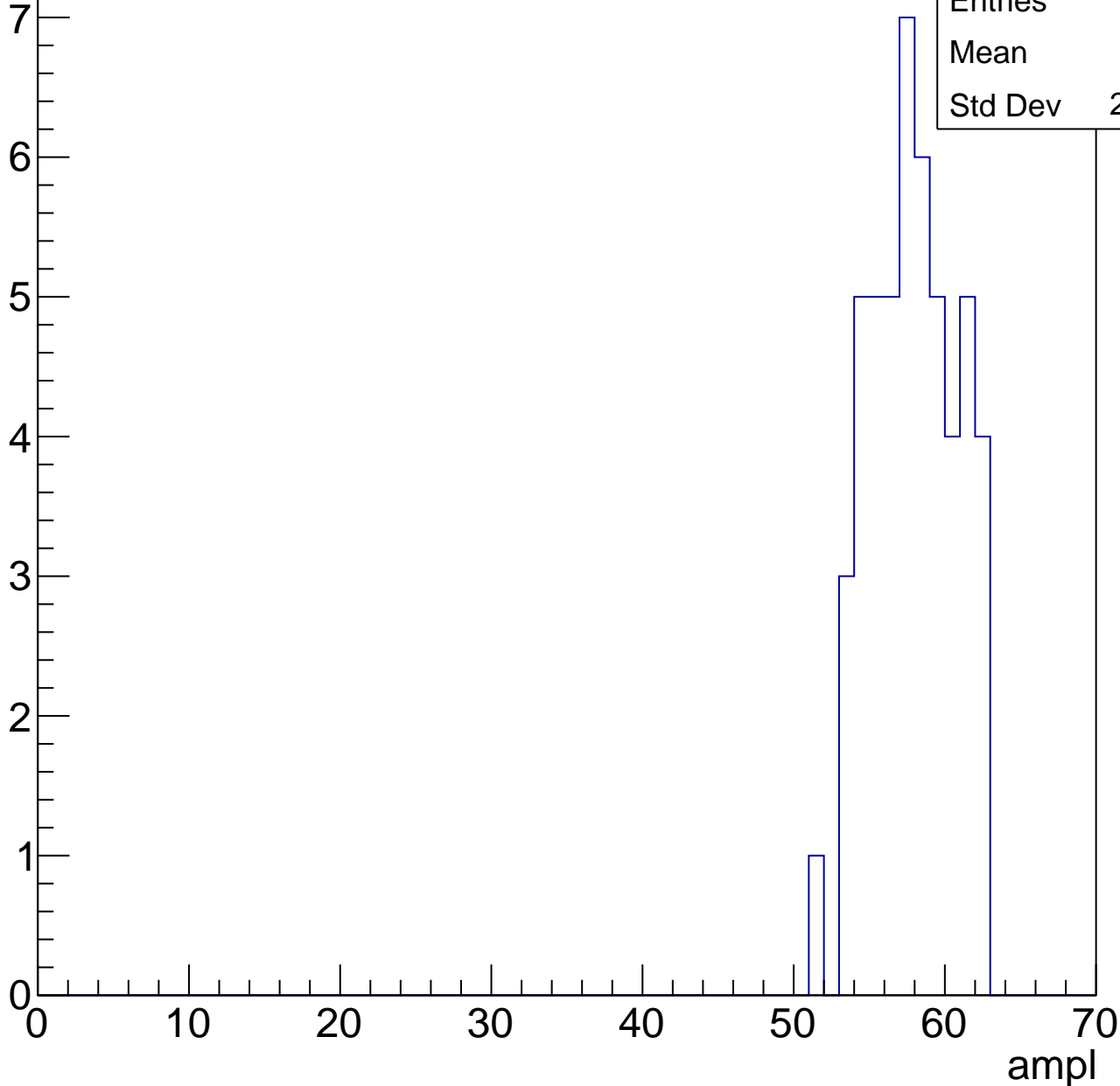


B0L001S, U23-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

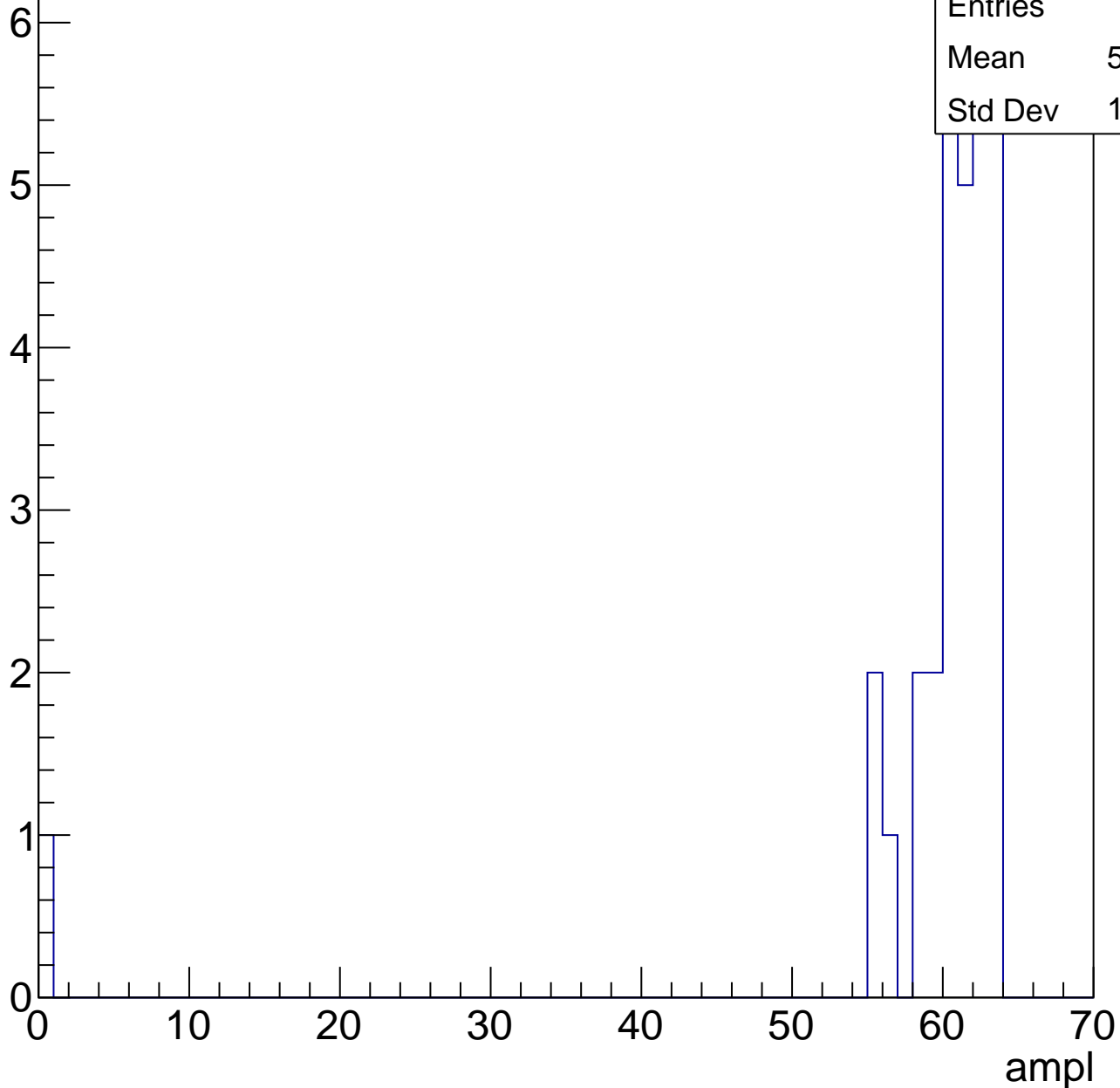
Entries	50
Mean	57.4
Std Dev	2.786



B0L001S, U23-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch12, adc0

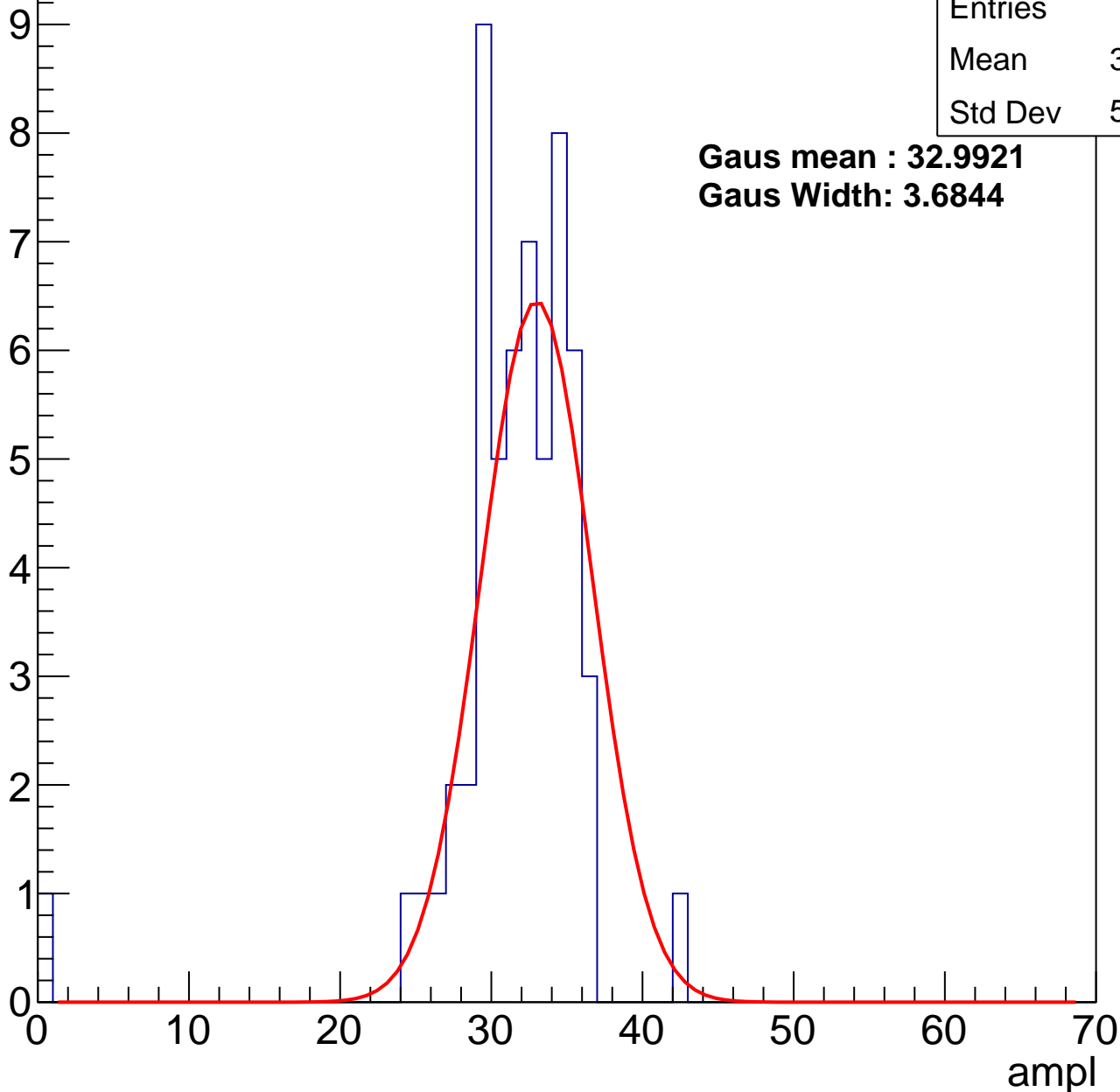
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.09
Std Dev	5.174

Gaus mean : 32.9921

Gaus Width: 3.6844

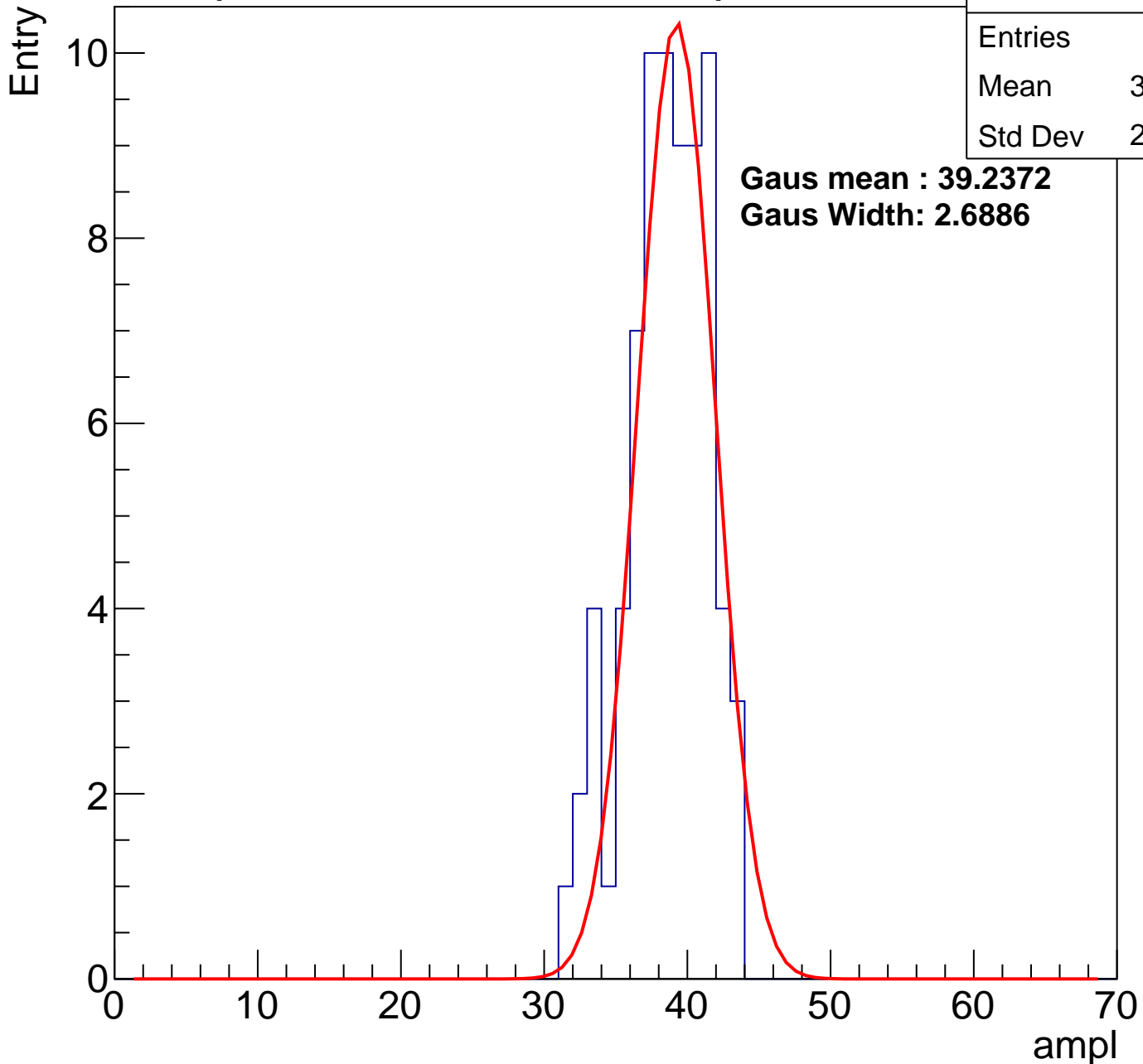


B0L001S, U23-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	38.12
Std Dev	2.809

Gaus mean : 39.2372
Gaus Width: 2.6886



B0L001S, U23-ch12, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	46.05
Std Dev	2.784

Gaus mean : 46.6469

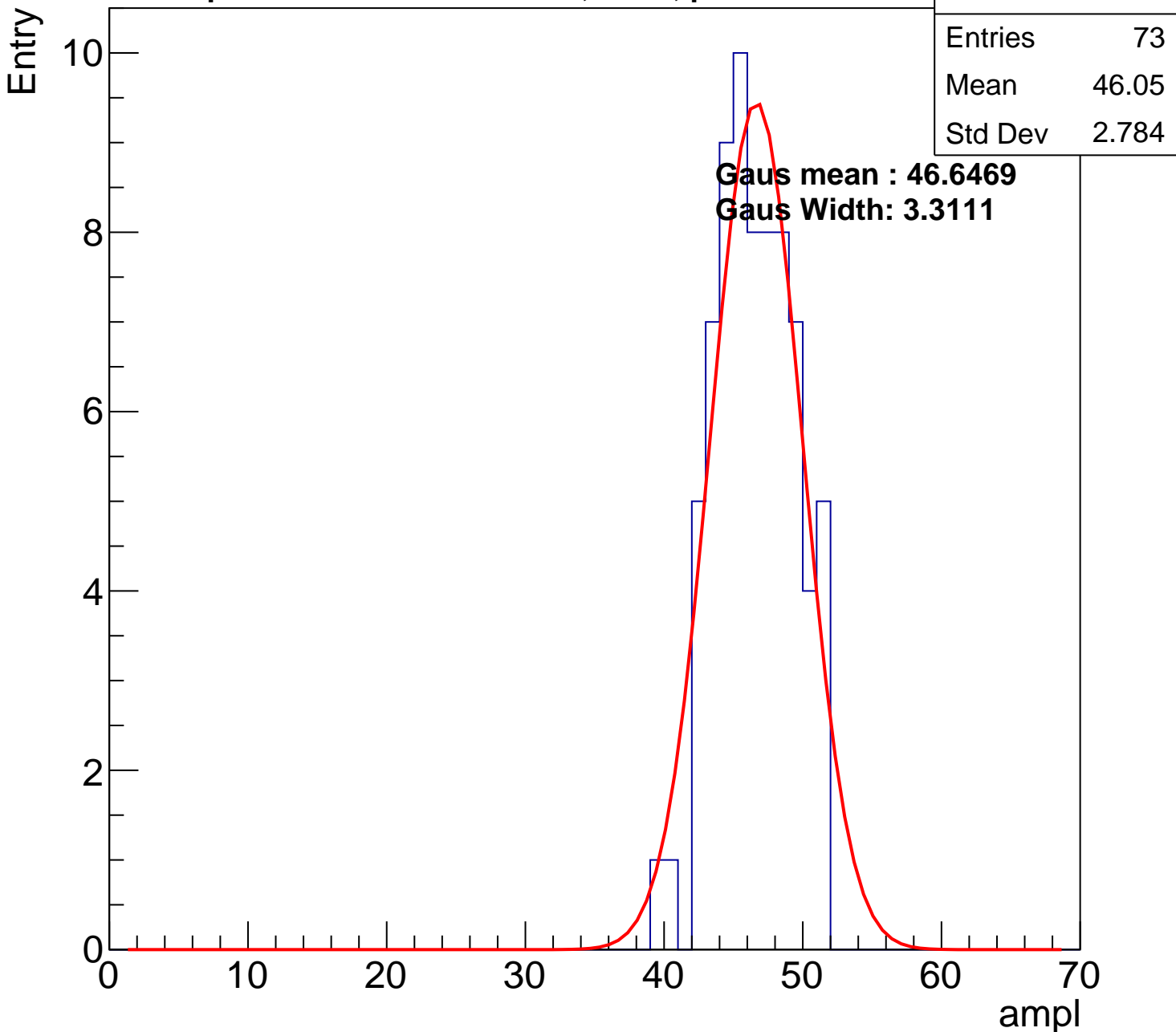
Gaus Width: 3.3111

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

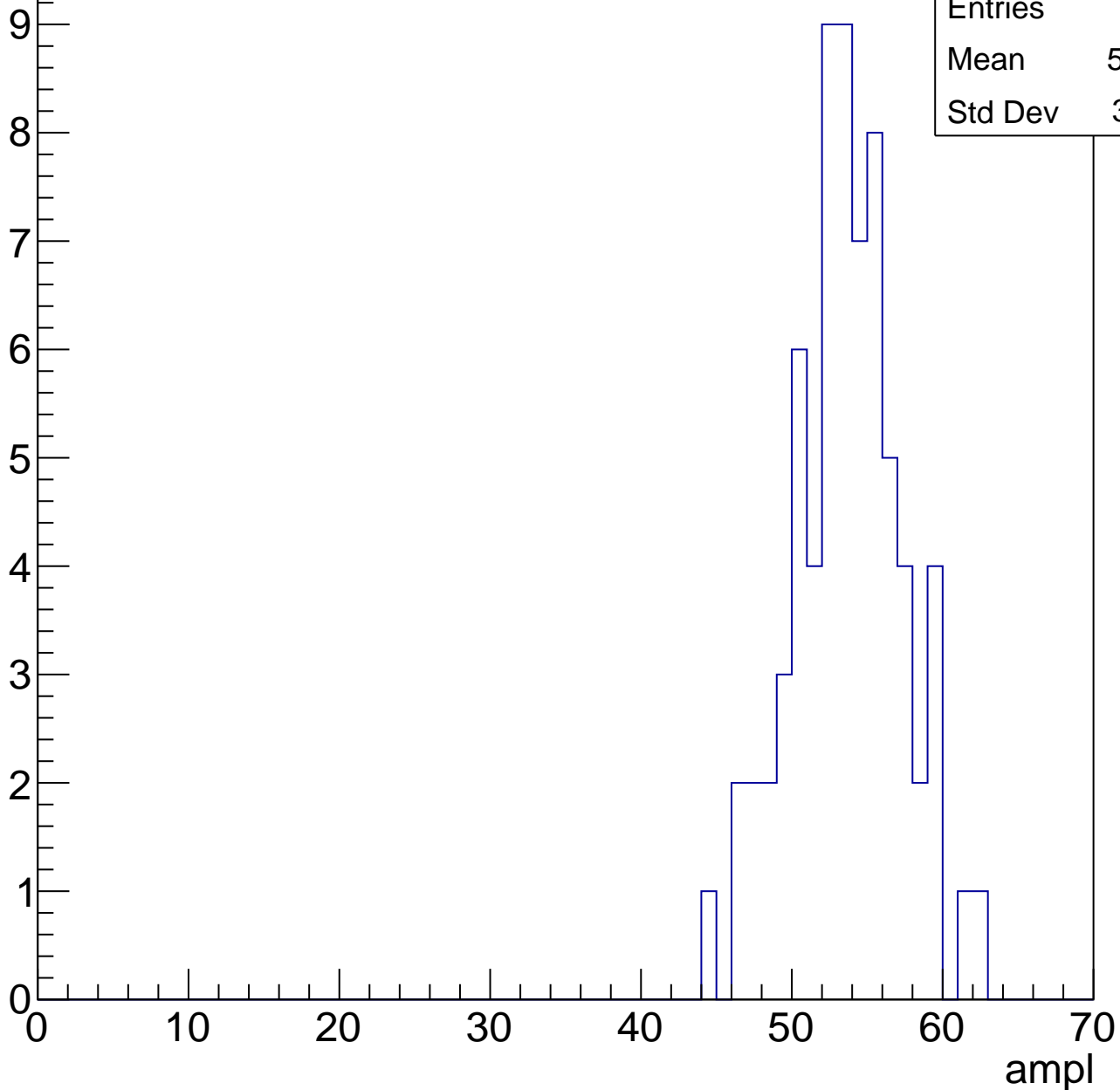


B0L001S, U23-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	53.19
Std Dev	3.611

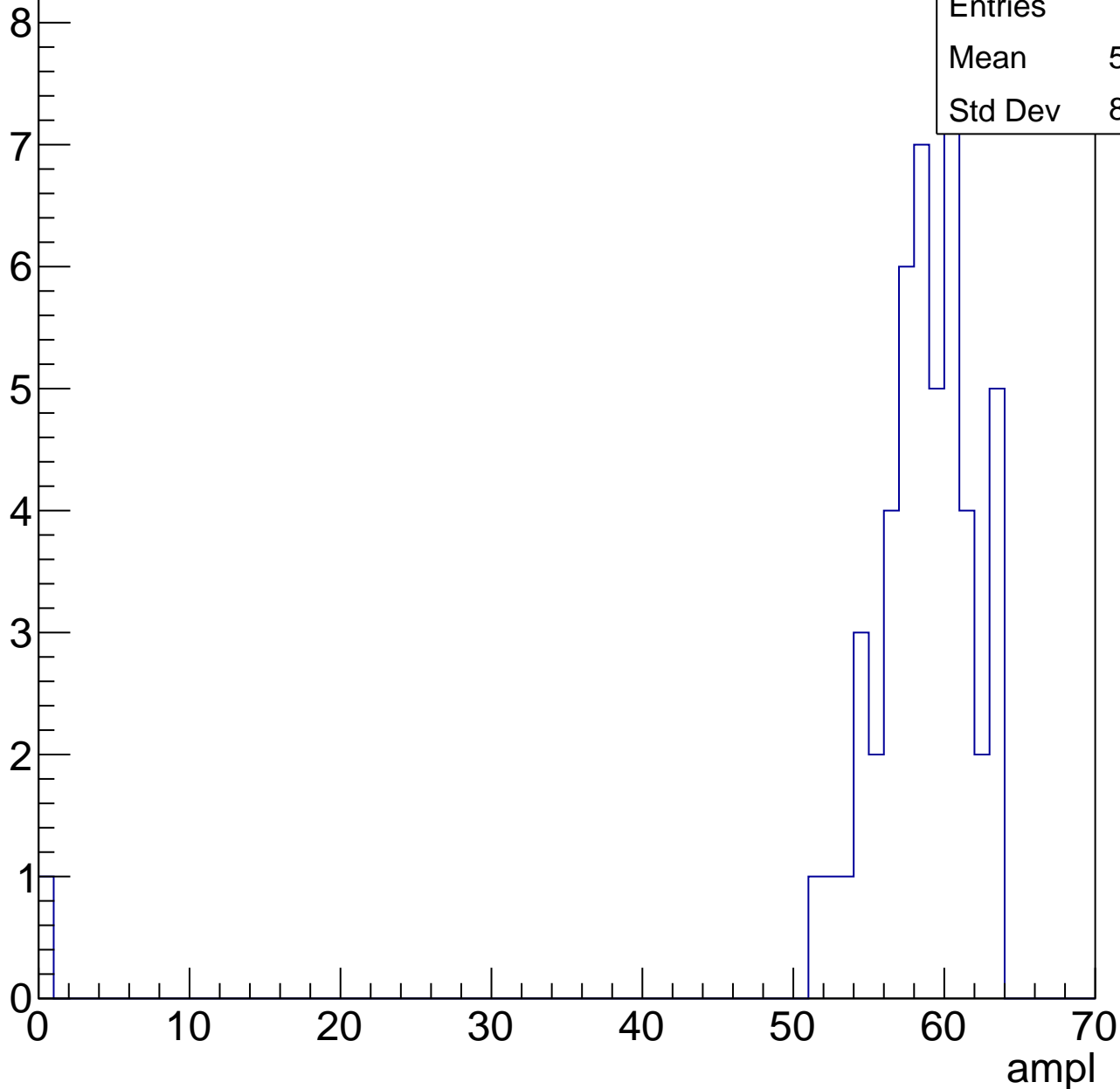


B0L001S, U23-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.16
Std Dev	8.668

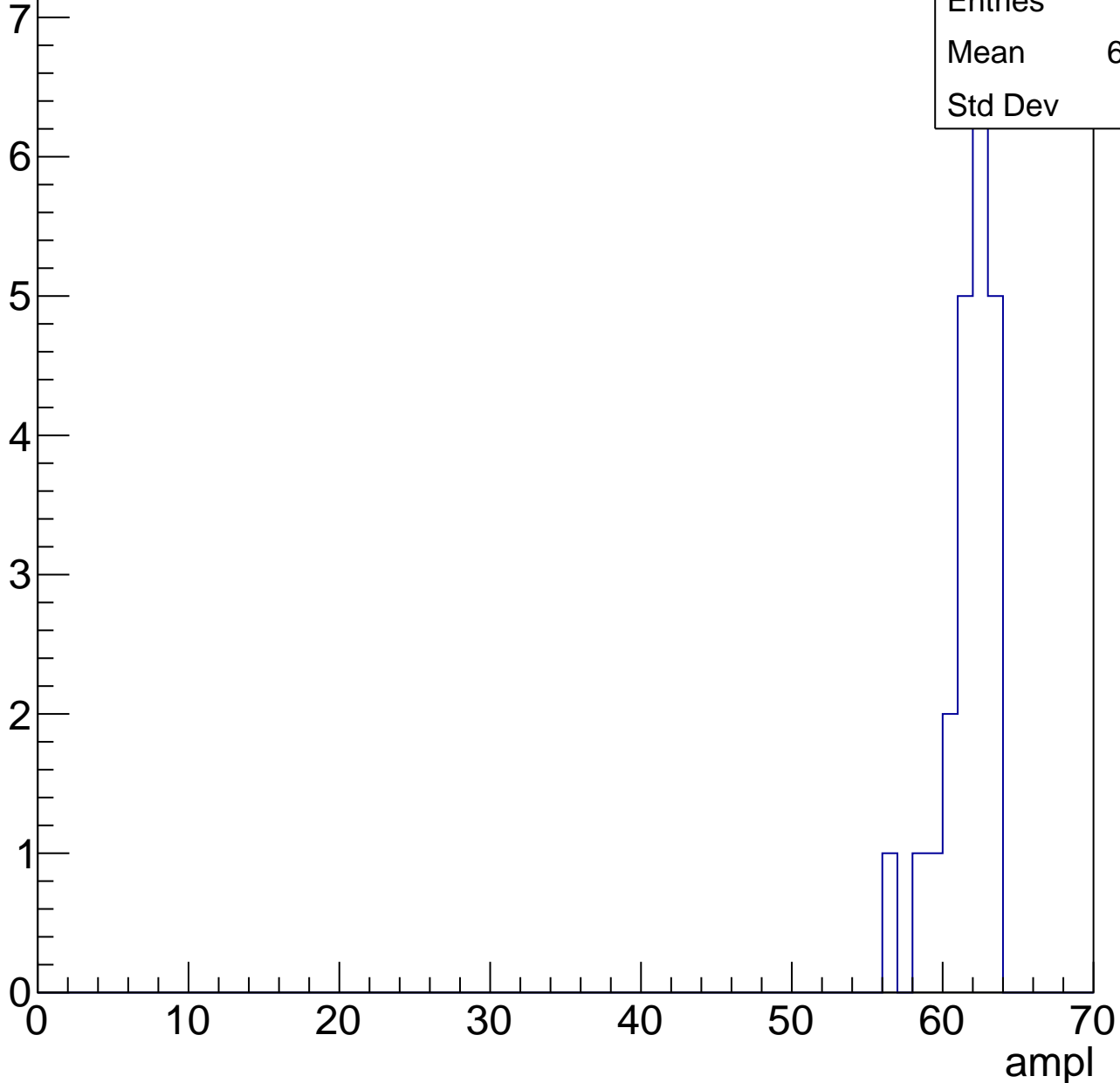


B0L001S, U23-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	61.23
Std Dev	1.73



B0L001S, U23-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch13, adc0

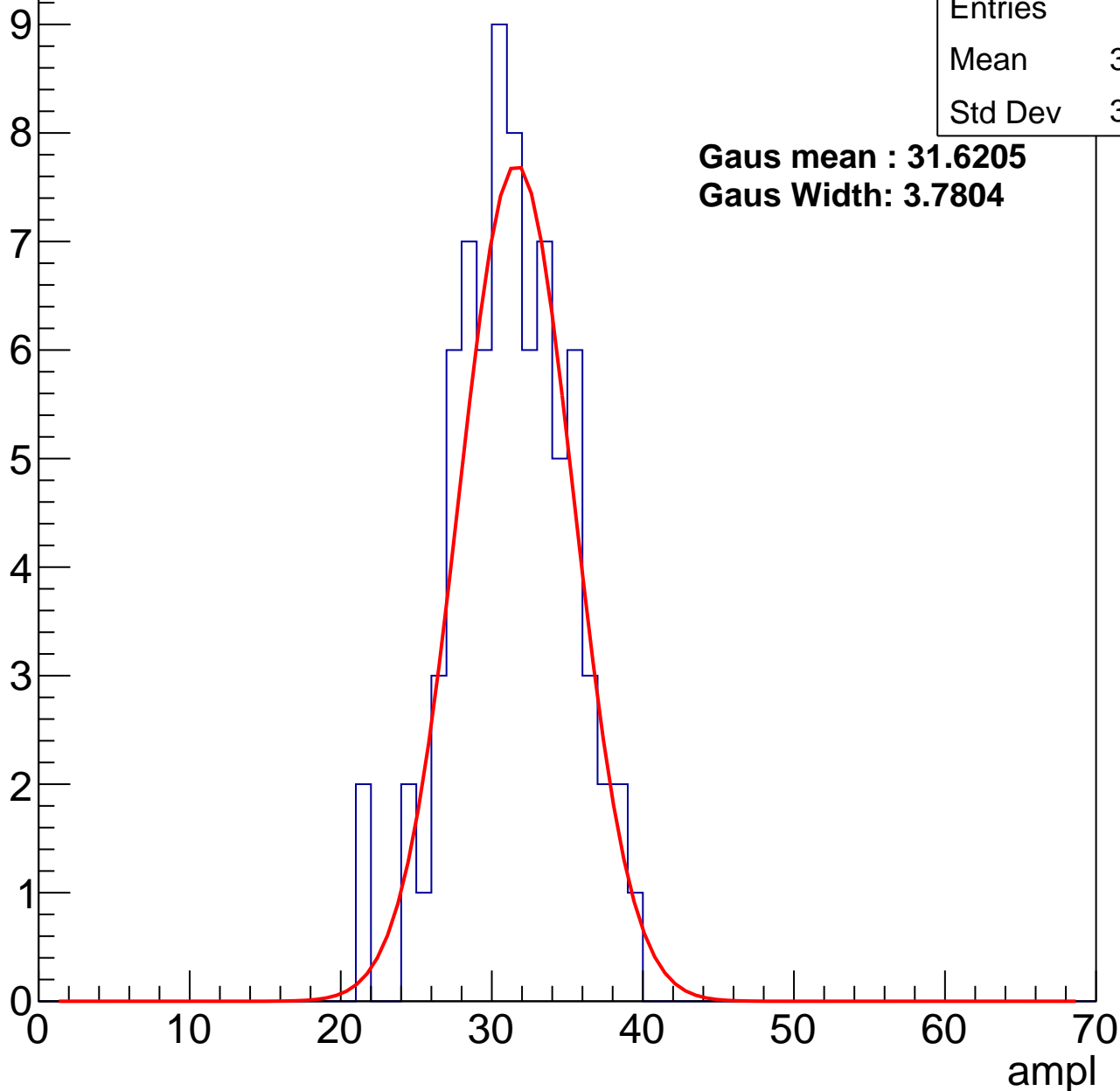
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.83
Std Dev	3.775

Gaus mean : 31.6205

Gaus Width: 3.7804



B0L001S, U23-ch13, adc1

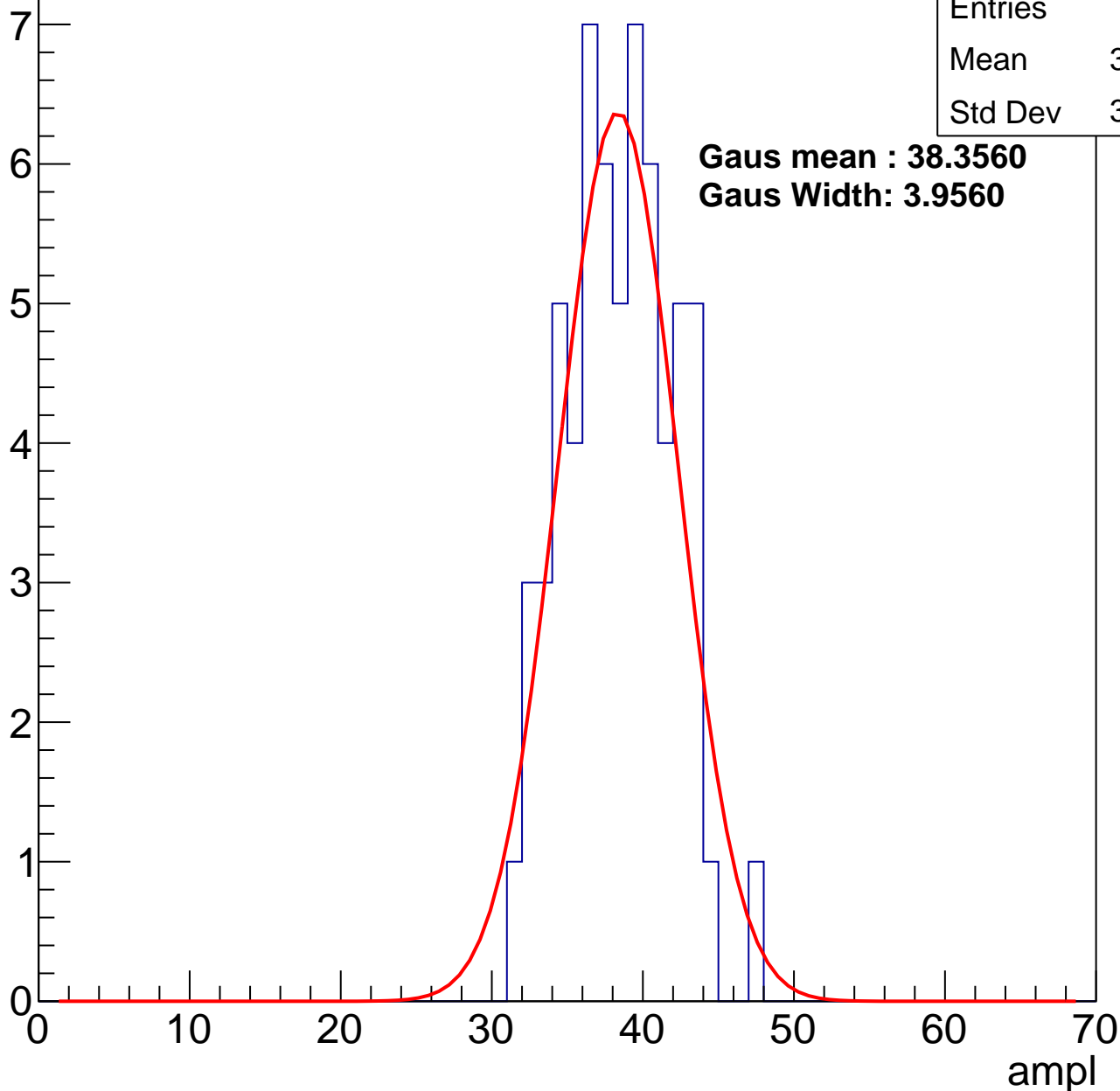
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.98
Std Dev	3.498

Gaus mean : 38.3560

Gaus Width: 3.9560



B0L001S, U23-ch13, adc2

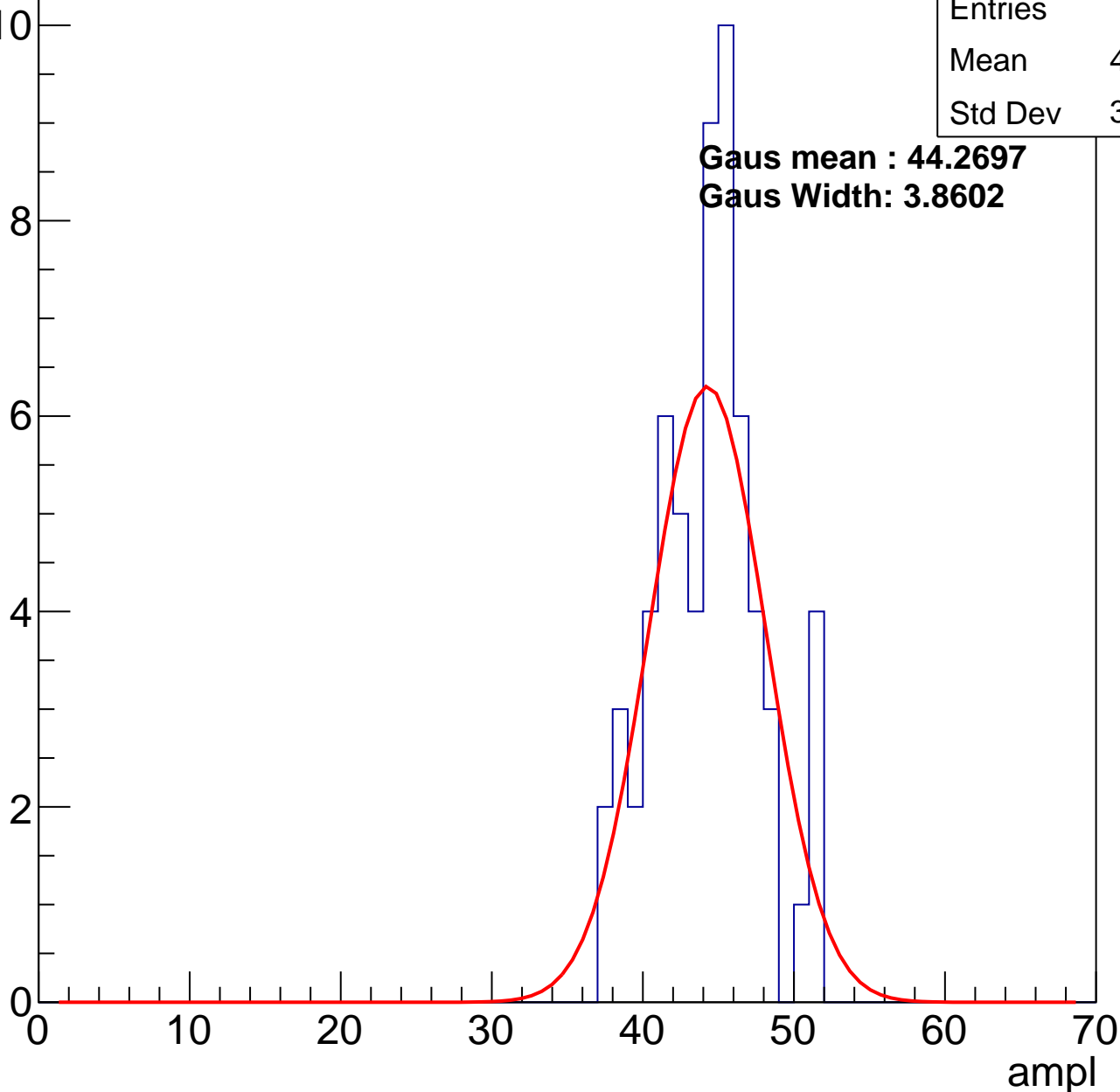
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.84
Std Dev	3.442

Gaus mean : 44.2697

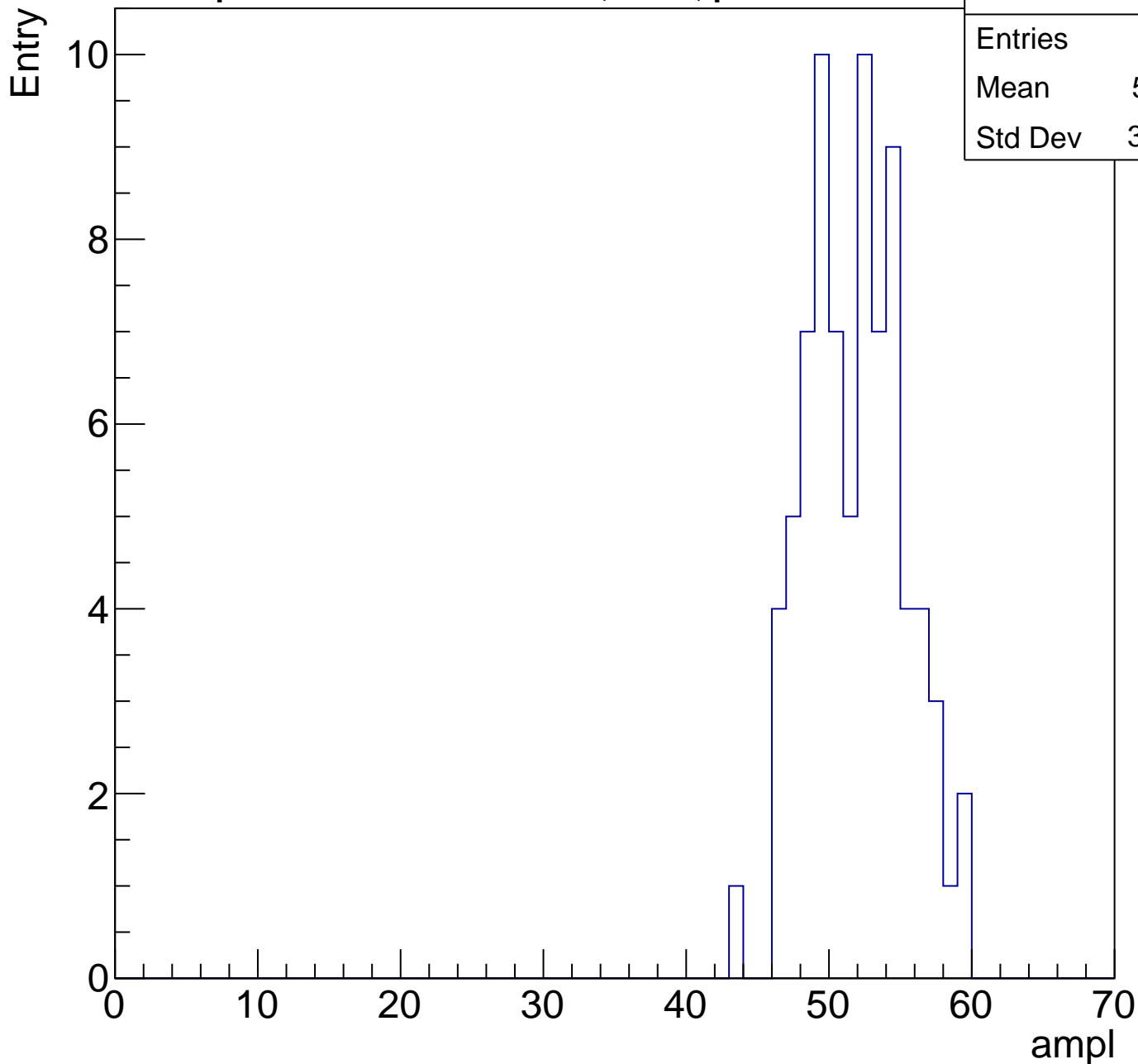
Gaus Width: 3.8602



B0L001S, U23-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	51.41
Std Dev	3.392

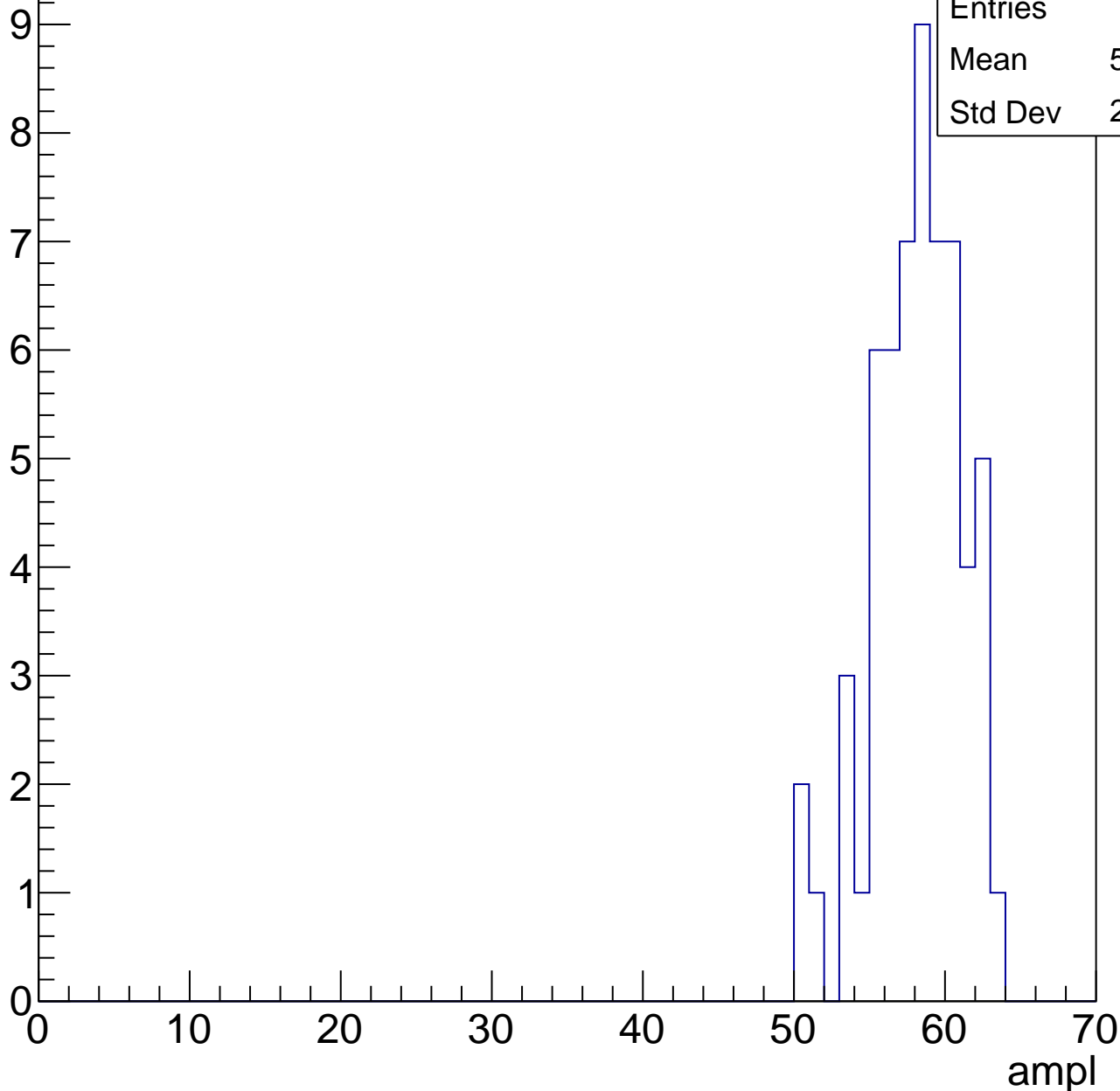


B0L001S, U23-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

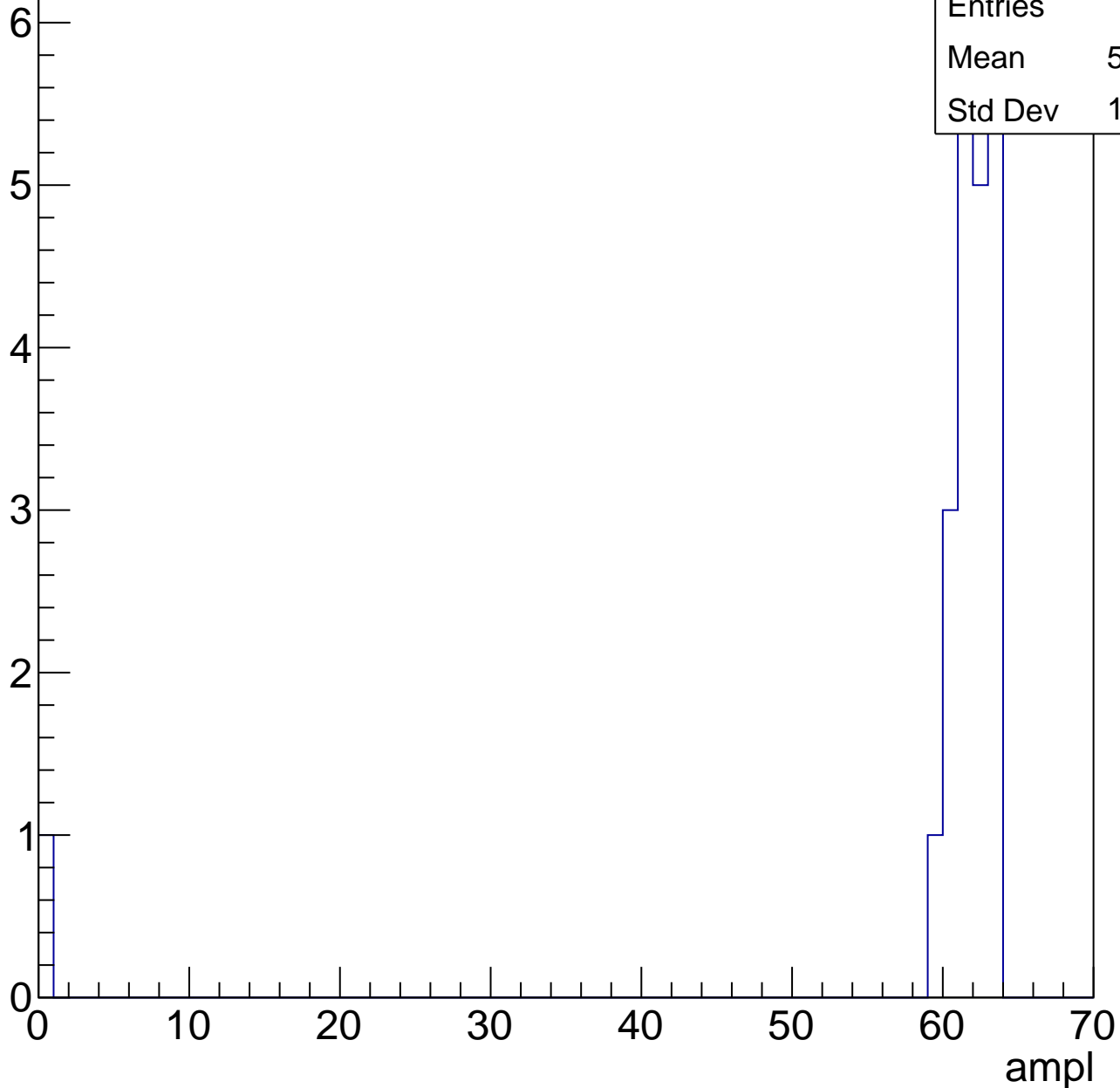
Entries	59
Mean	57.64
Std Dev	2.973



B0L001S, U23-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch14, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.11
Std Dev	6.003

Gaus mean : 31.8956

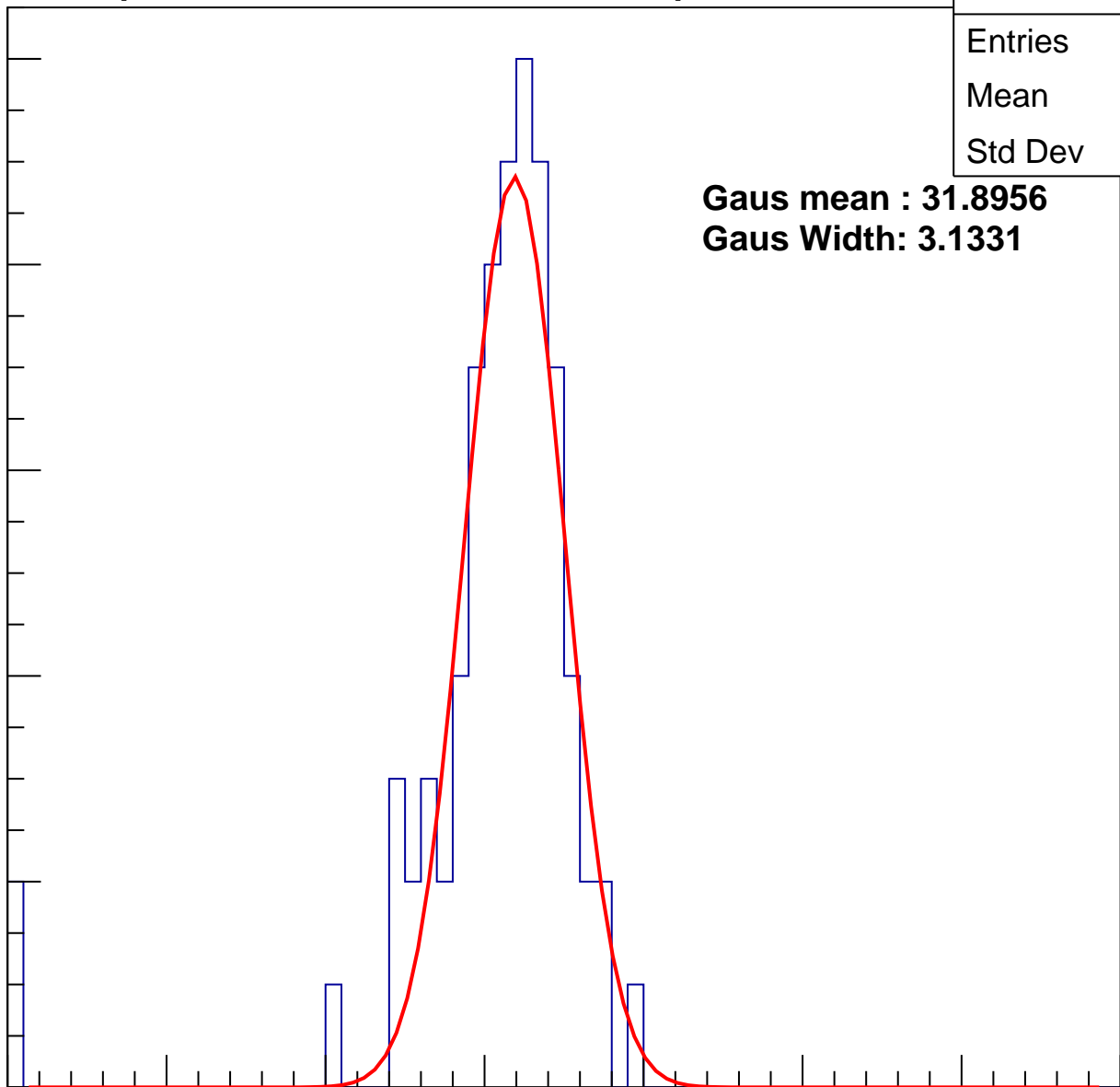
Gaus Width: 3.1331

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch14, adc1

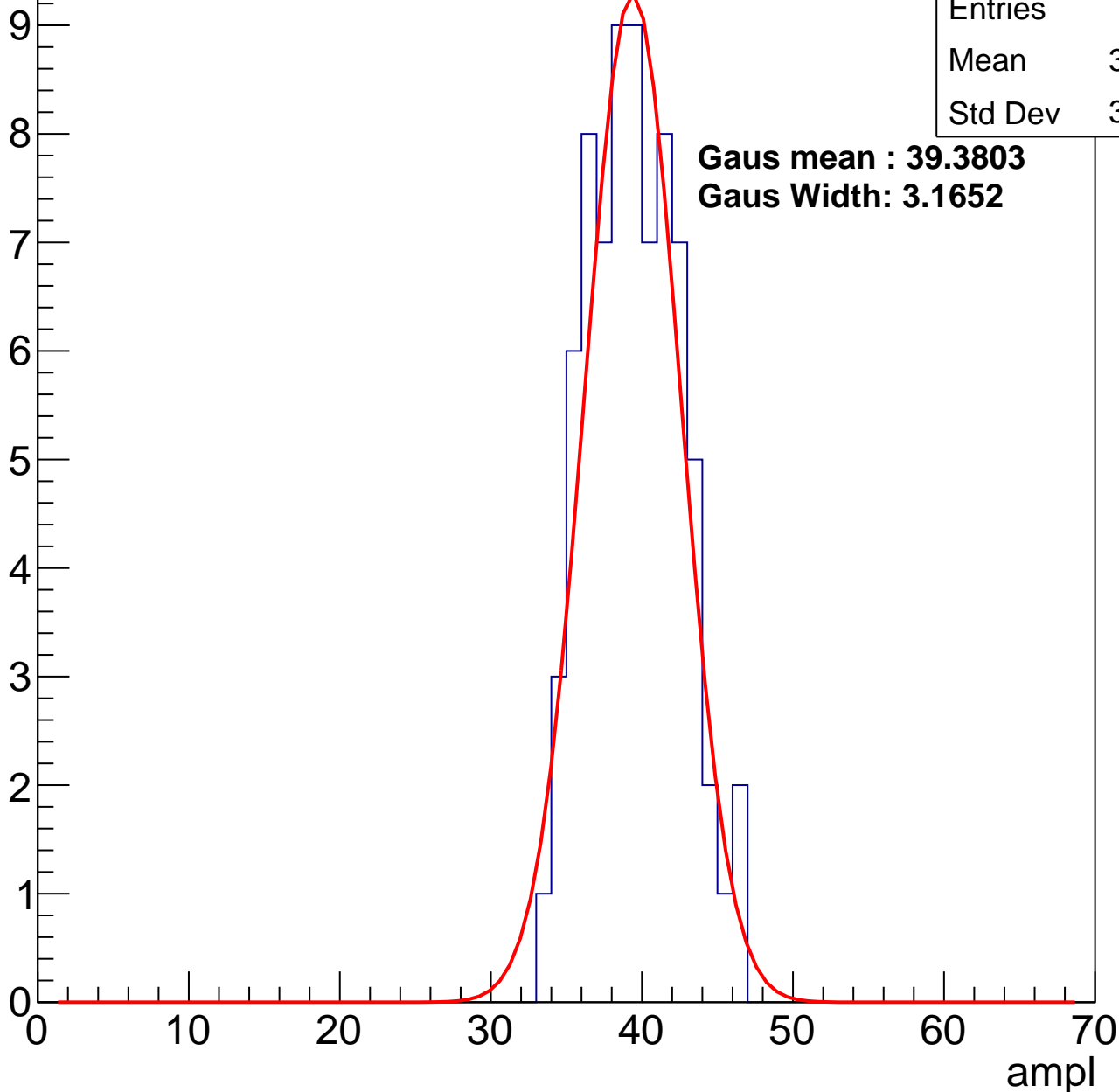
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	39.03
Std Dev	3.015

Gaus mean : 39.3803

Gaus Width: 3.1652



B0L001S, U23-ch14, adc2

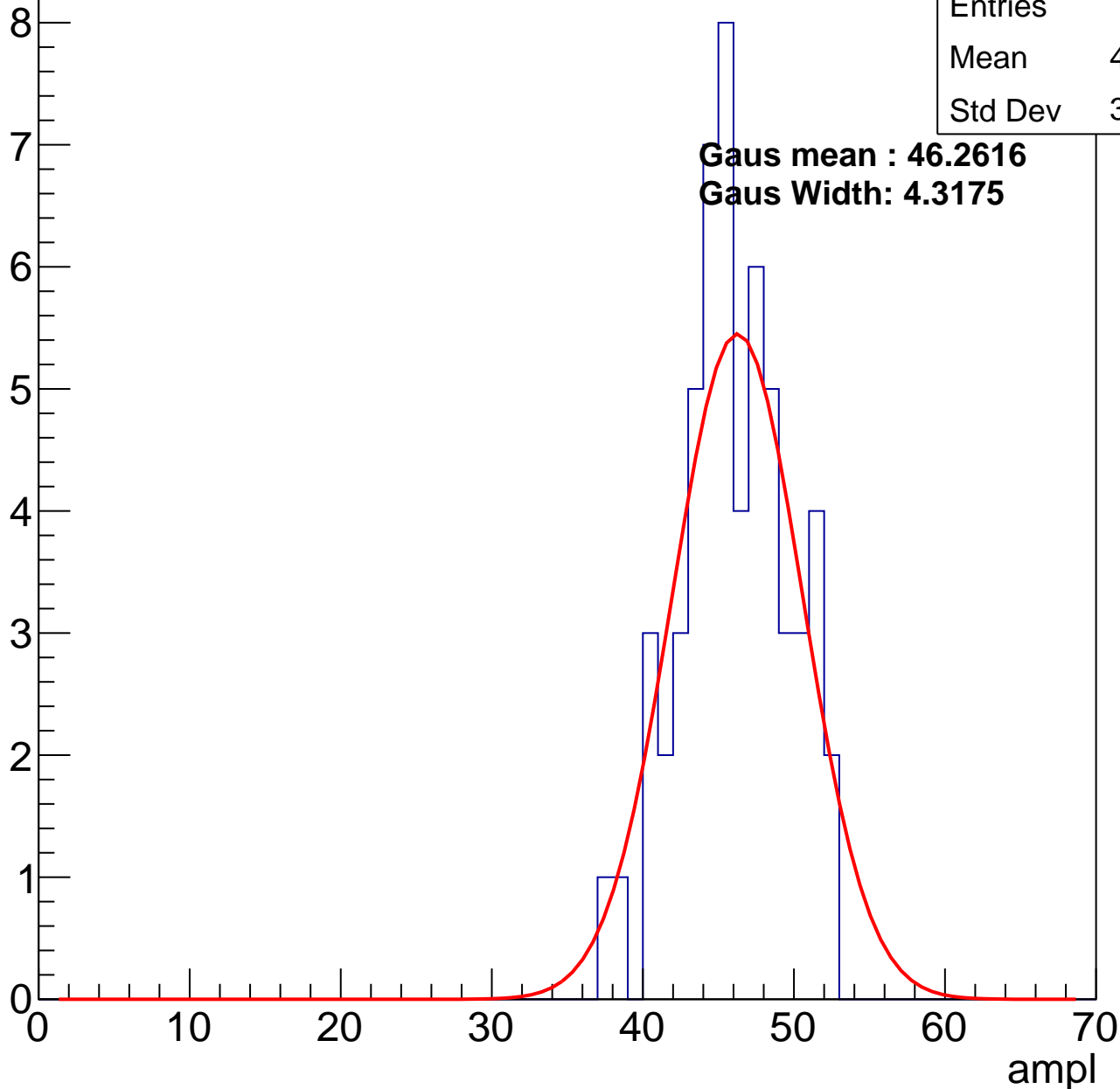
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.56
Std Dev	3.489

Gaus mean : 46.2616

Gaus Width: 4.3175

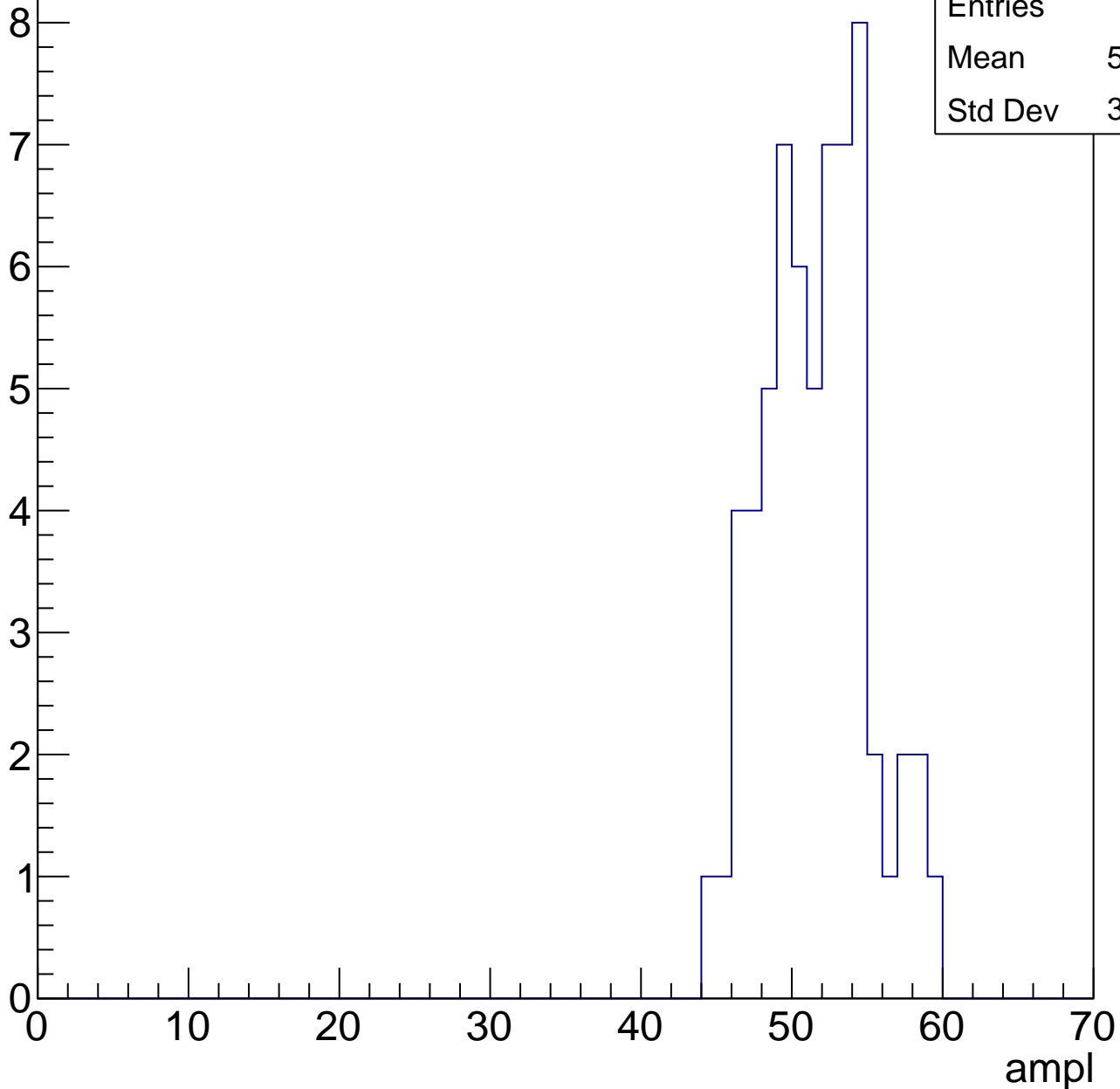


B0L001S, U23-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	51.13
Std Dev	3.392



B0L001S, U23-ch14, adc4

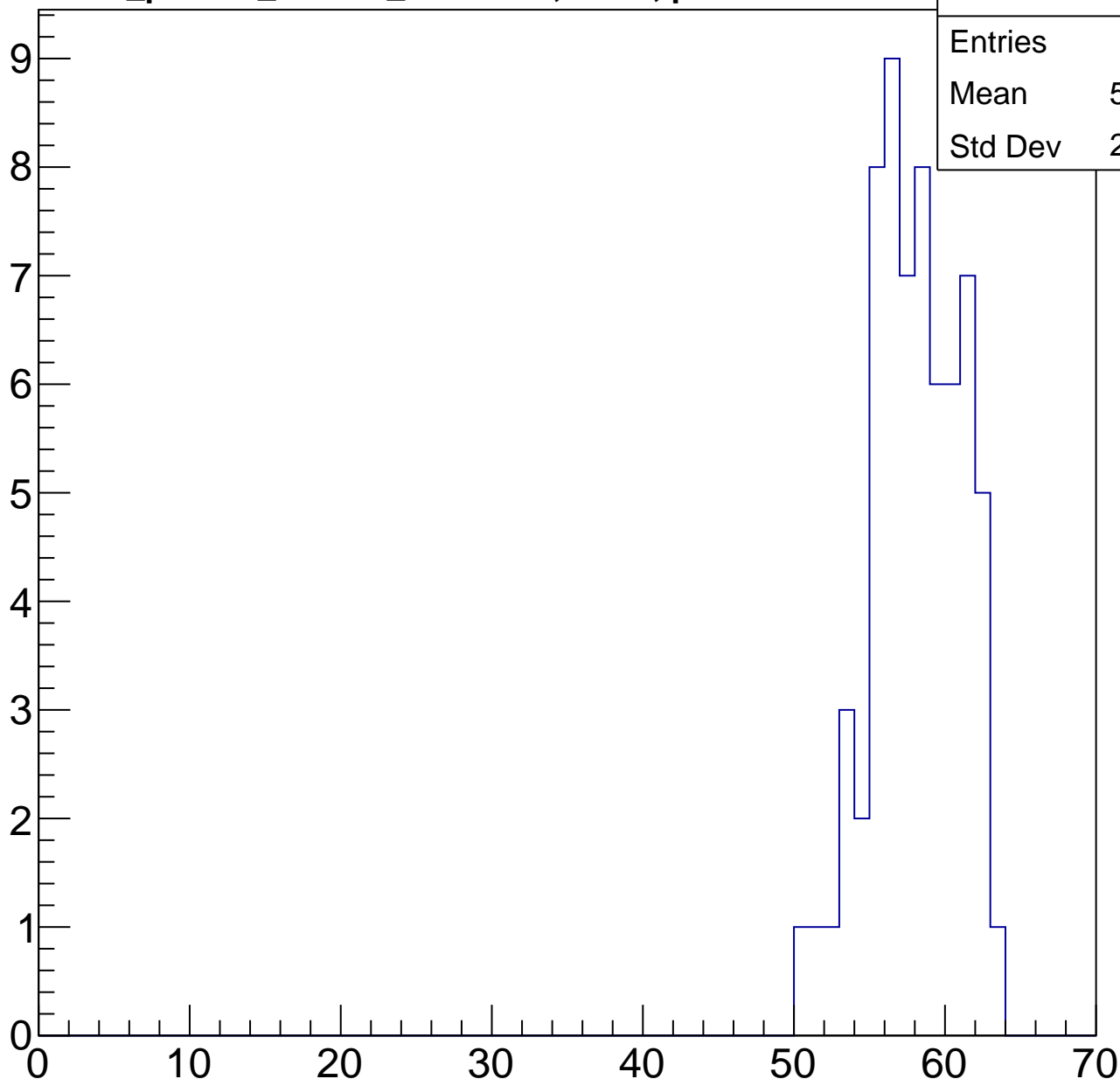
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	57.55
Std Dev	2.925

ampl

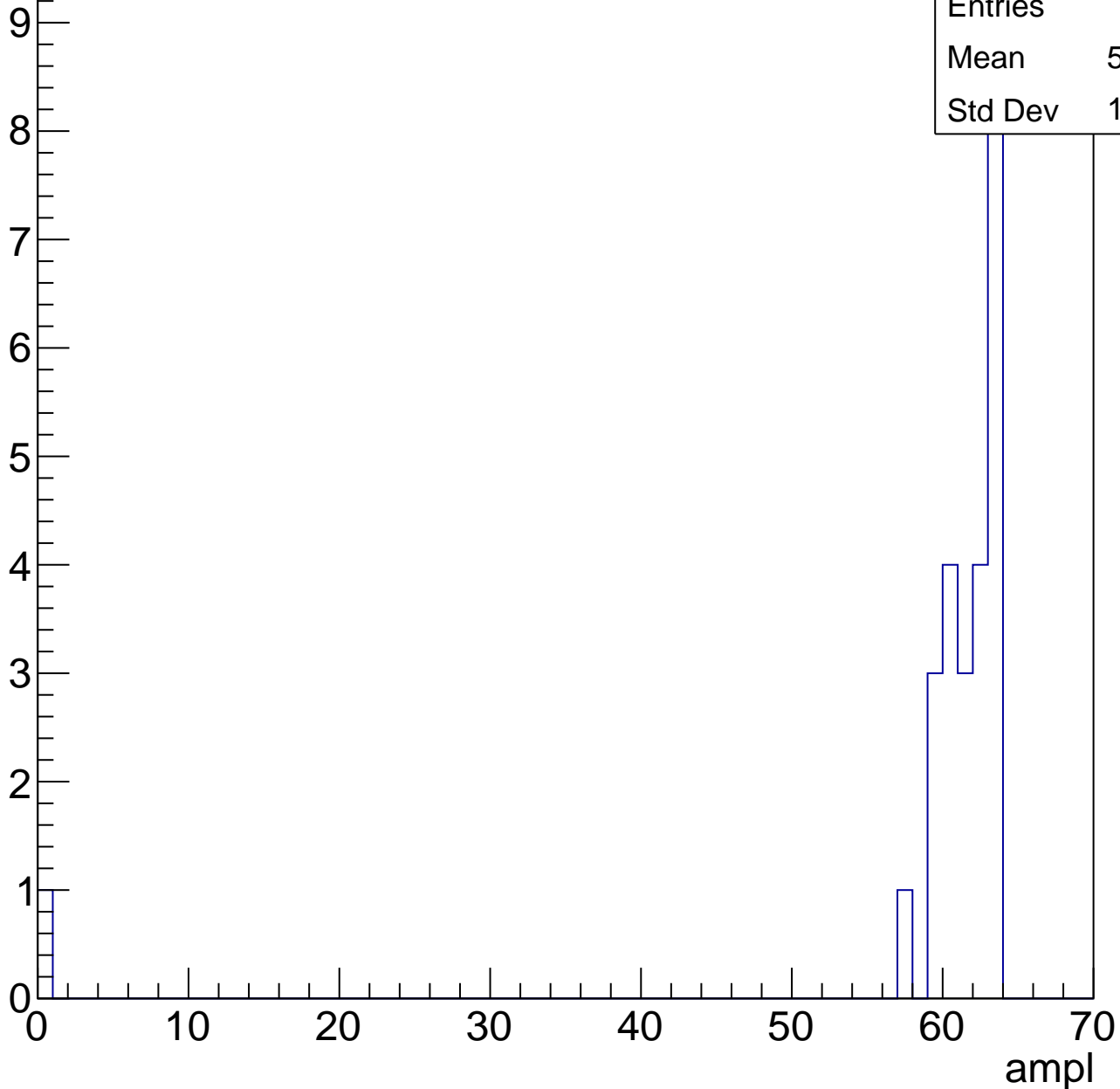


B0L001S, U23-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.88
Std Dev	12.13



B0L001S, U23-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch15, adc0

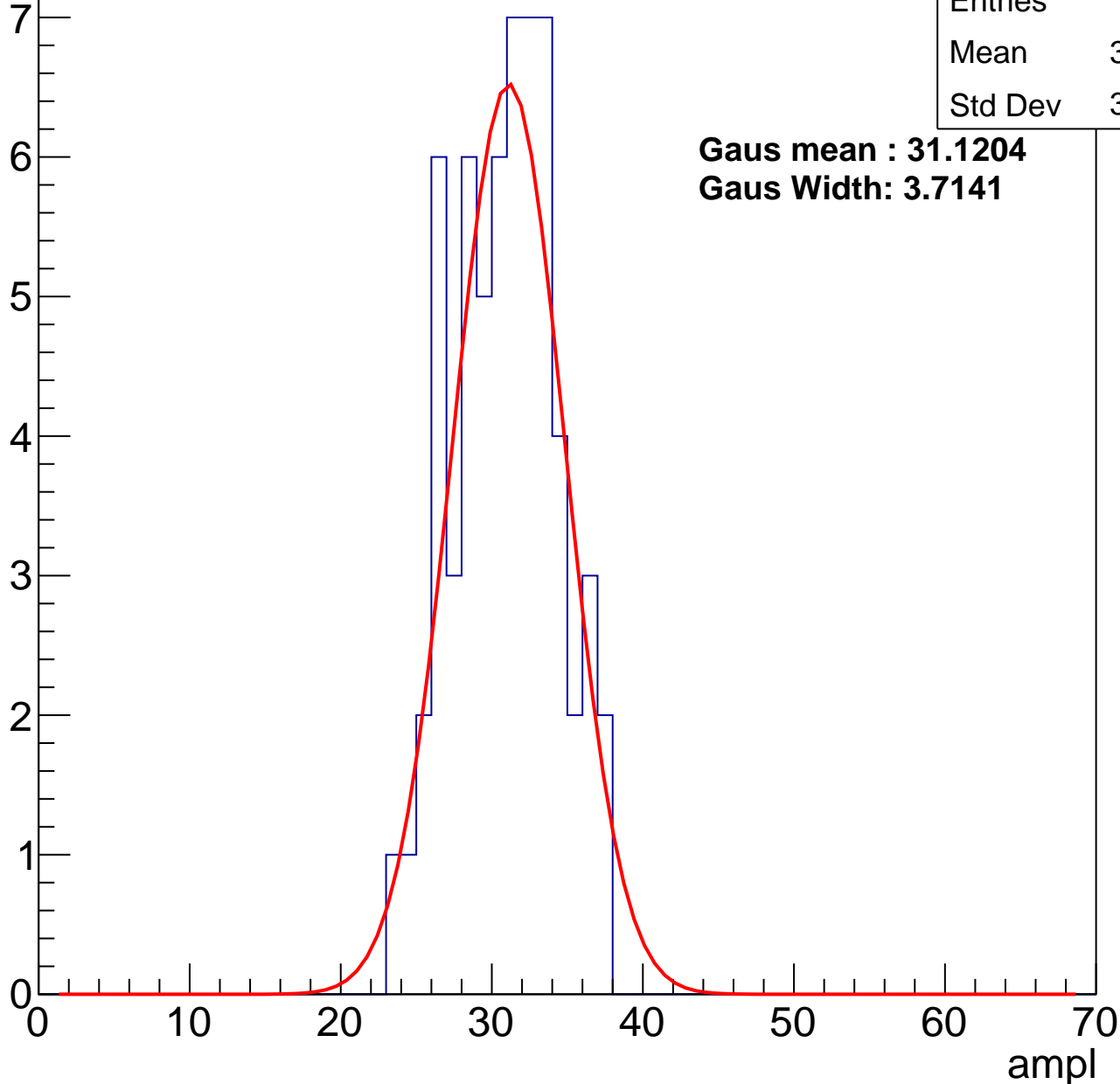
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.44
Std Dev	3.344

Gaus mean : 31.1204

Gaus Width: 3.7141



B0L001S, U23-ch15, adc1

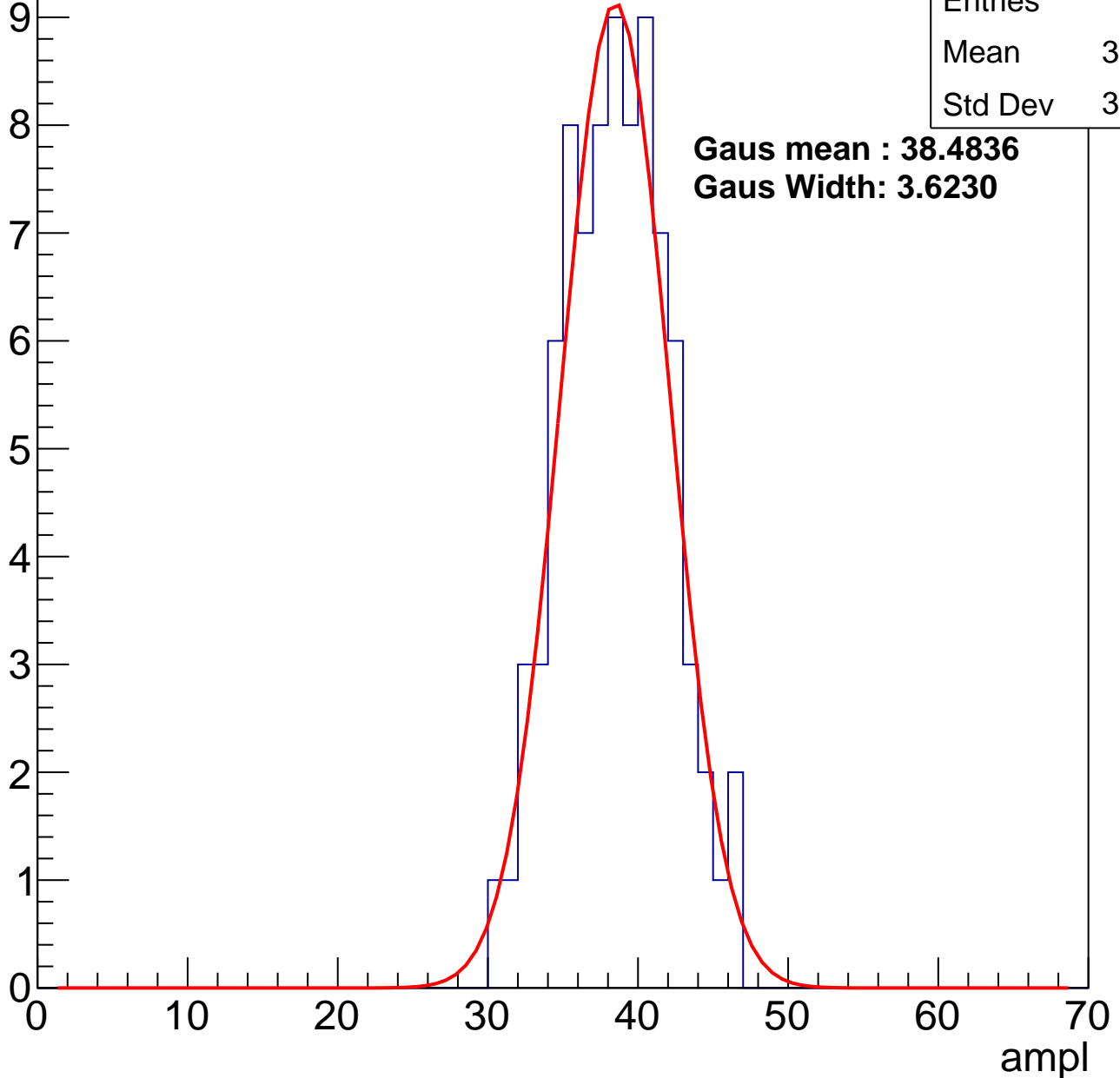
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	38.04
Std Dev	3.496

Gaus mean : 38.4836

Gaus Width: 3.6230



B0L001S, U23-ch15, adc2

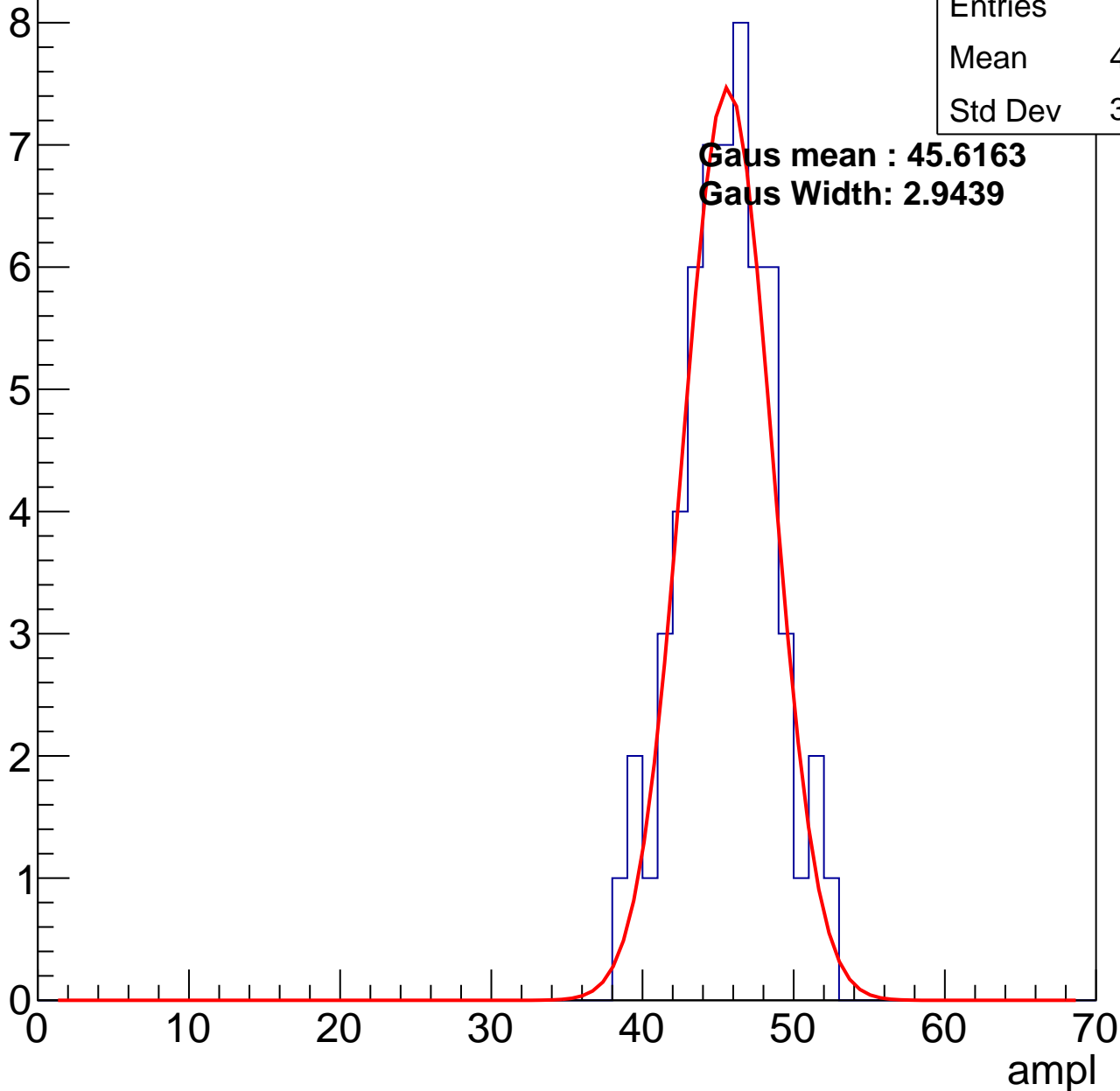
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.12
Std Dev	3.052

Gaus mean : 45.6163

Gaus Width: 2.9439

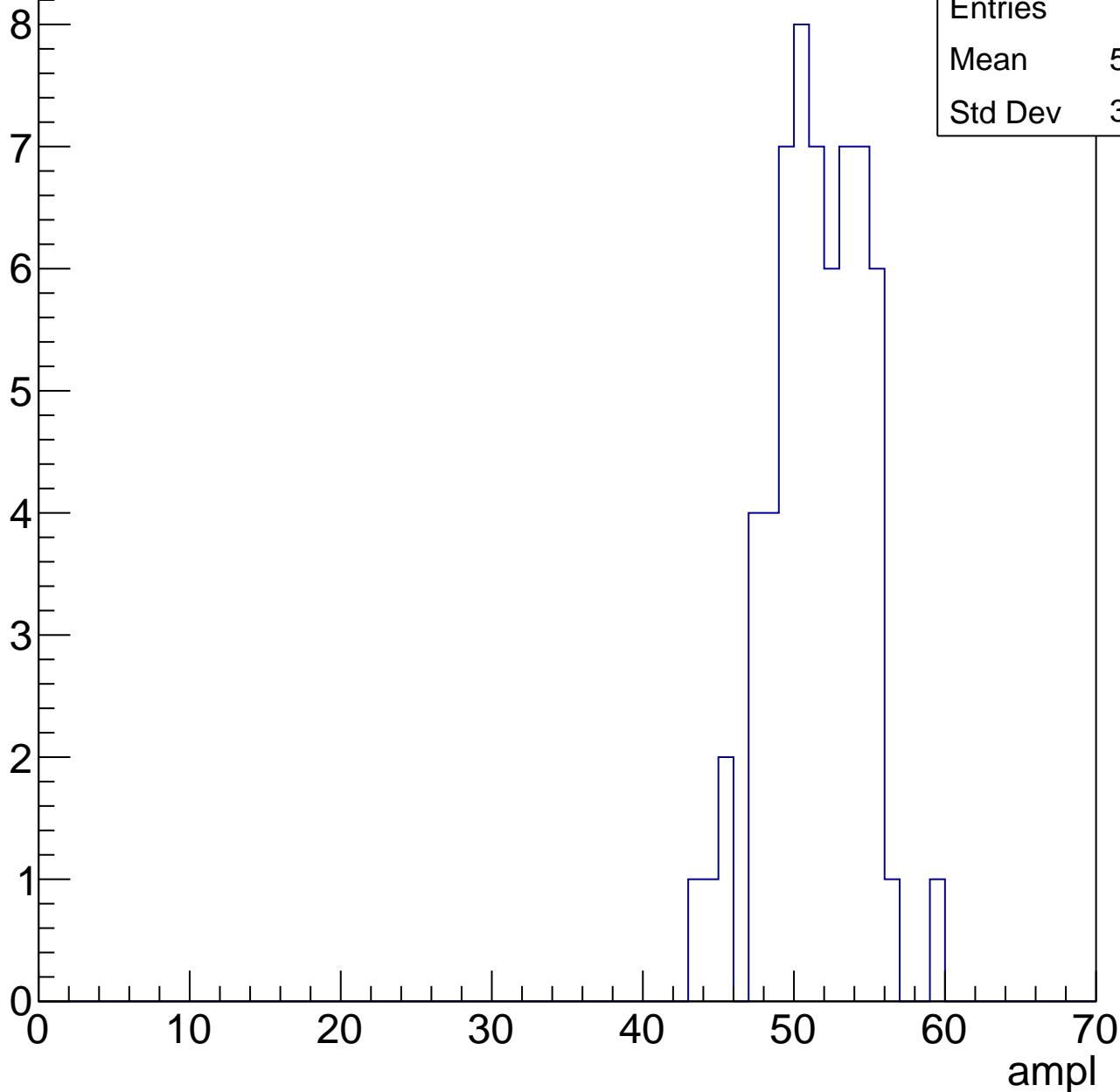


B0L001S, U23-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.02
Std Dev	3.119

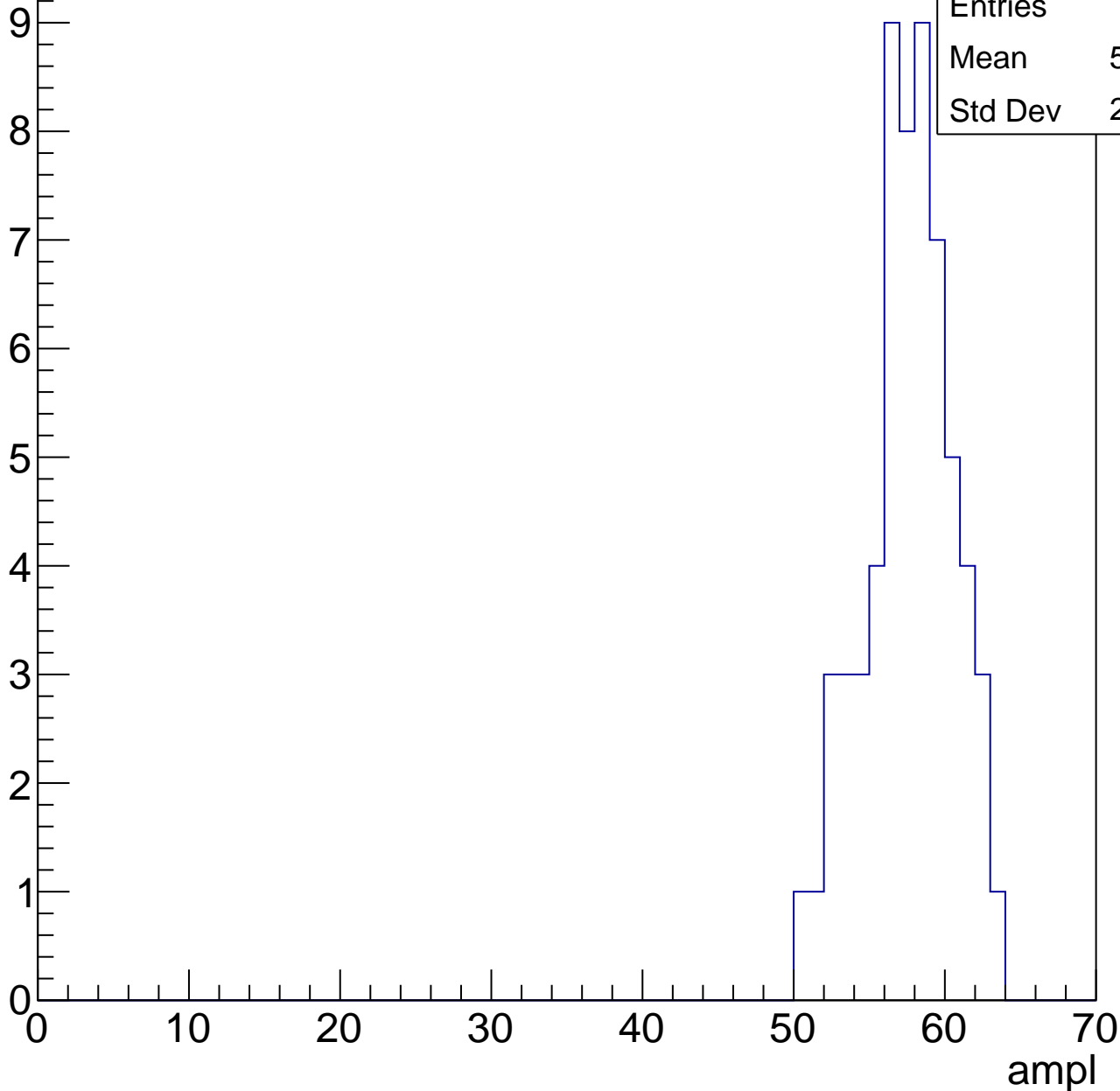


B0L001S, U23-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	57.15
Std Dev	2.908

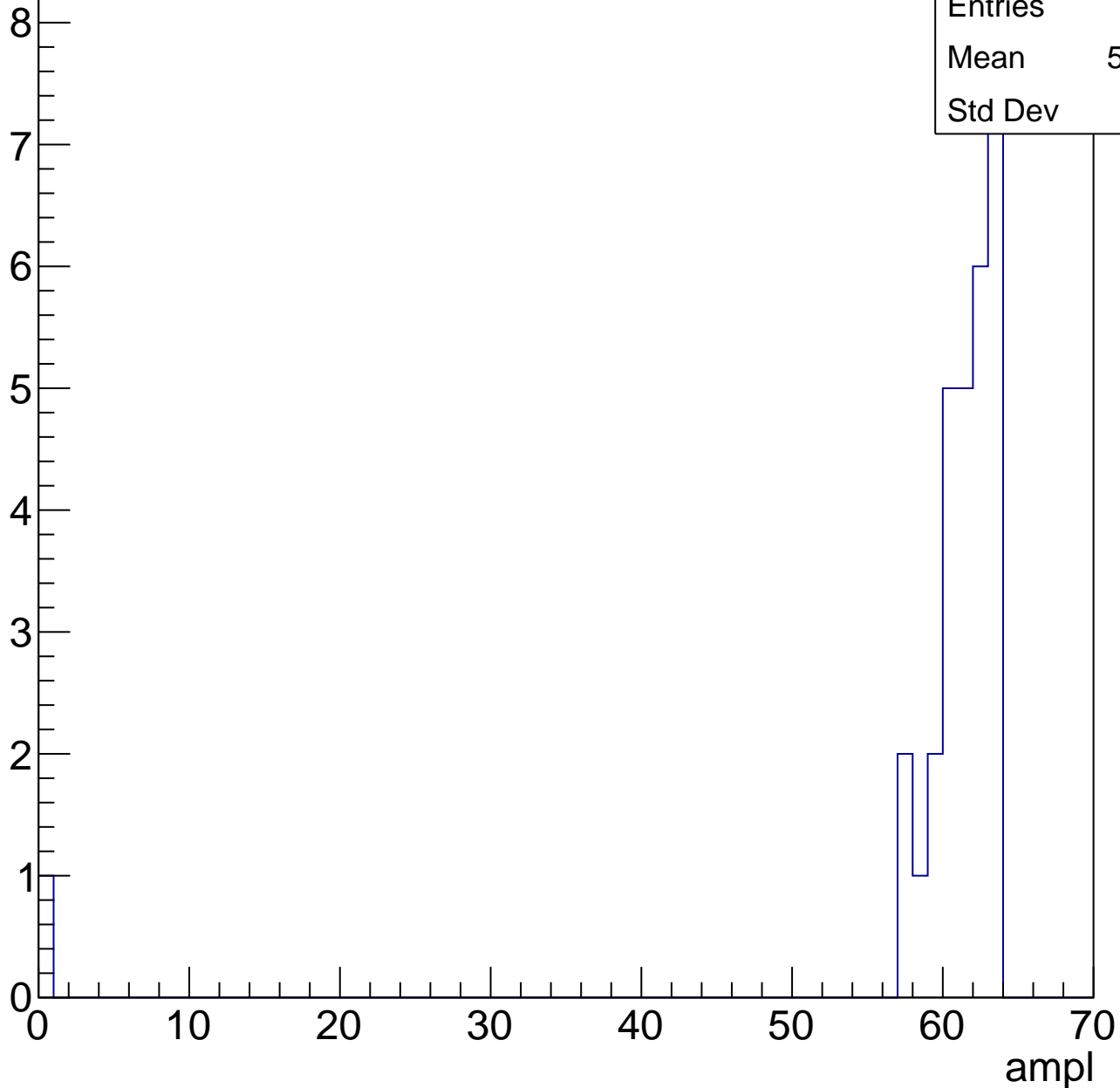


B0L001S, U23-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.03
Std Dev	11.1



B0L001S, U23-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch16, adc0

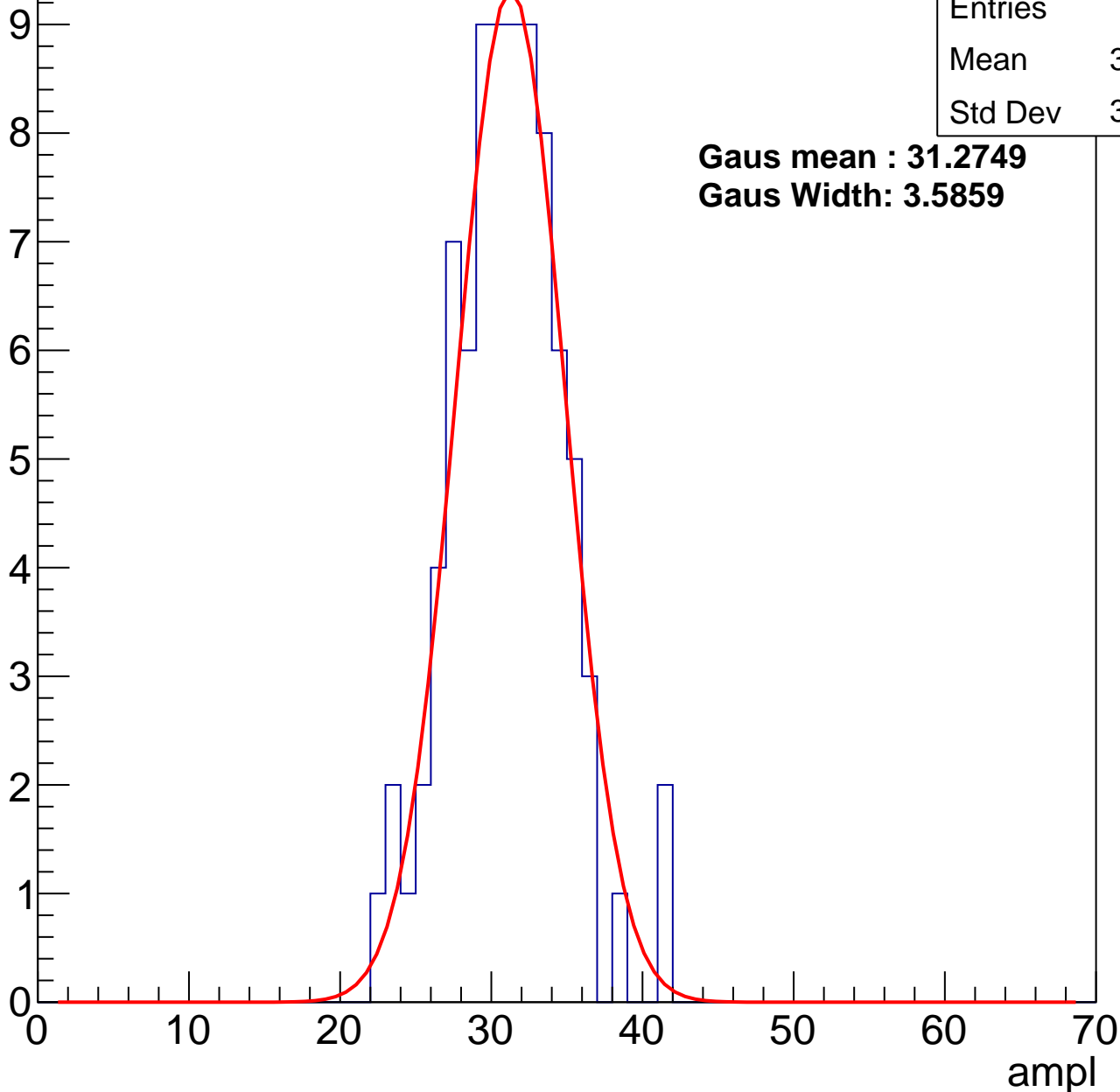
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	30.62
Std Dev	3.664

Gaus mean : 31.2749

Gaus Width: 3.5859



B0L001S, U23-ch16, adc1

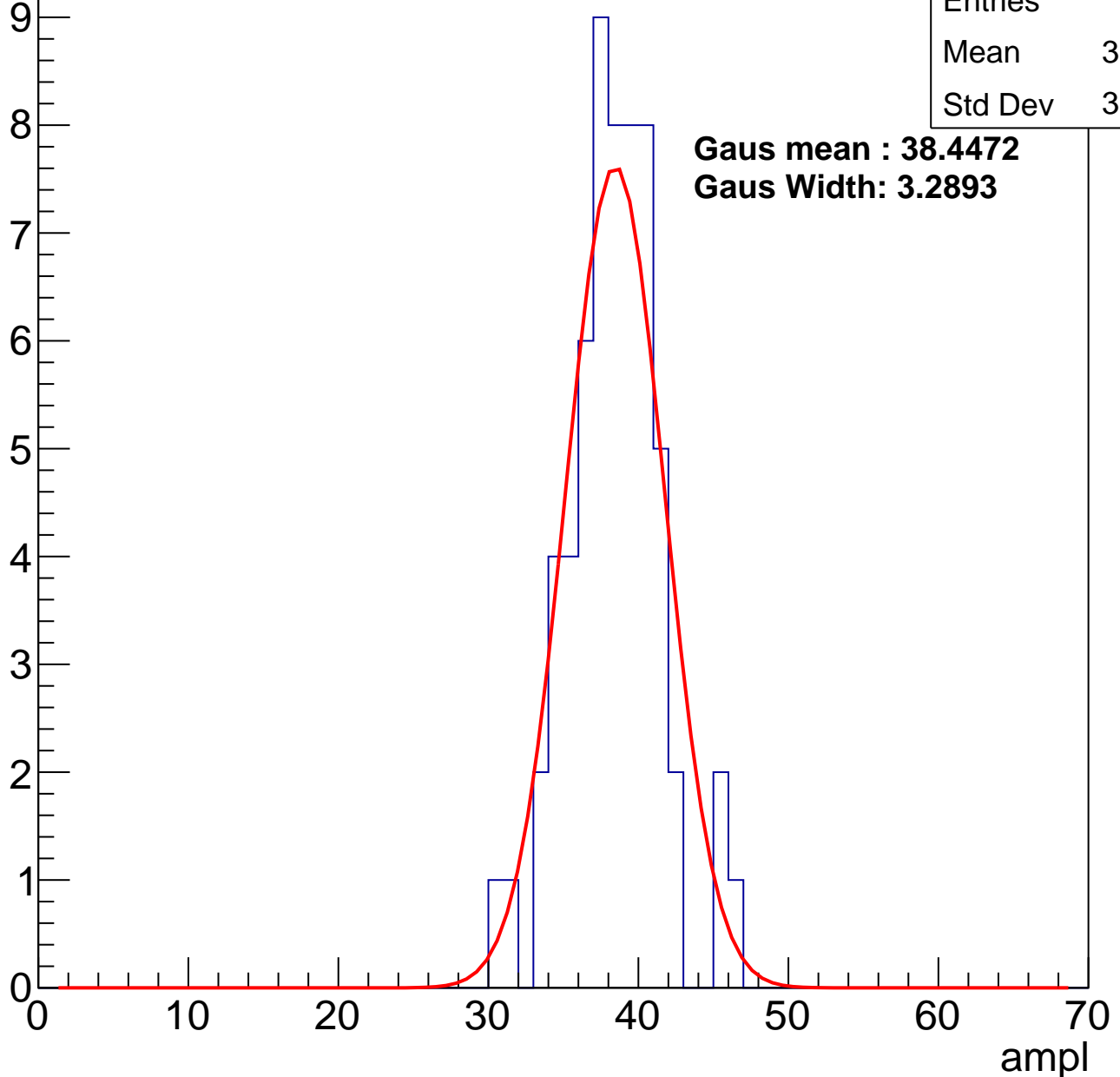
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.92
Std Dev	3.069

Gaus mean : 38.4472

Gaus Width: 3.2893



B0L001S, U23-ch16, adc2

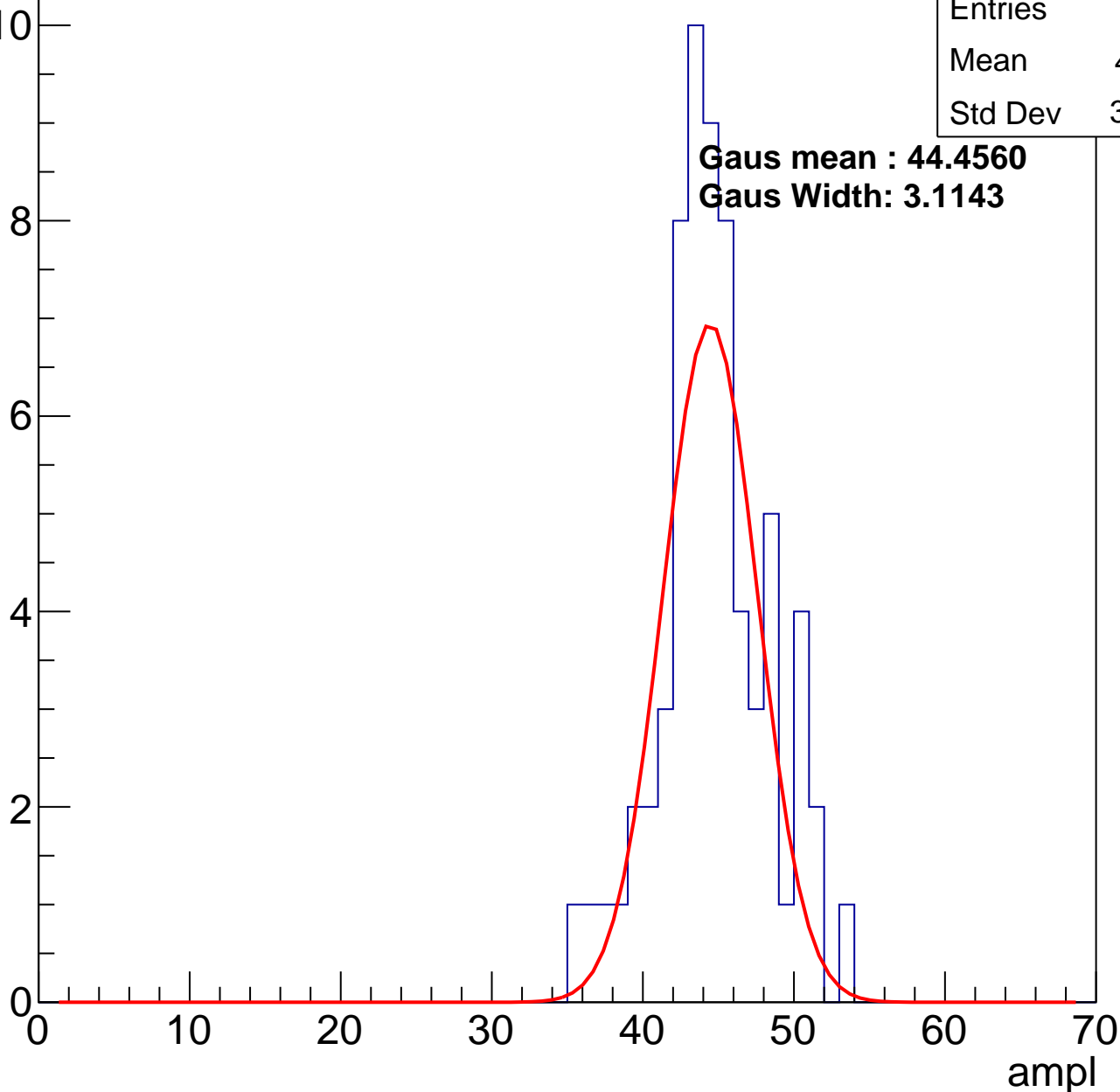
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.21
Std Dev	3.604

Gaus mean : 44.4560

Gaus Width: 3.1143

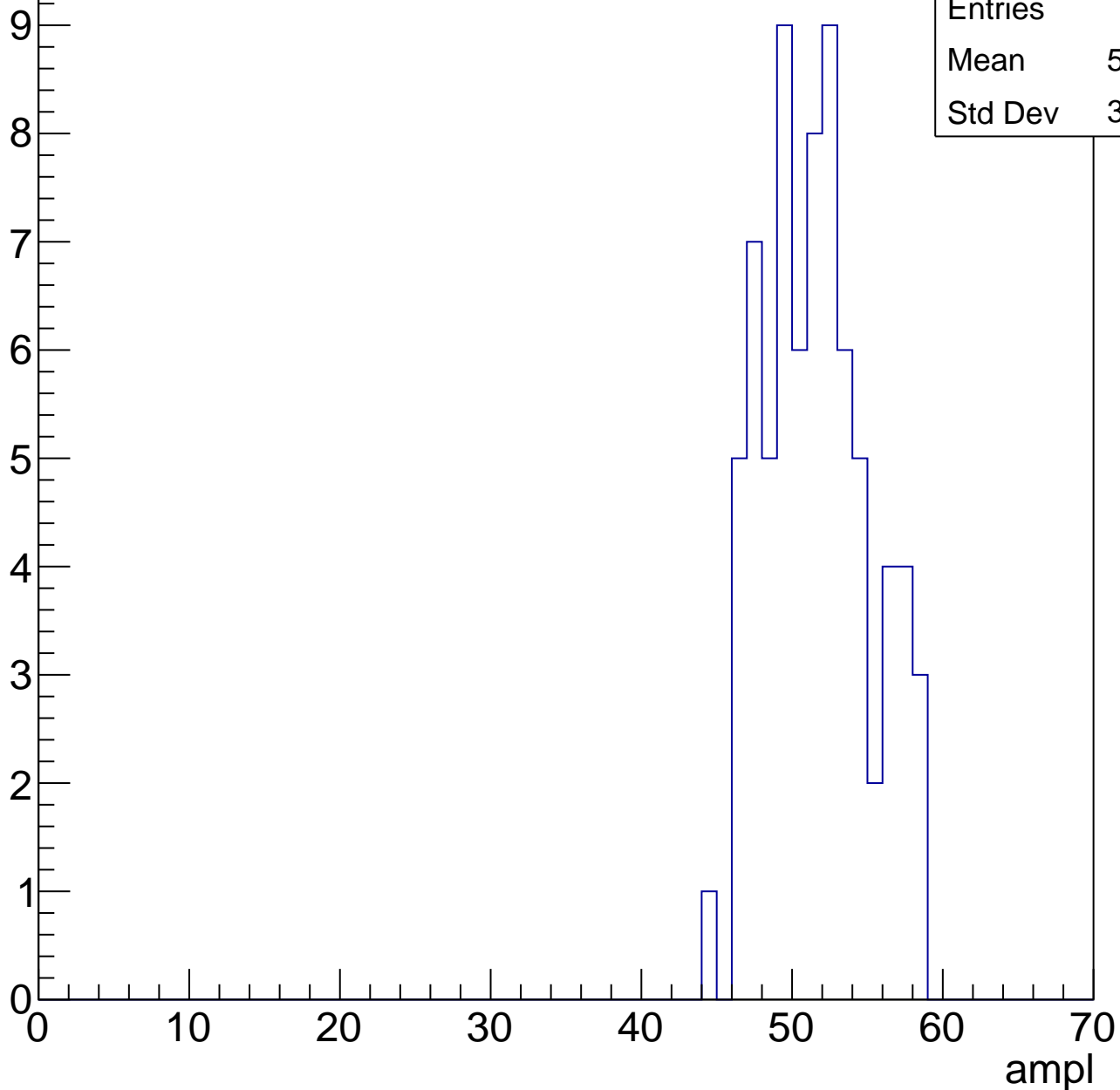


B0L001S, U23-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	51.14
Std Dev	3.434

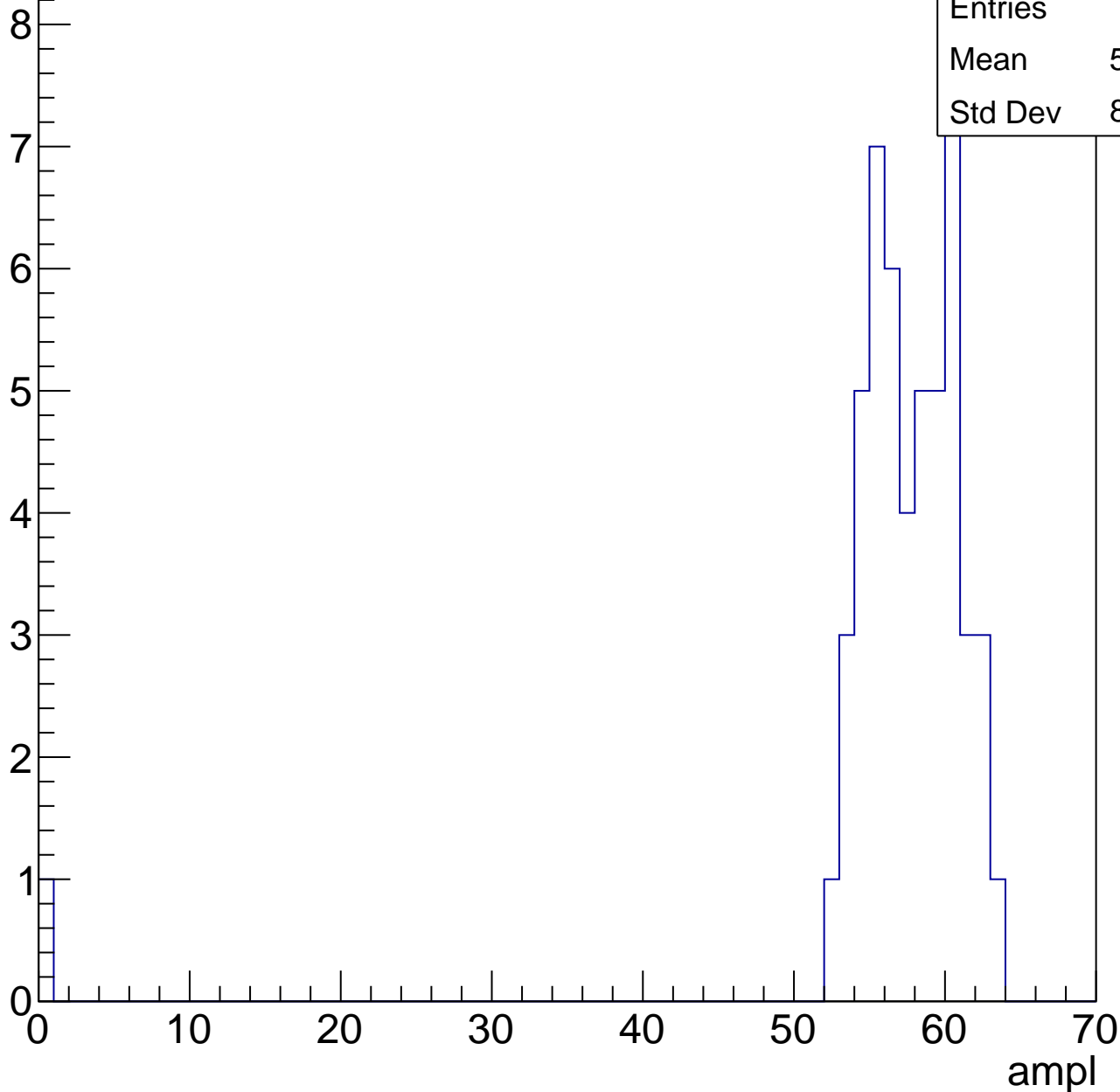


B0L001S, U23-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.29
Std Dev	8.356

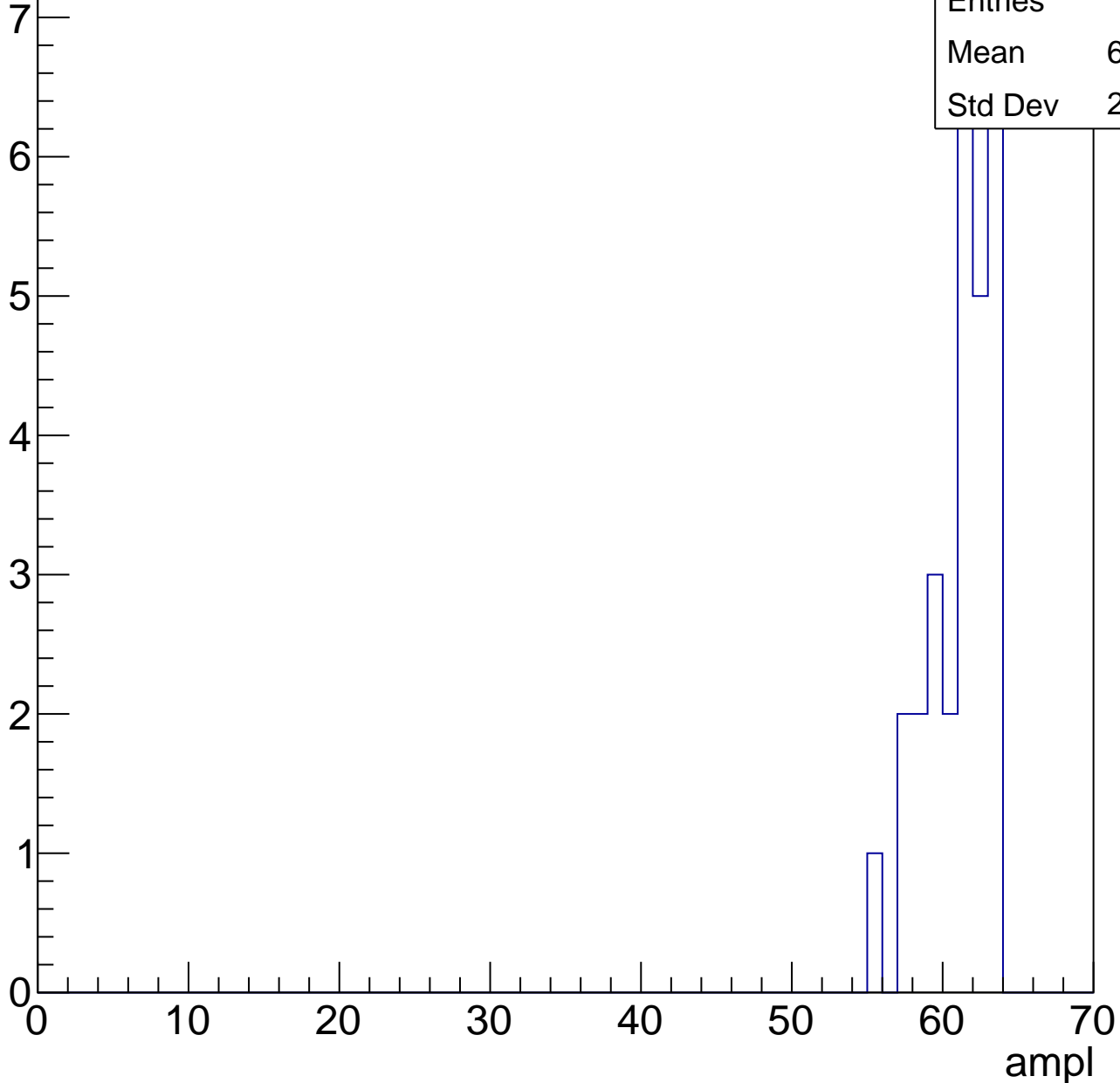


B0L001S, U23-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

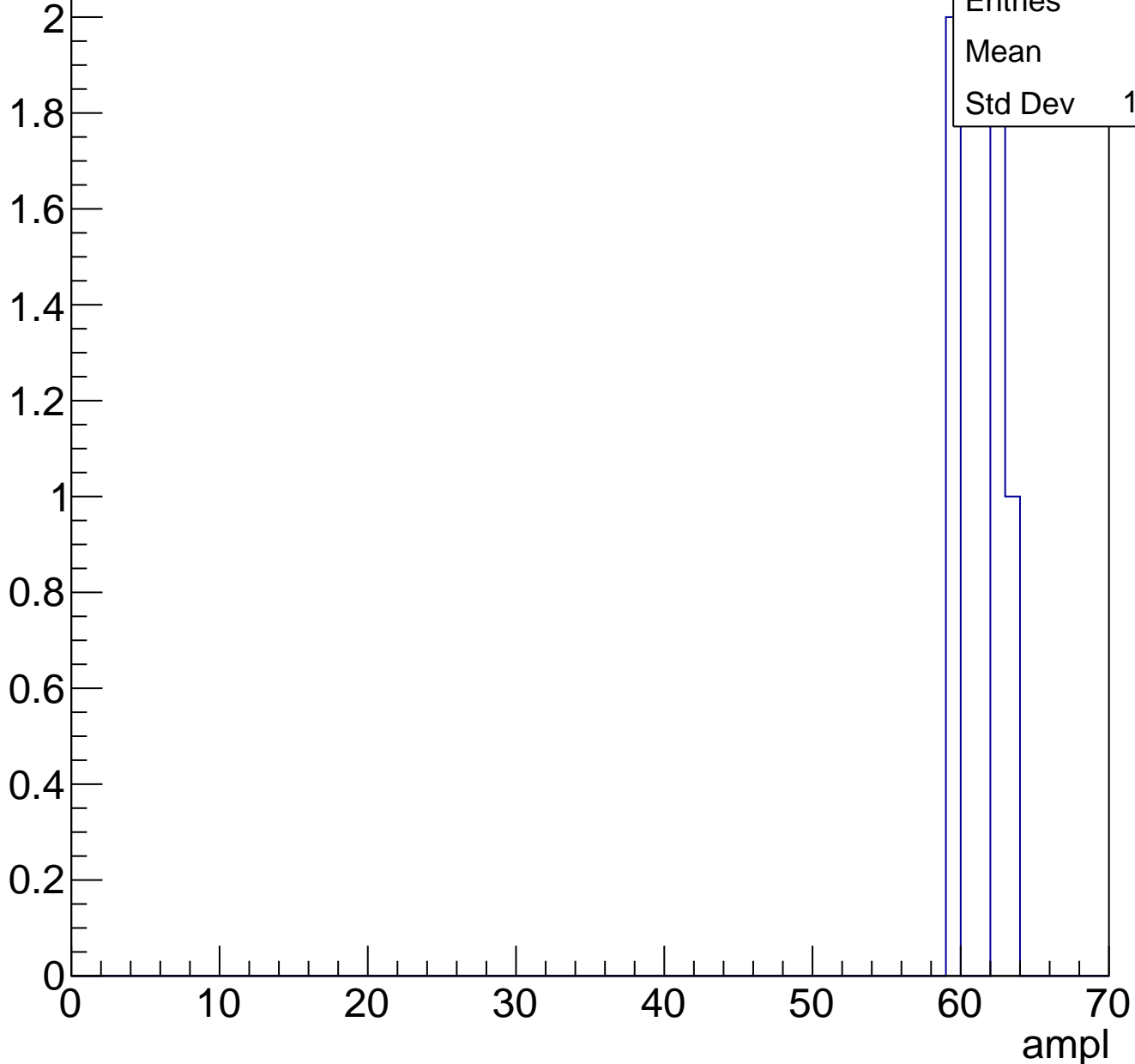
Entries	29
Mean	60.69
Std Dev	2.119



B0L001S, U23-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch17, adc0

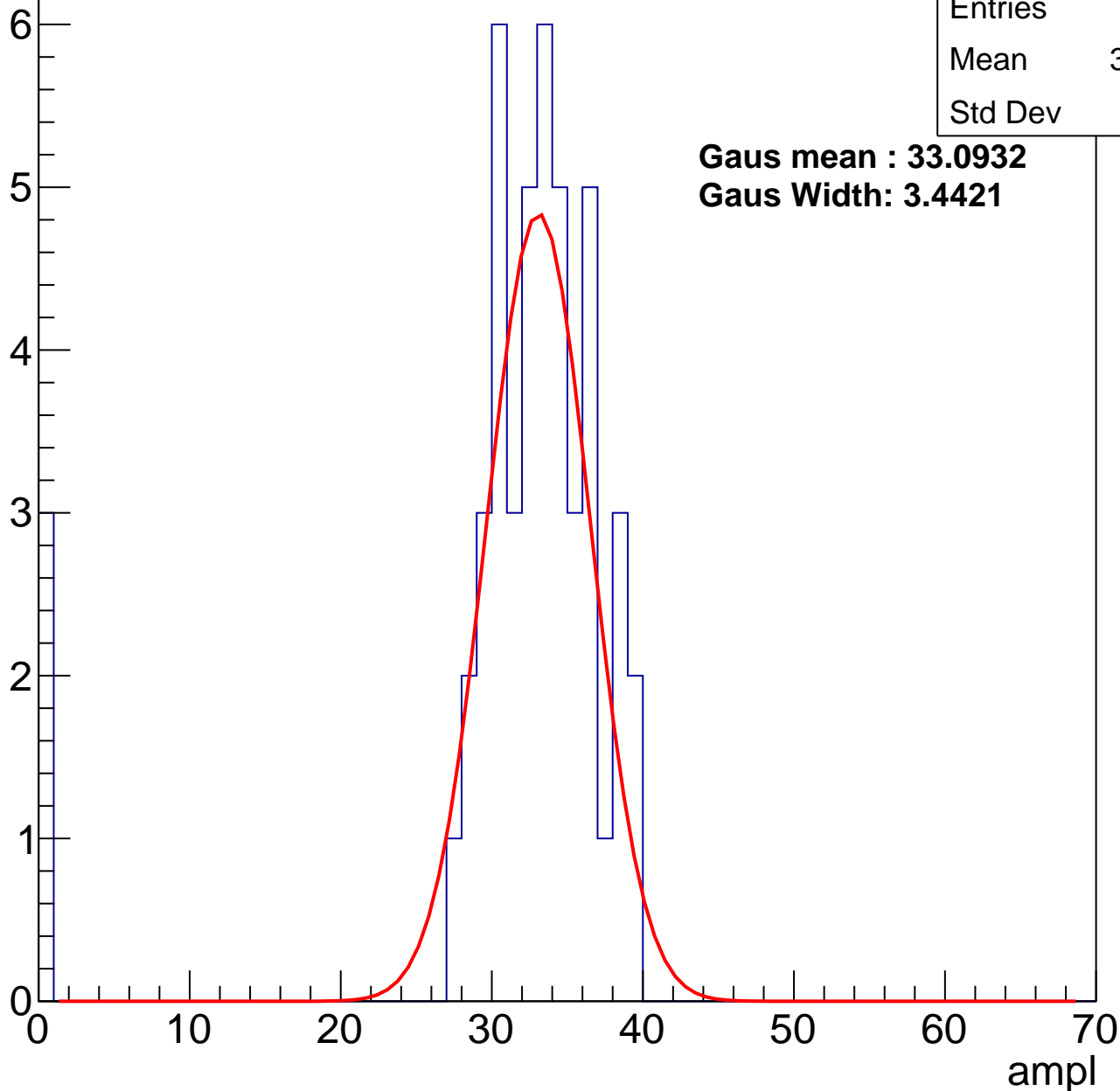
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	30.94
Std Dev	8.53

Gaus mean : 33.0932

Gaus Width: 3.4421



B0L001S, U23-ch17, adc1

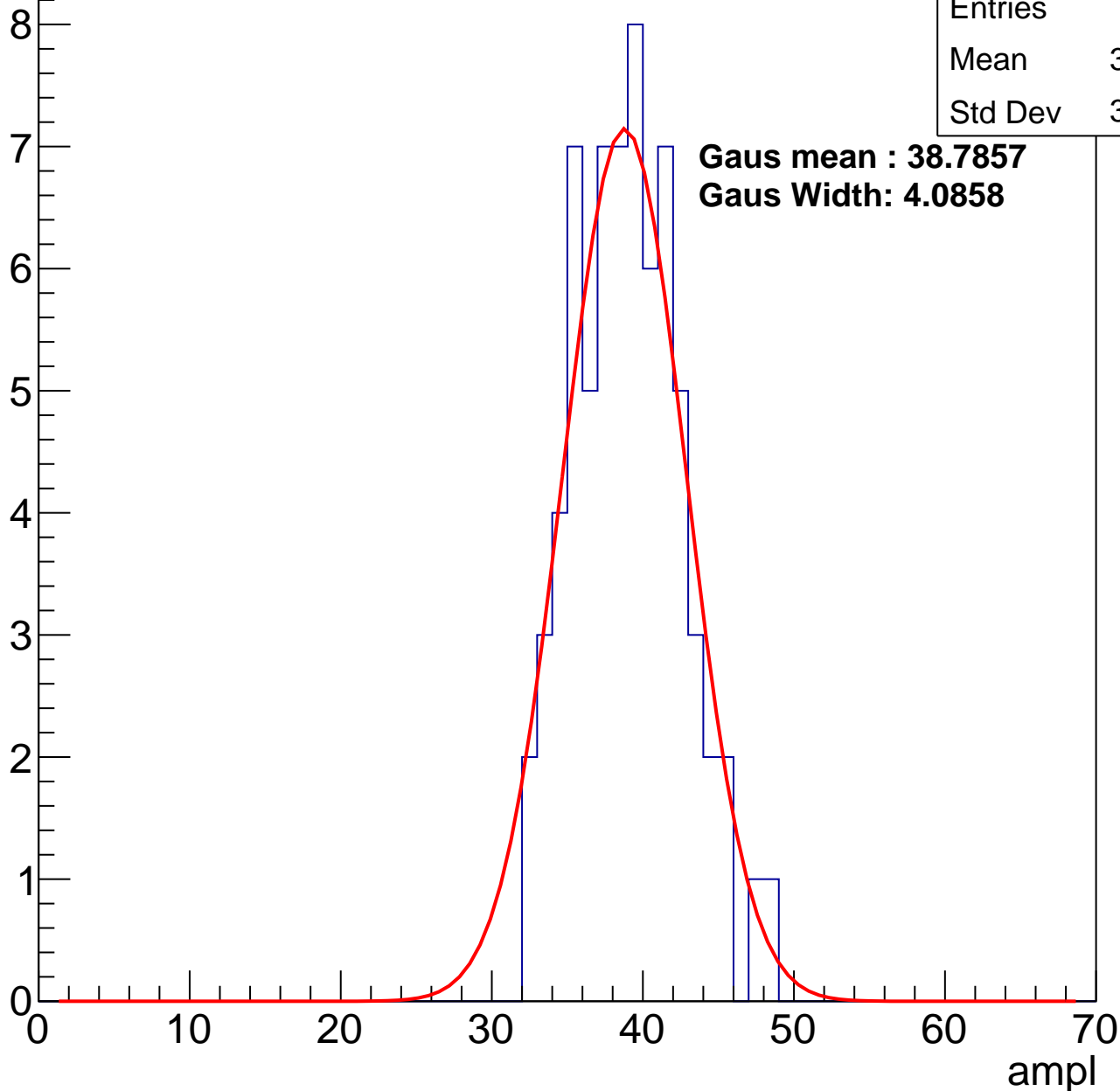
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.57
Std Dev	3.544

Gaus mean : 38.7857

Gaus Width: 4.0858



B0L001S, U23-ch17, adc2

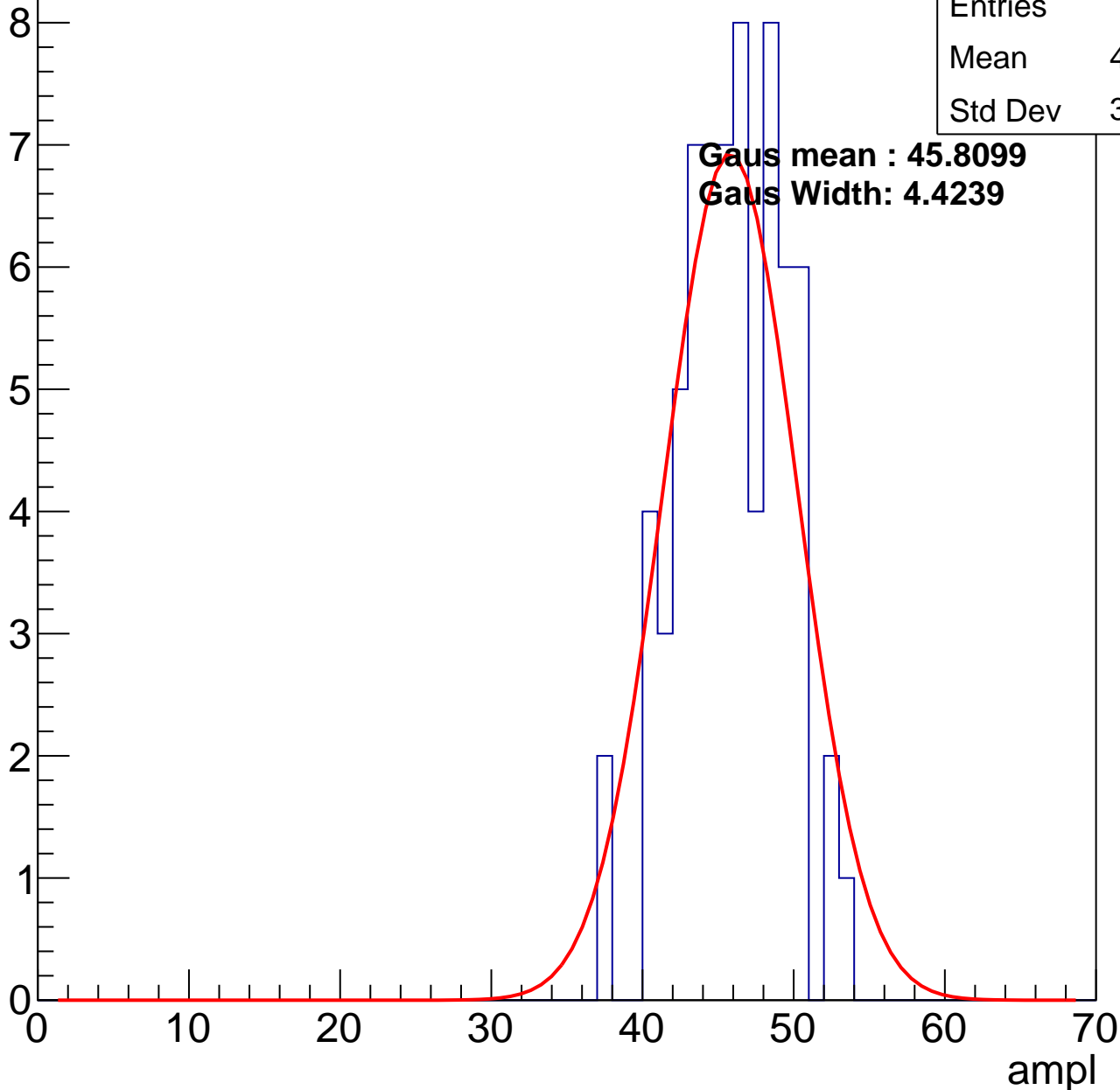
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	45.46
Std Dev	3.475

Gaus mean : 45.8099

Gaus Width: 4.4239

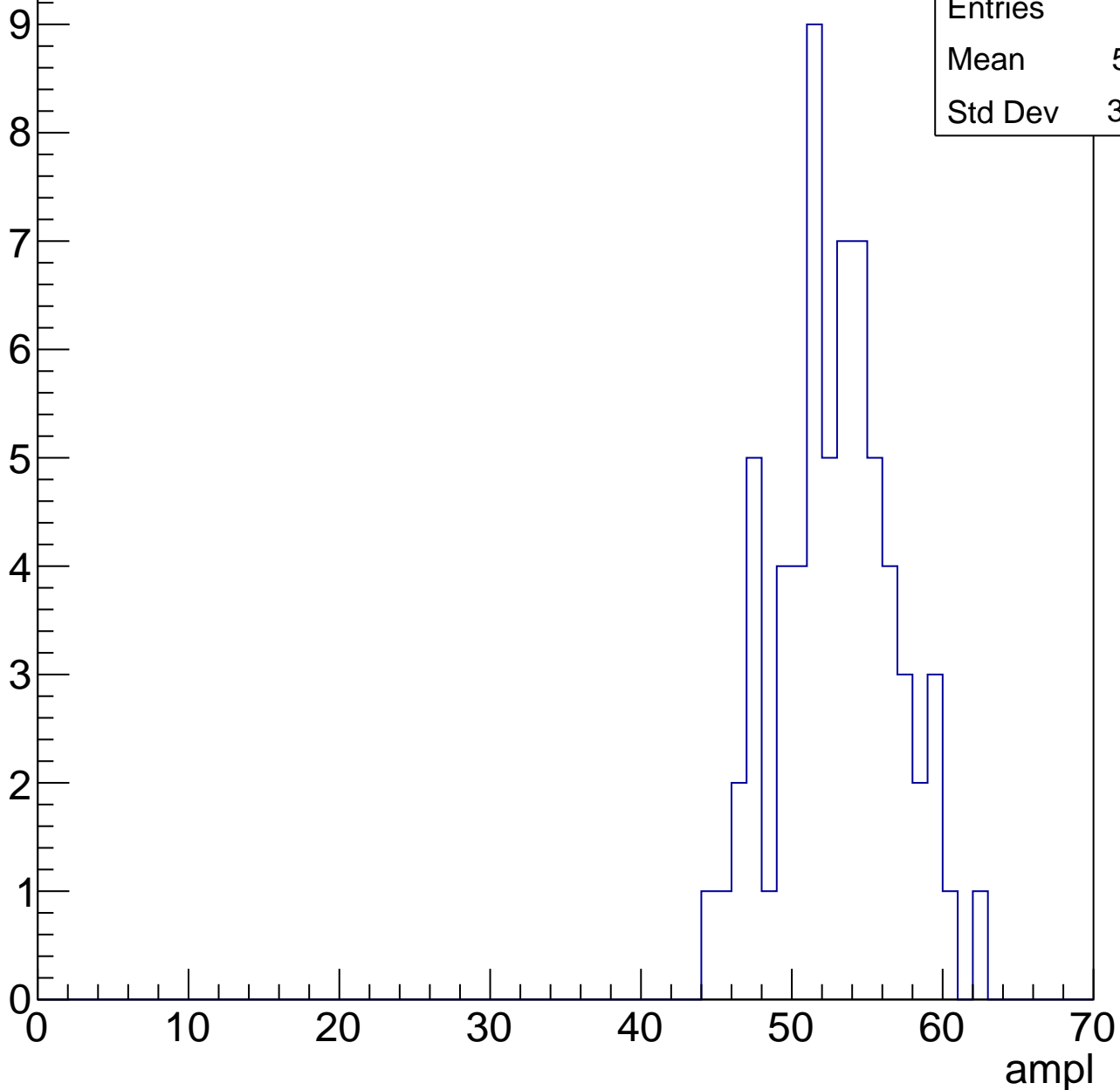


B0L001S, U23-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	52.51
Std Dev	3.868

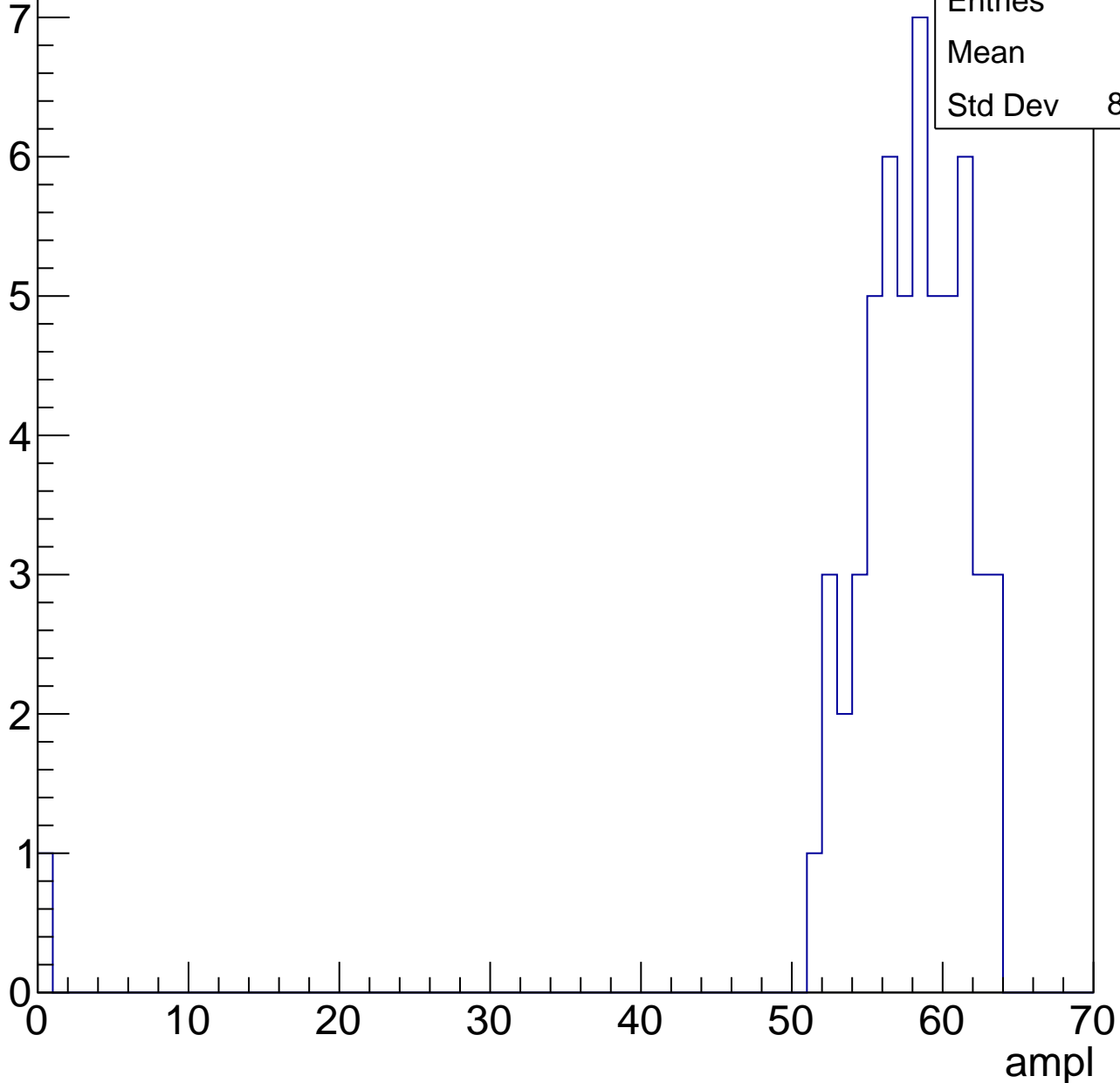


B0L001S, U23-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.6
Std Dev	8.298

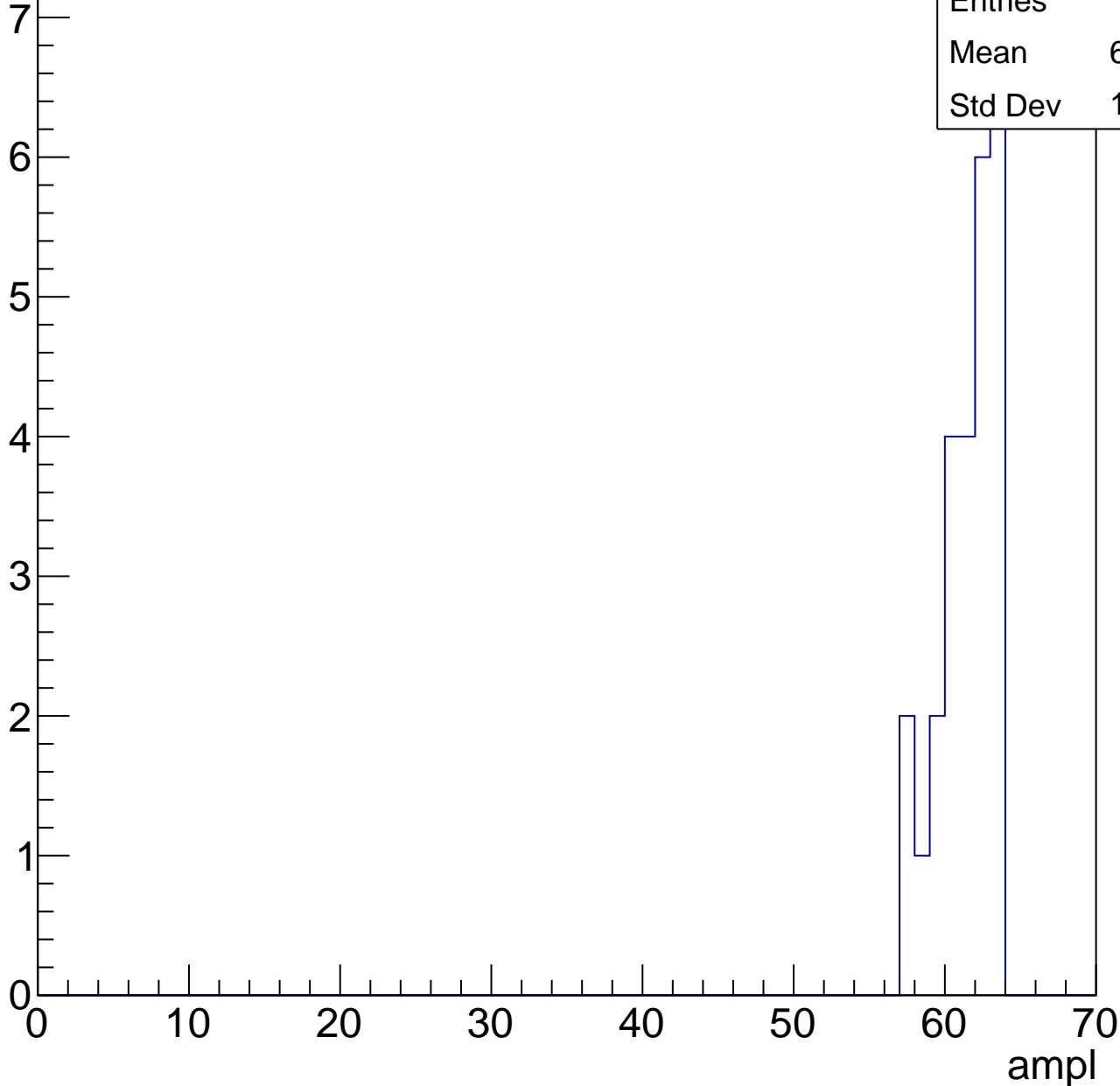


B0L001S, U23-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.04
Std Dev	1.829



B0L001S, U23-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch18, adc0

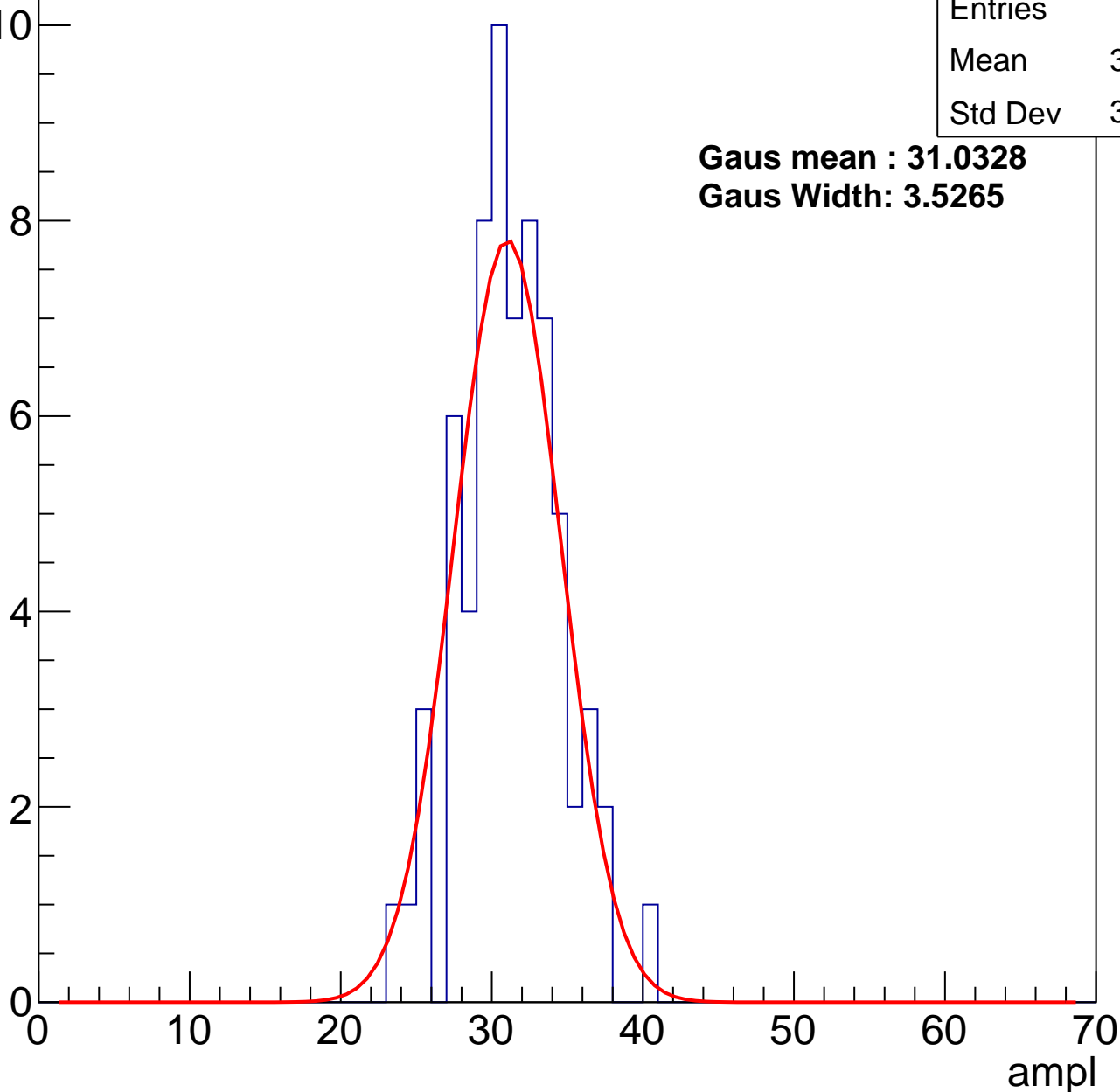
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.79
Std Dev	3.283

Gaus mean : 31.0328

Gaus Width: 3.5265



B0L001S, U23-ch18, adc1

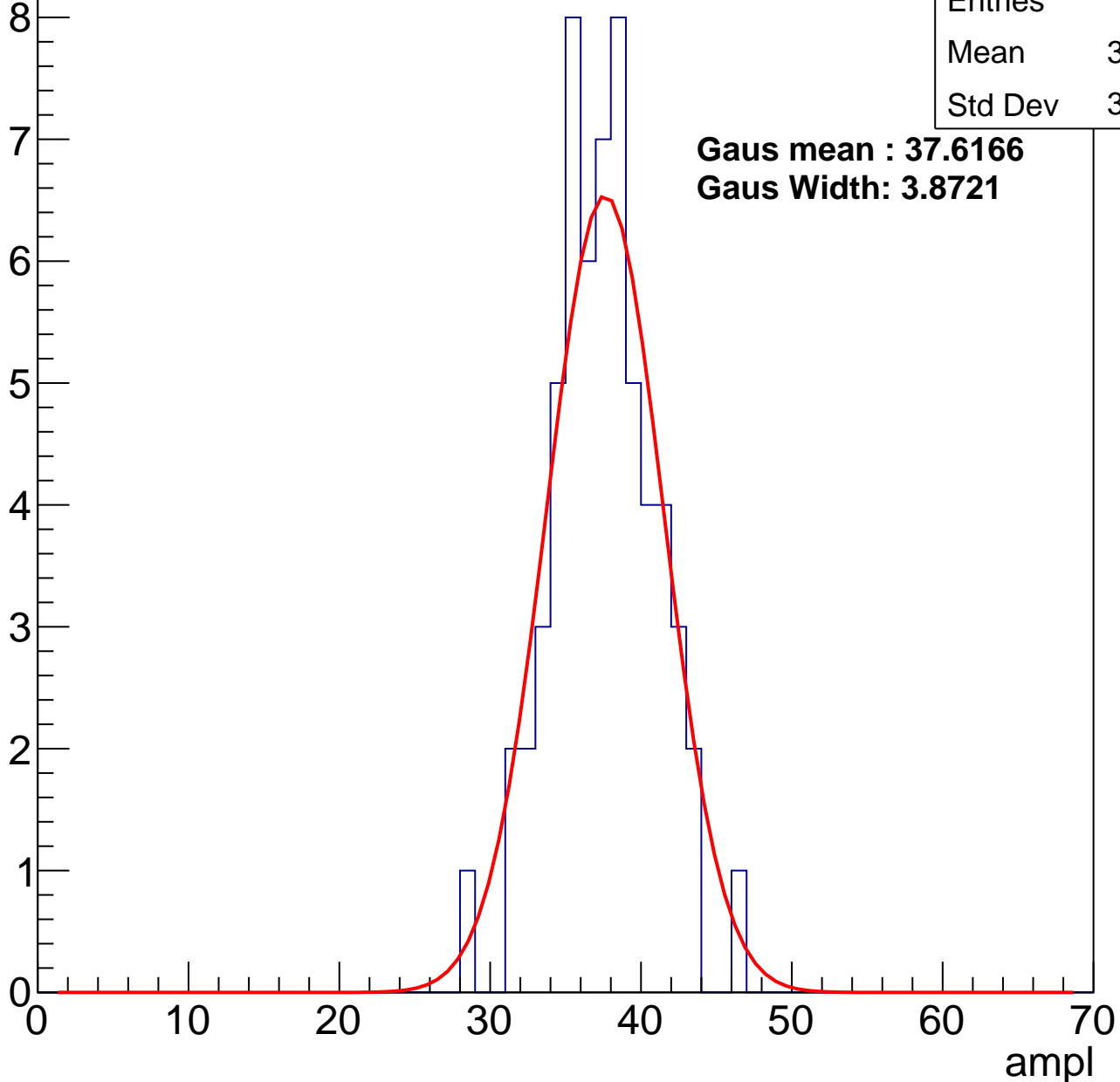
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.03
Std Dev	3.363

Gaus mean : 37.6166

Gaus Width: 3.8721



B0L001S, U23-ch18, adc2

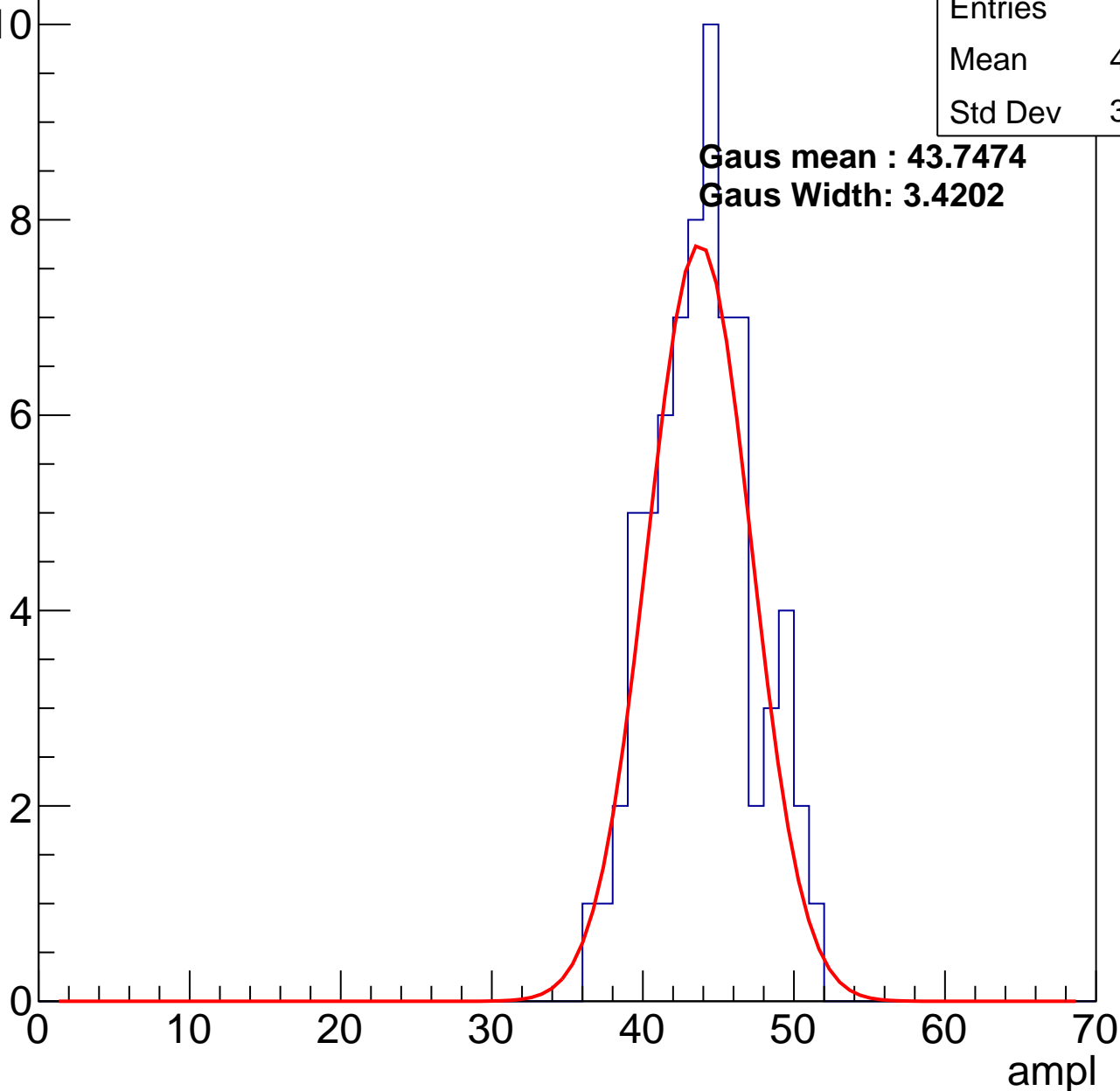
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.52
Std Dev	3.327

Gaus mean : 43.7474

Gaus Width: 3.4202

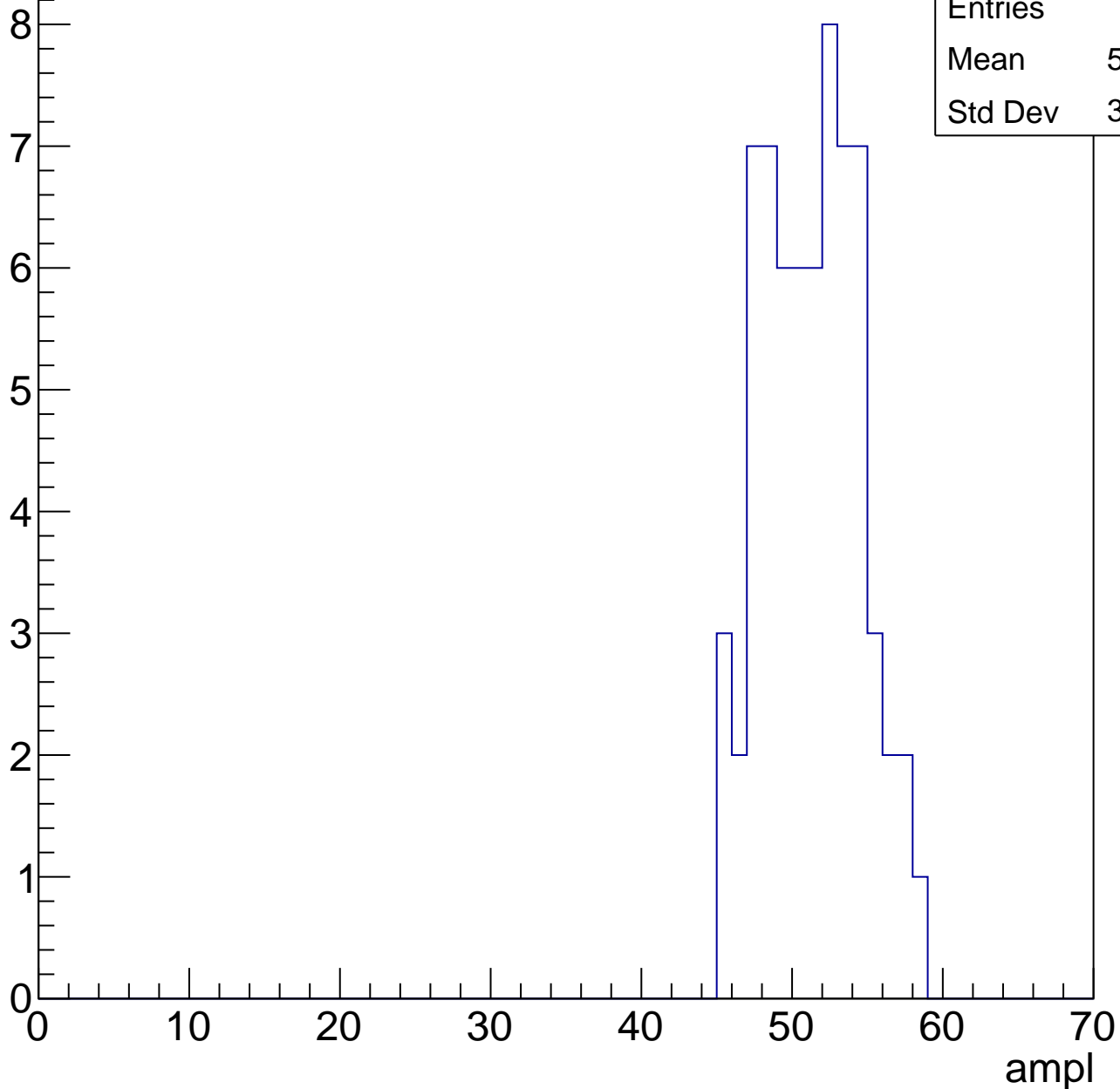


B0L001S, U23-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.84
Std Dev	3.184

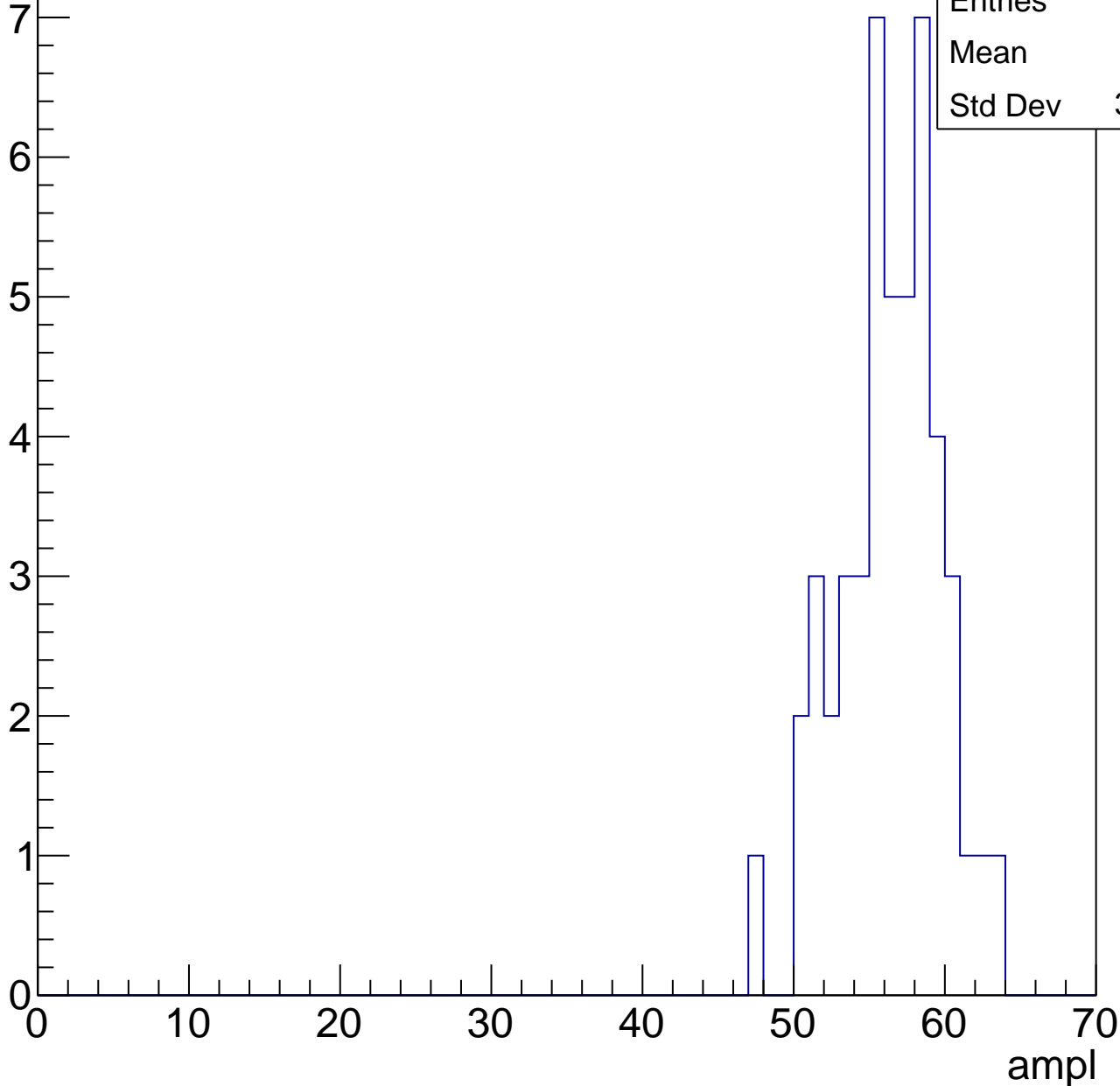


B0L001S, U23-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	55.9
Std Dev	3.331

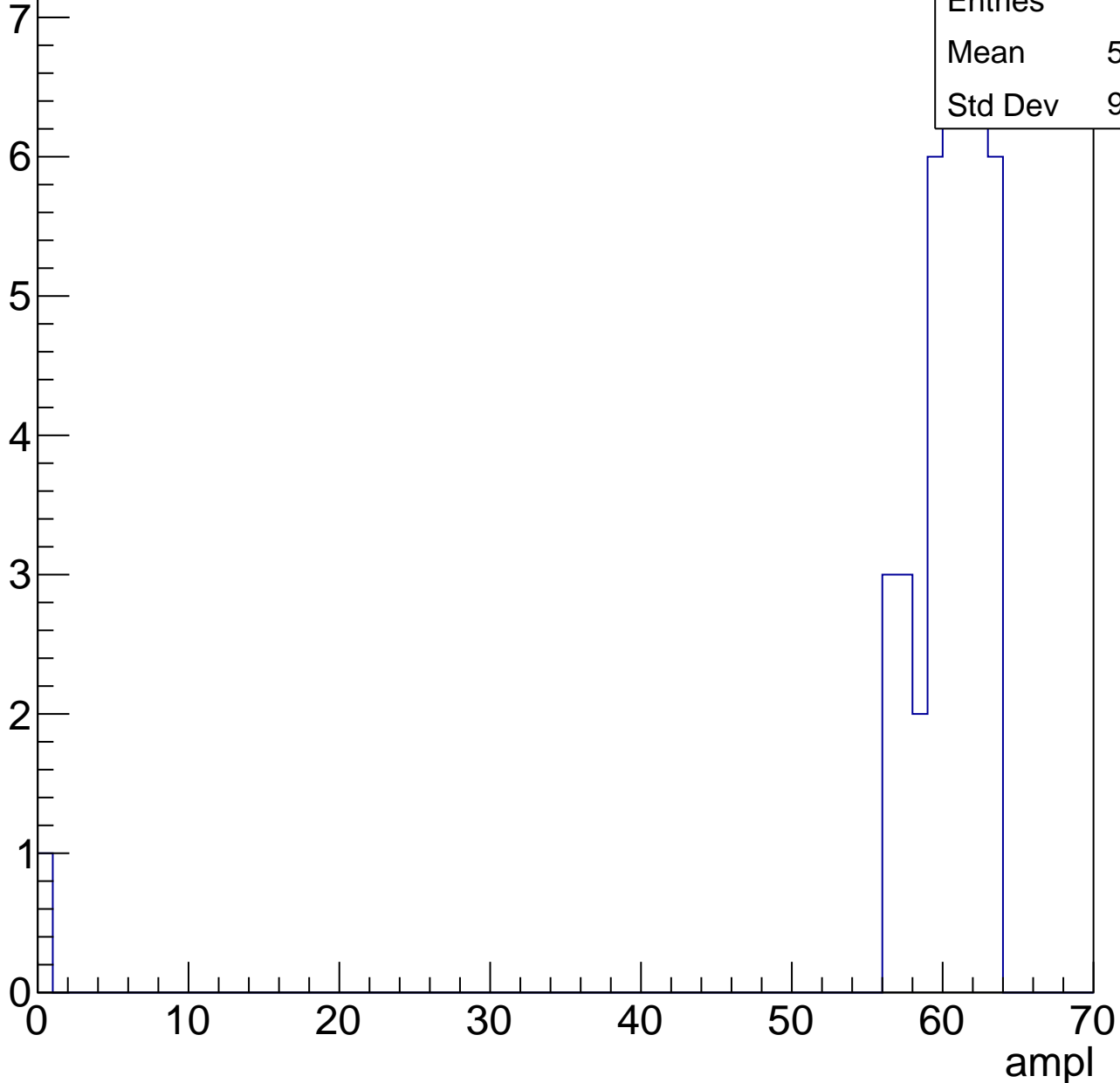


B0L001S, U23-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.76
Std Dev	9.403



B0L001S, U23-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch19, adc0

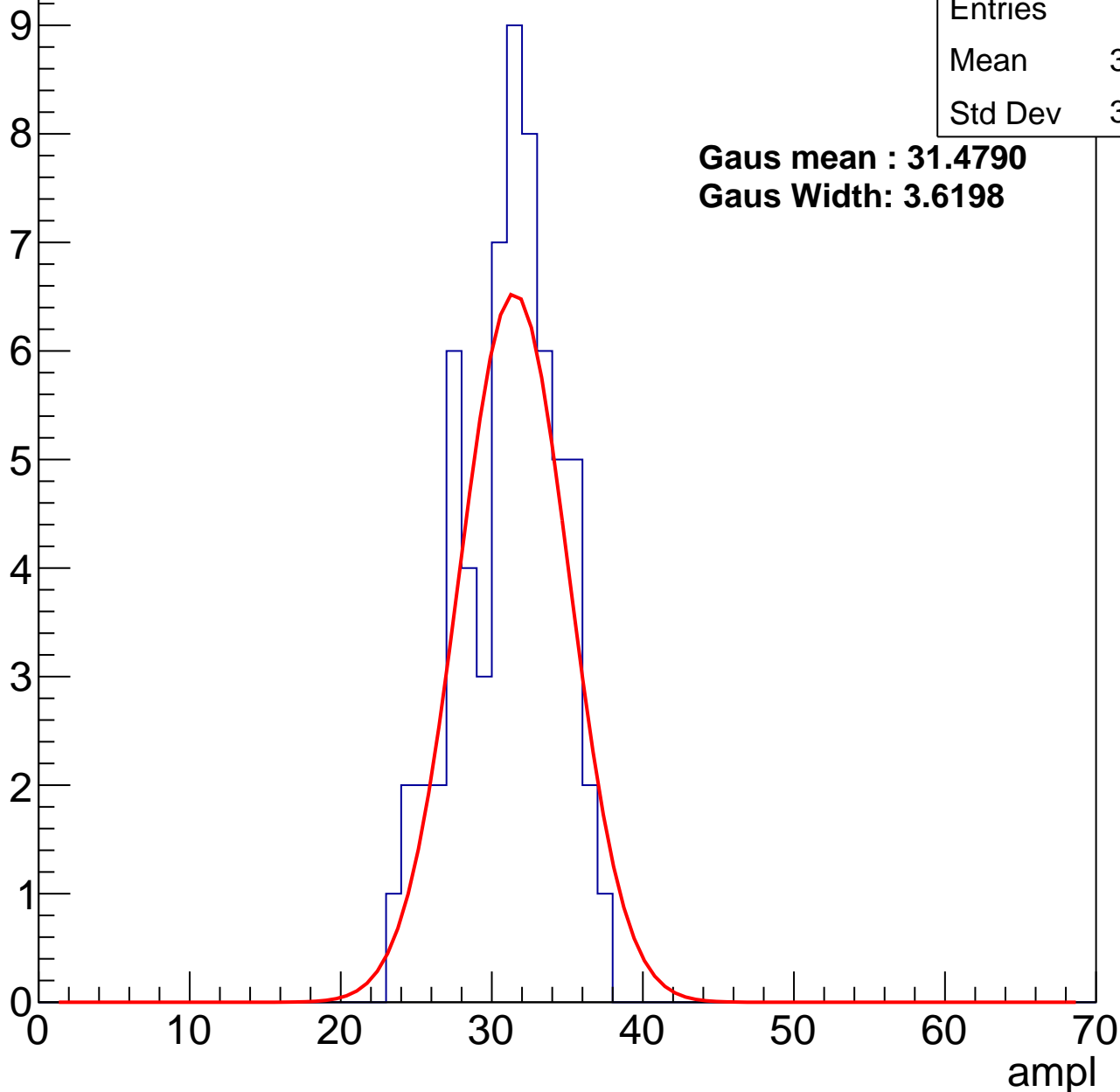
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.65
Std Dev	3.262

Gaus mean : 31.4790

Gaus Width: 3.6198



B0L001S, U23-ch19, adc1

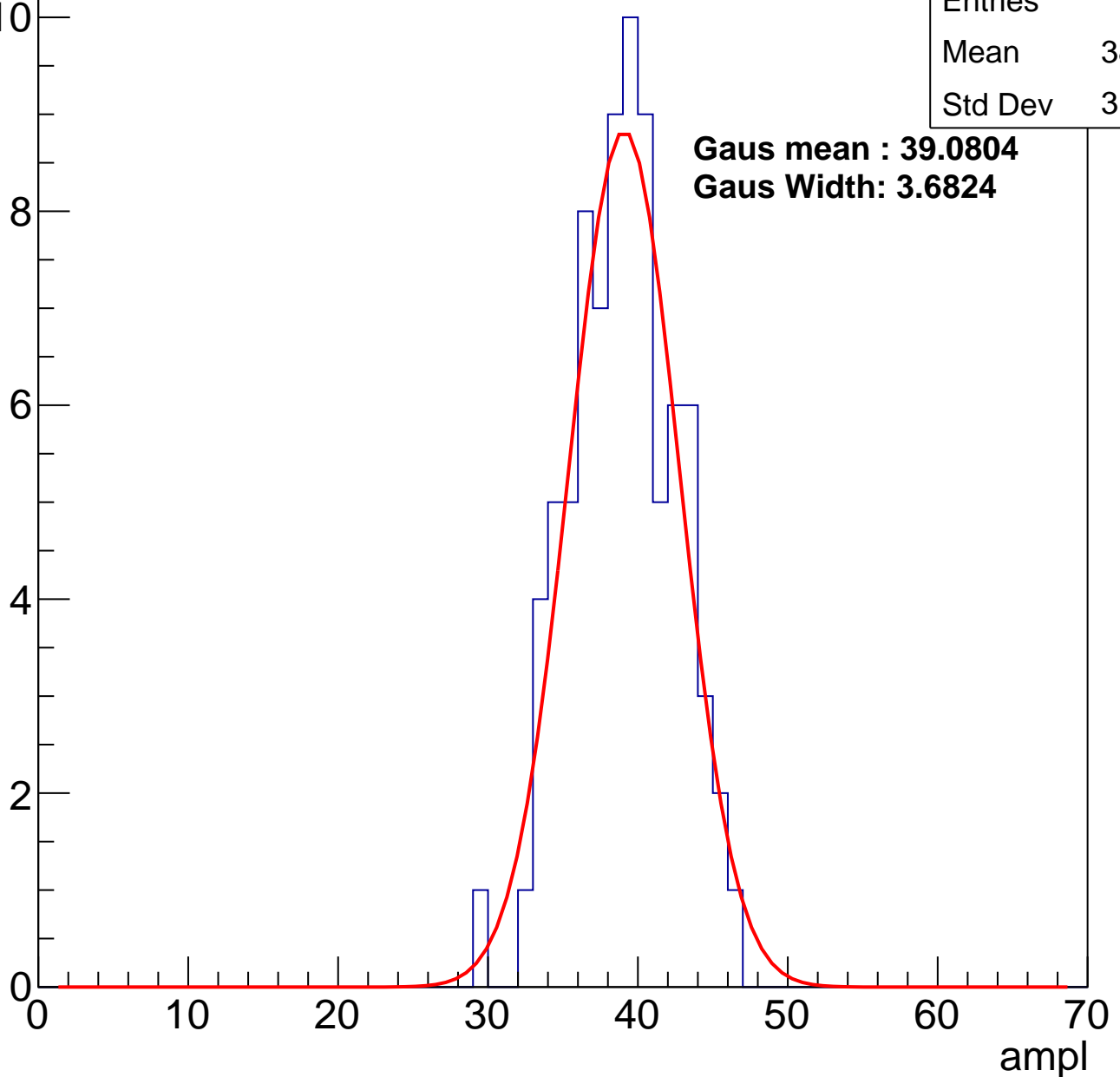
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	38.54
Std Dev	3.429

Gaus mean : 39.0804

Gaus Width: 3.6824



B0L001S, U23-ch19, adc2

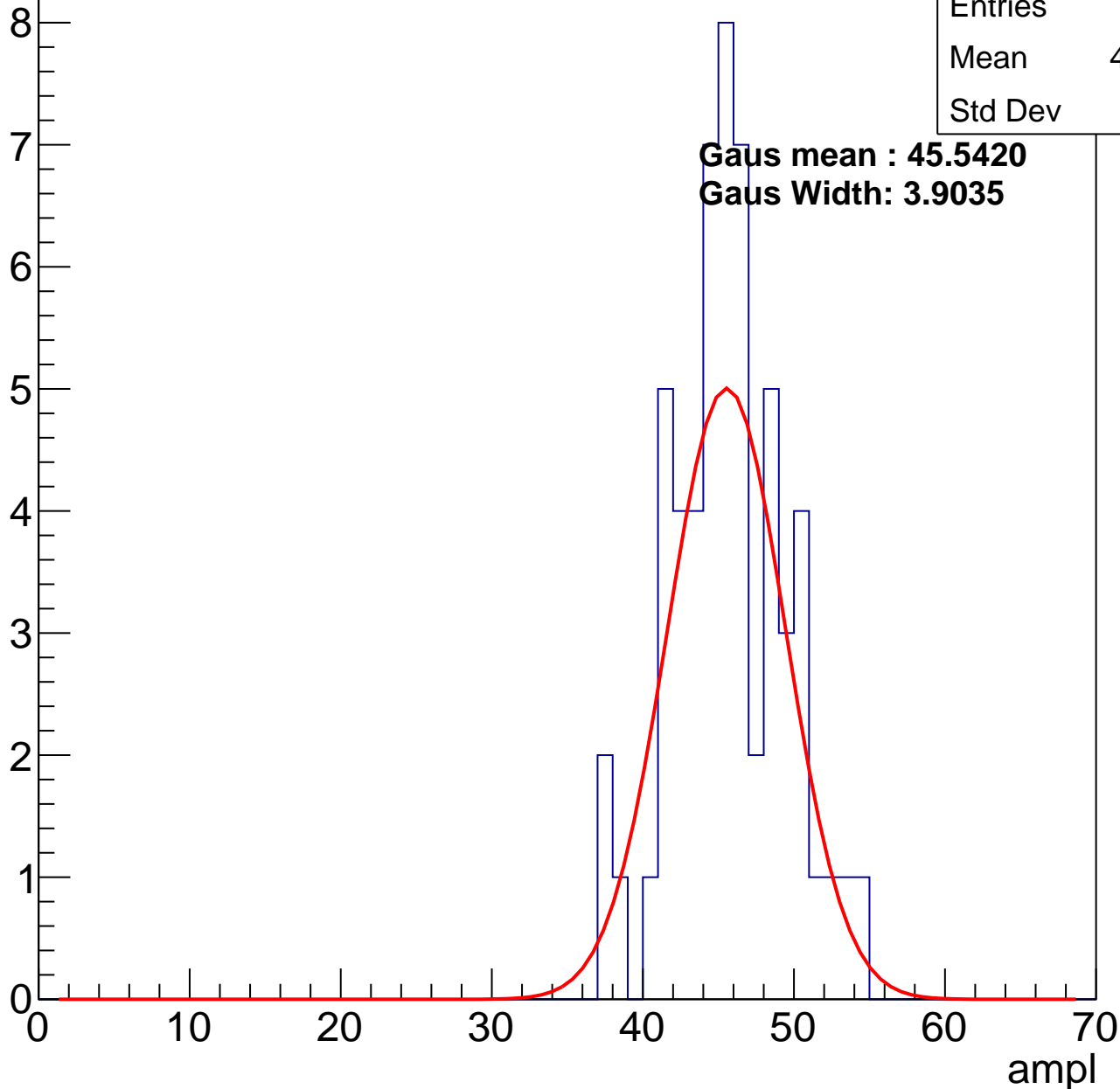
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.23
Std Dev	3.69

Gaus mean : 45.5420

Gaus Width: 3.9035

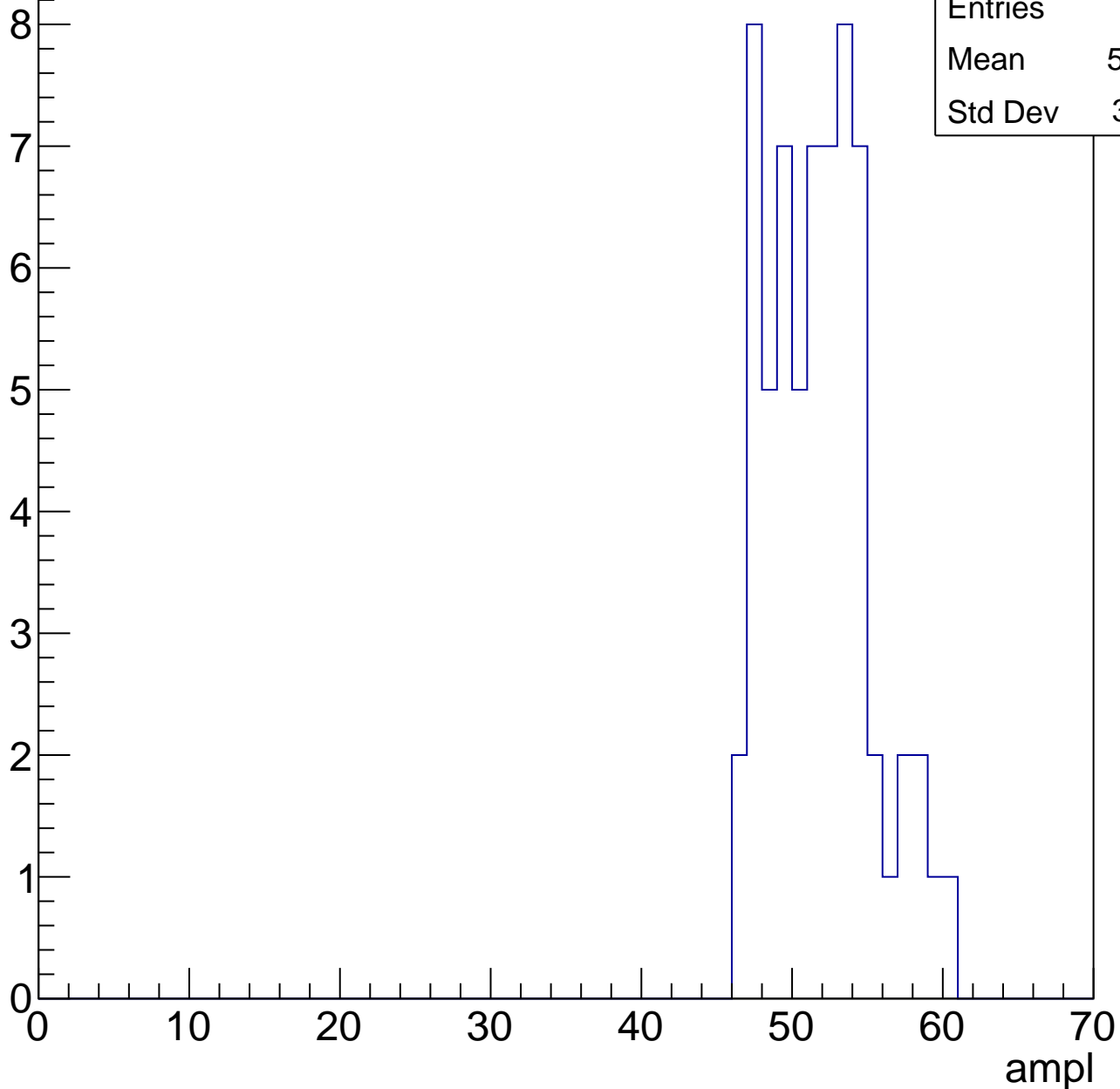


B0L001S, U23-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.37
Std Dev	3.331

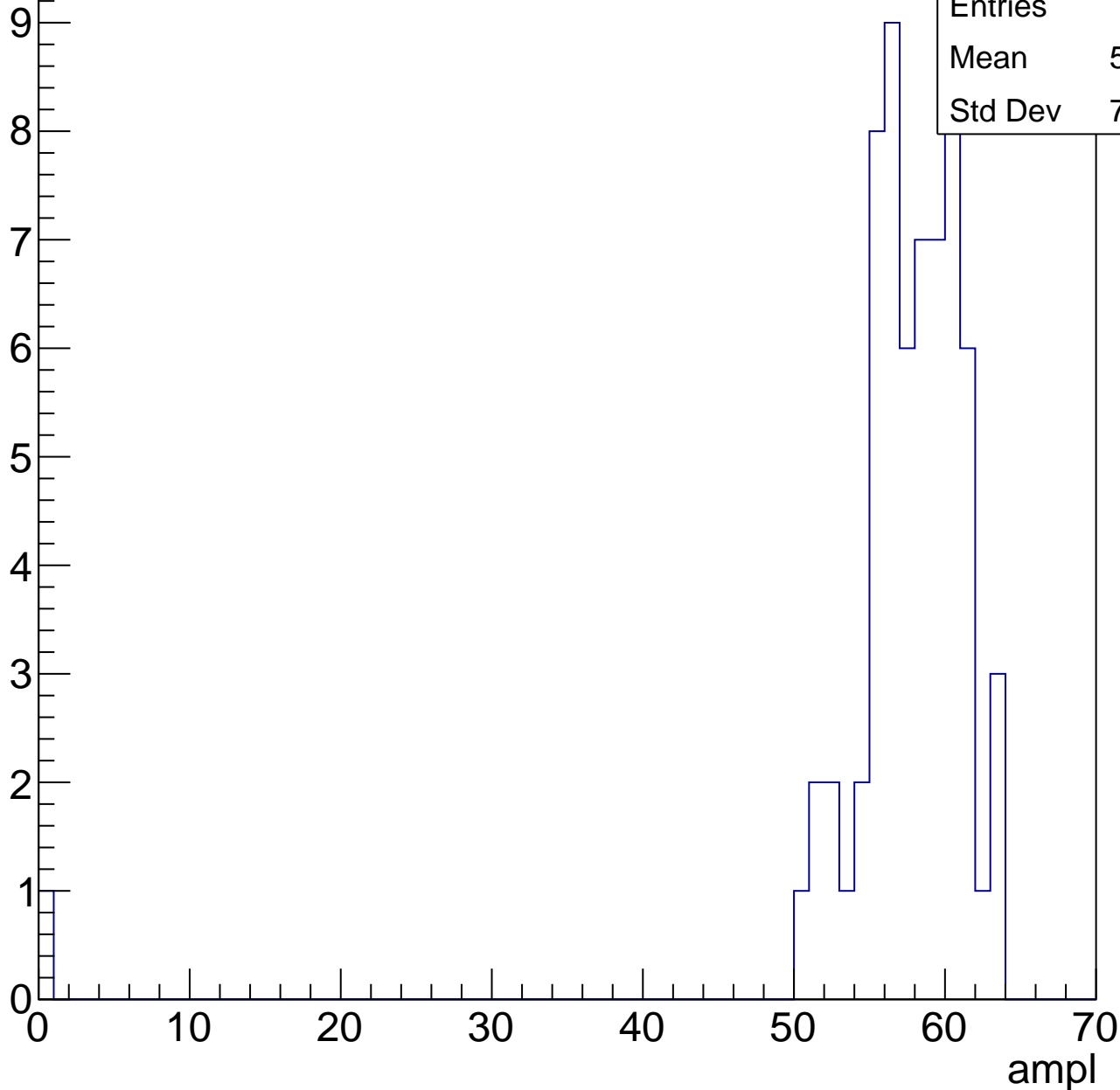


B0L001S, U23-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.55
Std Dev	7.724

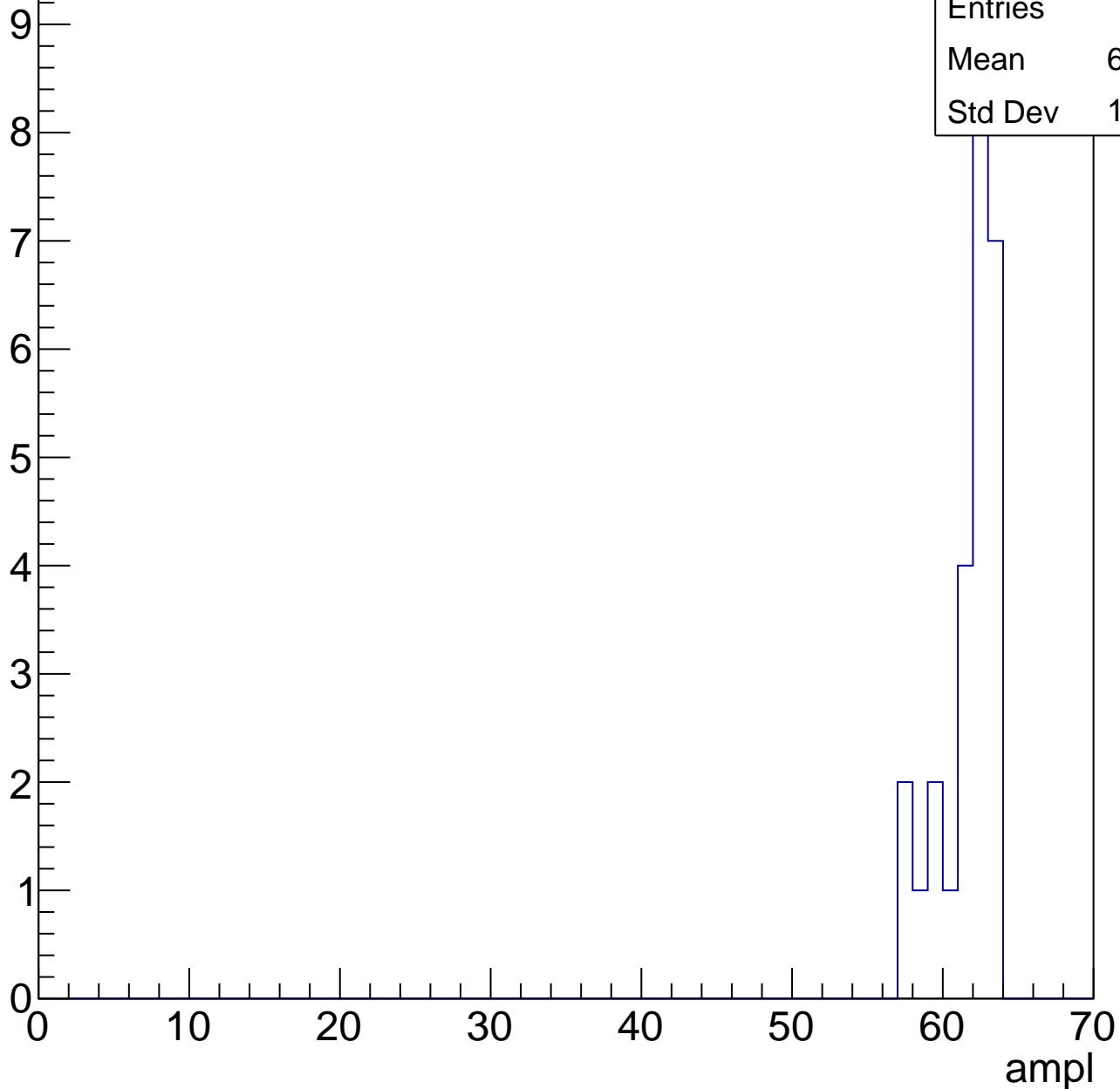


B0L001S, U23-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.27
Std Dev	1.809



B0L001S, U23-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch19, adc7

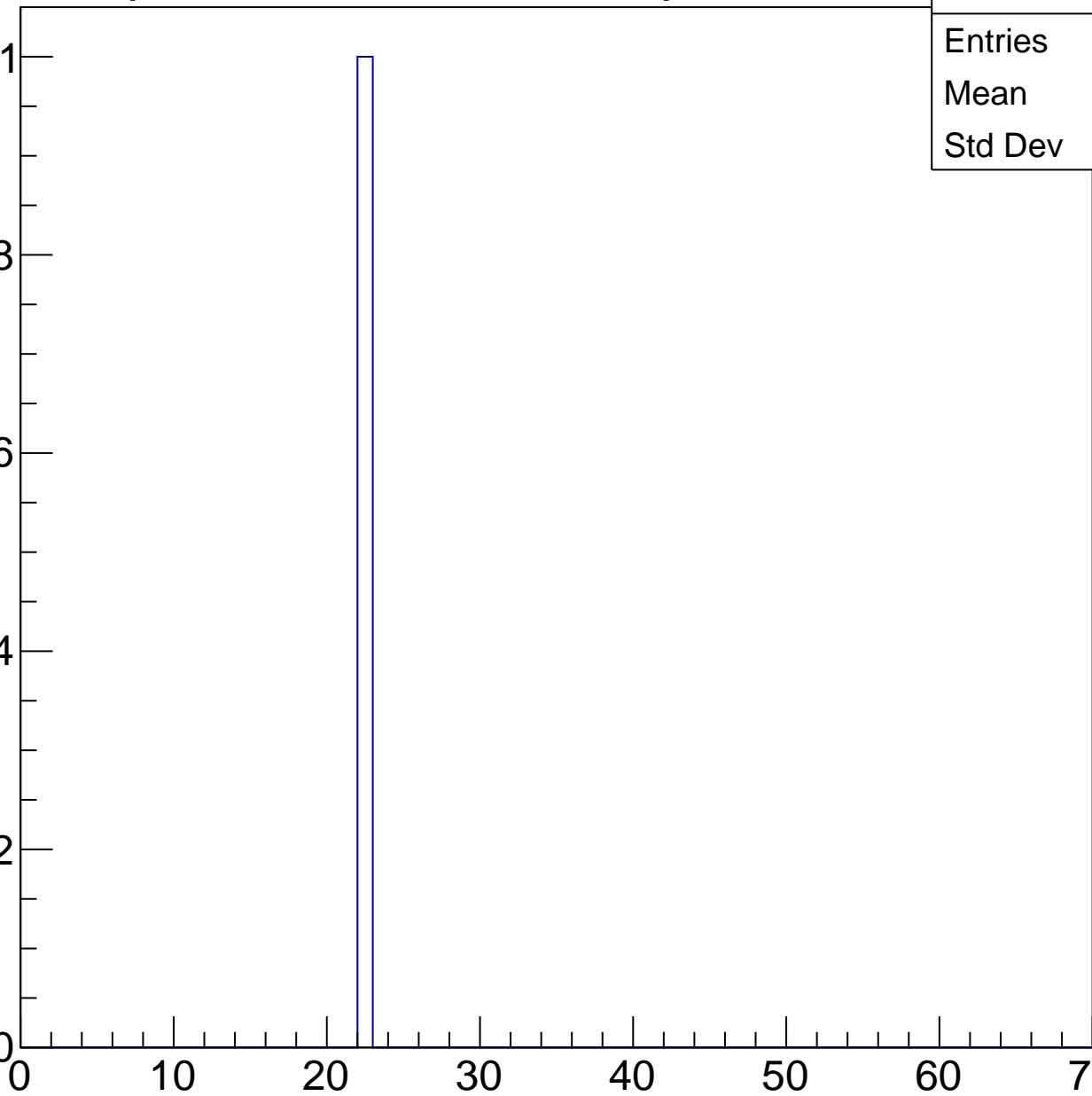
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl



B0L001S, U23-ch20, adc0

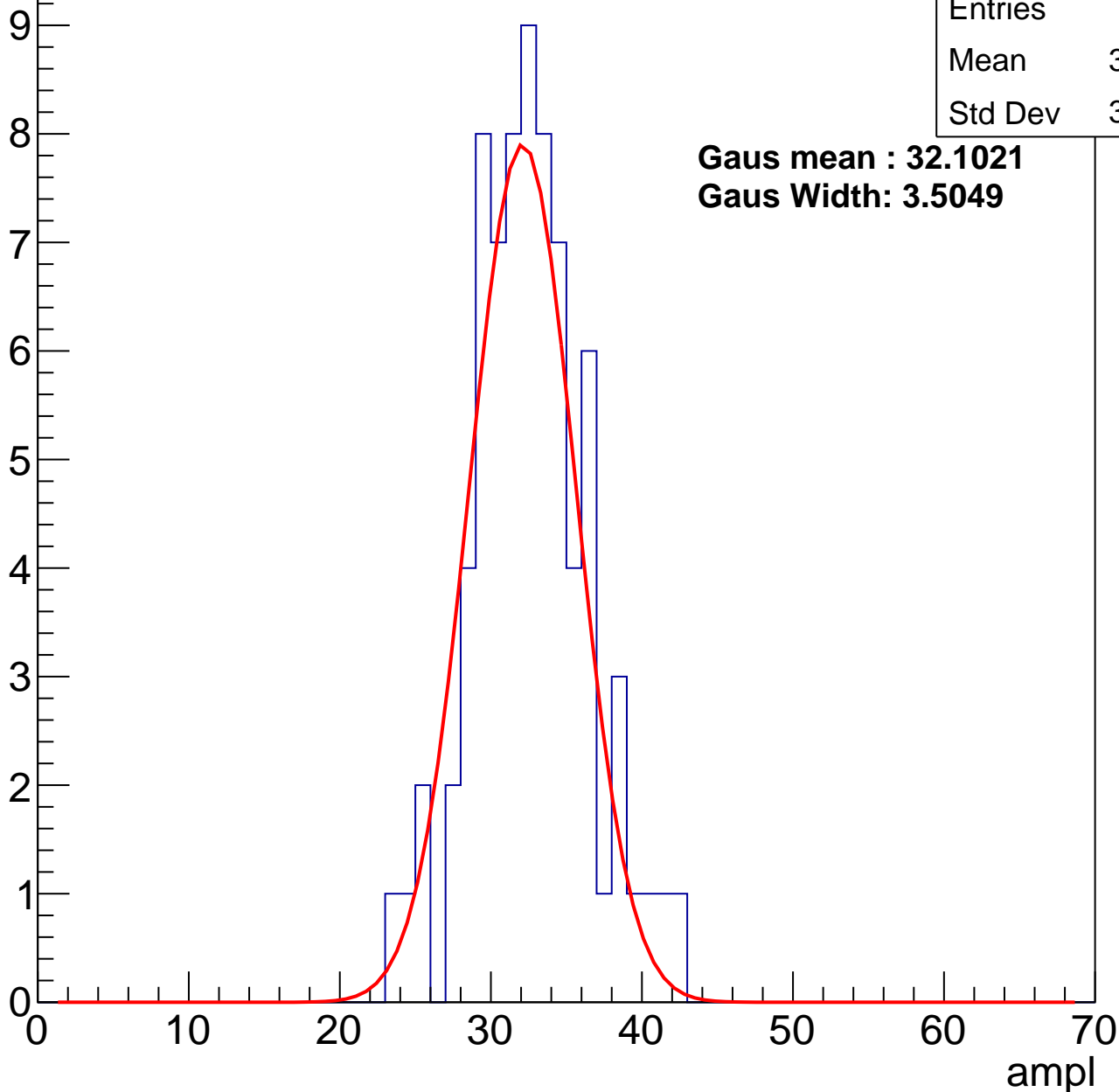
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	32.16
Std Dev	3.756

Gaus mean : 32.1021

Gaus Width: 3.5049



B0L001S, U23-ch20, adc1

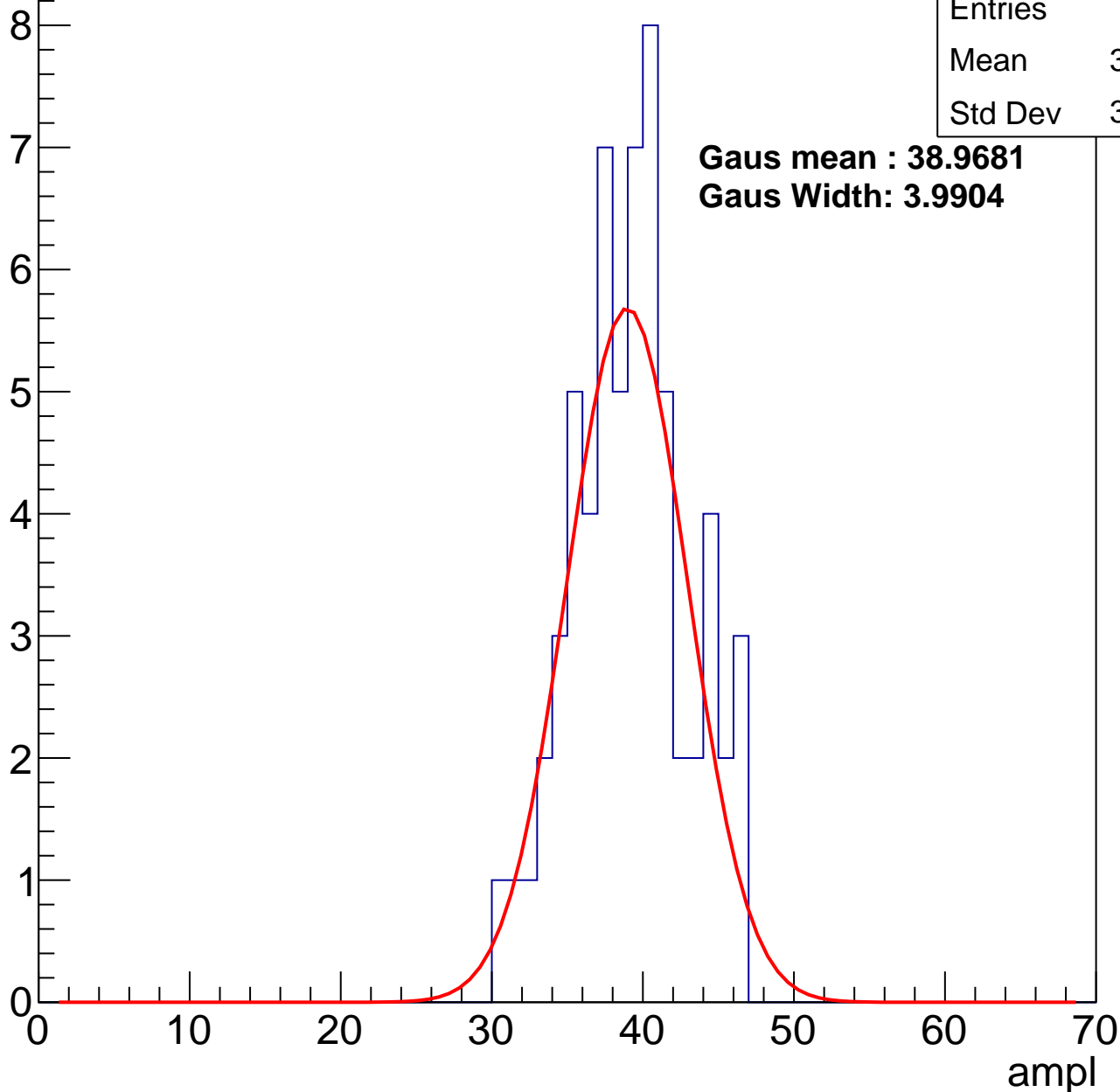
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	38.73
Std Dev	3.772

Gaus mean : 38.9681

Gaus Width: 3.9904



B0L001S, U23-ch20, adc2

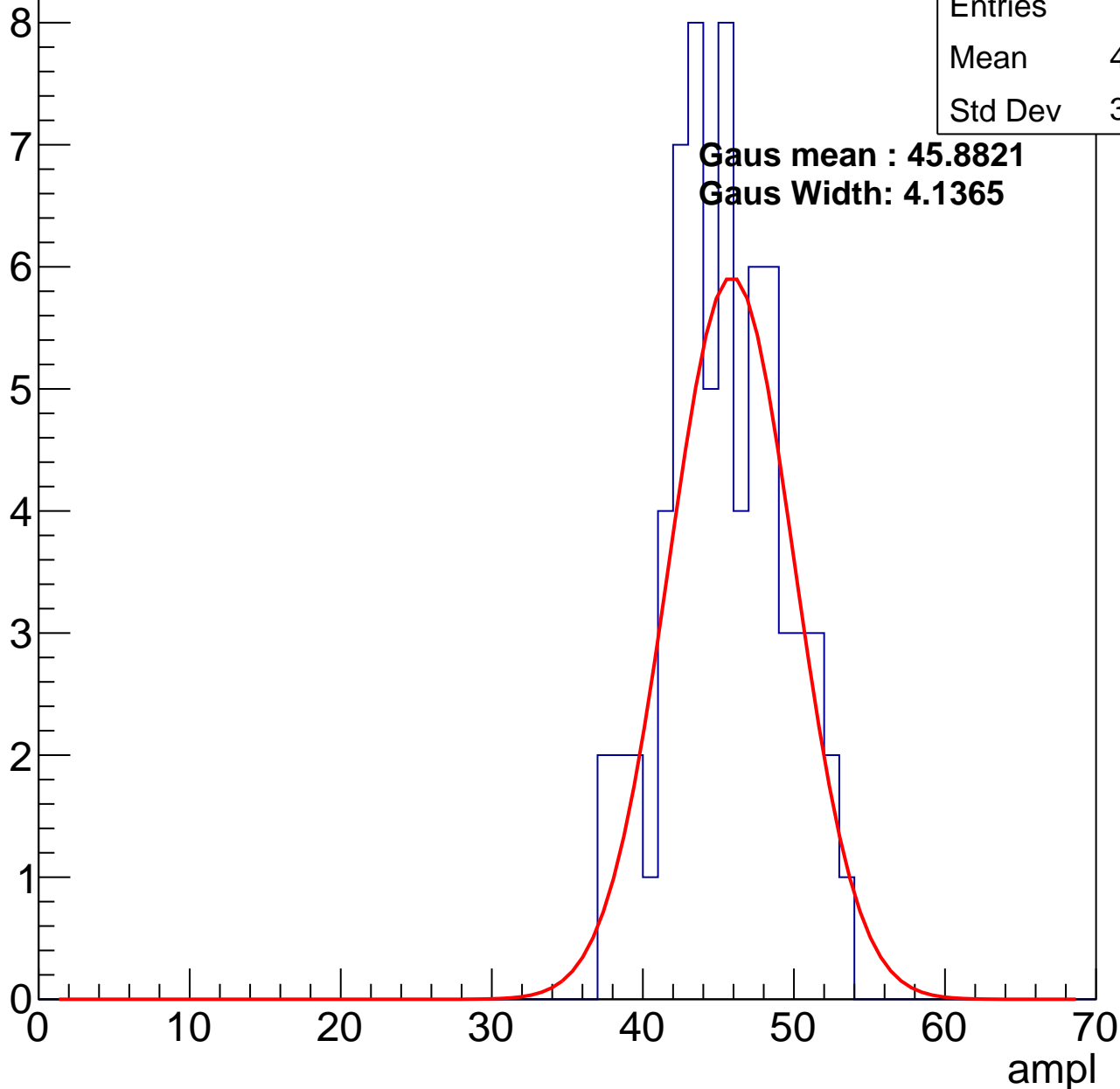
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.94
Std Dev	3.789

Gaus mean : 45.8821

Gaus Width: 4.1365

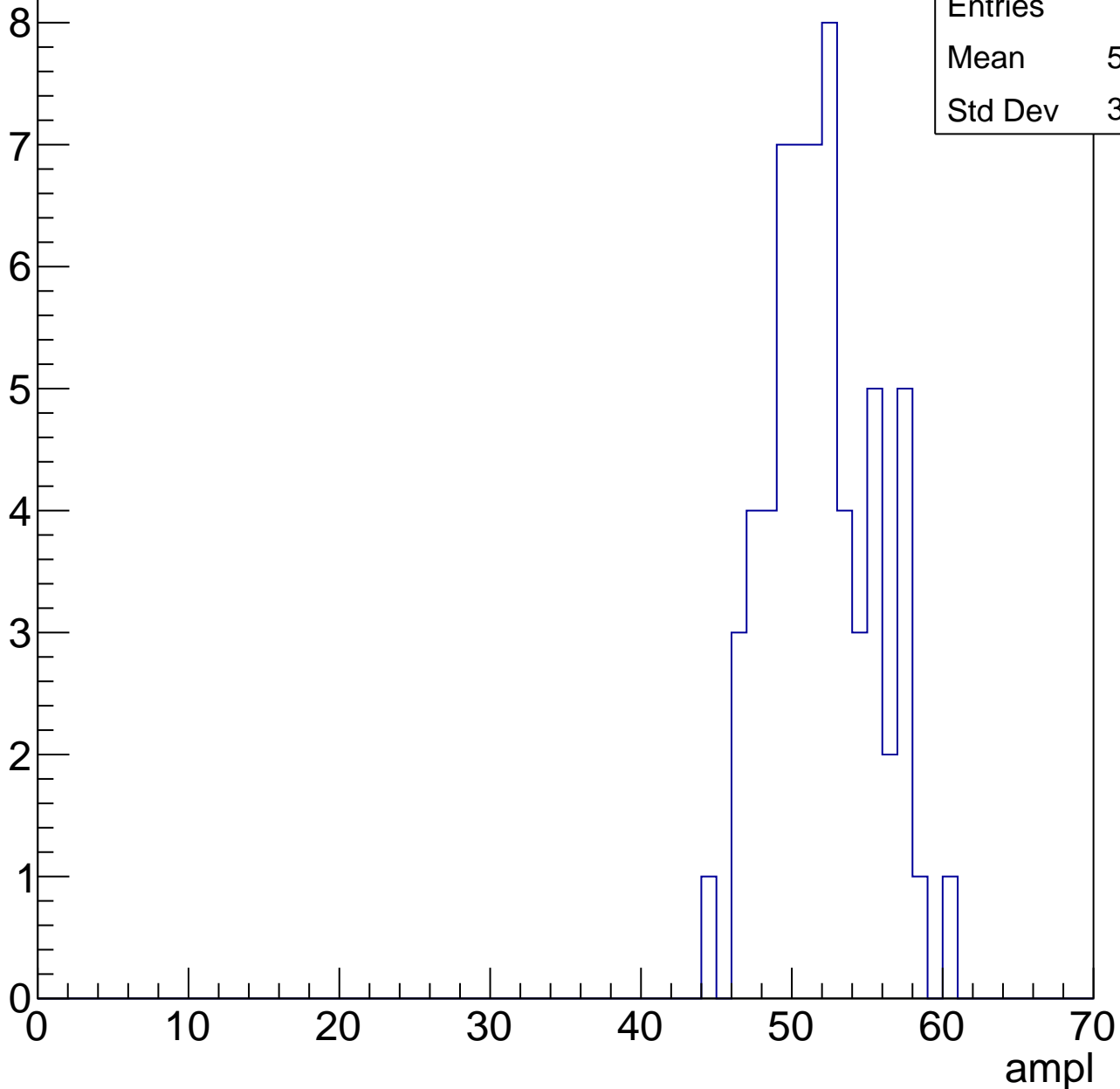


B0L001S, U23-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.48
Std Dev	3.444

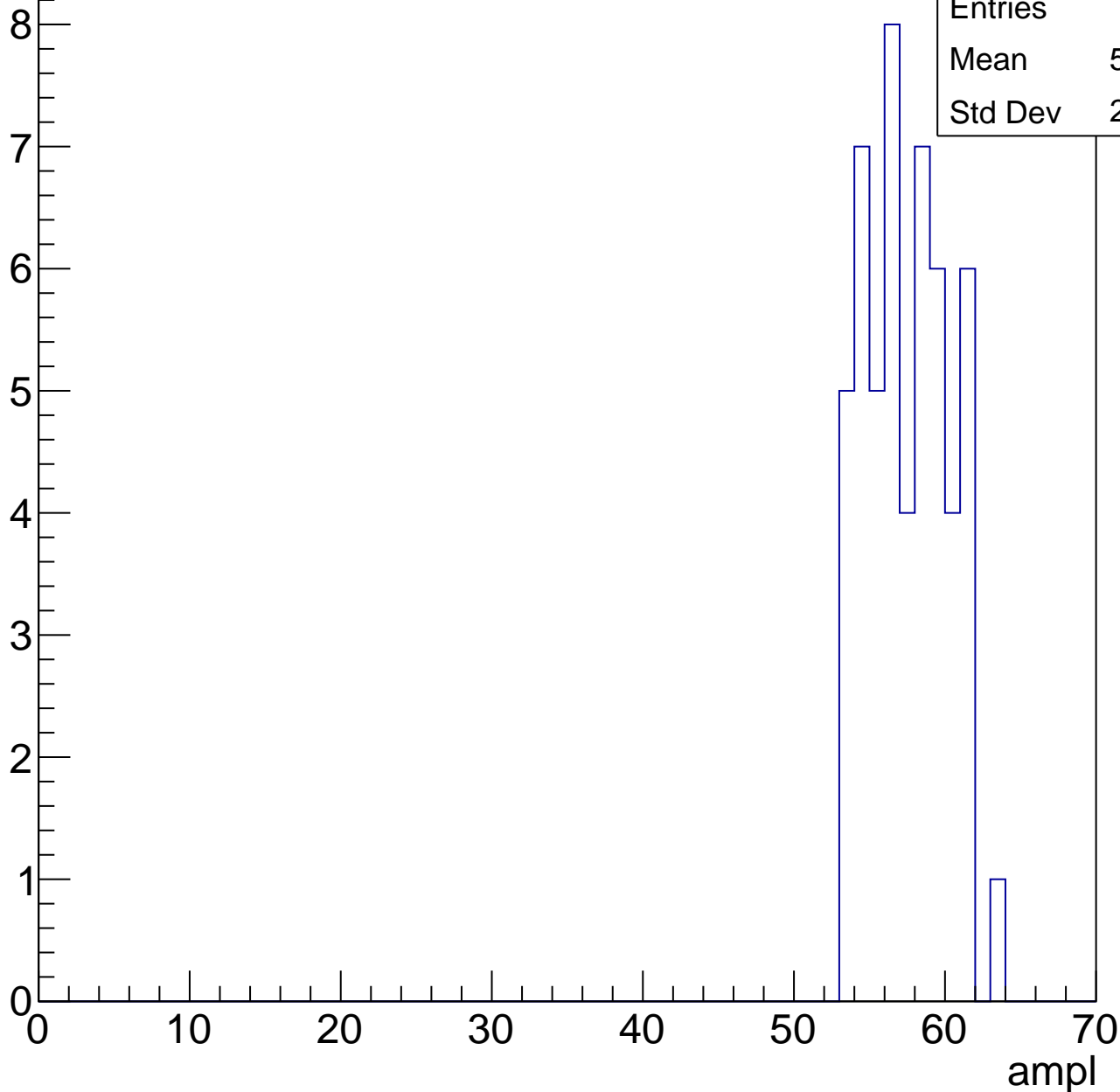


B0L001S, U23-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.04
Std Dev	2.642

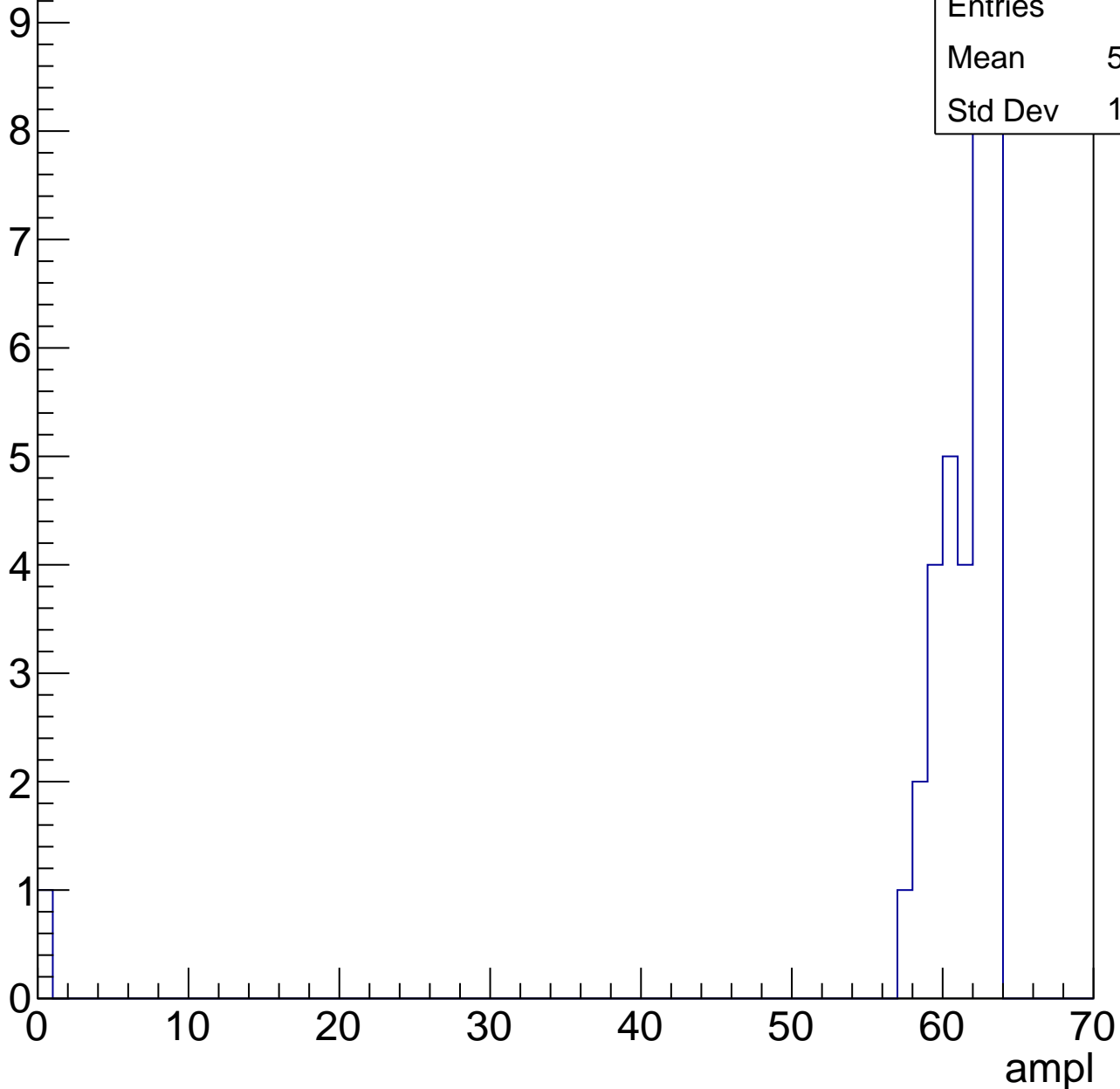


B0L001S, U23-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	59.29
Std Dev	10.46



B0L001S, U23-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch21, adc0

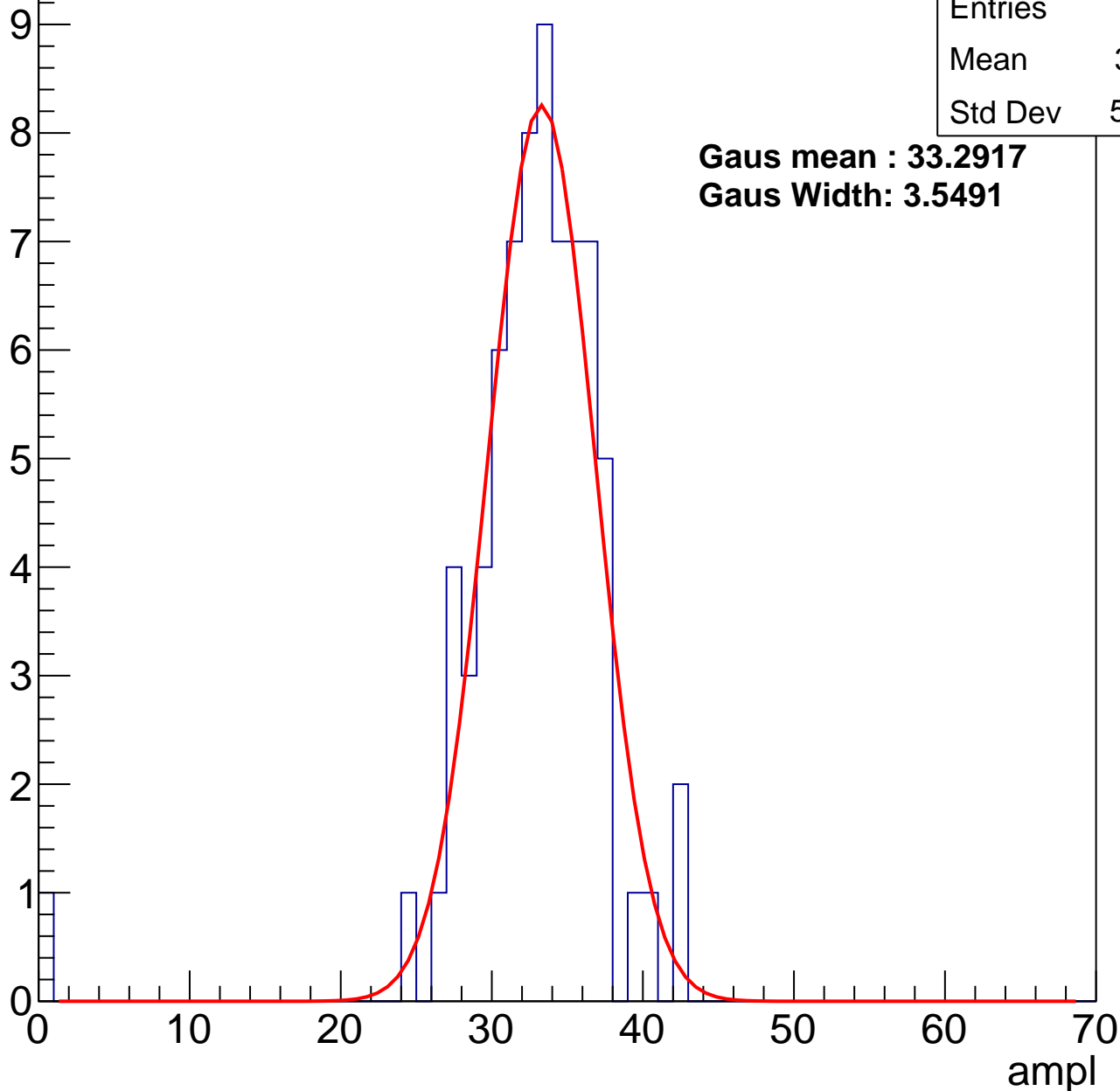
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	32.31
Std Dev	5.175

Gaus mean : 33.2917

Gaus Width: 3.5491



B0L001S, U23-ch21, adc1

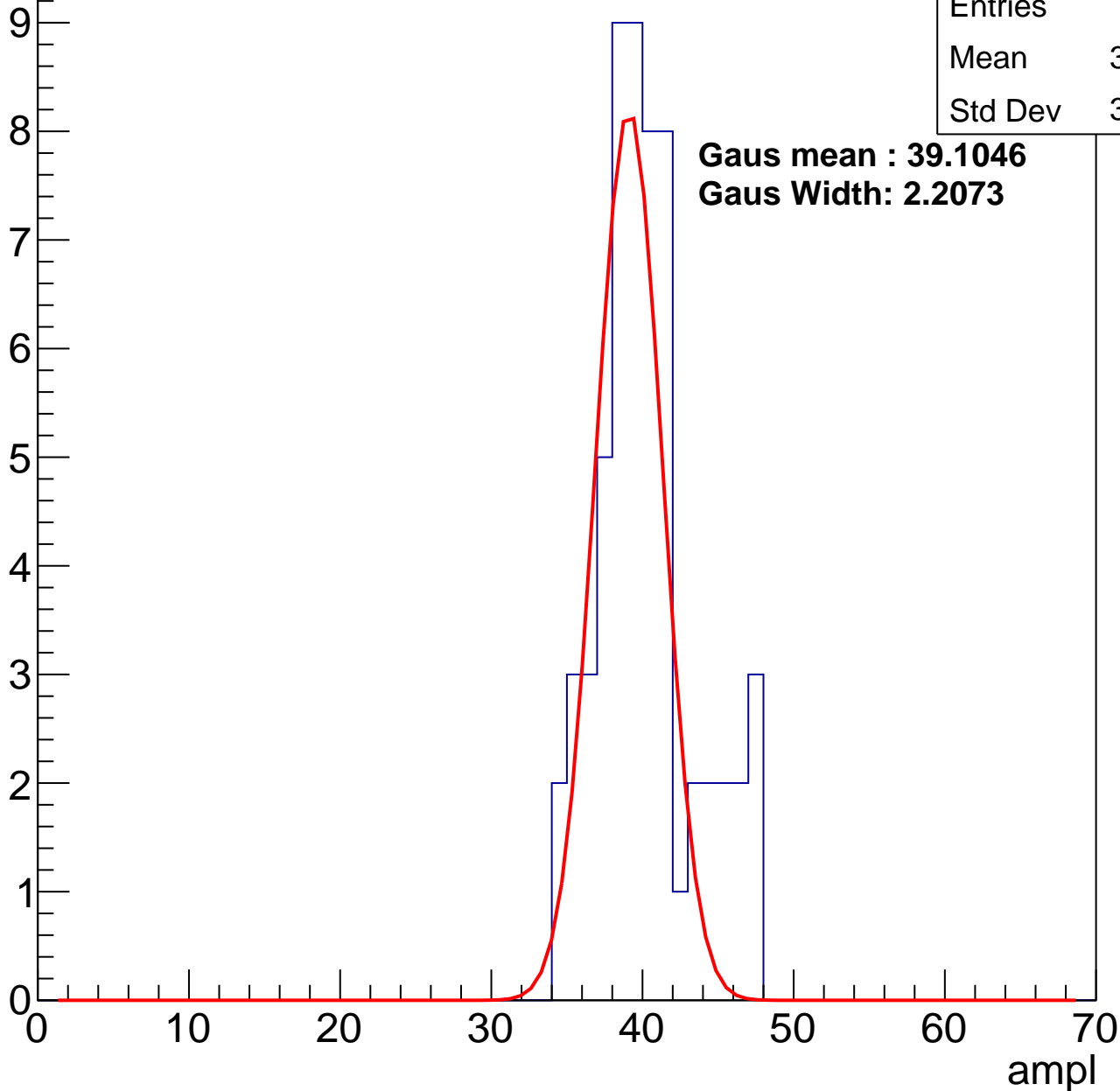
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	39.76
Std Dev	3.222

Gaus mean : 39.1046

Gaus Width: 2.2073



B0L001S, U23-ch21, adc2

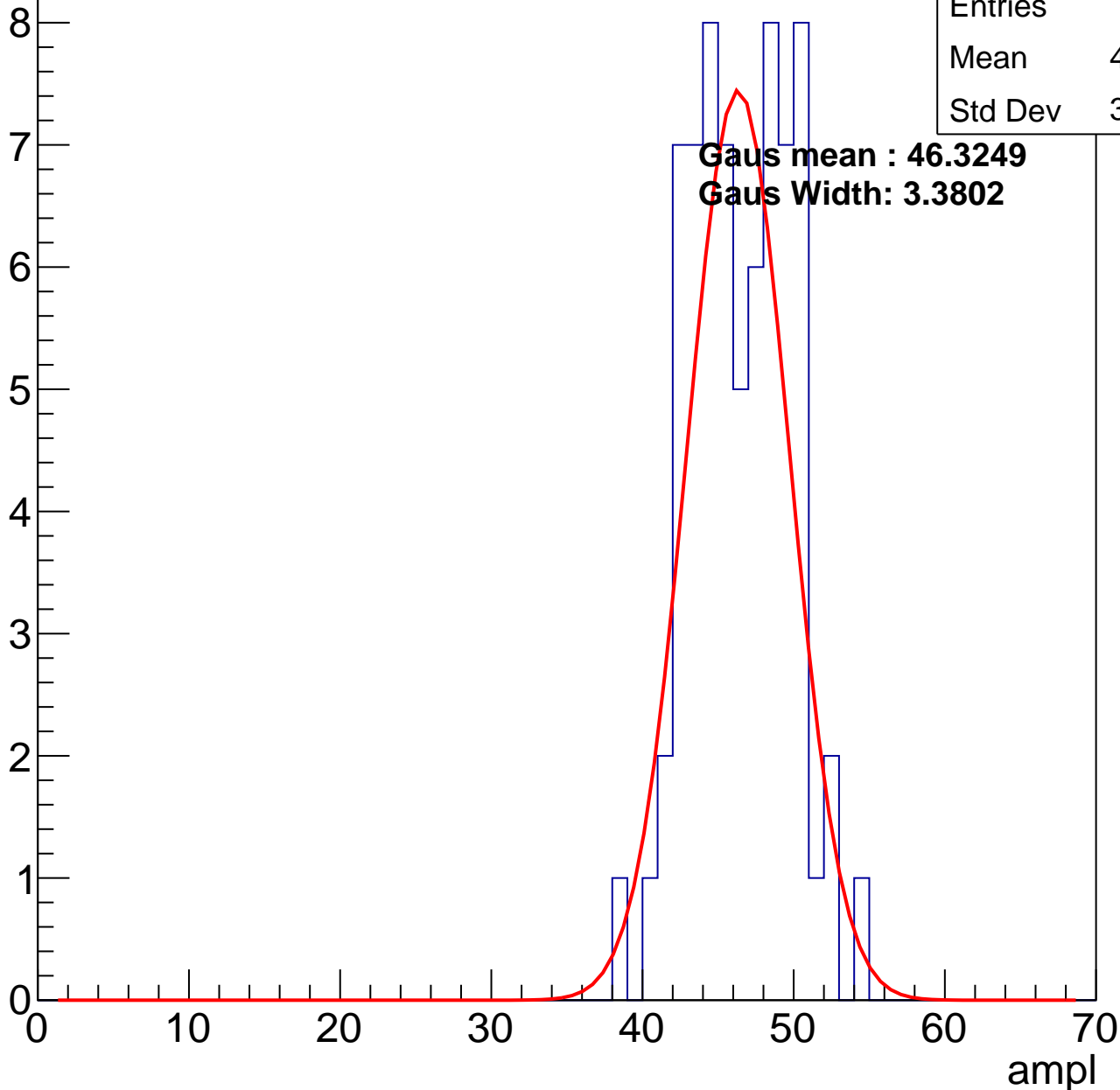
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	46.06
Std Dev	3.258

Gaus mean : 46.3249

Gaus Width: 3.3802

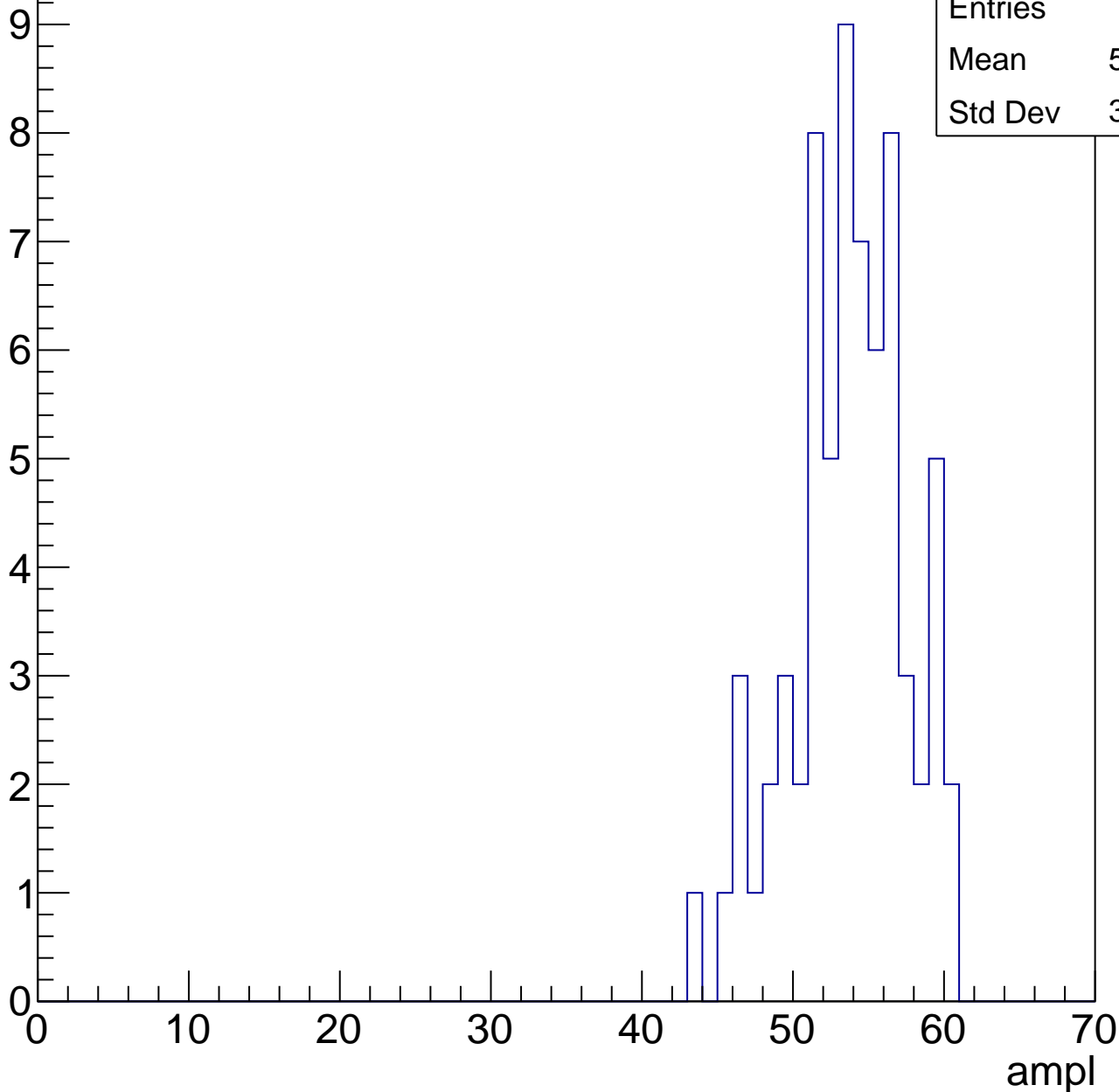


B0L001S, U23-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	53.22
Std Dev	3.788

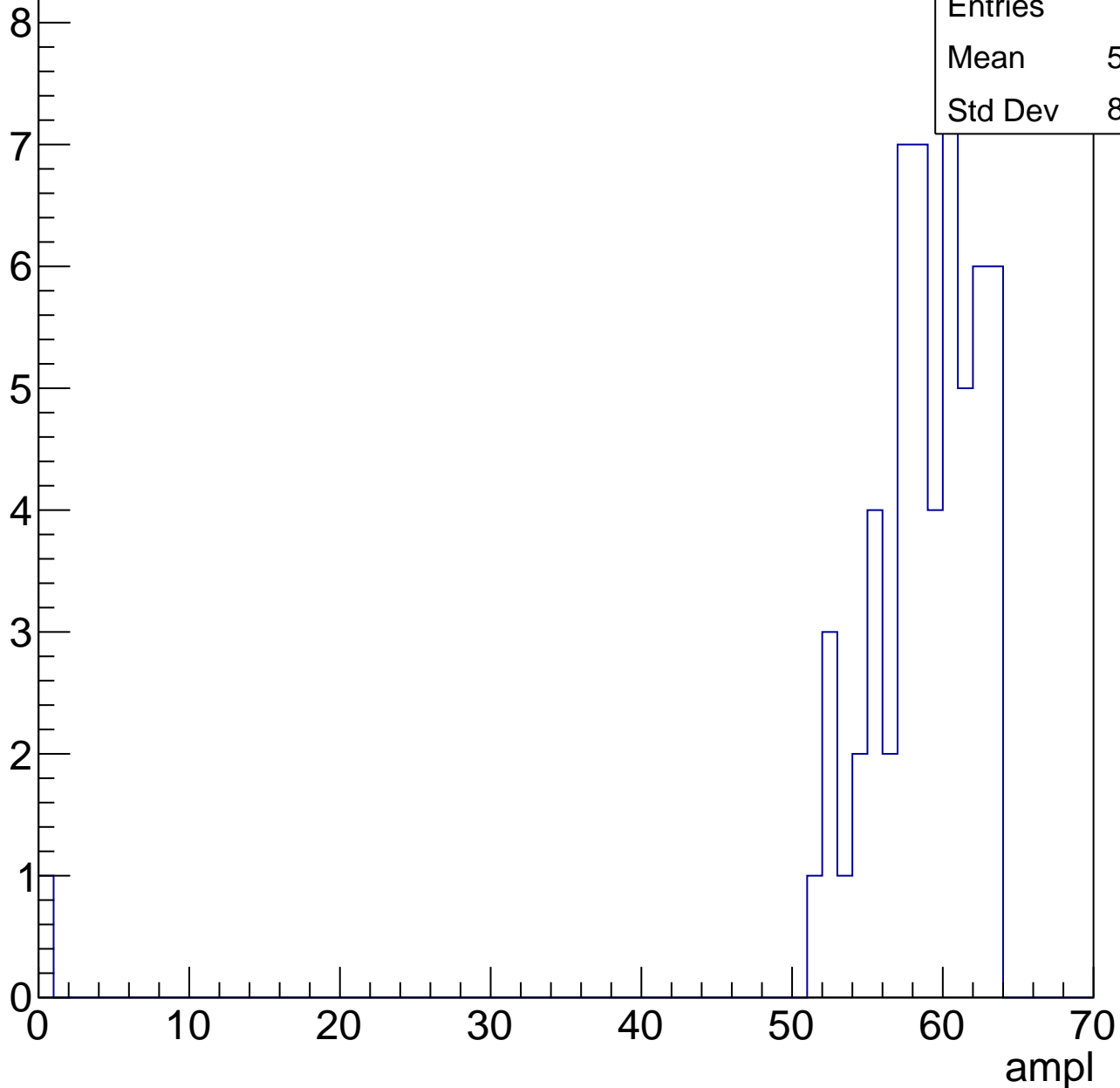


B0L001S, U23-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.47
Std Dev	8.314

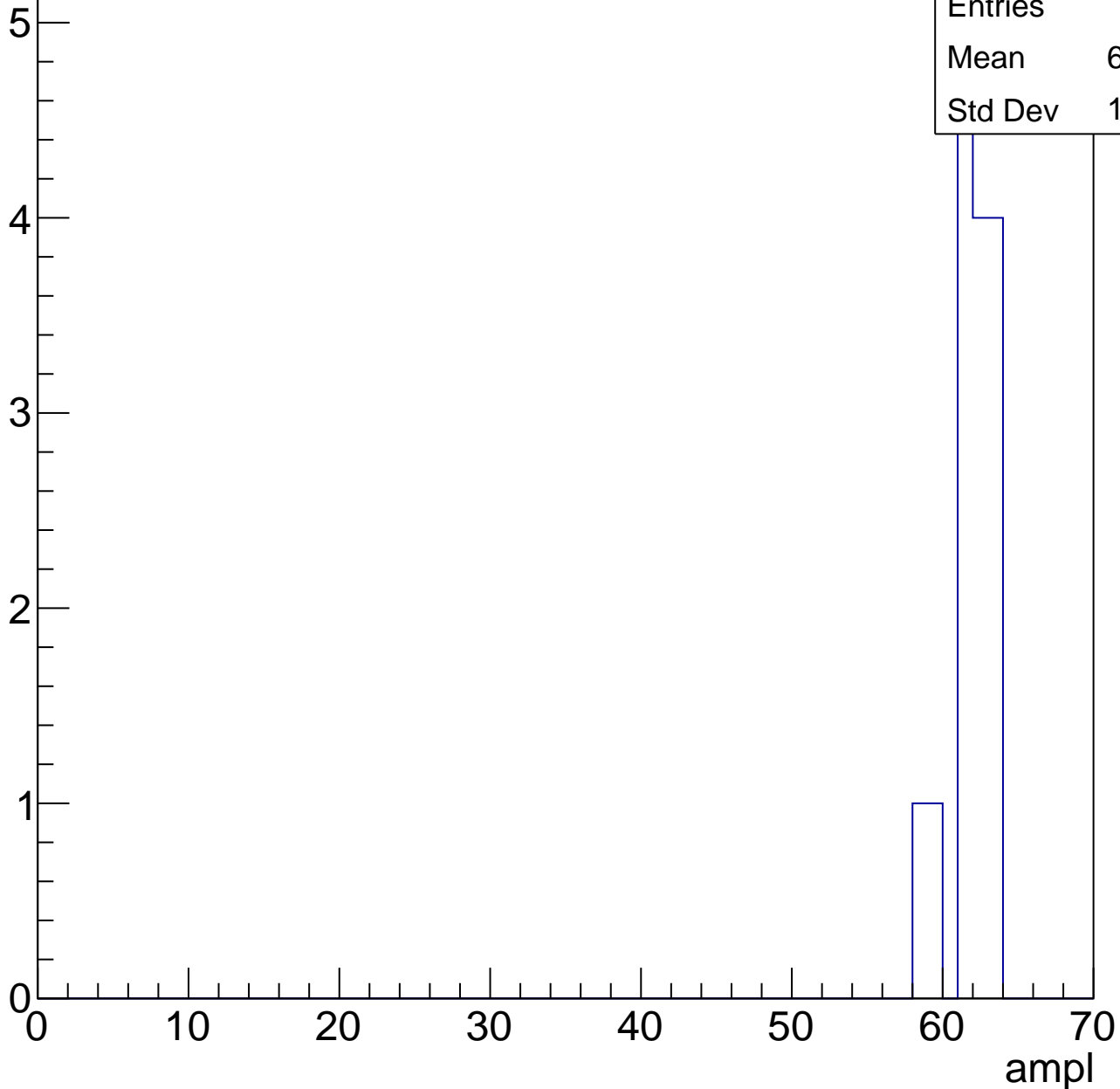


B0L001S, U23-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.47
Std Dev	1.408



B0L001S, U23-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch22, adc0

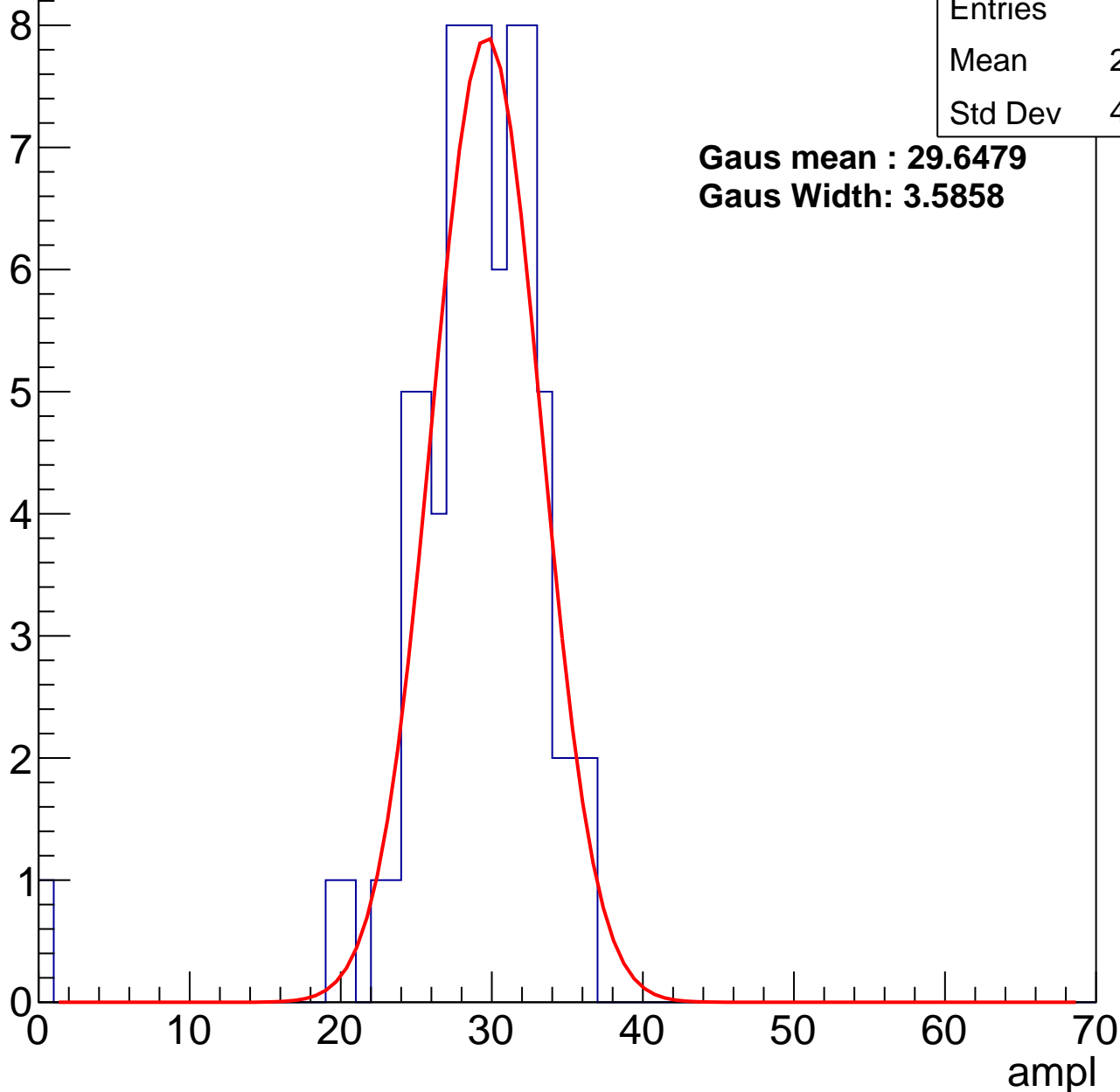
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	28.47
Std Dev	4.835

Gaus mean : 29.6479

Gaus Width: 3.5858



B0L001S, U23-ch22, adc1

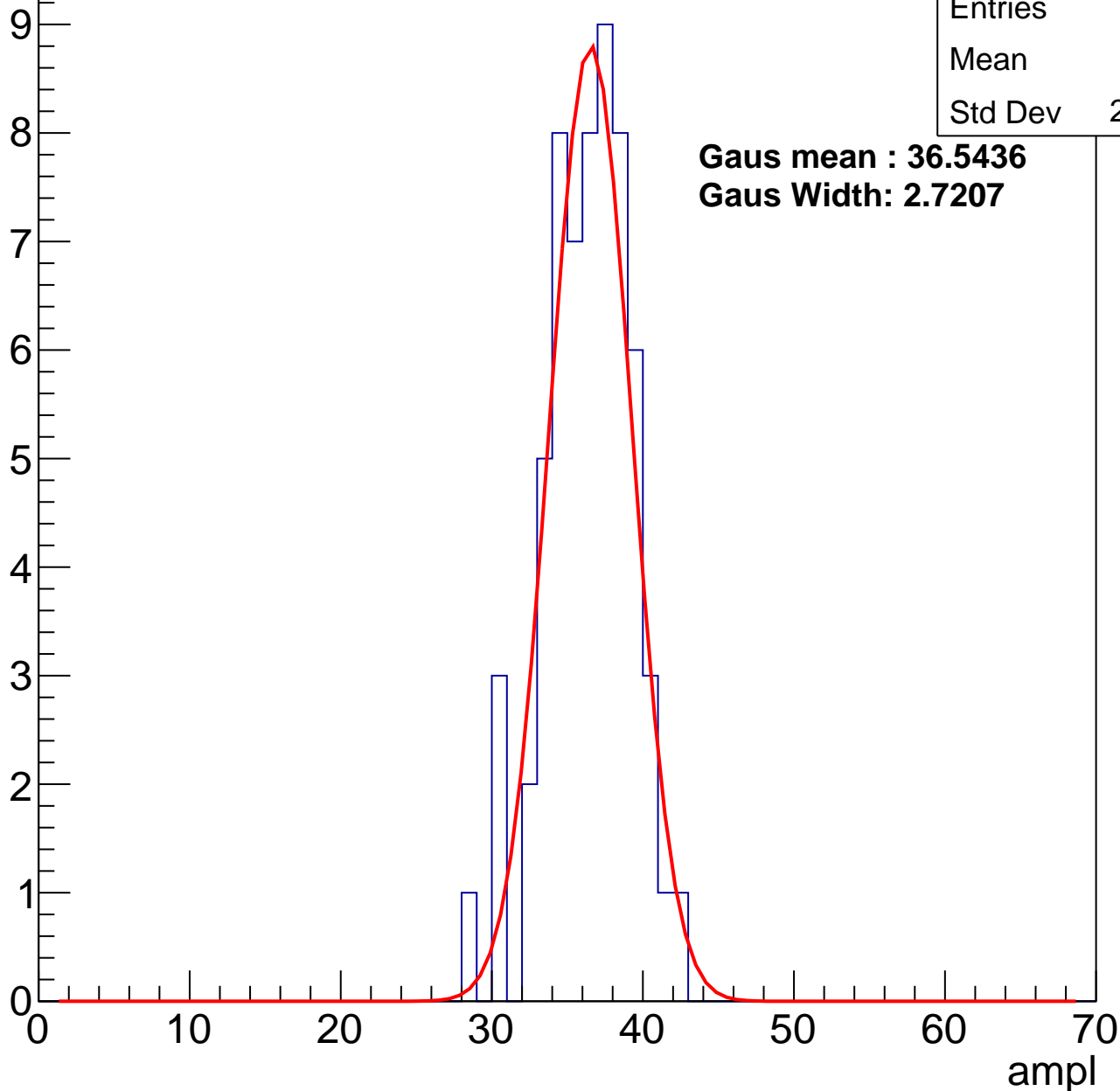
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	35.9
Std Dev	2.815

Gaus mean : 36.5436

Gaus Width: 2.7207



B0L001S, U23-ch22, adc2

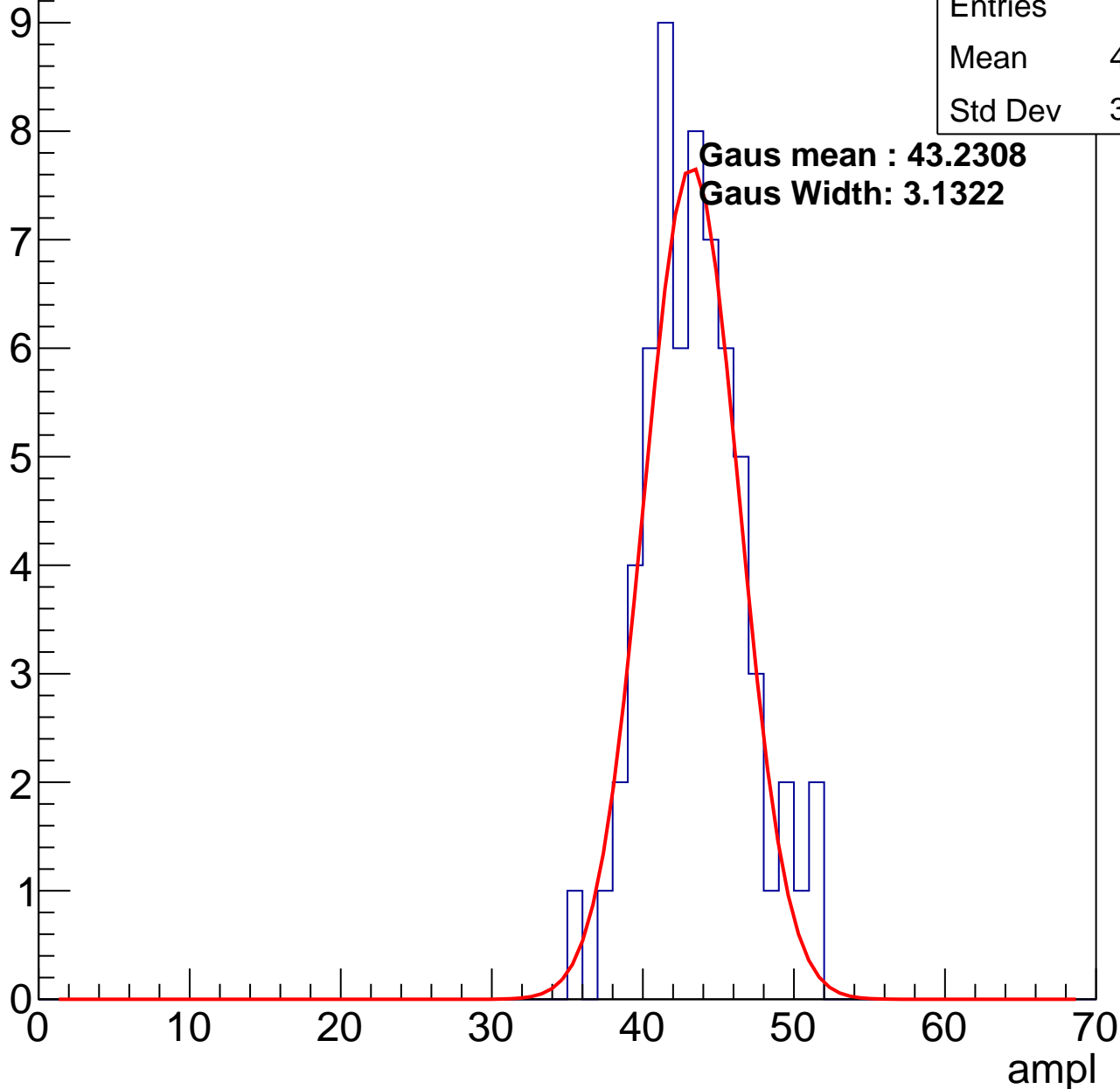
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.06
Std Dev	3.325

Gaus mean : 43.2308

Gaus Width: 3.1322

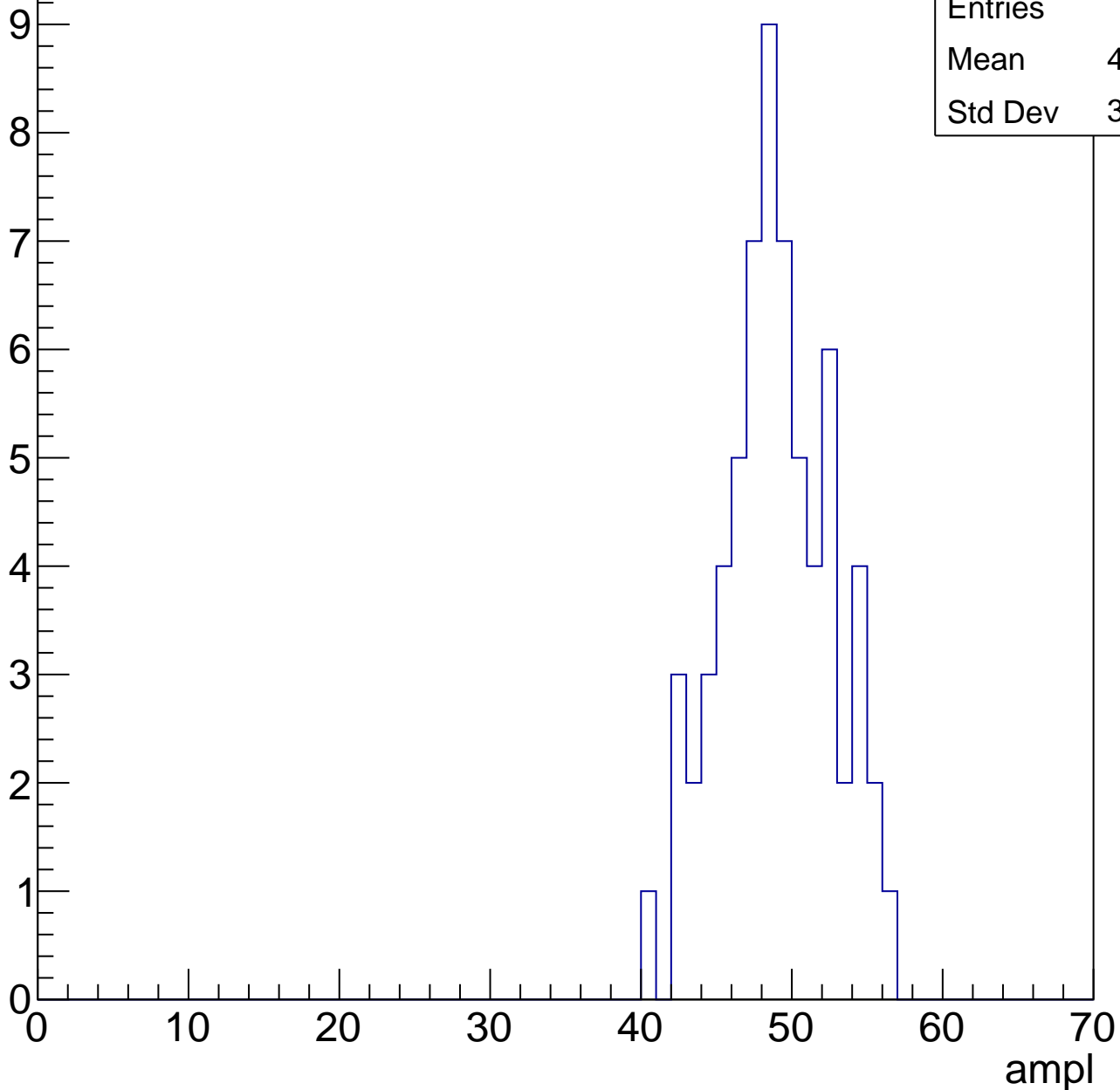


B0L001S, U23-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	48.49
Std Dev	3.578

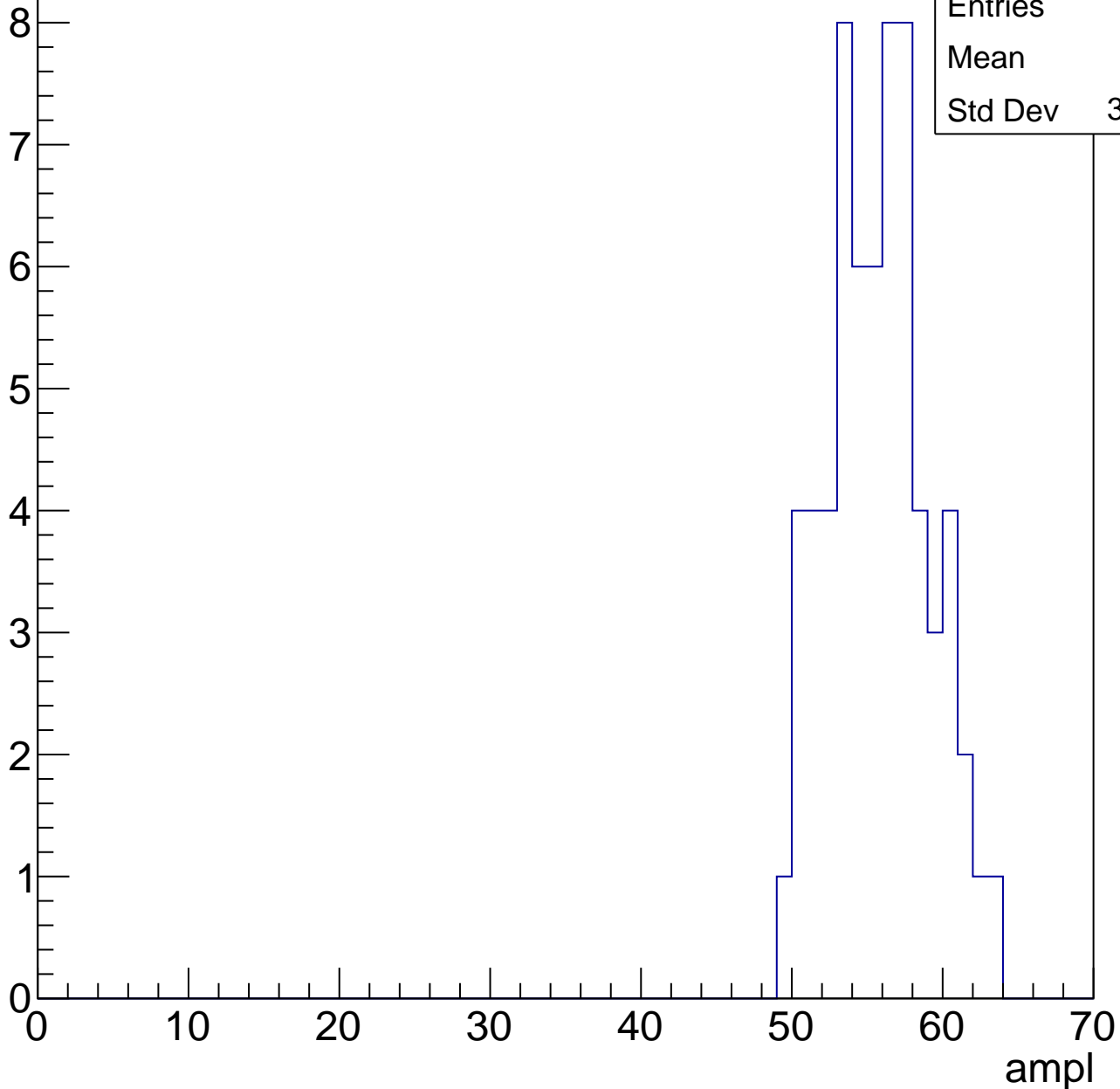


B0L001S, U23-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.3
Std Dev	3.253

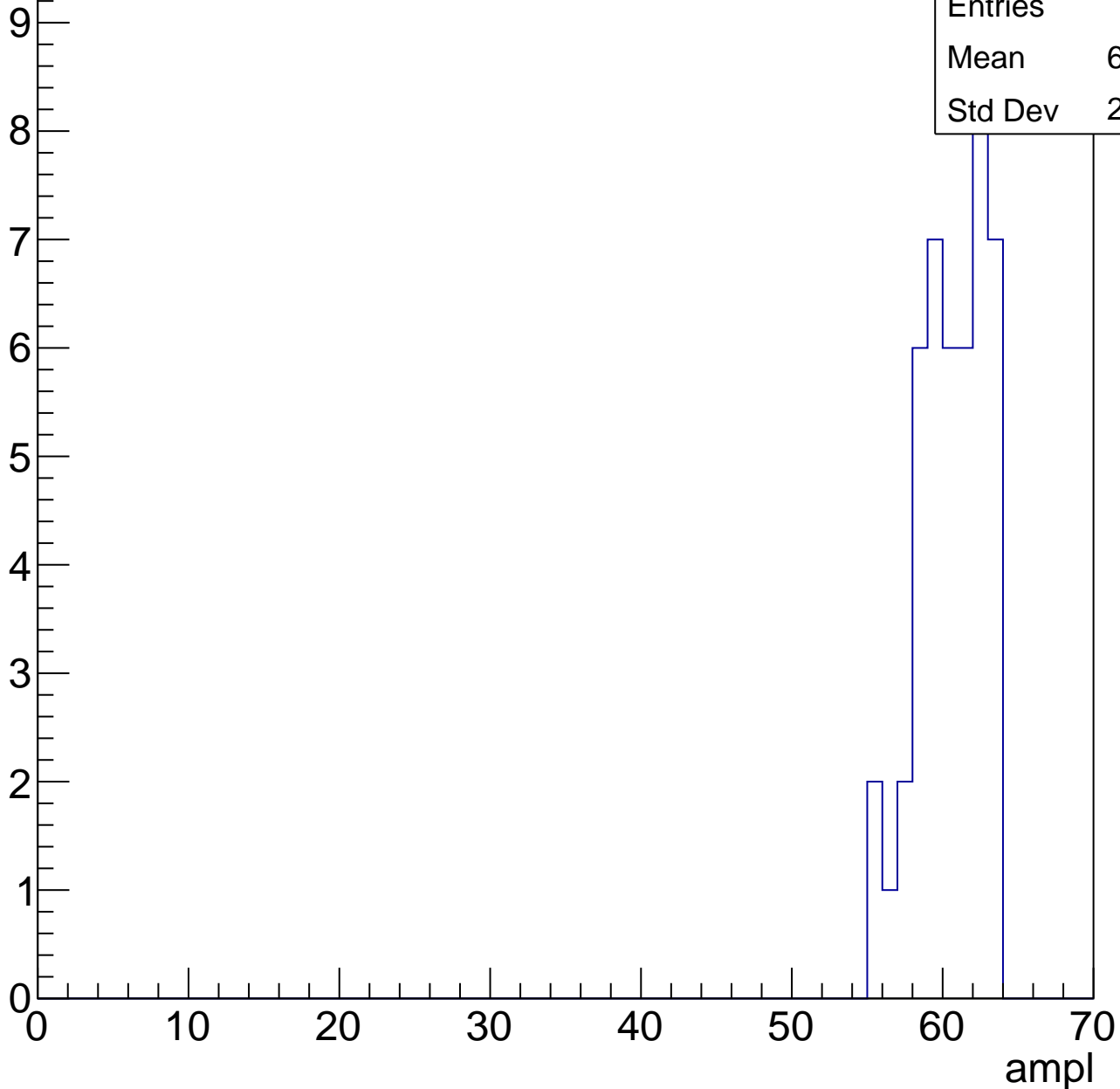


B0L001S, U23-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

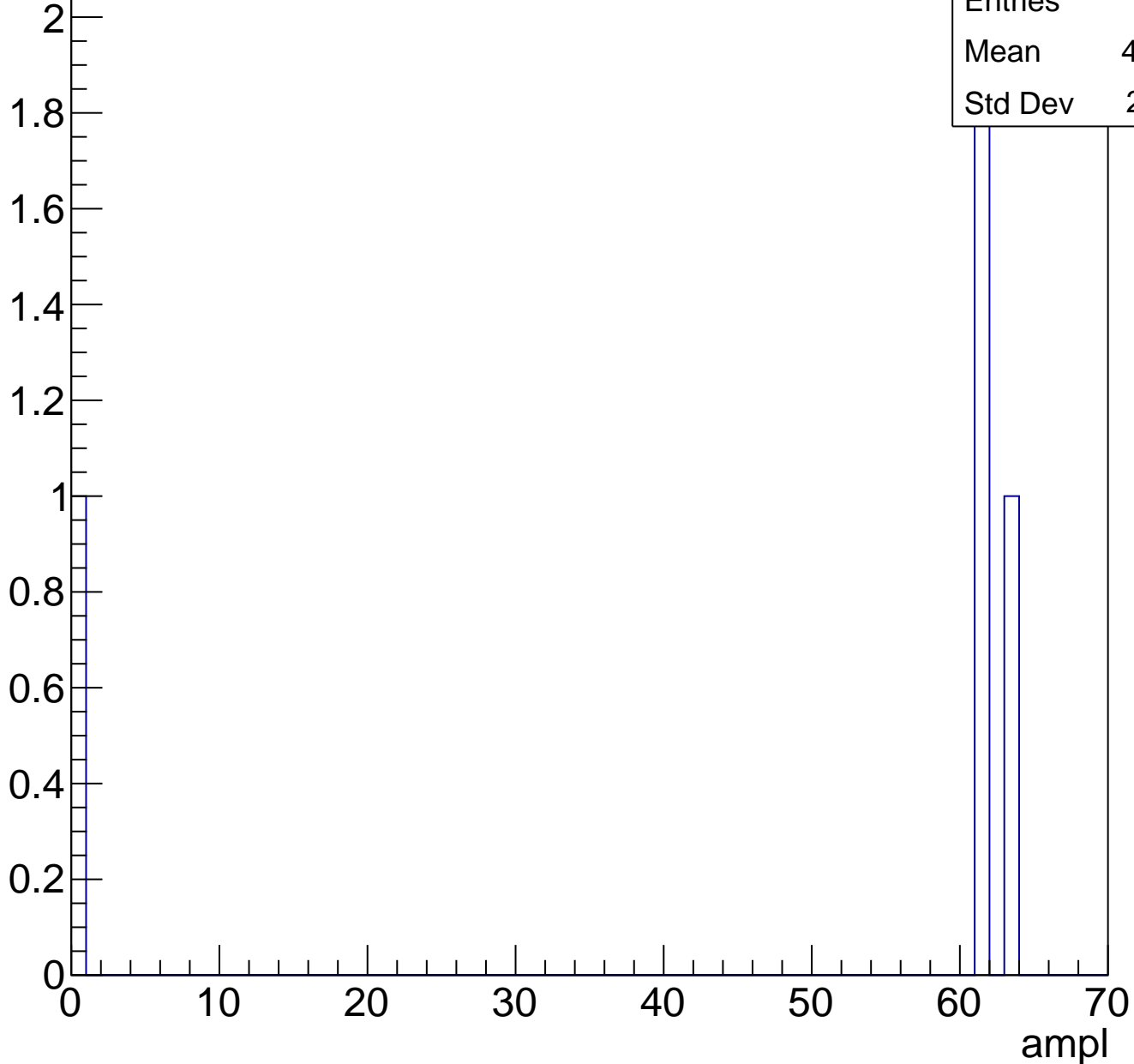
Entries	46
Mean	60.13
Std Dev	2.183



B0L001S, U23-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

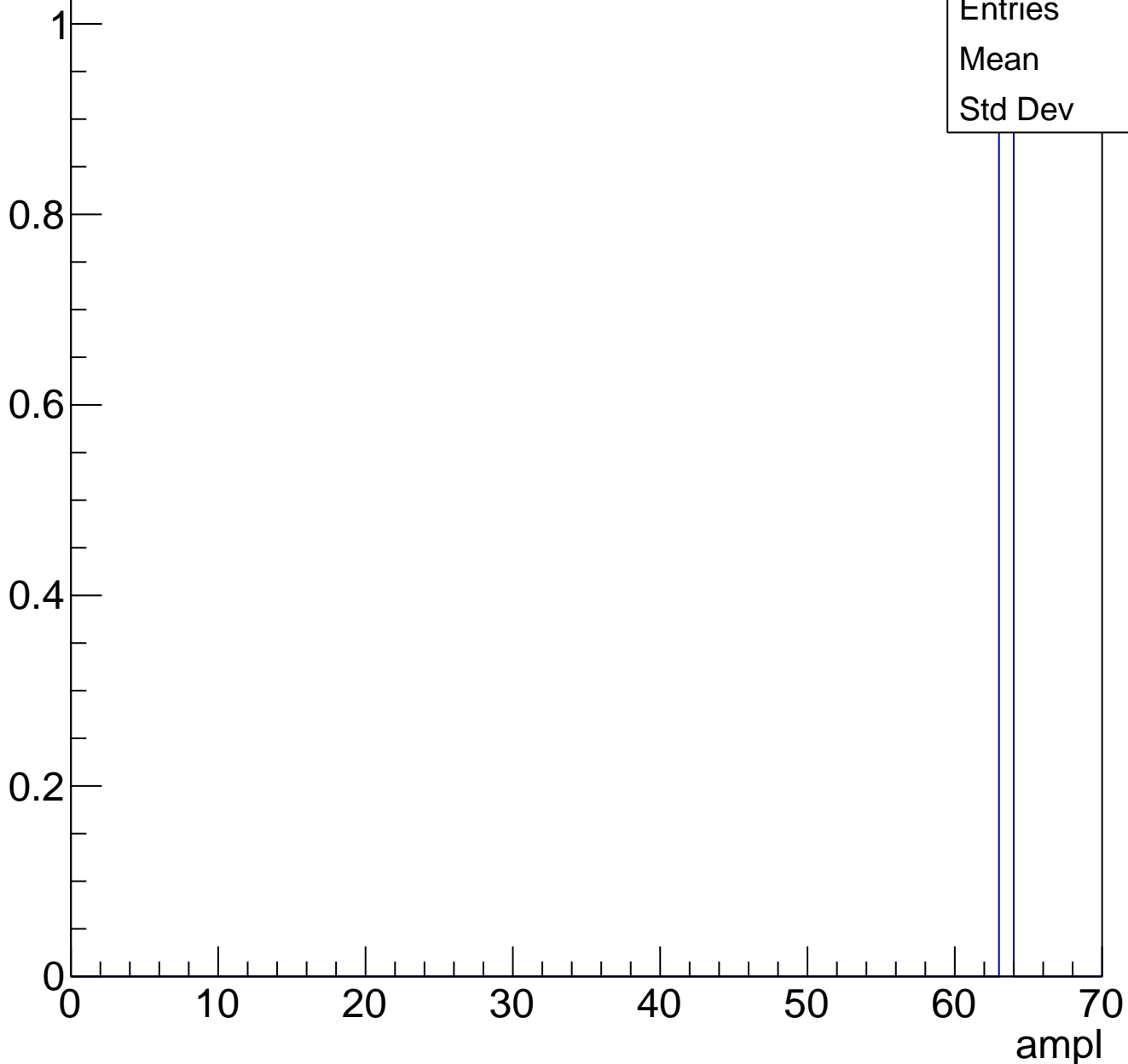
Entry



B0L001S, U23-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch23, adc0

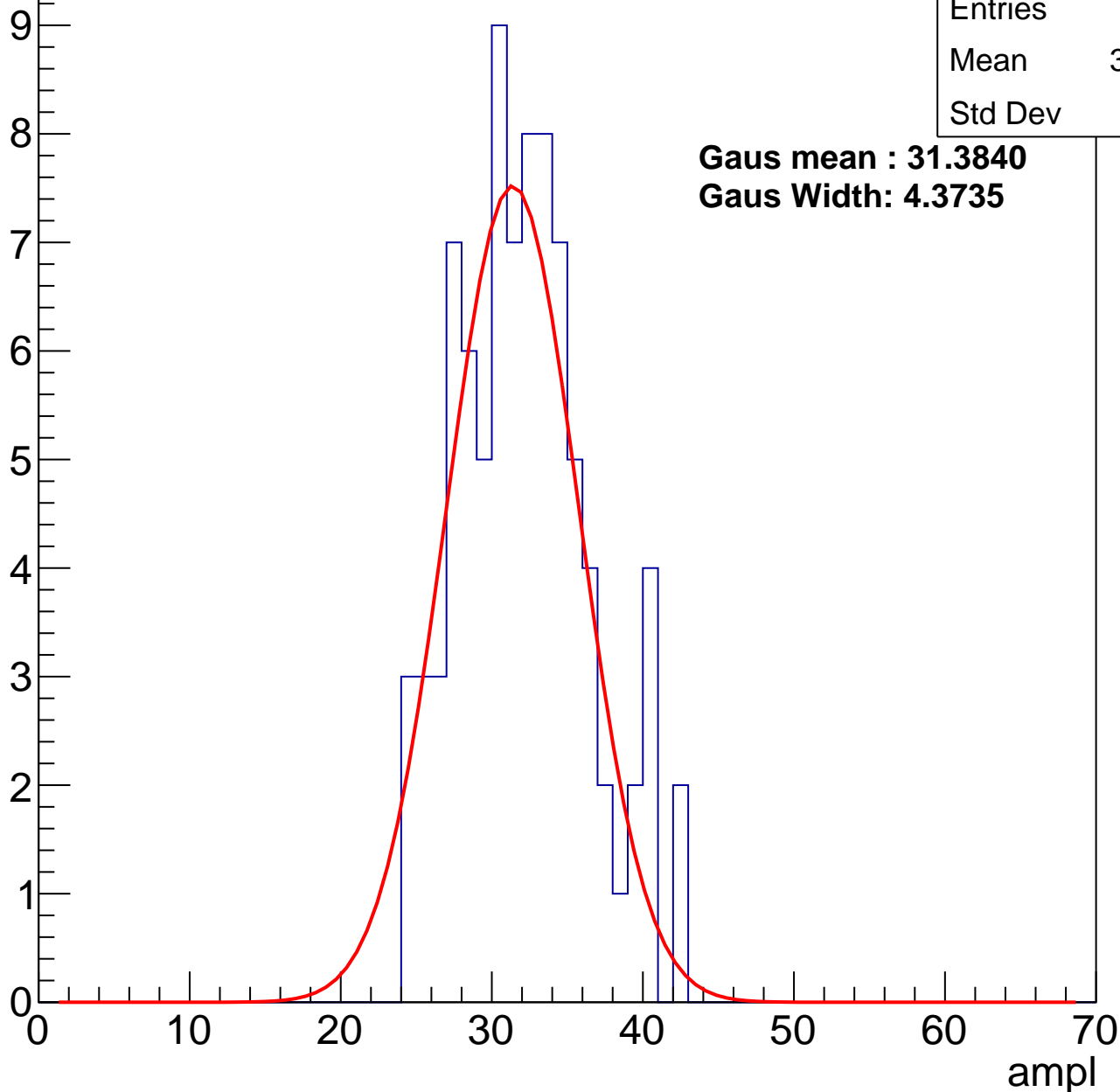
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	31.69
Std Dev	4.29

Gaus mean : 31.3840

Gaus Width: 4.3735



B0L001S, U23-ch23, adc1

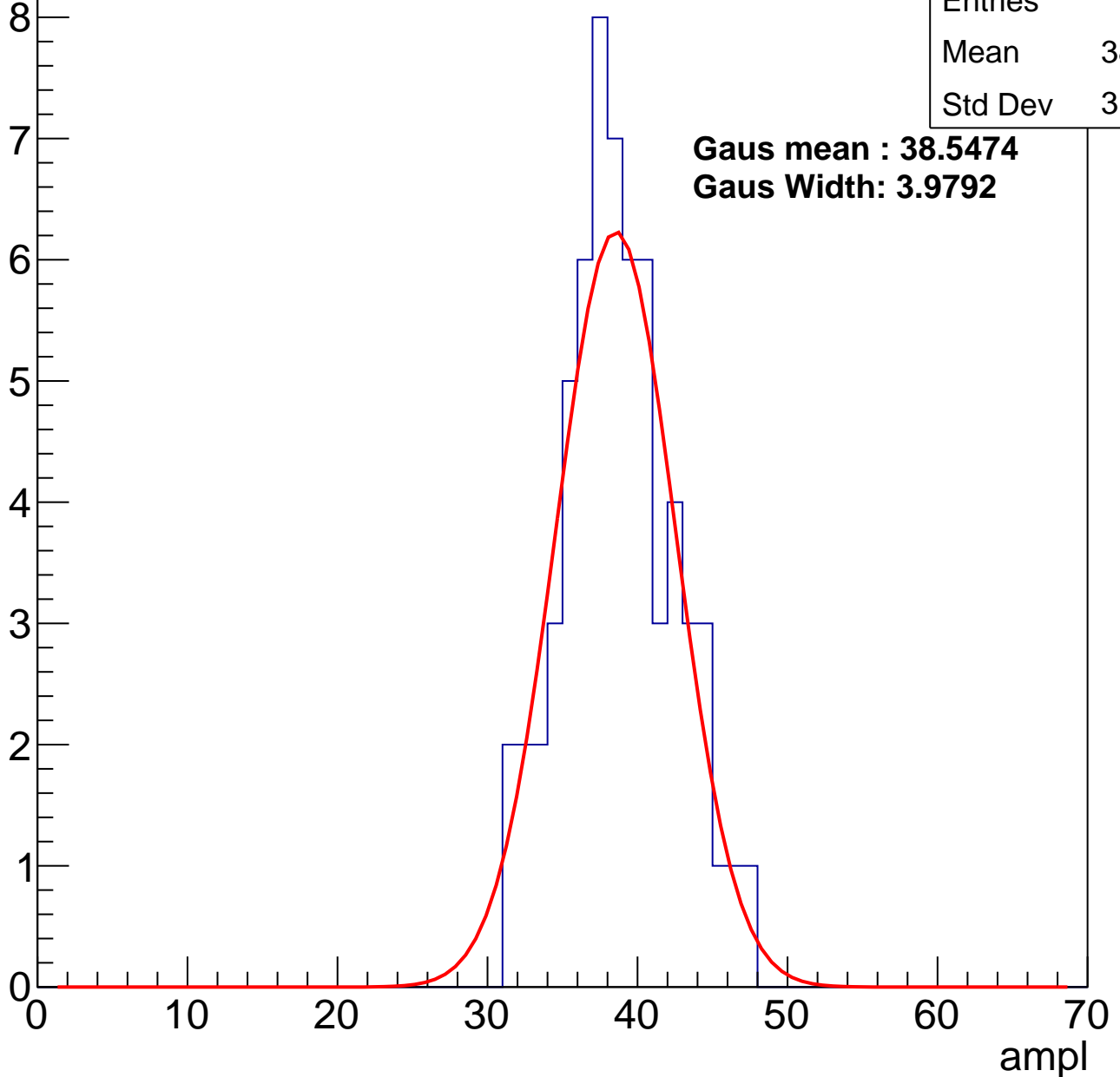
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.27
Std Dev	3.648

Gaus mean : 38.5474

Gaus Width: 3.9792



B0L001S, U23-ch23, adc2

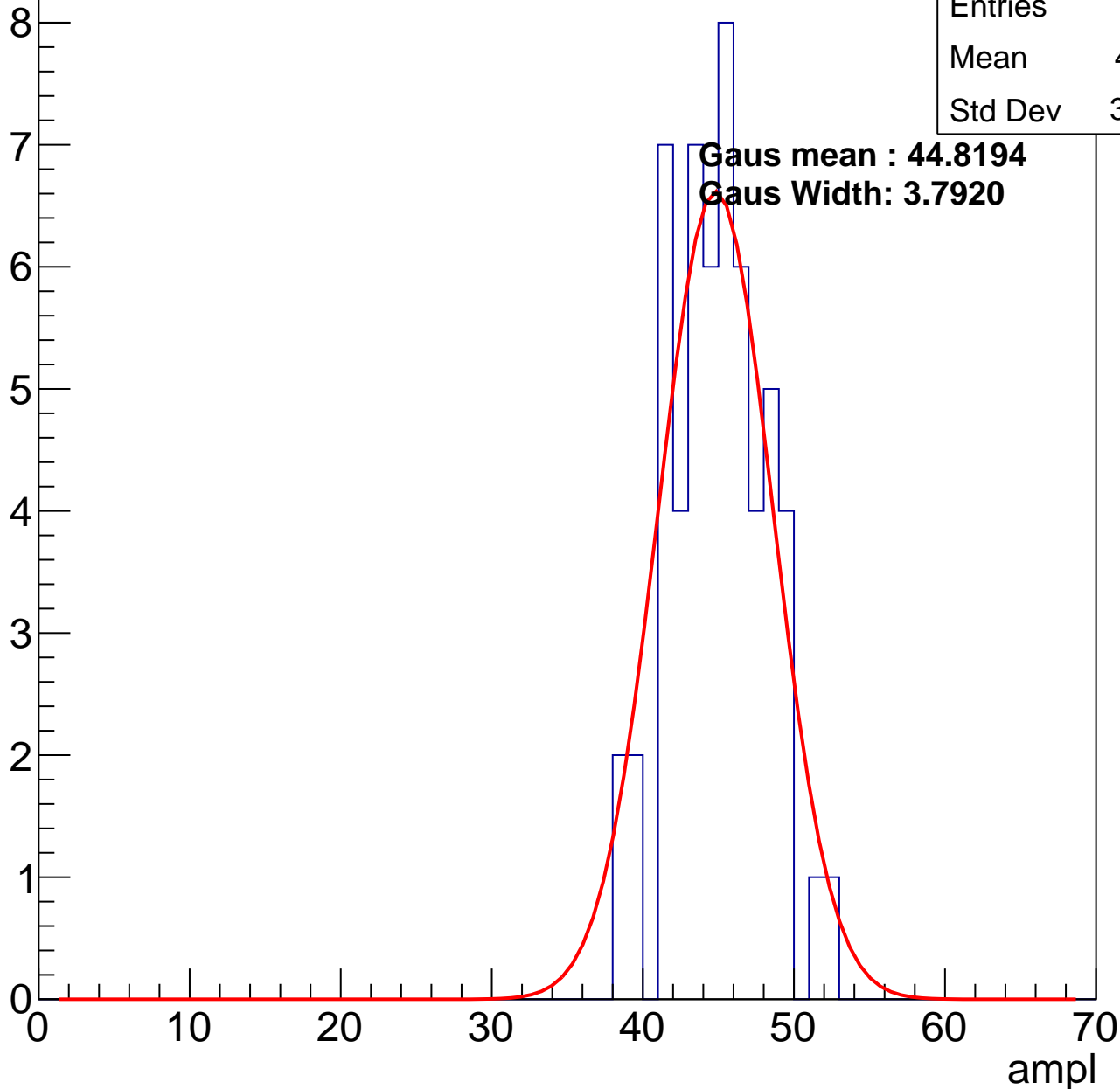
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.51
Std Dev	3.118

Gaus mean : 44.8194

Gaus Width: 3.7920

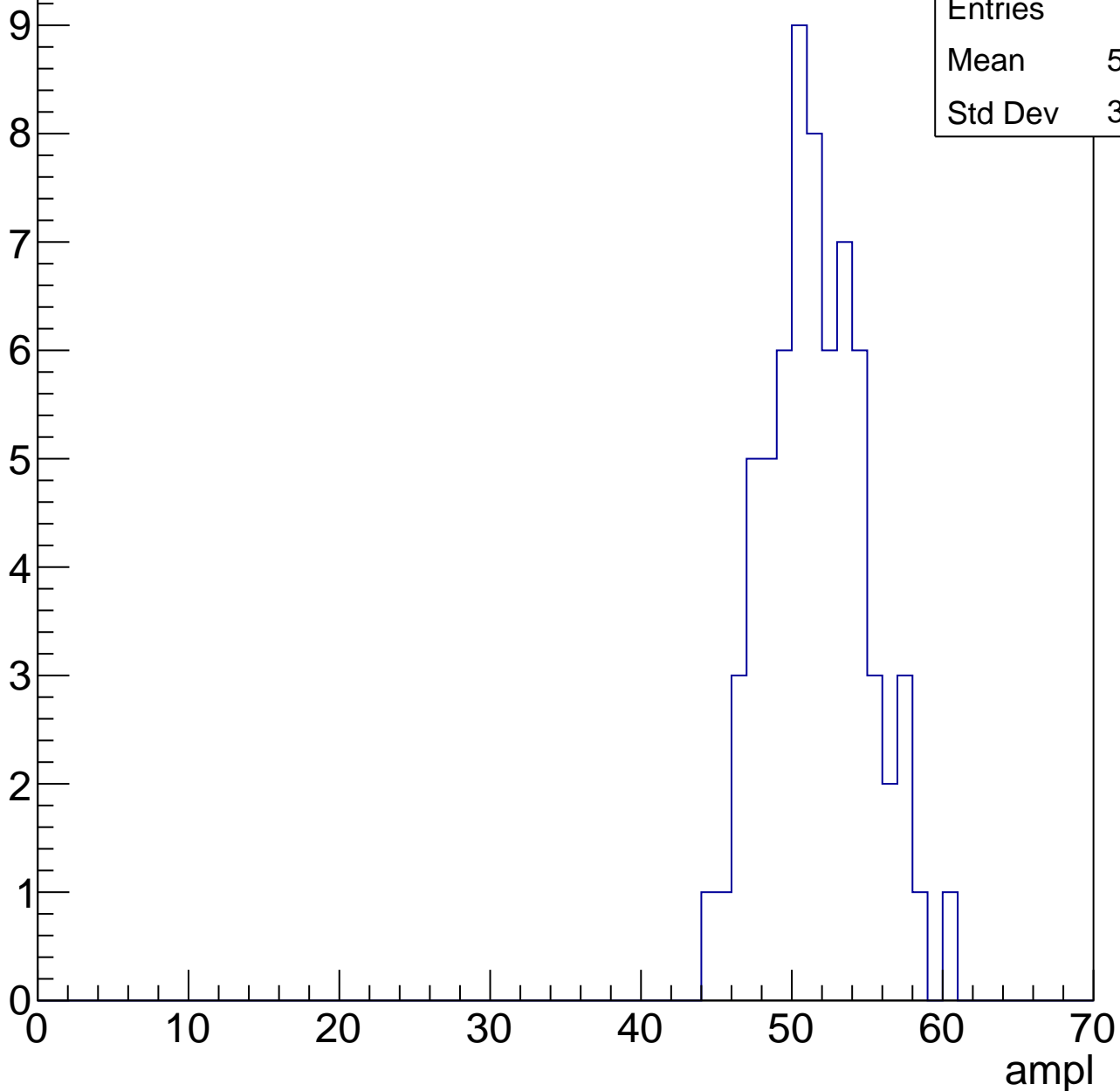


B0L001S, U23-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	51.15
Std Dev	3.325

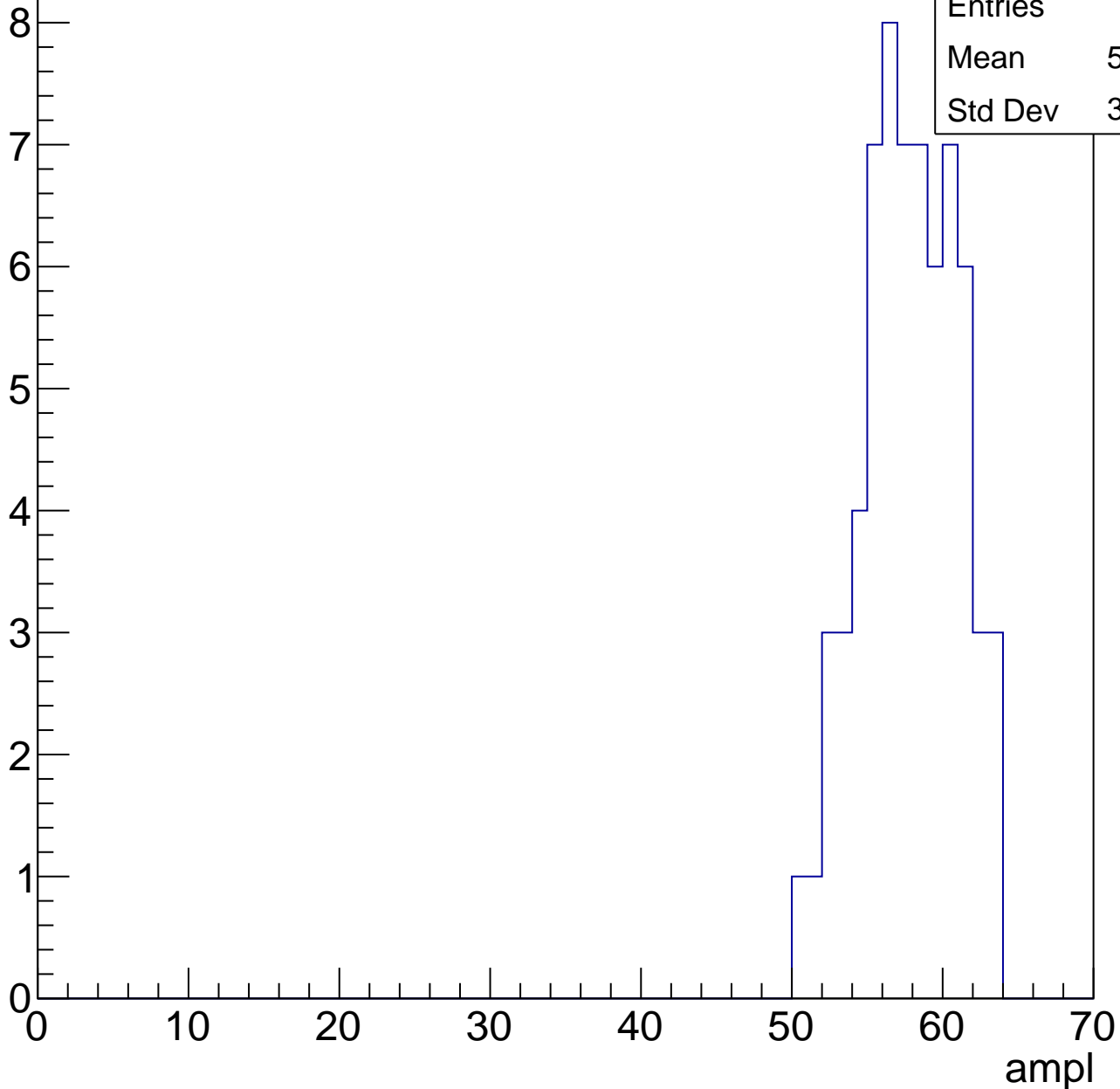


B0L001S, U23-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	57.35
Std Dev	3.126

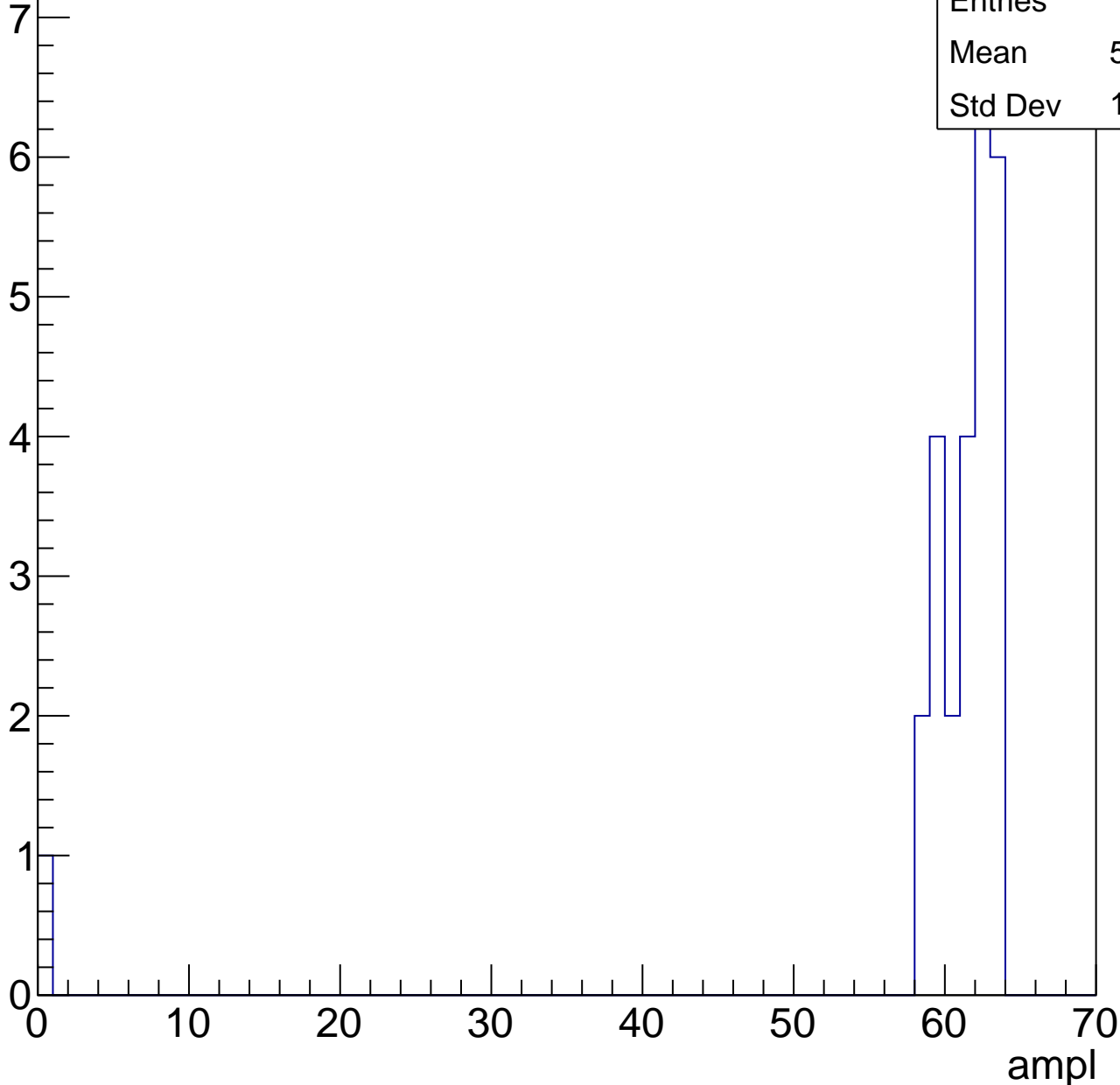


B0L001S, U23-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.77
Std Dev	11.86



B0L001S, U23-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch24, adc0

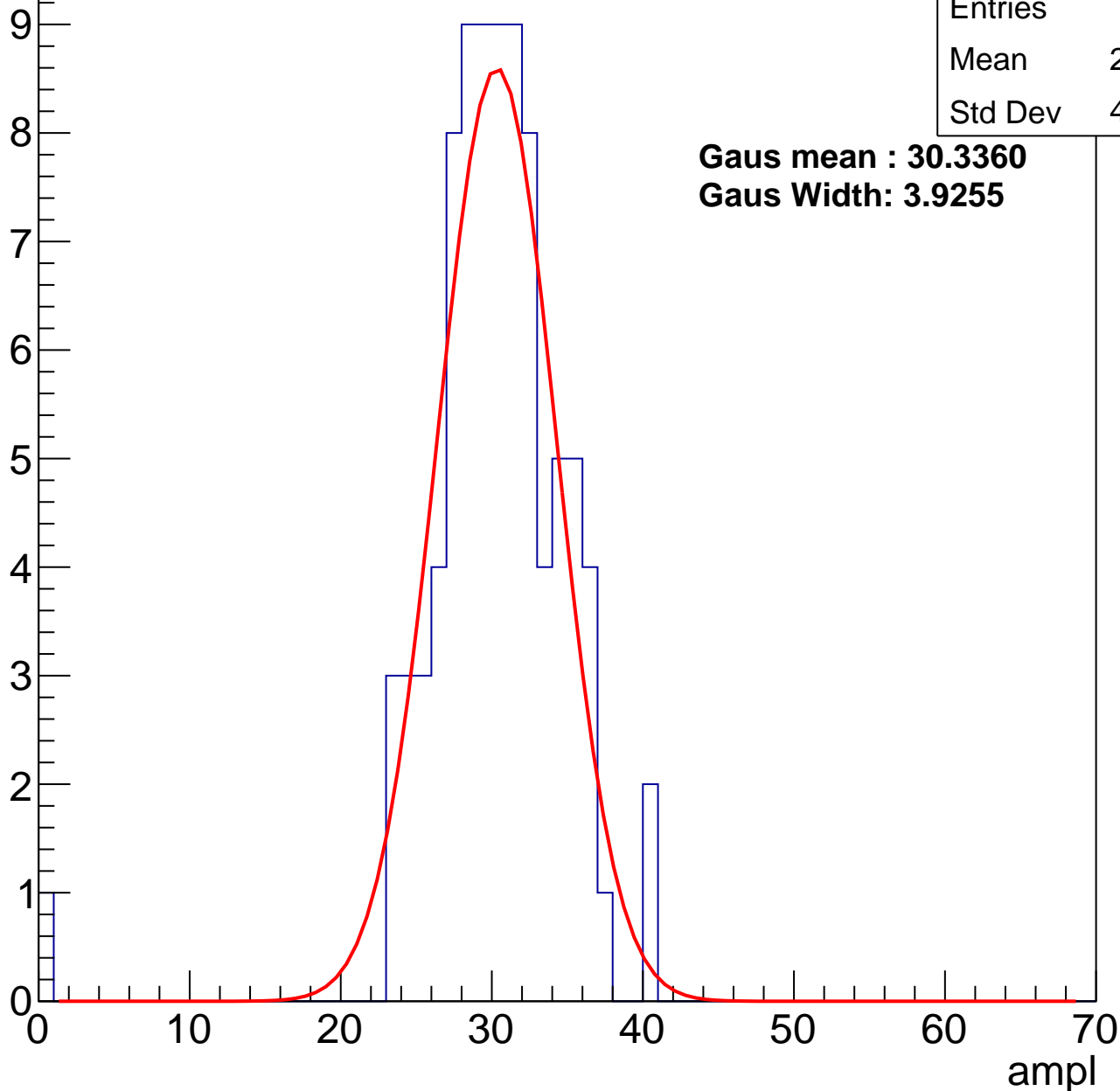
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	29.79
Std Dev	4.892

Gaus mean : 30.3360

Gaus Width: 3.9255



B0L001S, U23-ch24, adc1

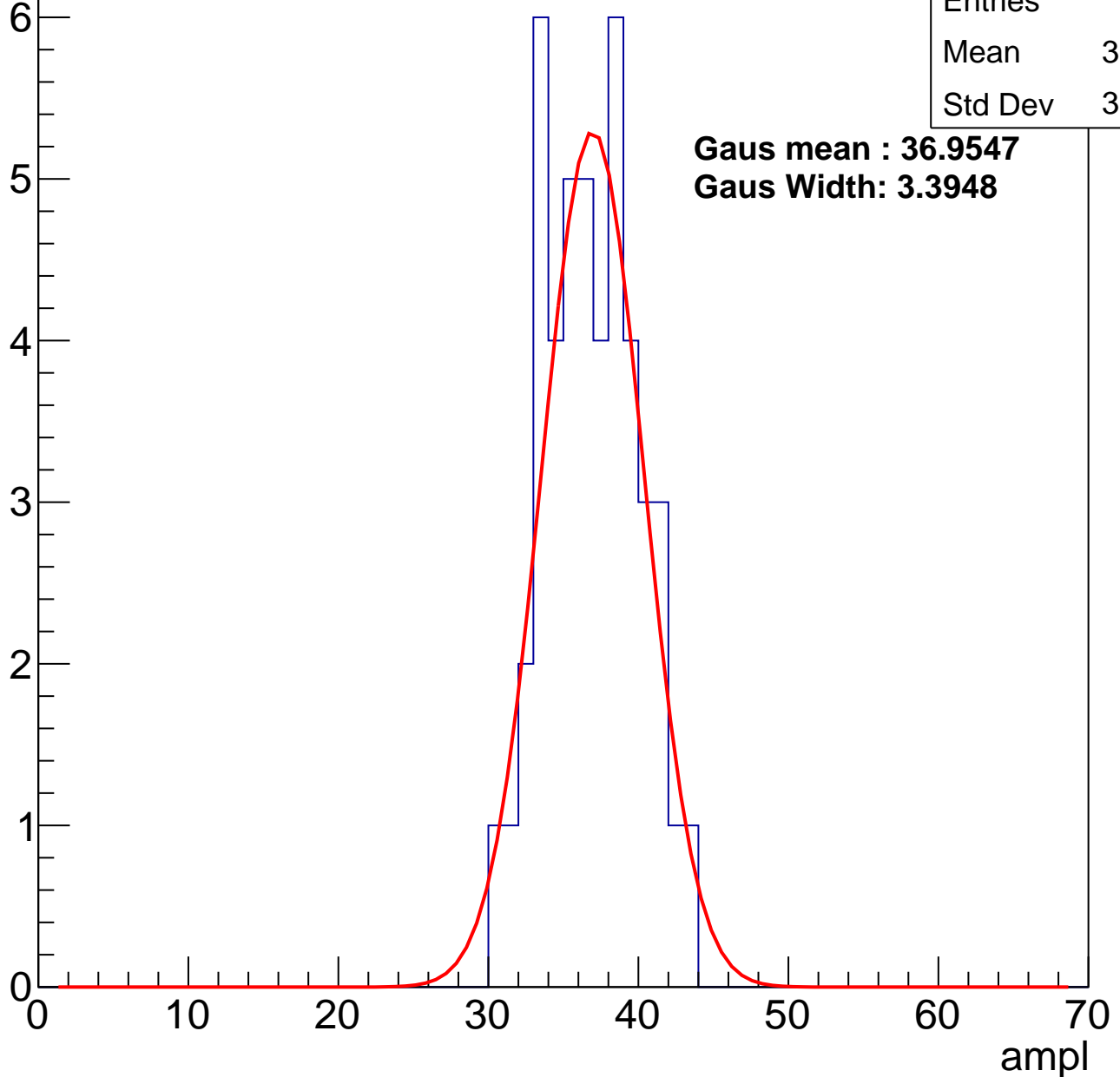
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	36.39
Std Dev	3.068

Gaus mean : 36.9547

Gaus Width: 3.3948



B0L001S, U23-ch24, adc2

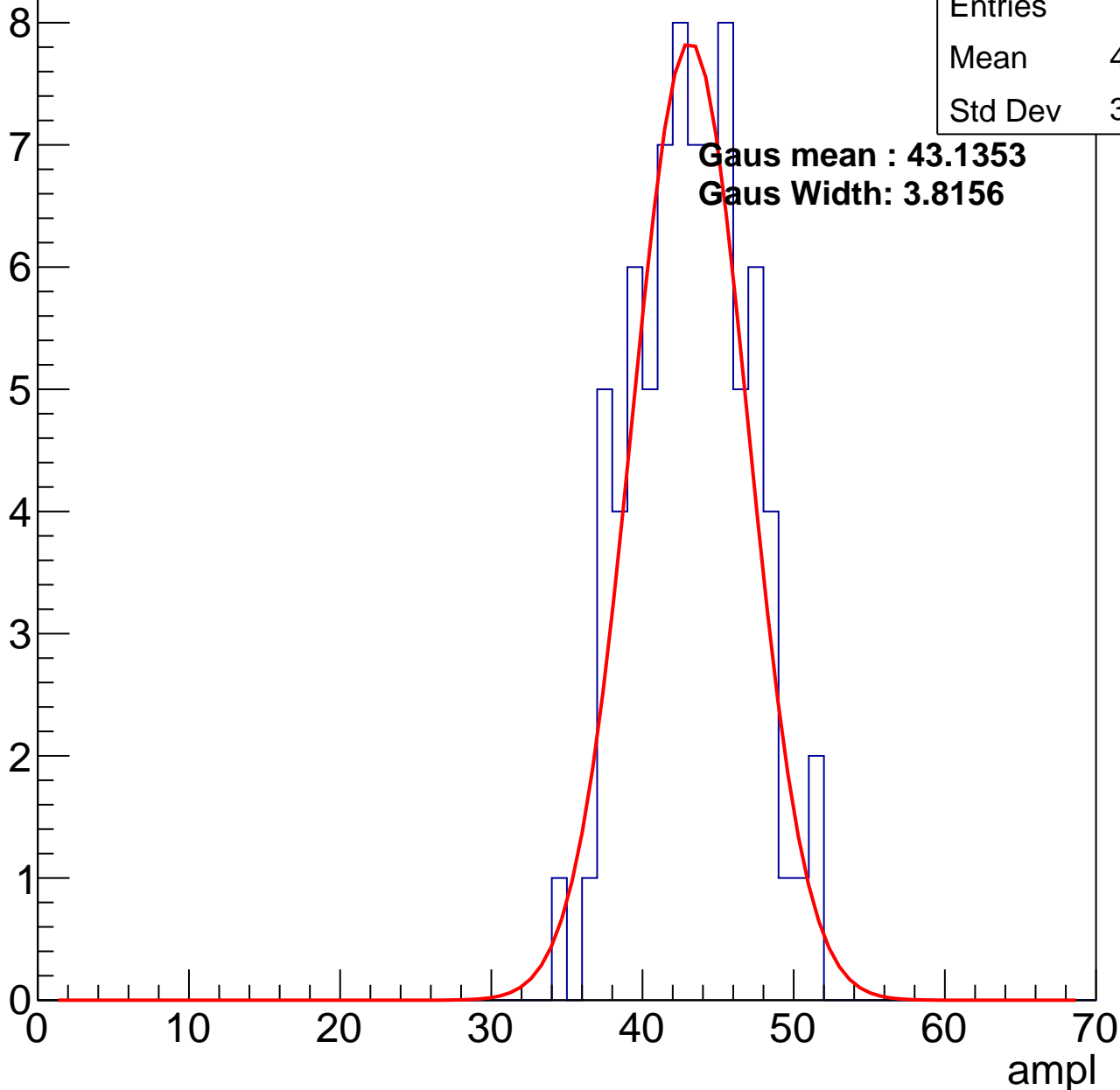
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	42.79
Std Dev	3.715

Gaus mean : 43.1353

Gaus Width: 3.8156

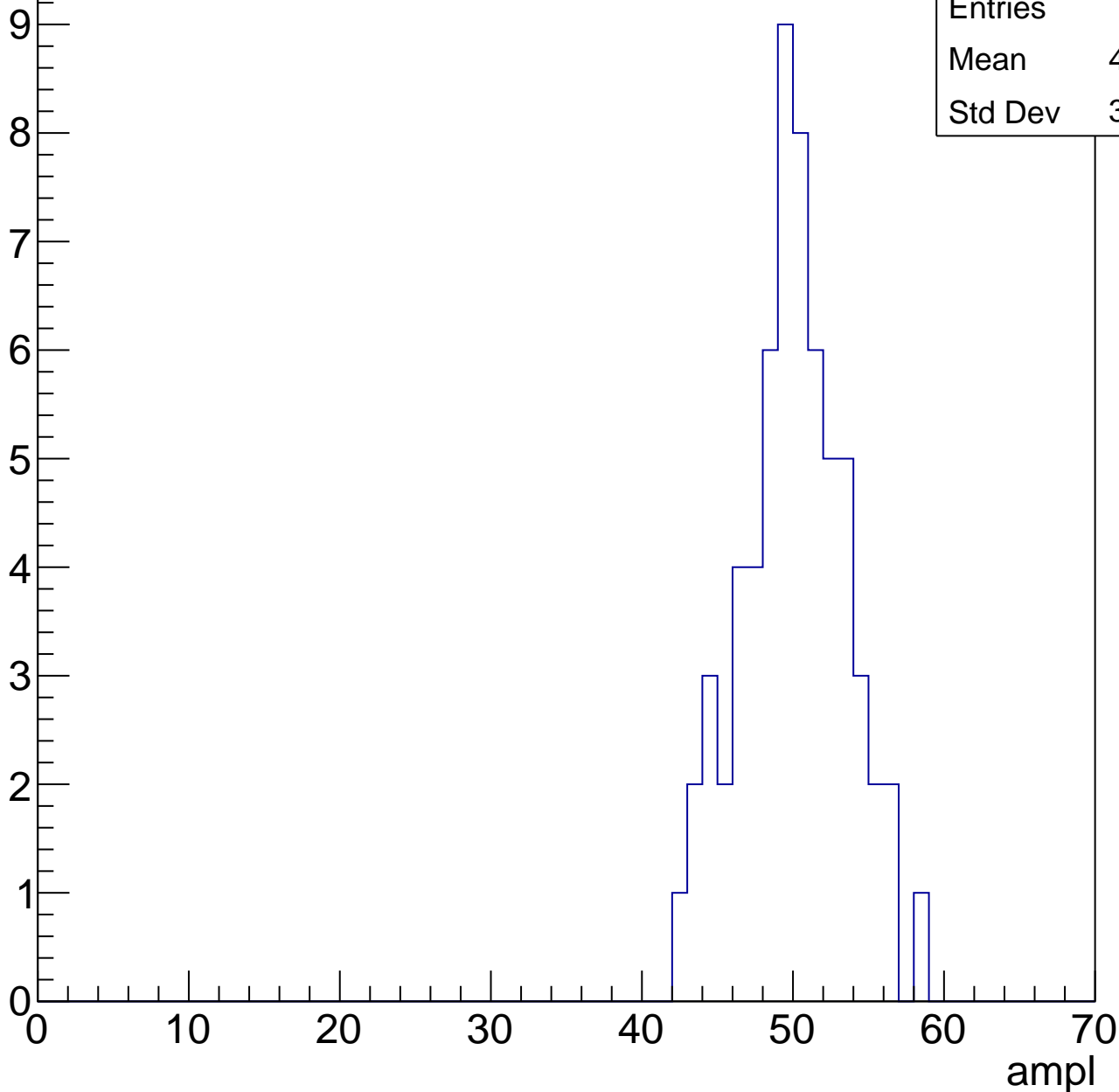


B0L001S, U23-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	49.59
Std Dev	3.444

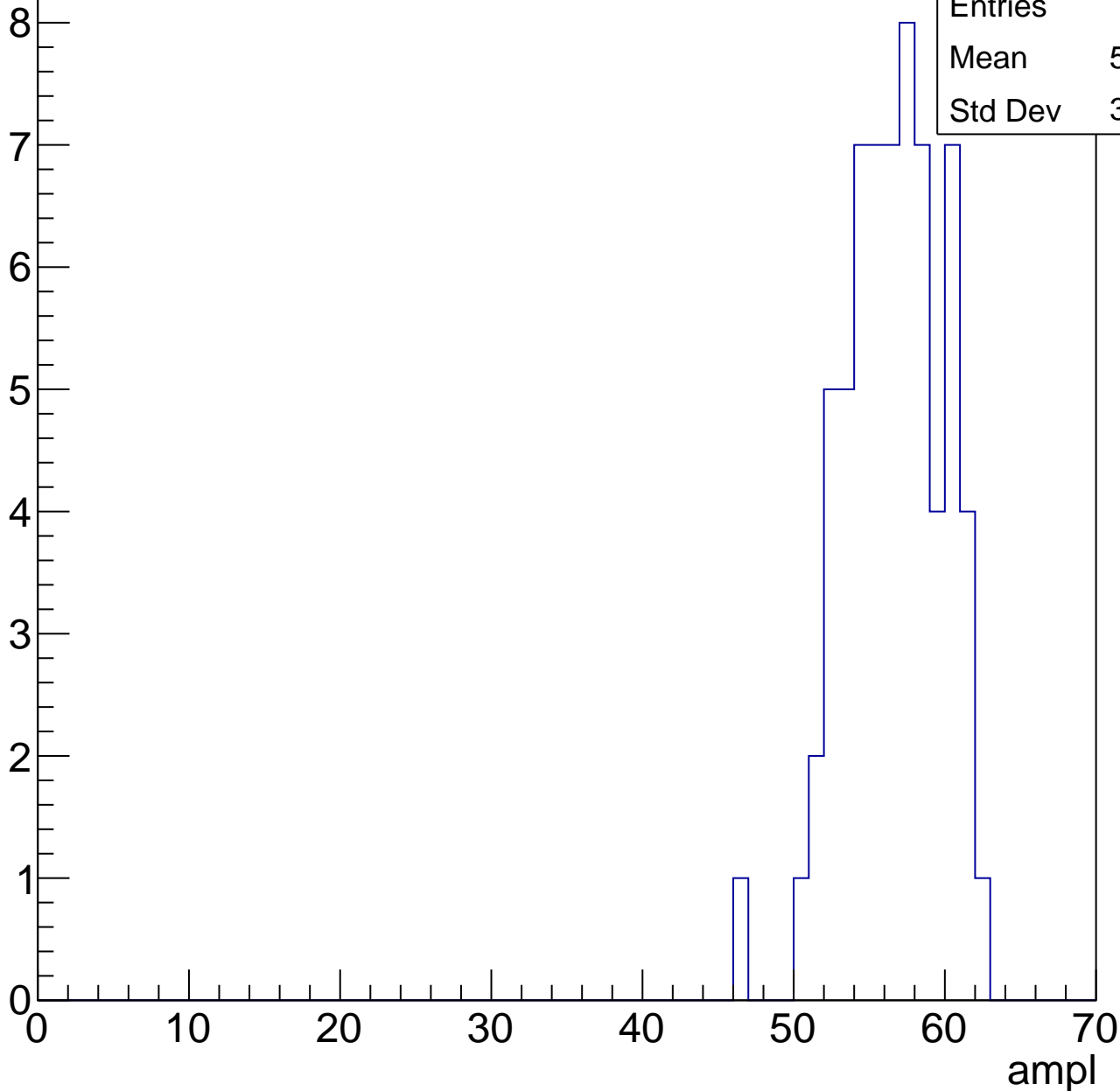


B0L001S, U23-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.09
Std Dev	3.175

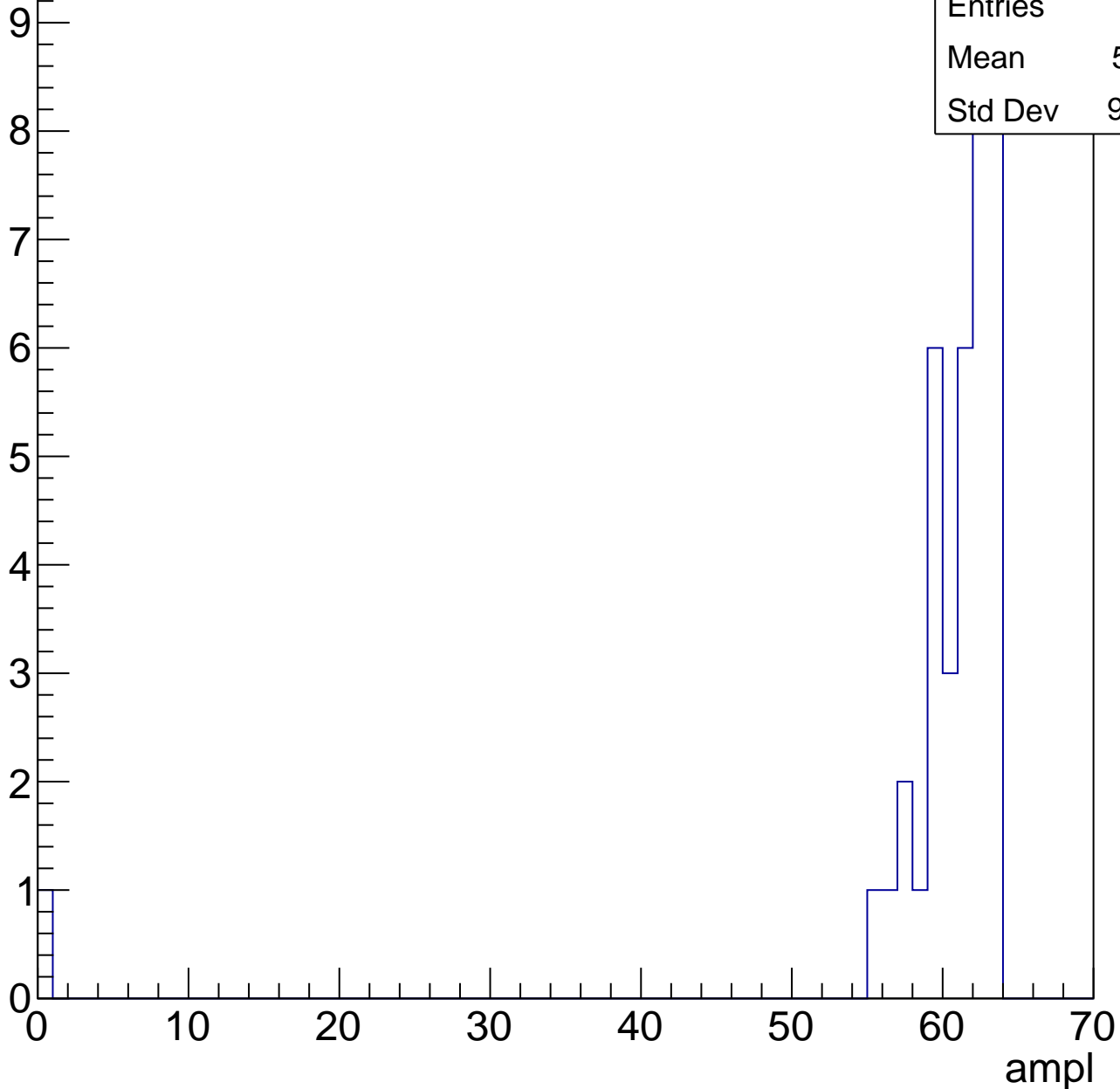


B0L001S, U23-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

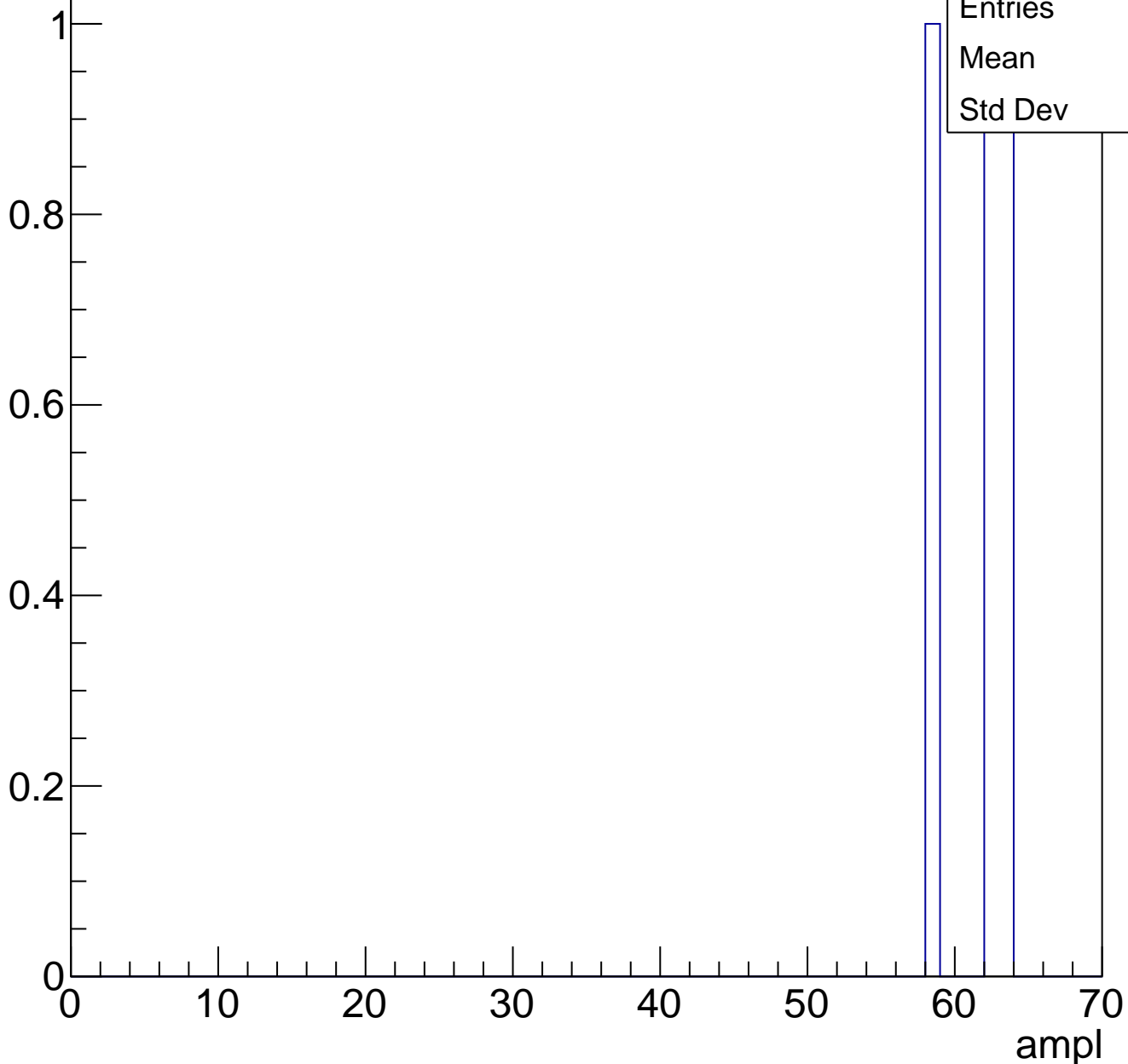
Entries	38
Mean	59.11
Std Dev	9.944



B0L001S, U23-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch25, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	32.43
Std Dev	3.548

Gaus mean : 33.0310

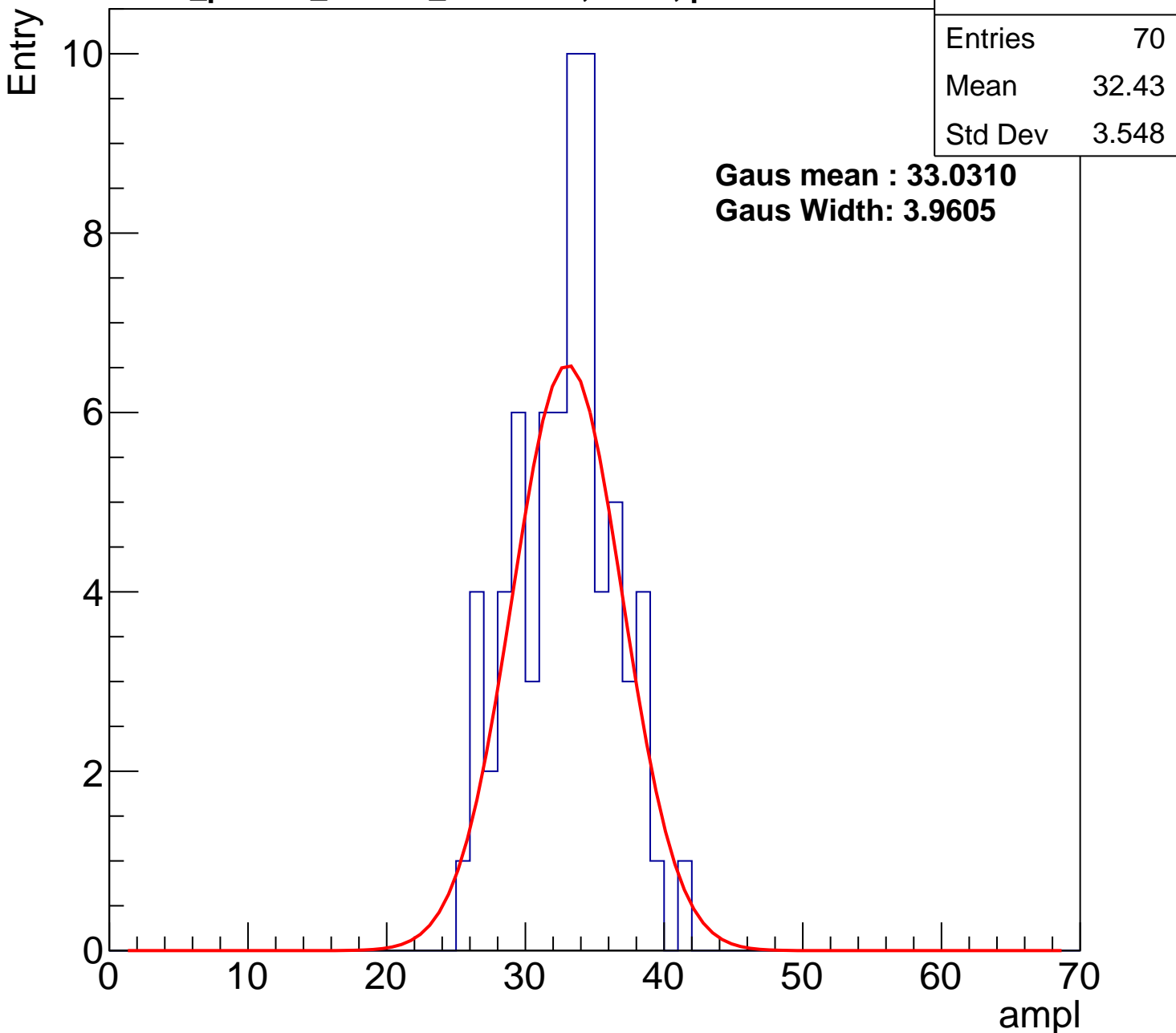
Gaus Width: 3.9605

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch25, adc1

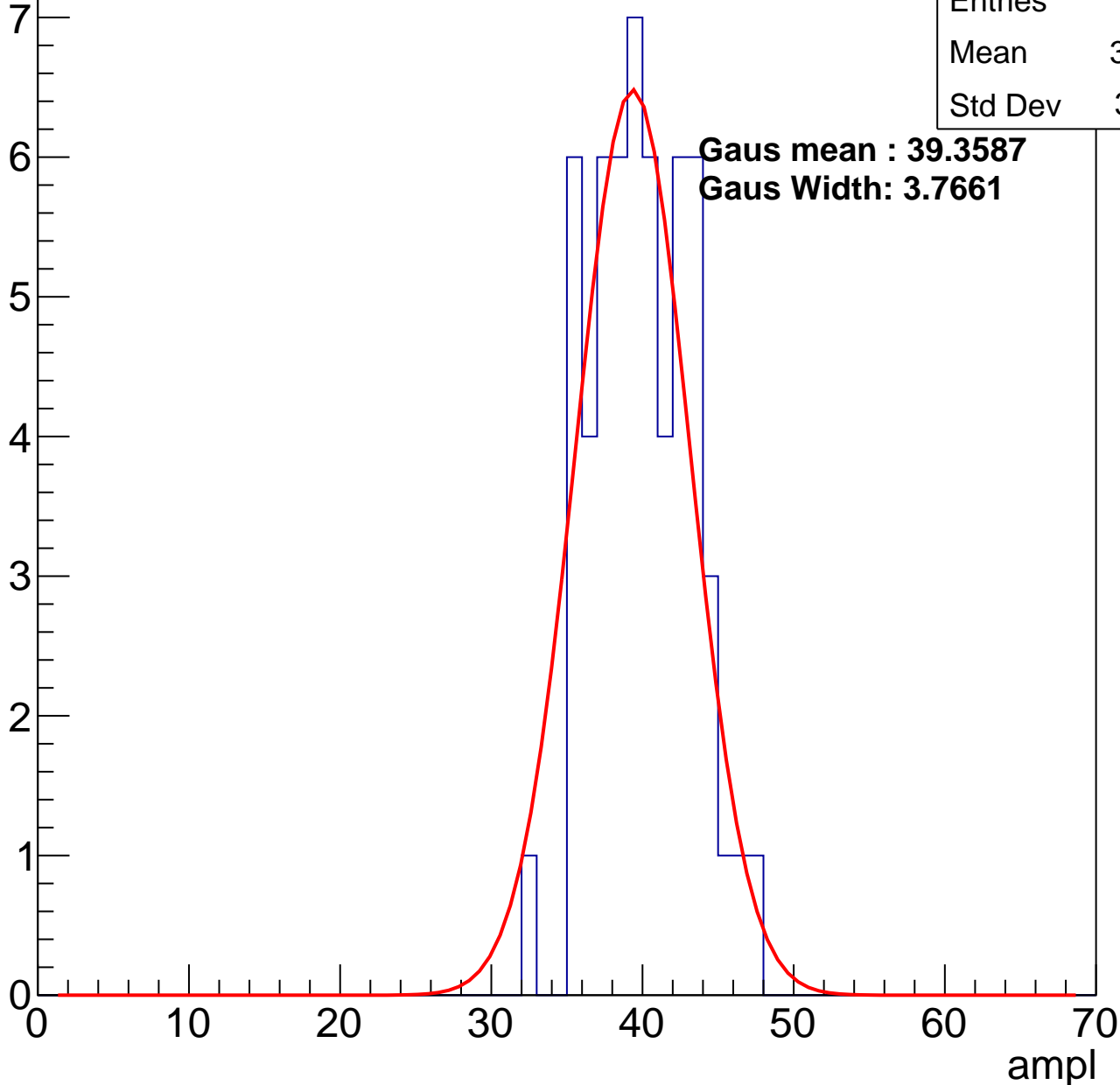
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	39.53
Std Dev	3.191

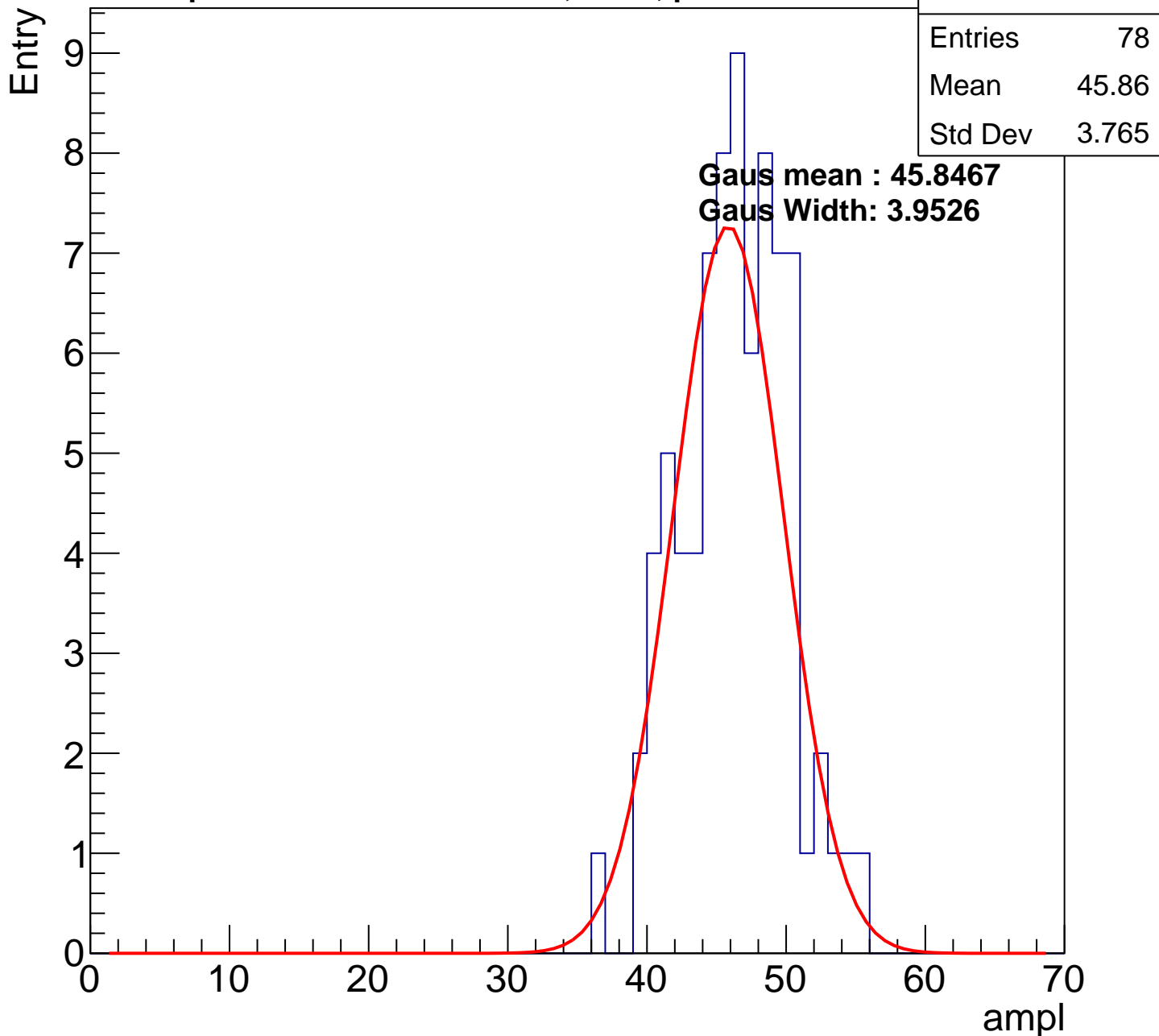
Gaus mean : 39.3587

Gaus Width: 3.7661



B0L001S, U23-ch25, adc2

calib_packv5_042523_0143.root, FC#9, port A1

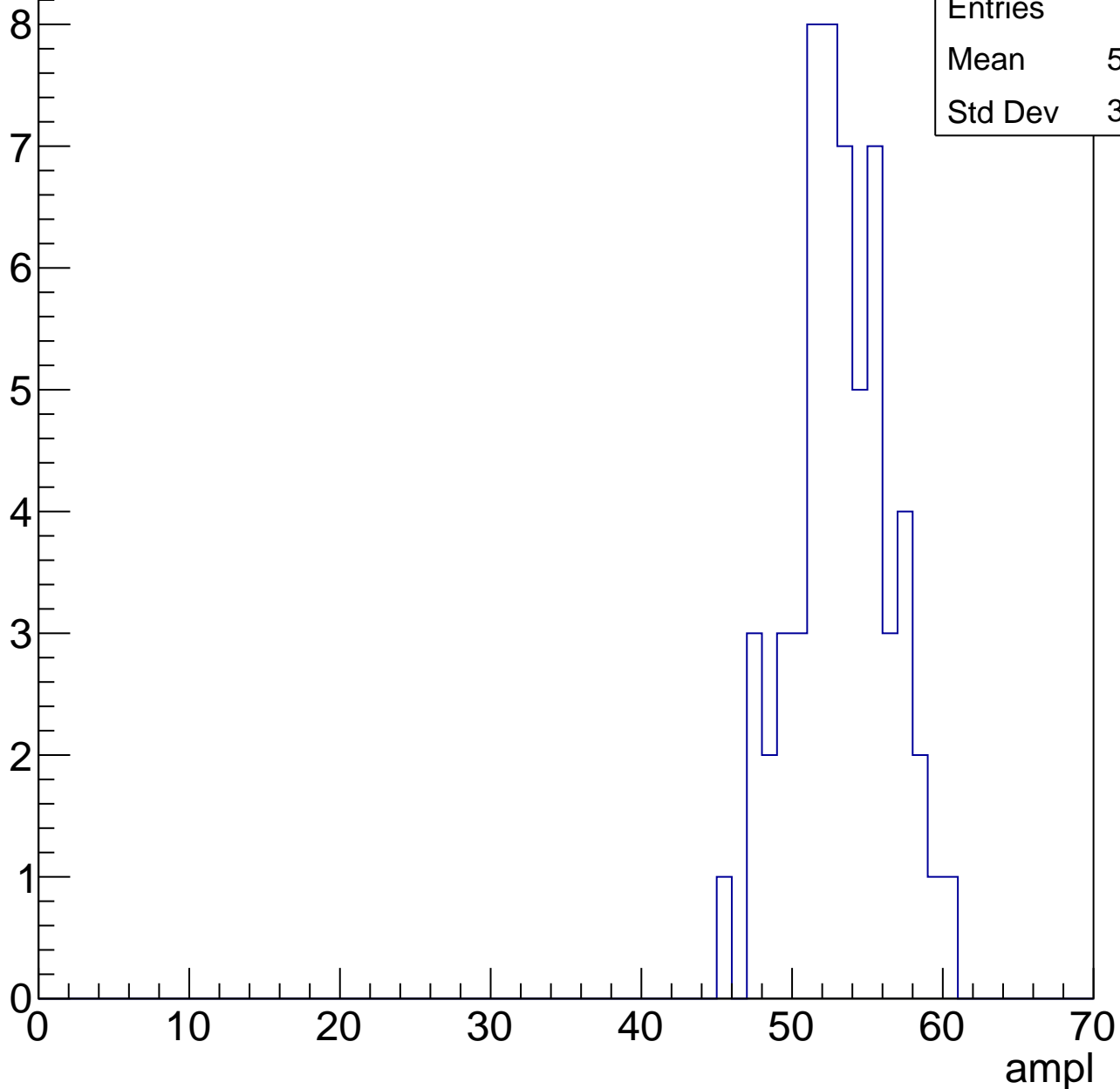


B0L001S, U23-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

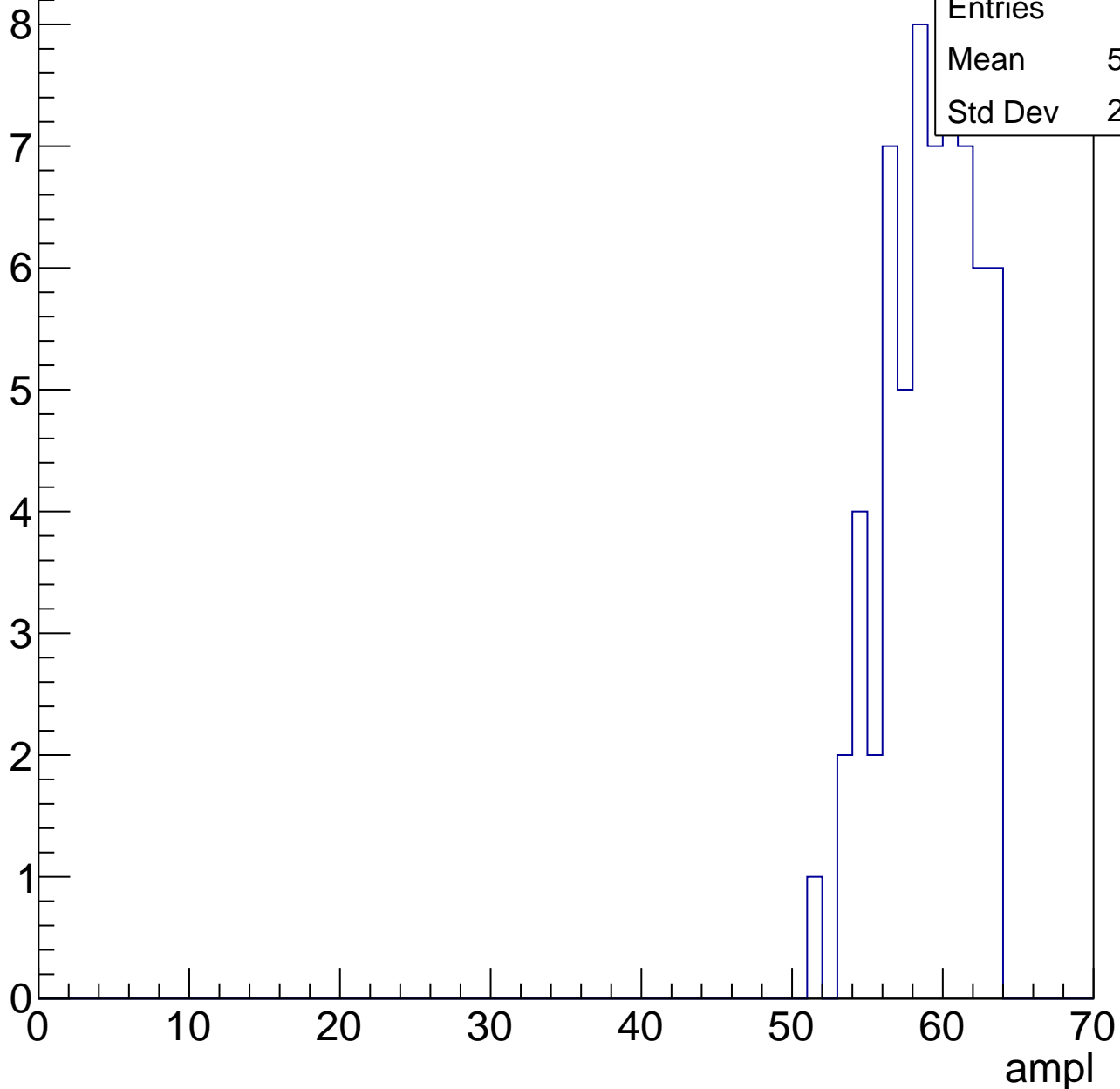
Entries	58
Mean	52.76
Std Dev	3.196



B0L001S, U23-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

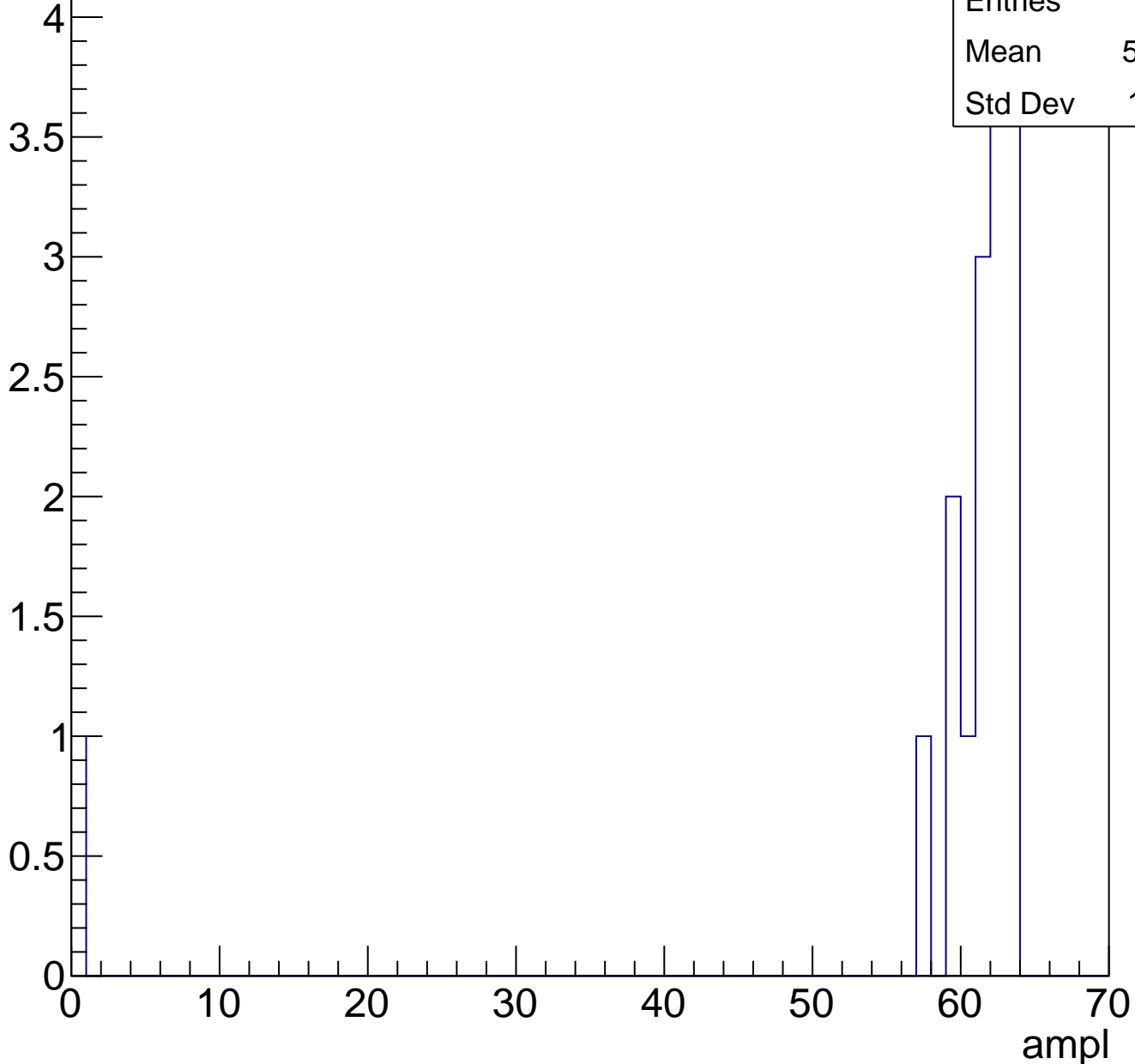
Entry



B0L001S, U23-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch26, adc0

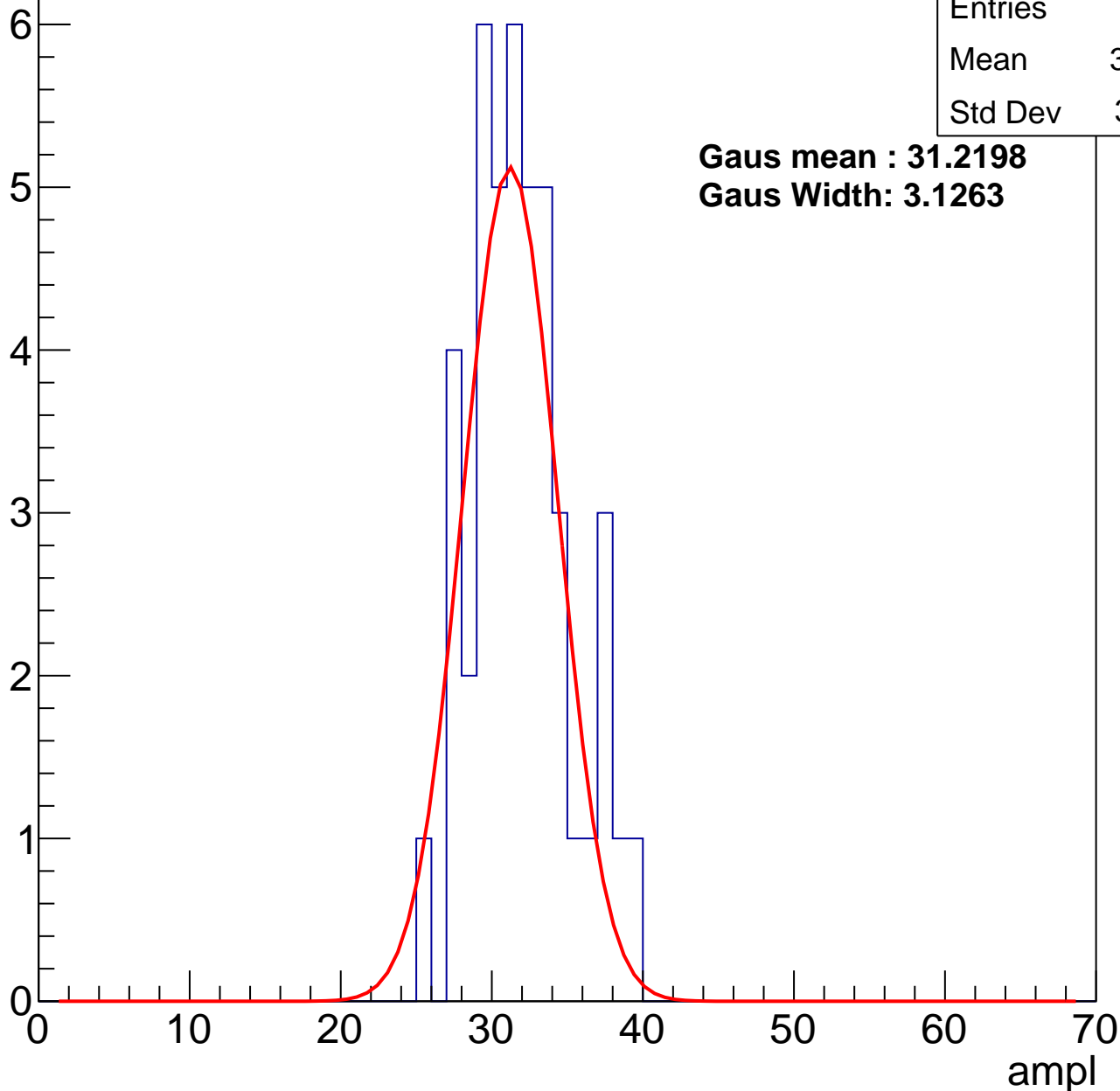
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	31.48
Std Dev	3.201

Gaus mean : 31.2198

Gaus Width: 3.1263



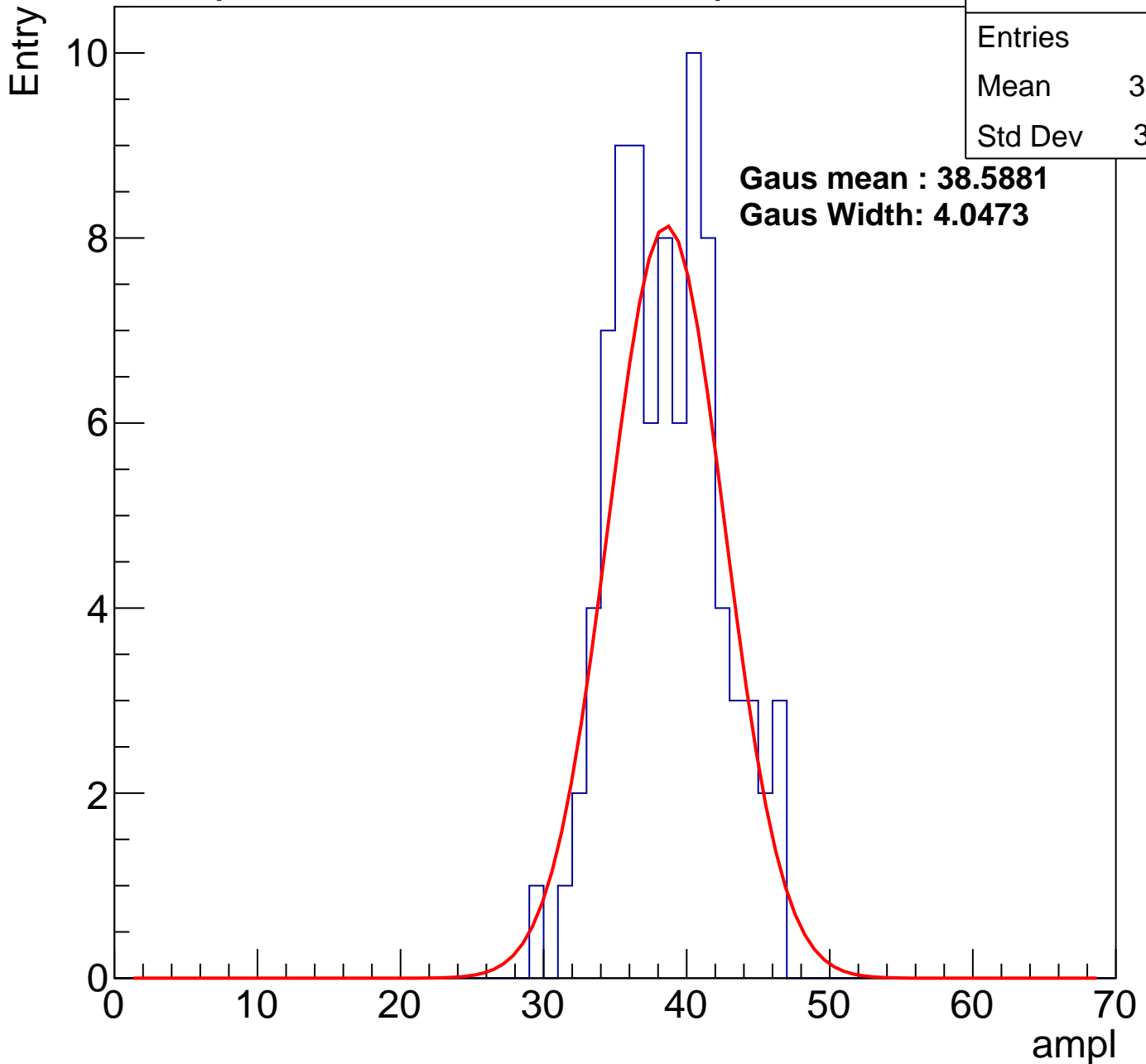
B0L001S, U23-ch26, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	38.12
Std Dev	3.721

Gaus mean : 38.5881

Gaus Width: 4.0473



B0L001S, U23-ch26, adc2

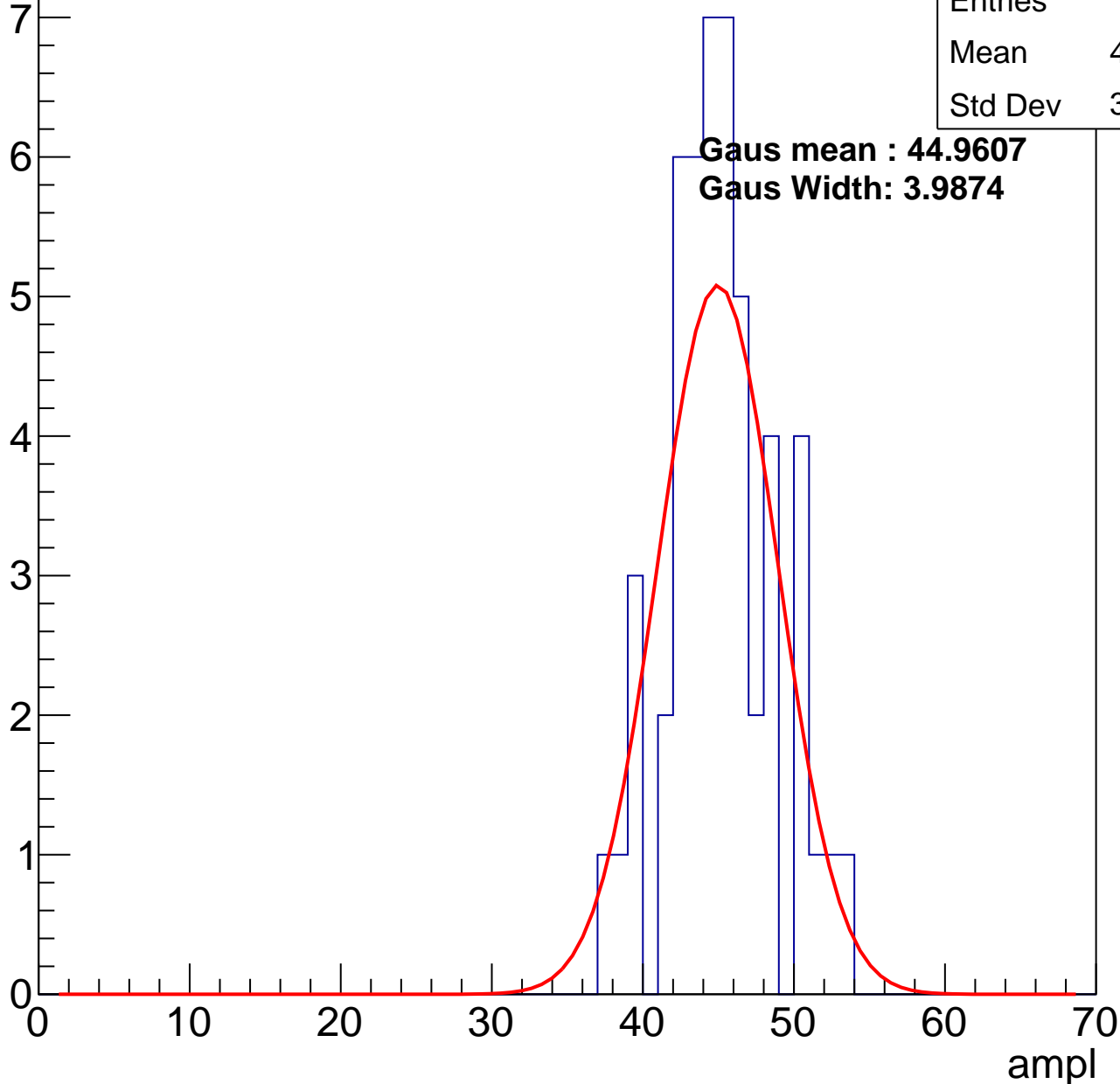
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.69
Std Dev	3.517

Gaus mean : 44.9607

Gaus Width: 3.9874

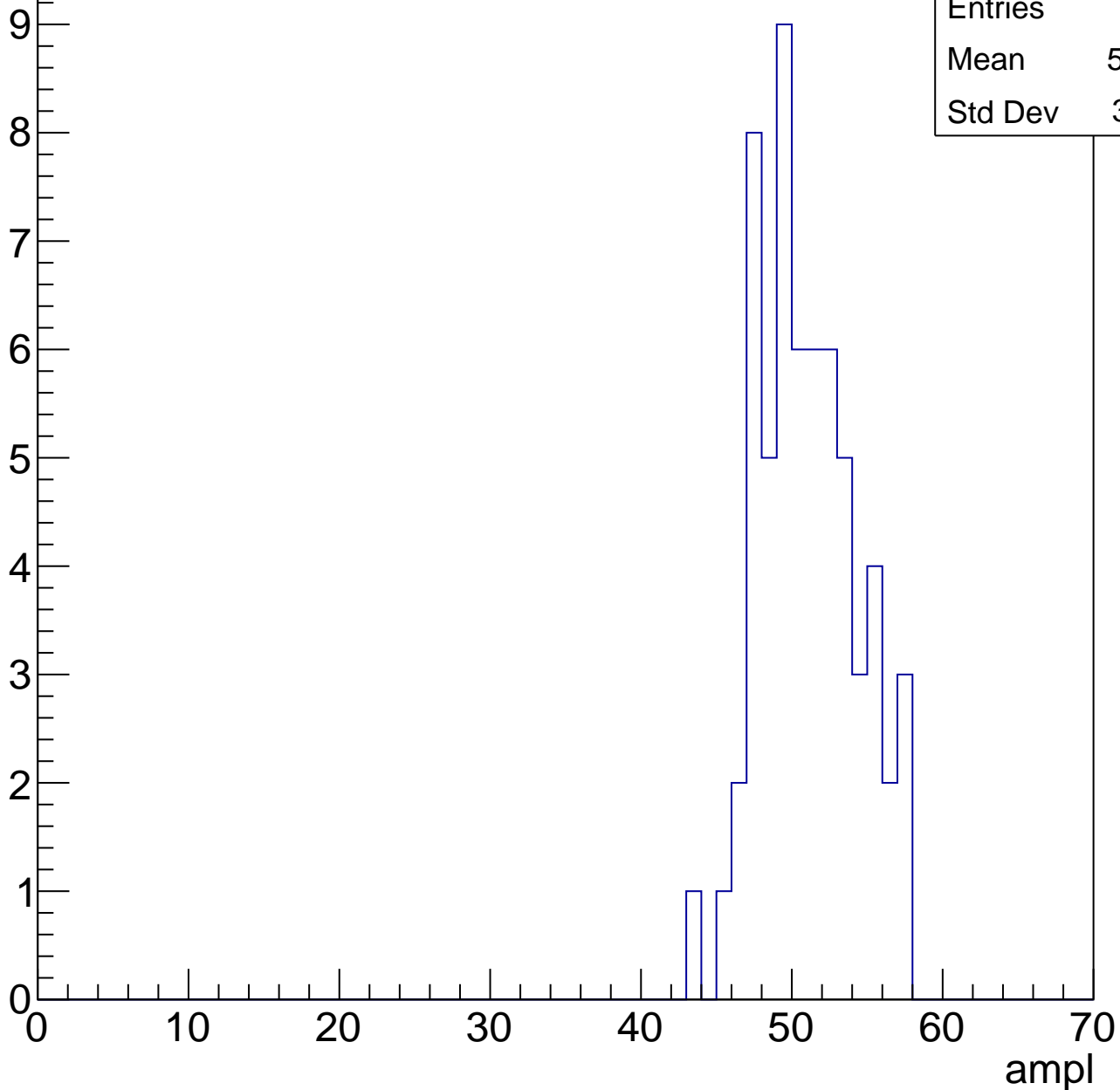


B0L001S, U23-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.57
Std Dev	3.211

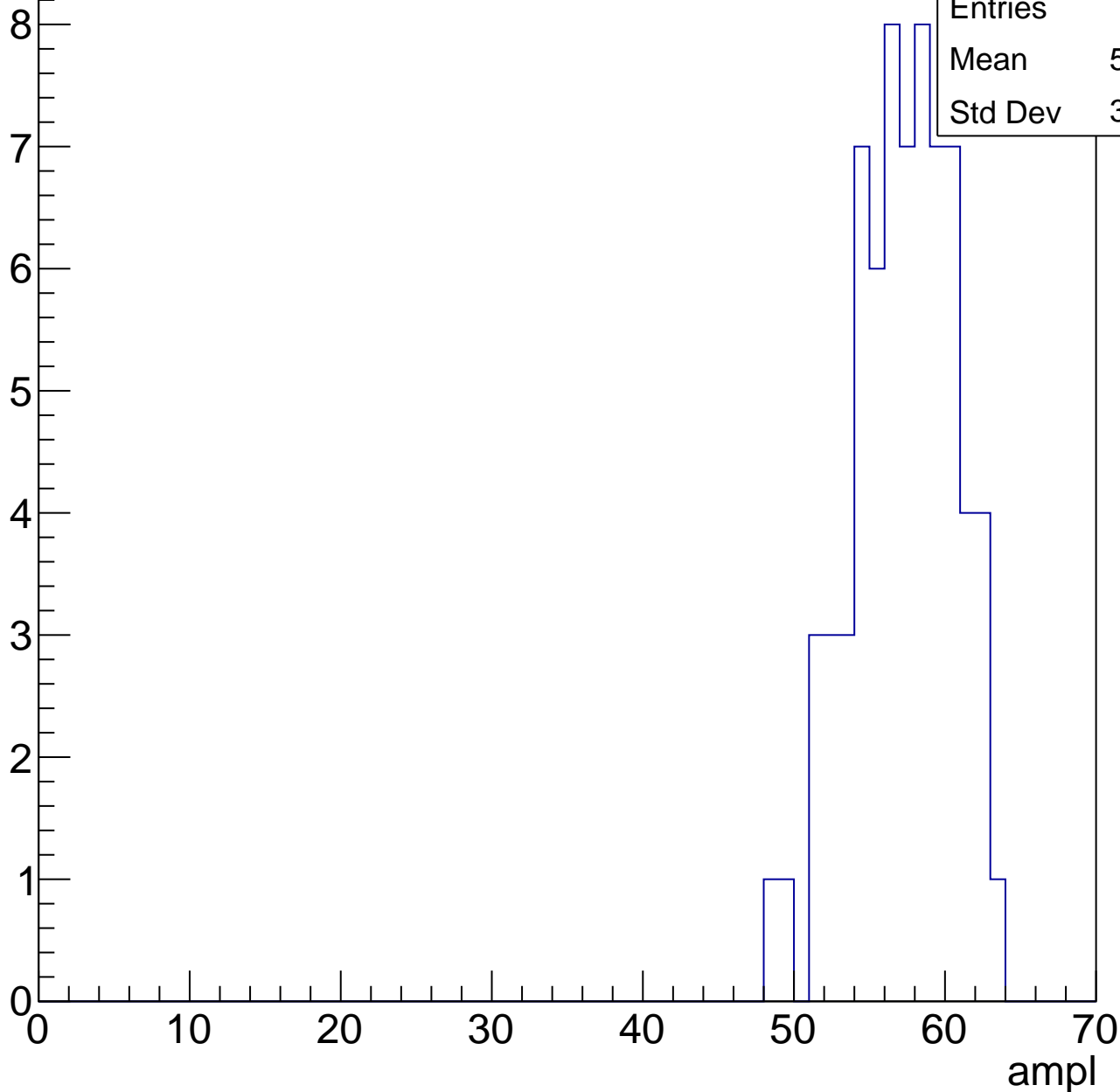


B0L001S, U23-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	56.74
Std Dev	3.307

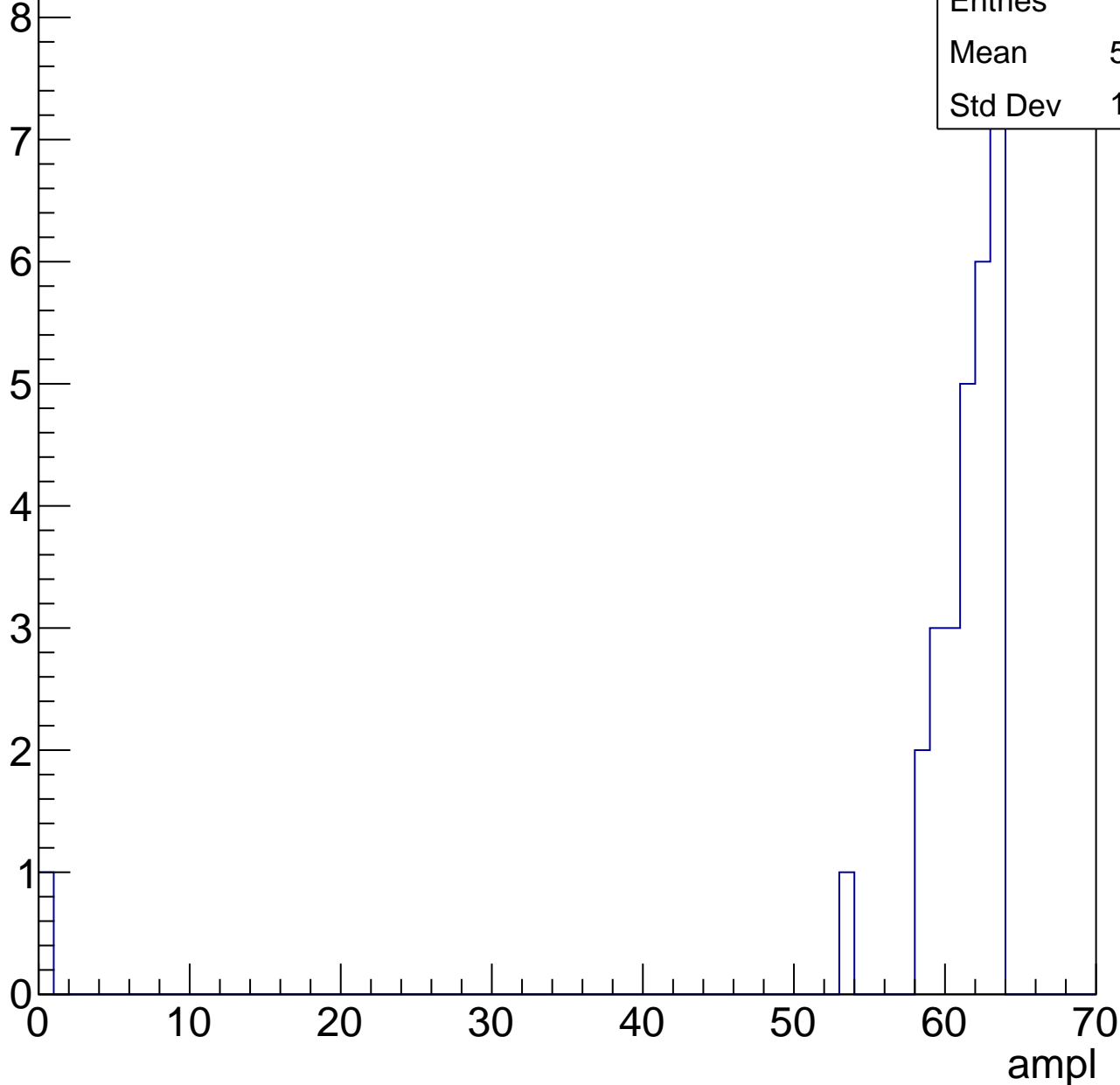


B0L001S, U23-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.86
Std Dev	11.33



B0L001S, U23-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch27, adc0

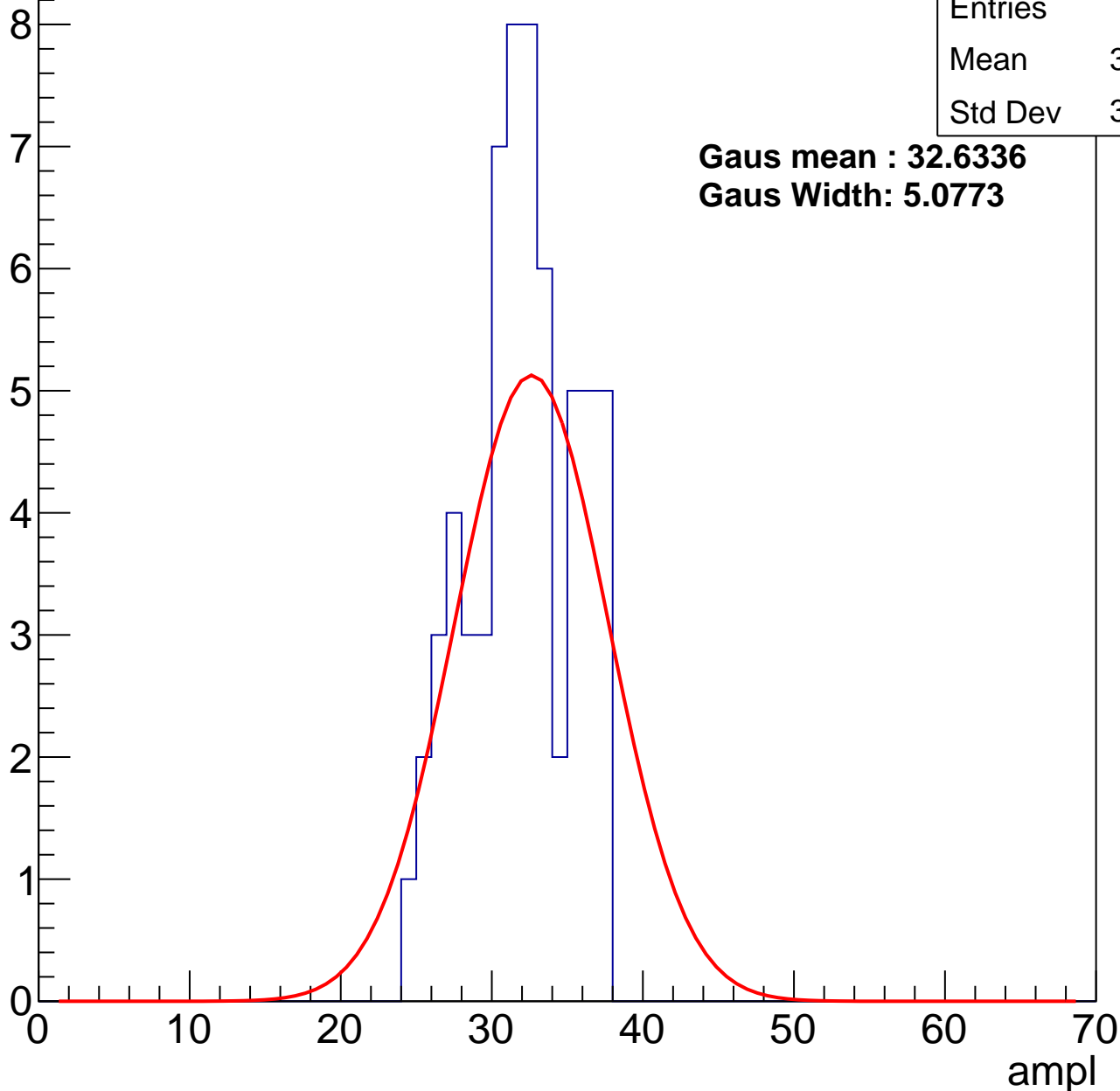
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.47
Std Dev	3.425

Gaus mean : 32.6336

Gaus Width: 5.0773



B0L001S, U23-ch27, adc1

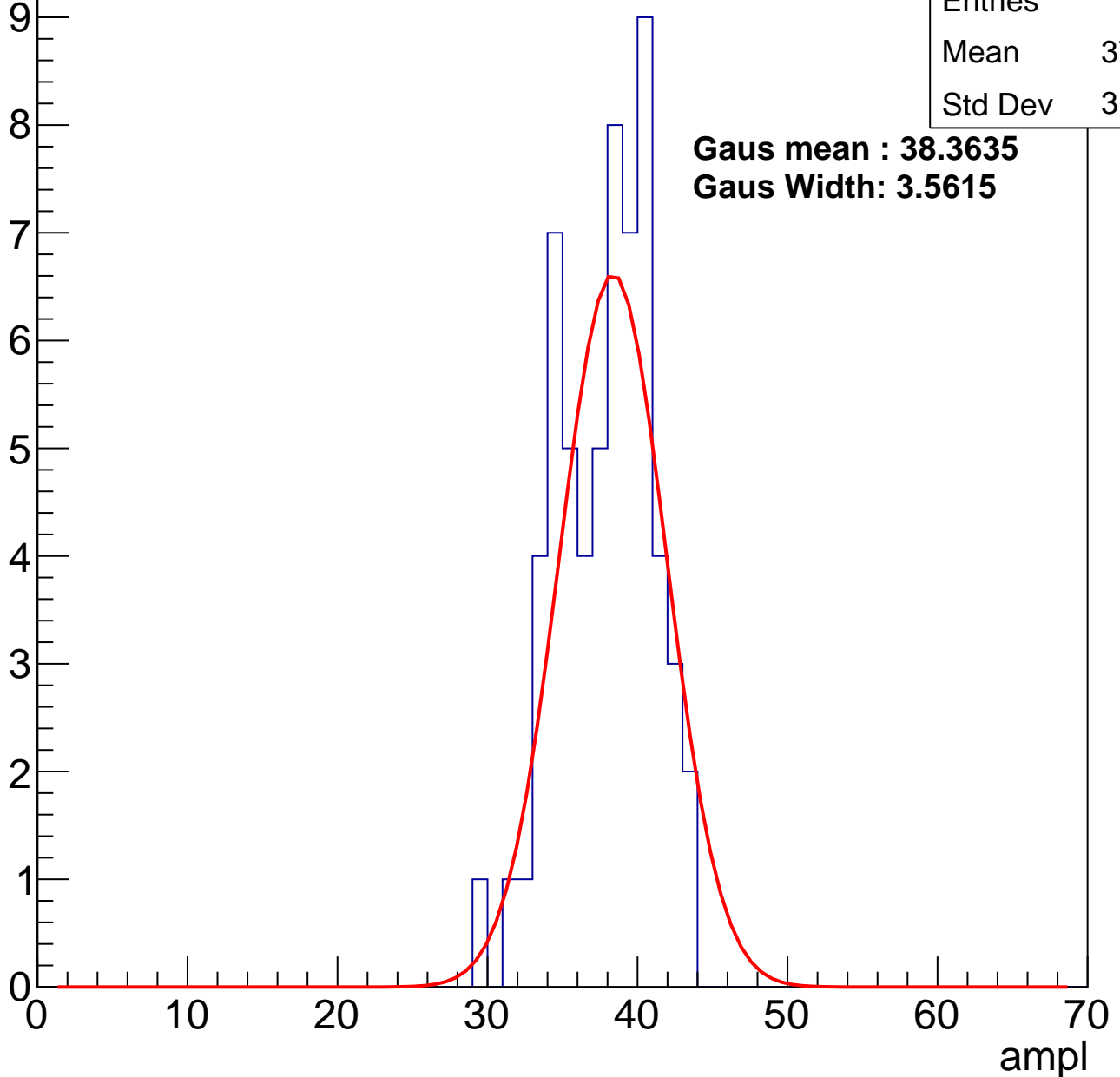
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.36
Std Dev	3.126

Gaus mean : 38.3635

Gaus Width: 3.5615



B0L001S, U23-ch27, adc2

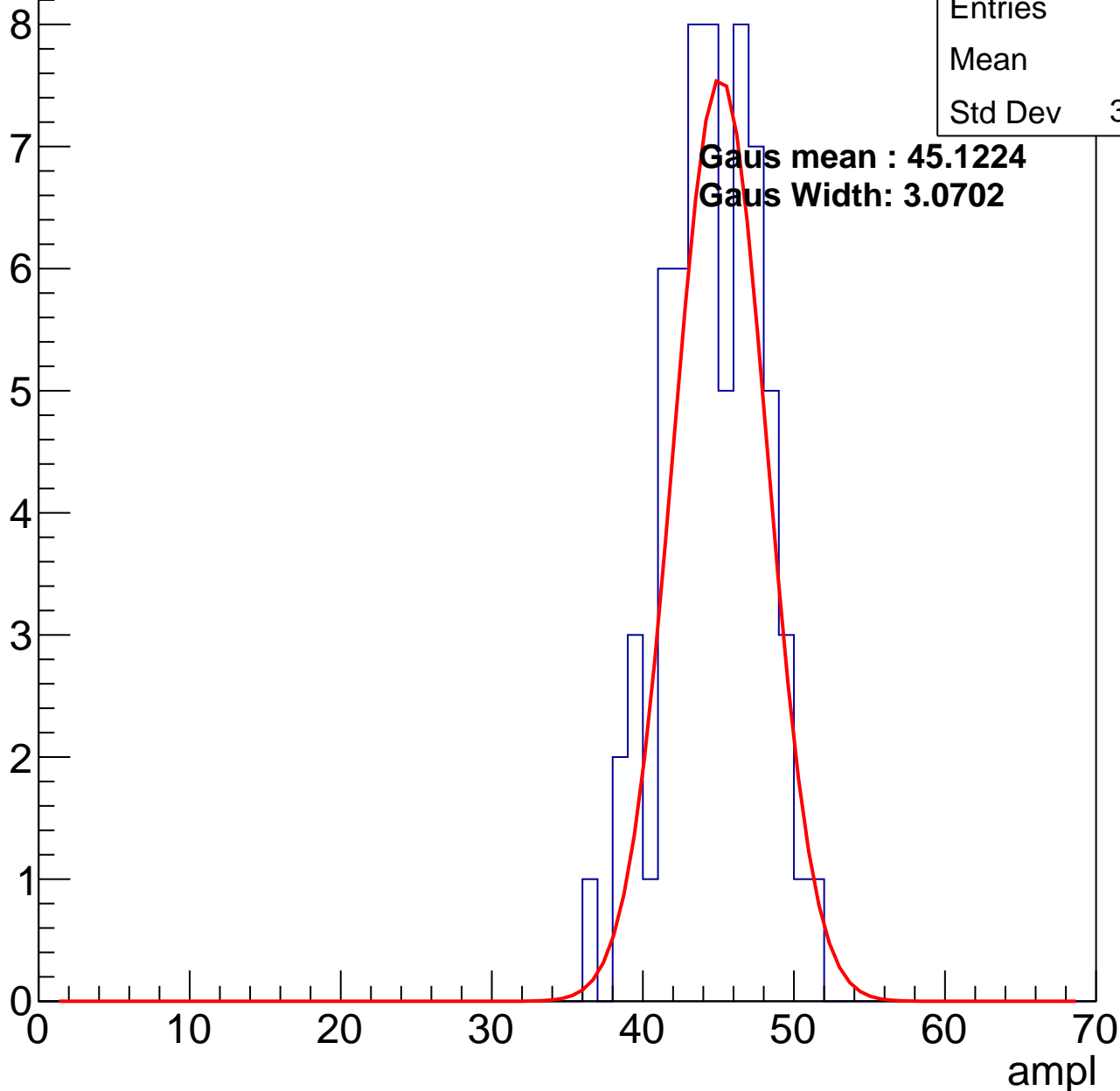
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.2
Std Dev	3.163

Gaus mean : 45.1224

Gaus Width: 3.0702



B0L001S, U23-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	51.11
Std Dev	3.492

Entry

10

8

6

4

2

0

0

10

20

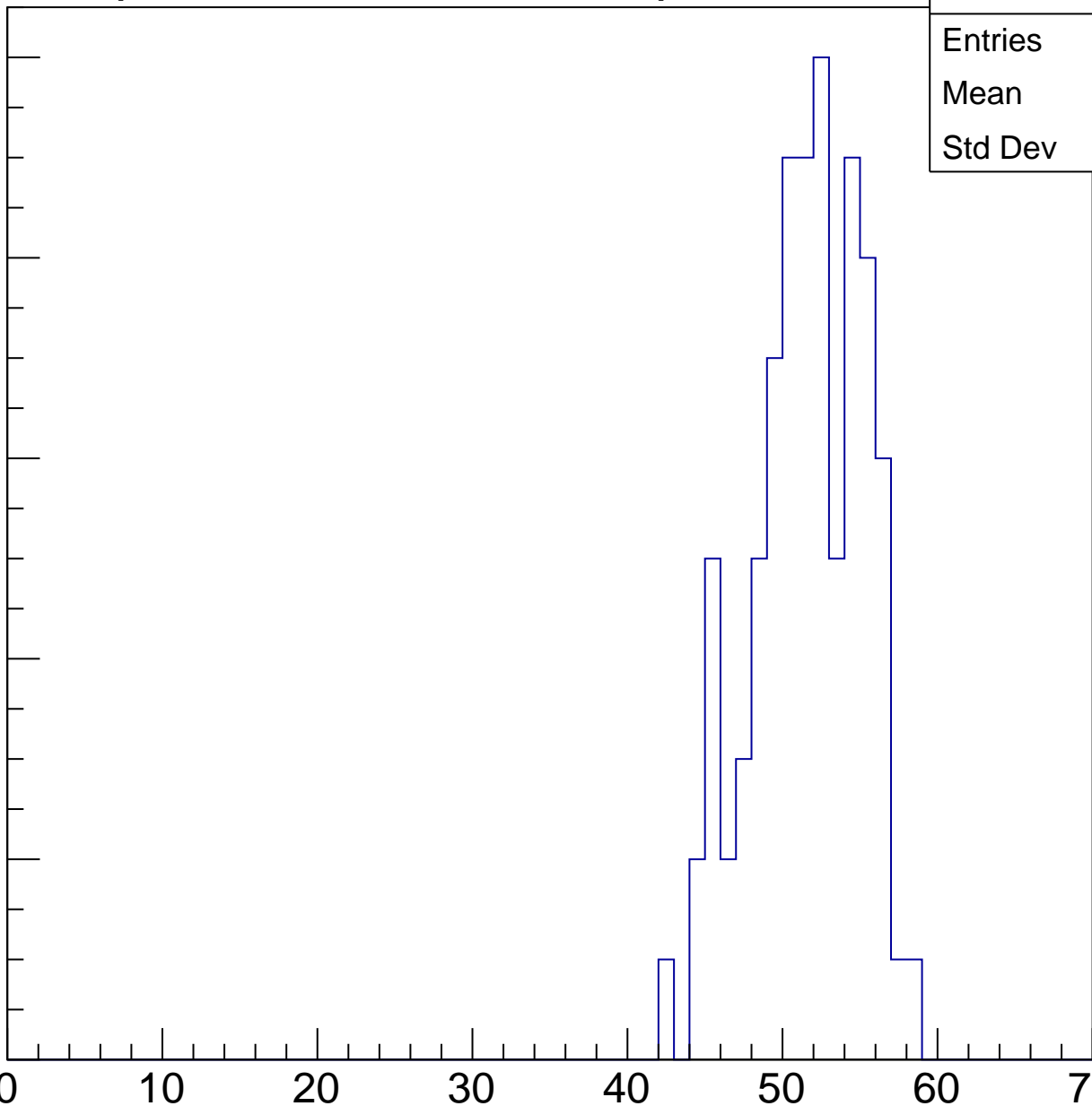
30

40

50

60

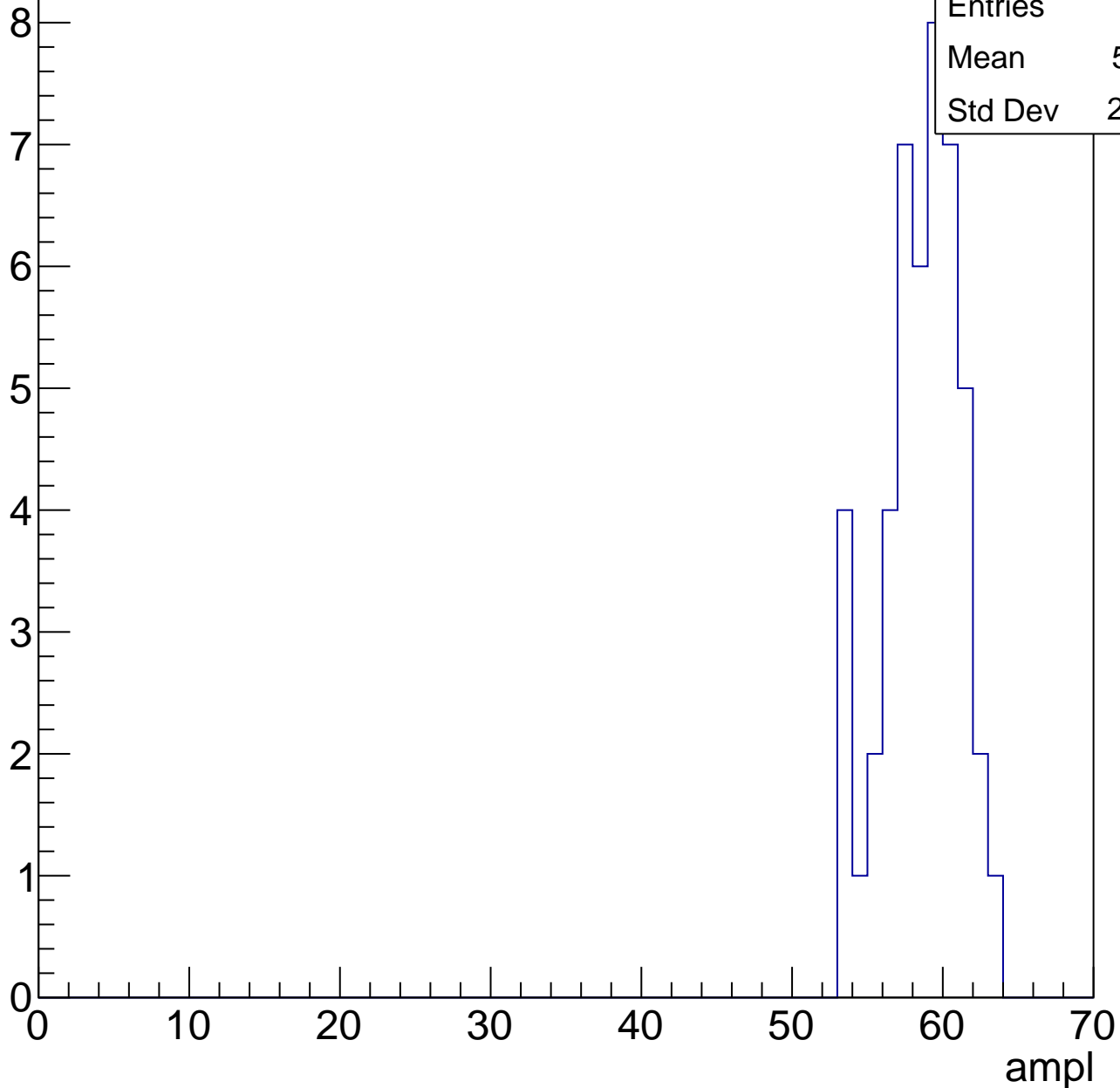
ampl



B0L001S, U23-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



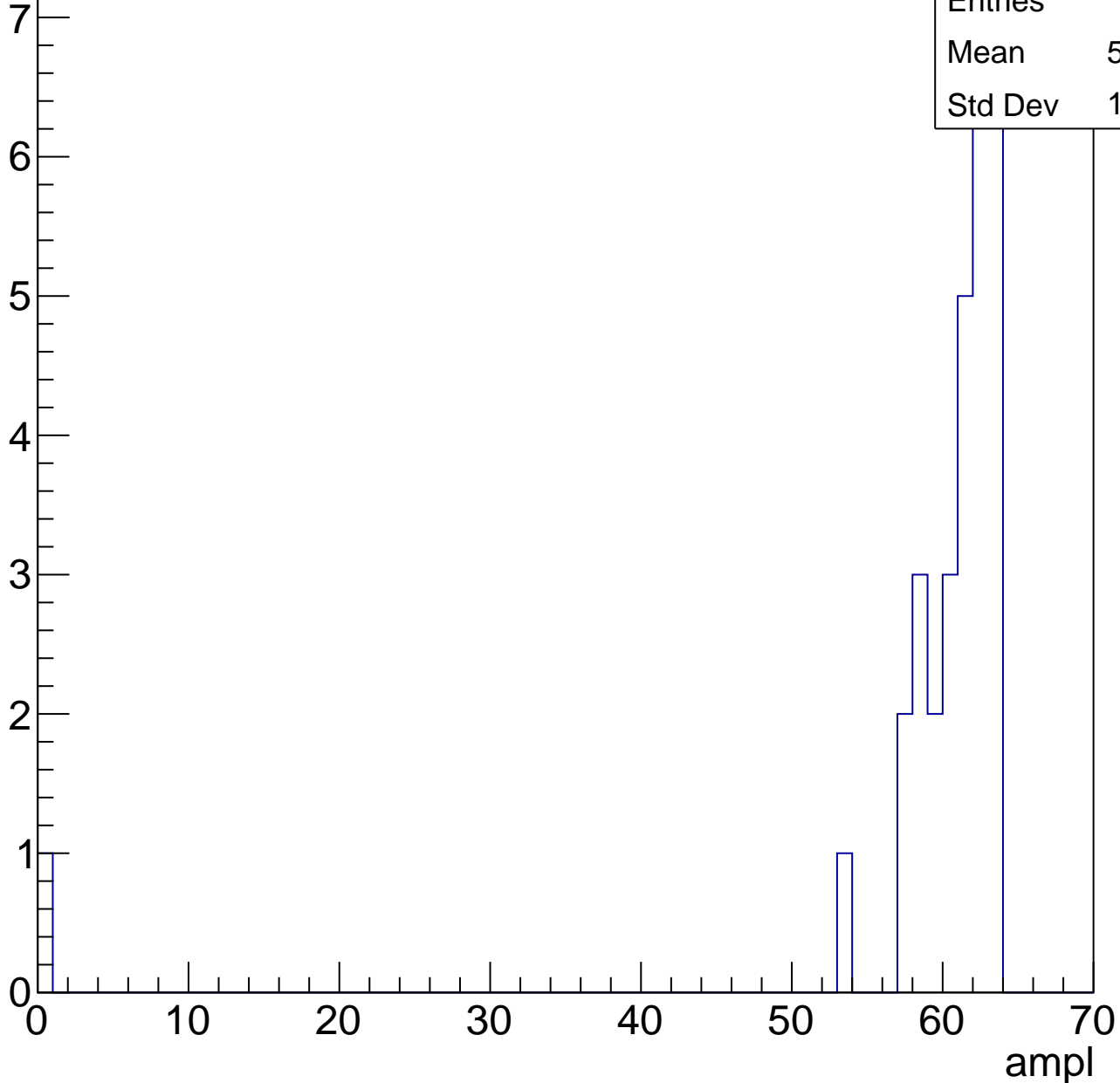
Entries	47
Mean	58.11
Std Dev	2.503

B0L001S, U23-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.68
Std Dev	10.96



B0L001S, U23-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch28, adc0

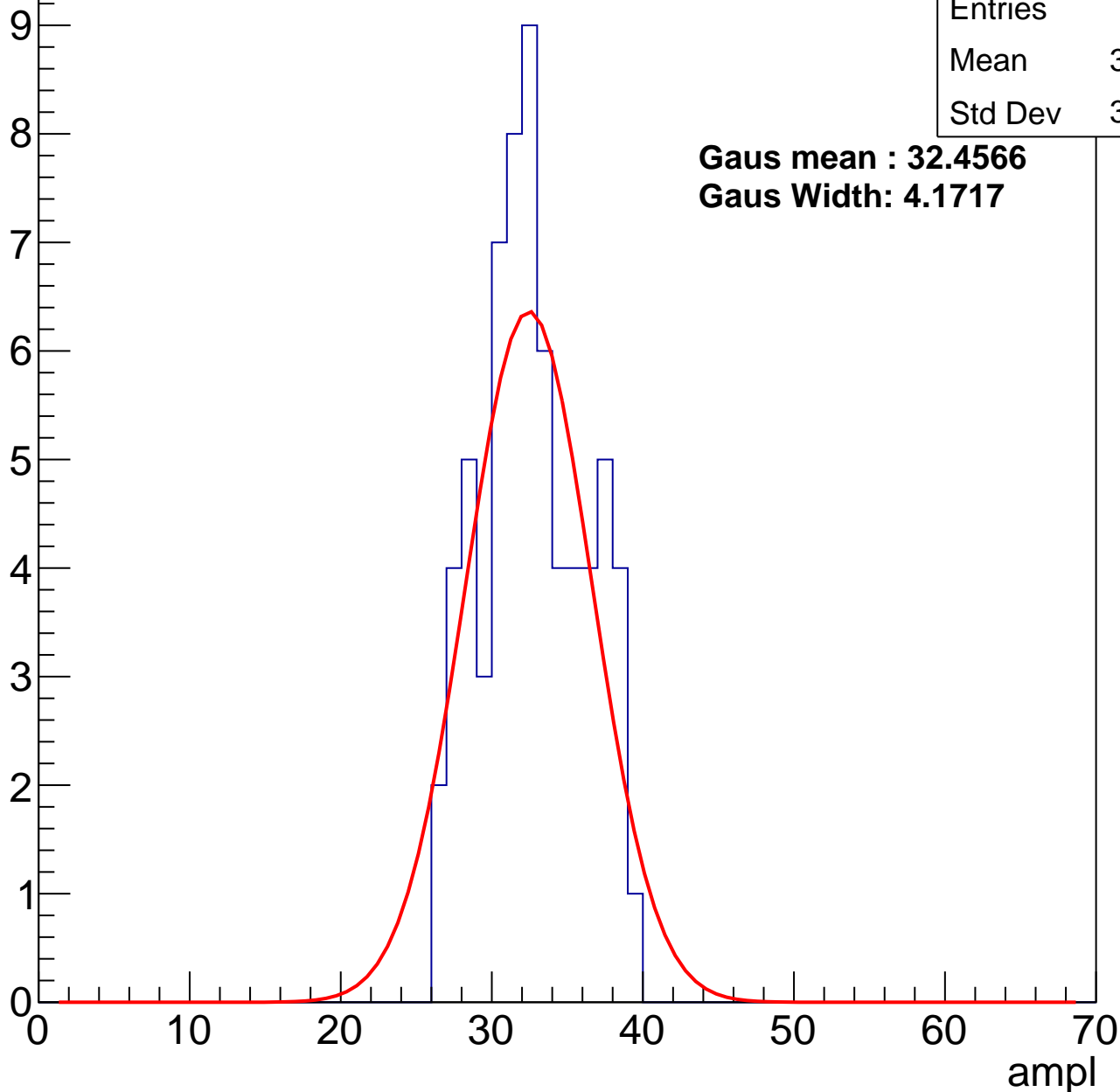
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	32.23
Std Dev	3.375

Gaus mean : 32.4566

Gaus Width: 4.1717



B0L001S, U23-ch28, adc1

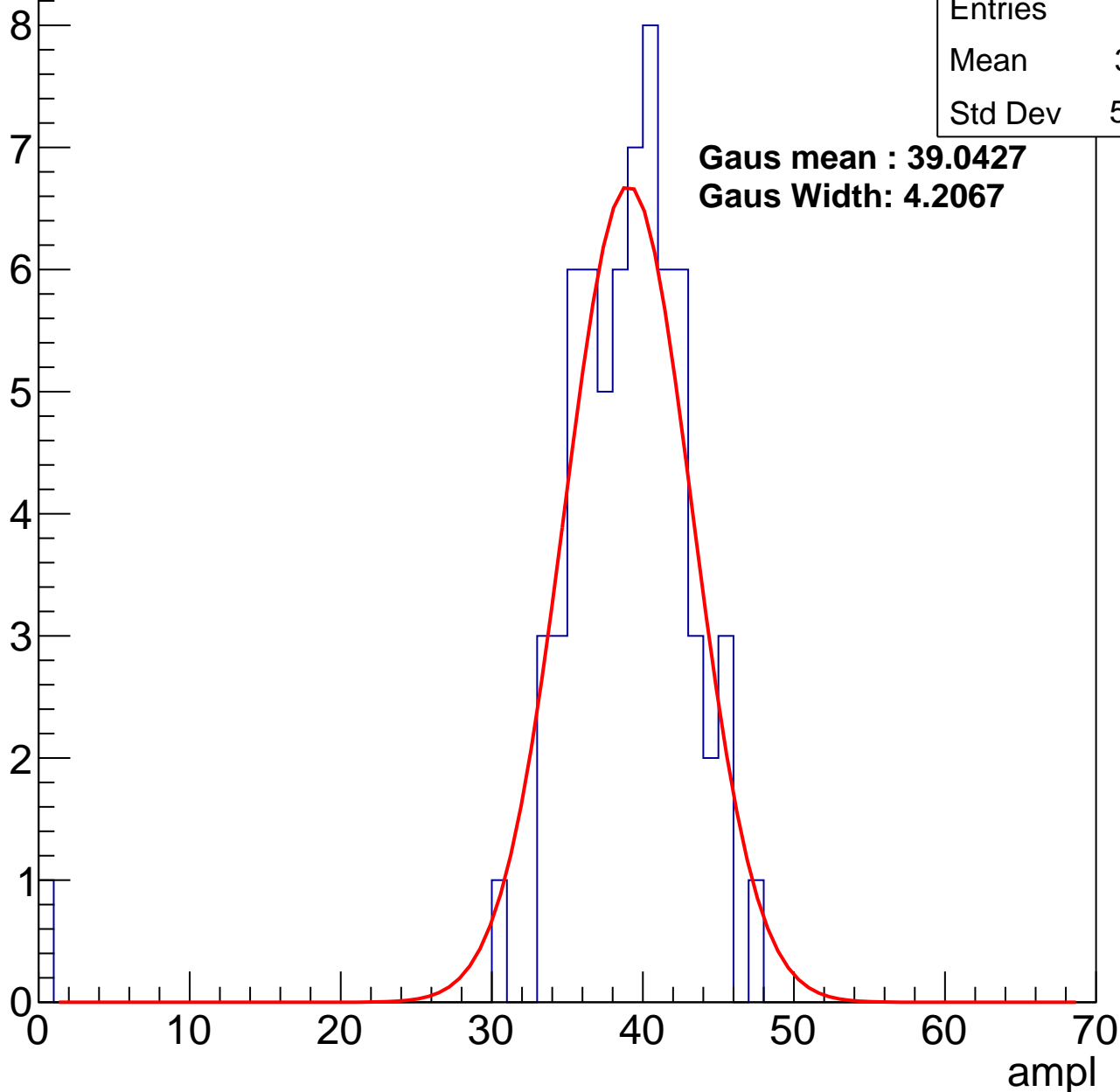
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.21
Std Dev	5.827

Gaus mean : 39.0427

Gaus Width: 4.2067



B0L001S, U23-ch28, adc2

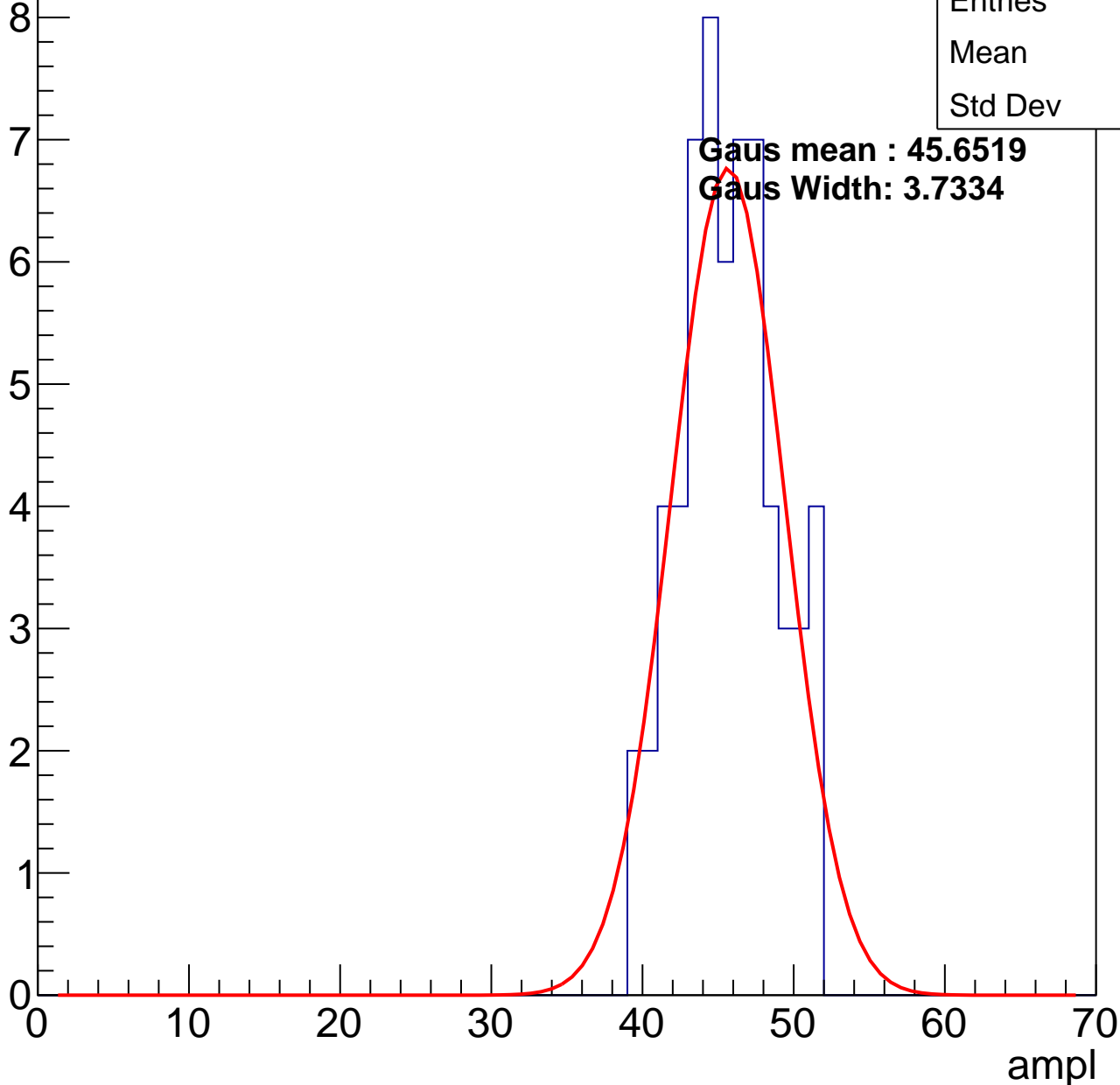
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	45.2
Std Dev	3.12

Gaus mean : 45.6519

Gaus Width: 3.7334

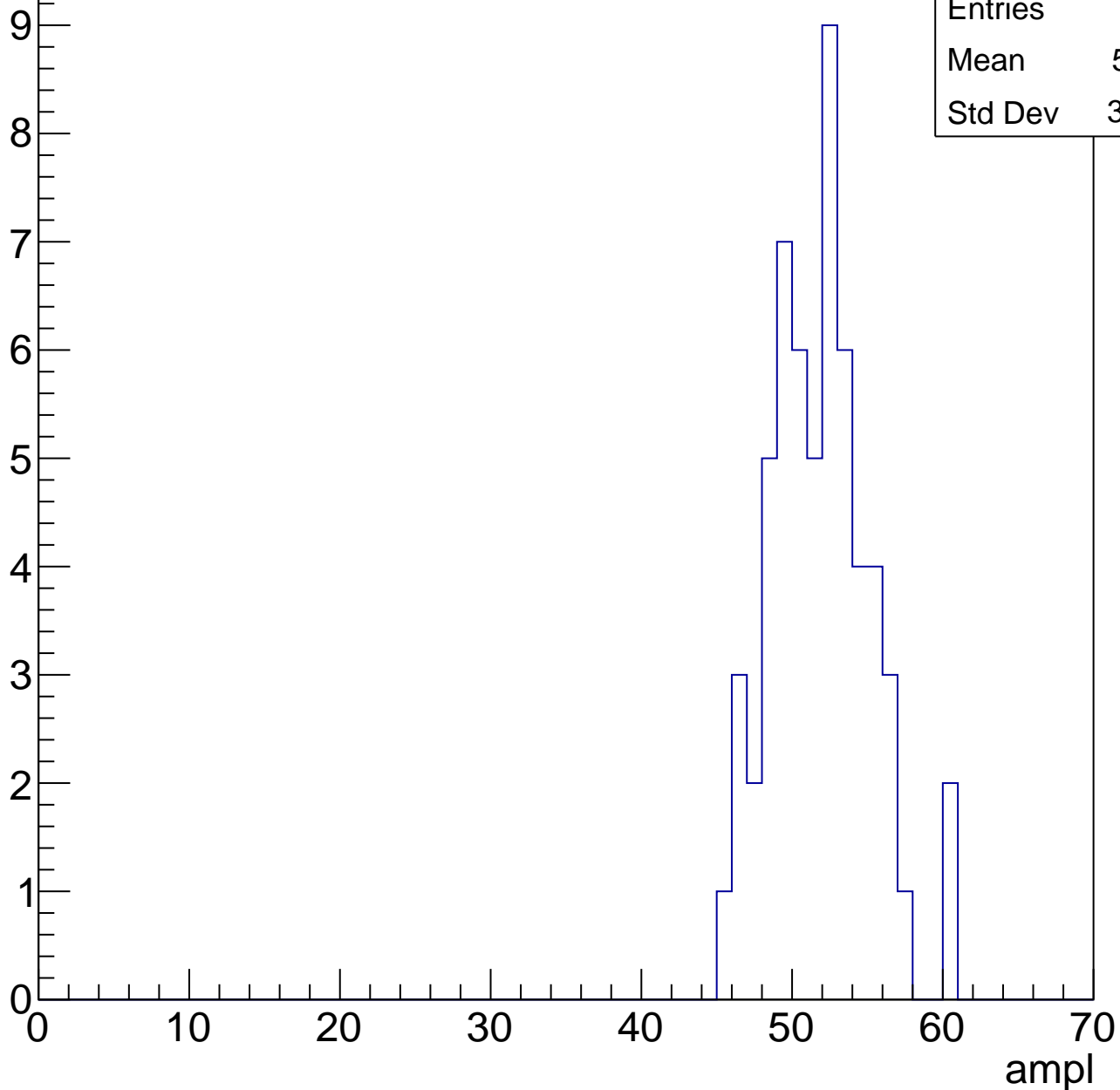


B0L001S, U23-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

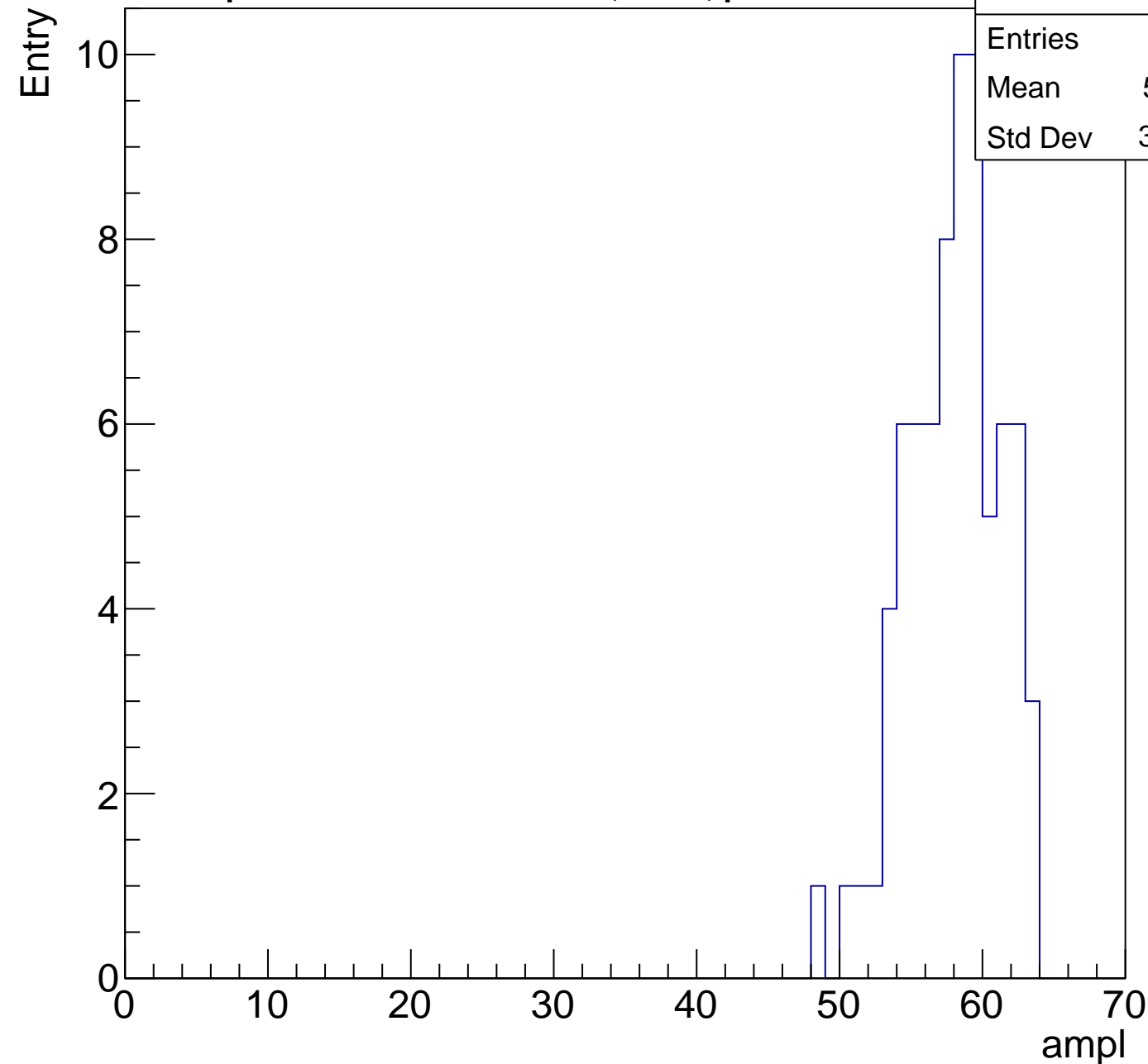
Entries	58
Mean	51.41
Std Dev	3.264



B0L001S, U23-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	57.51
Std Dev	3.223

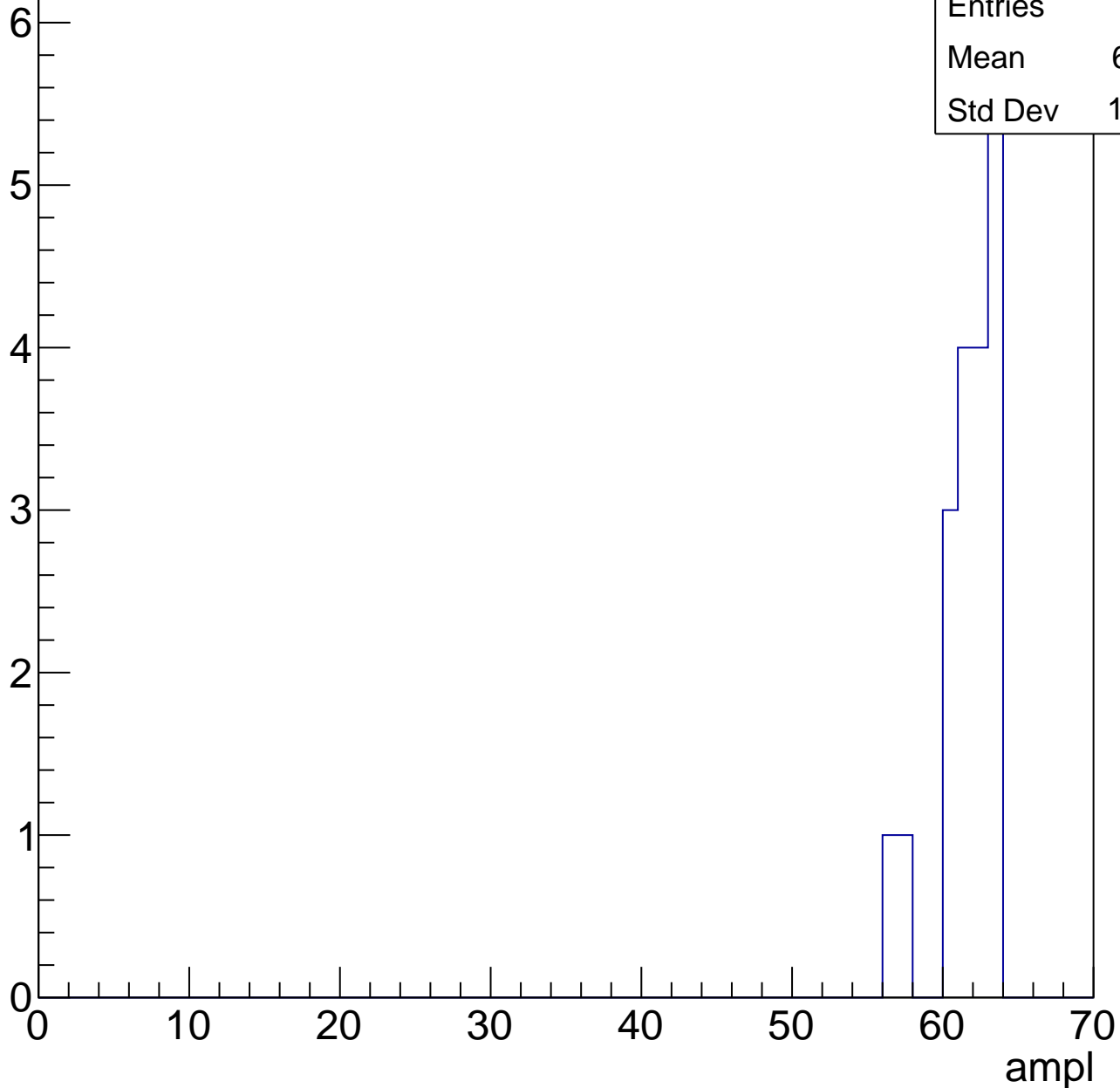


B0L001S, U23-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	61.21
Std Dev	1.935



B0L001S, U23-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch29, adc0

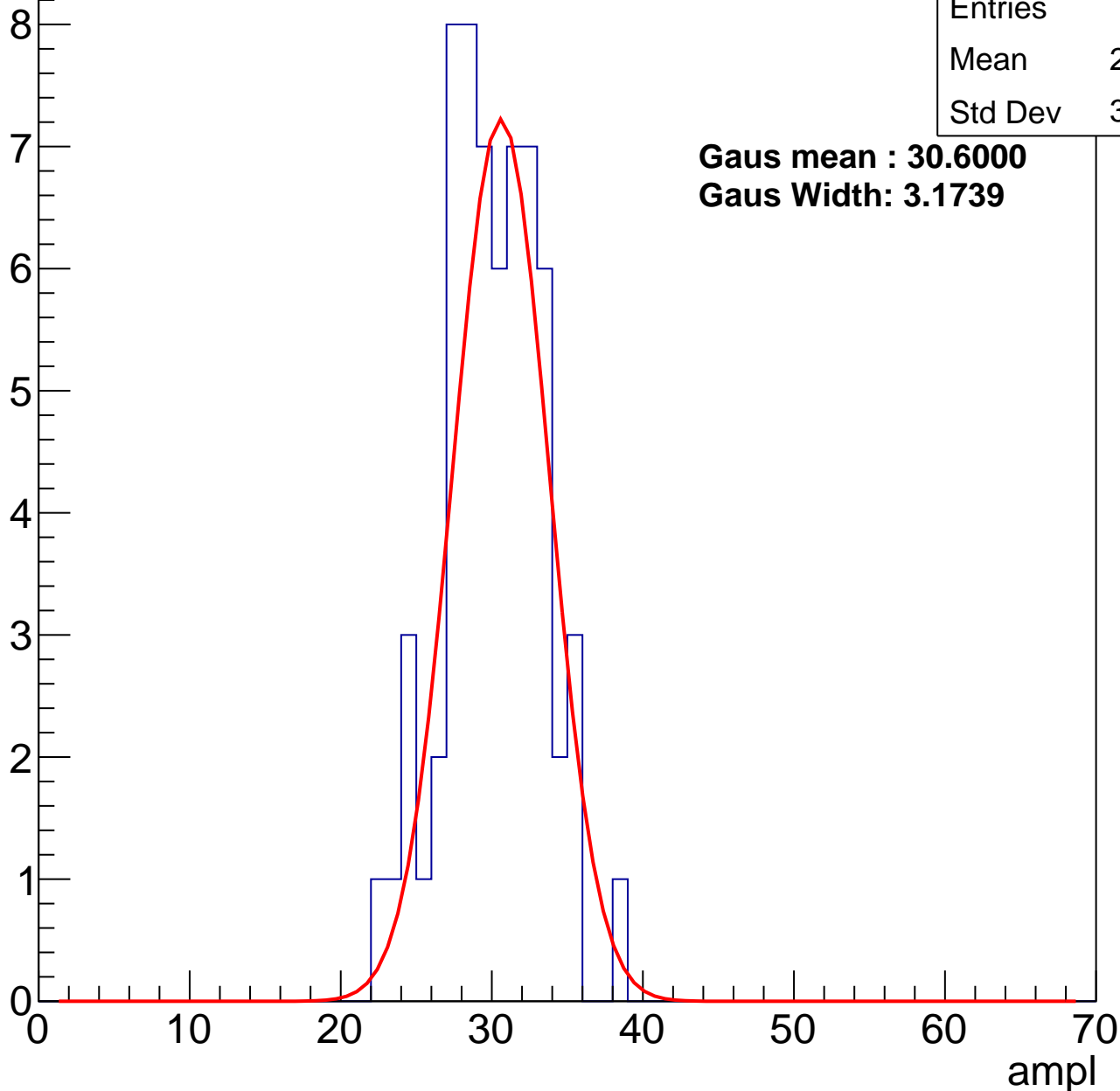
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.63
Std Dev	3.189

Gaus mean : 30.6000

Gaus Width: 3.1739



B0L001S, U23-ch29, adc1

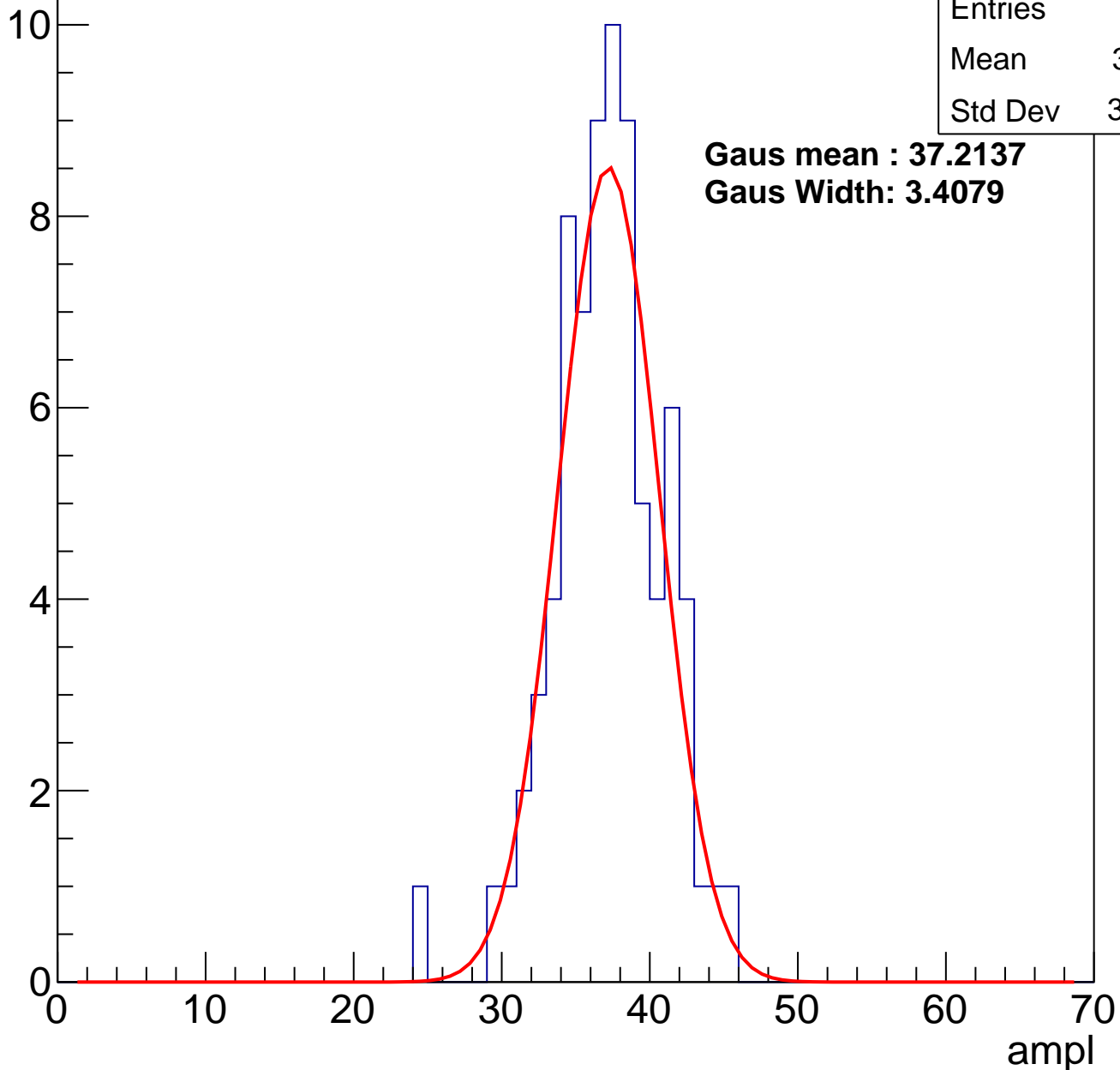
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	36.71
Std Dev	3.614

Gaus mean : 37.2137

Gaus Width: 3.4079

Entry



B0L001S, U23-ch29, adc2

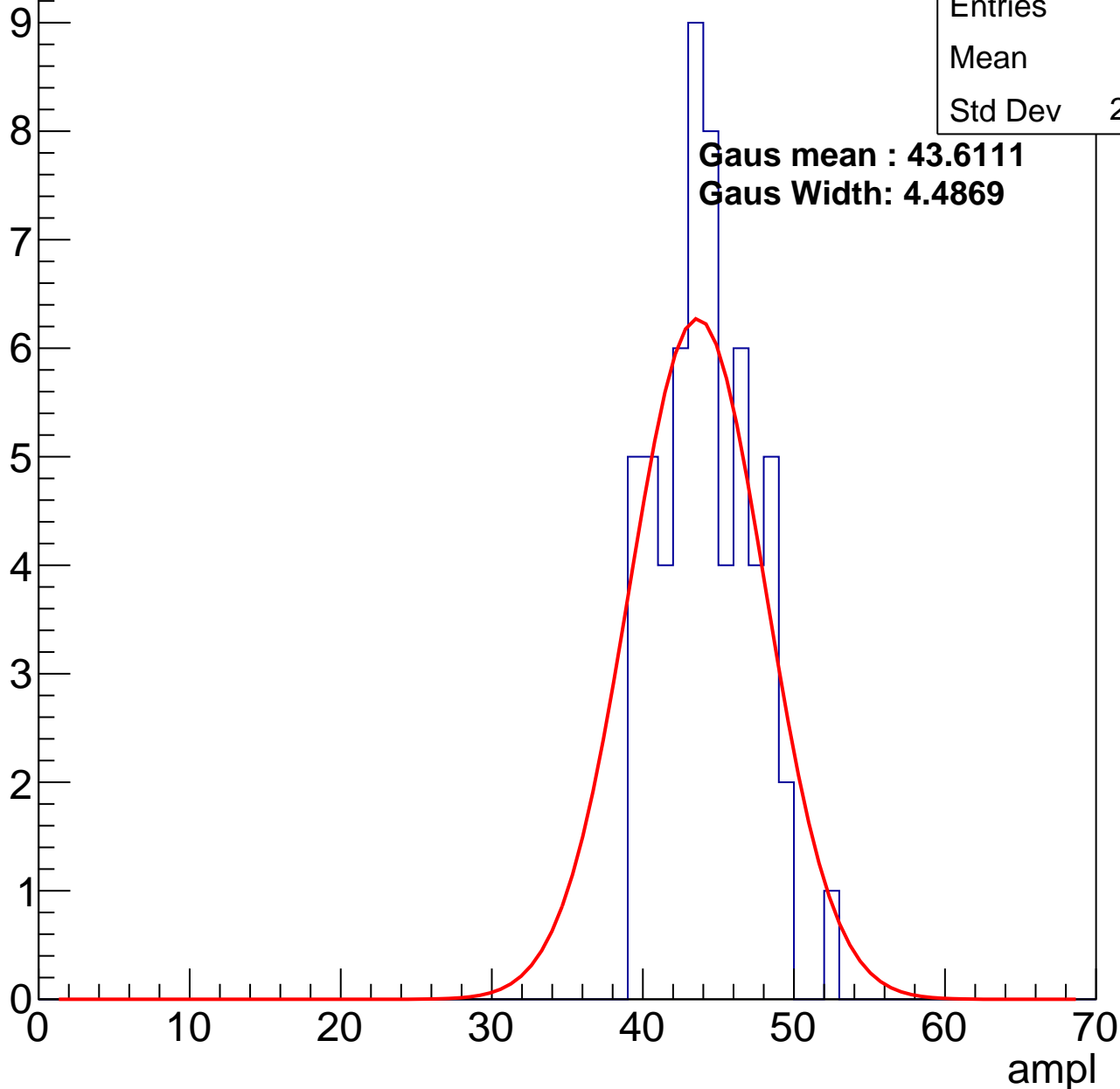
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.8
Std Dev	2.996

Gaus mean : 43.6111

Gaus Width: 4.4869

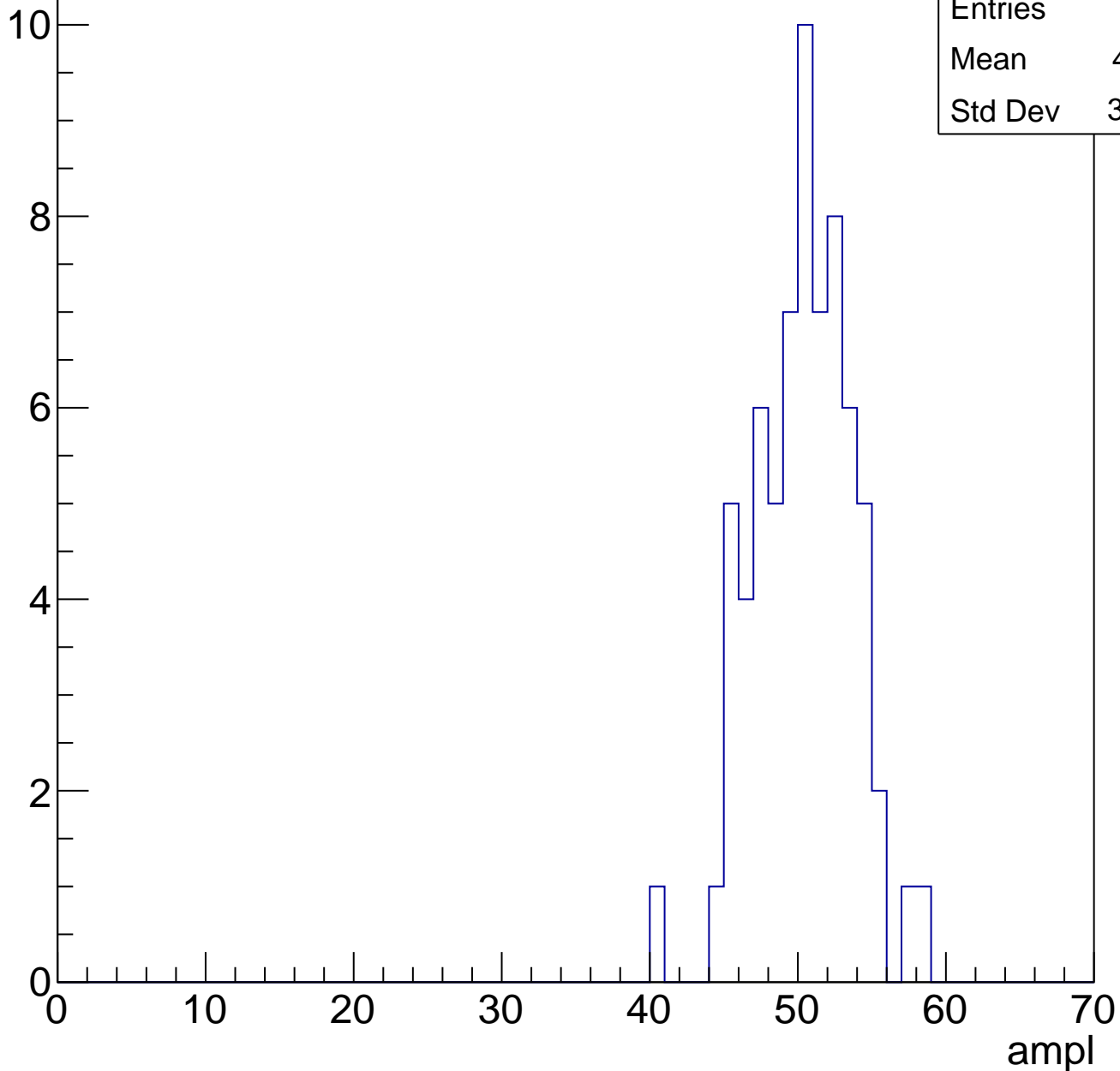


B0L001S, U23-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	49.91
Std Dev	3.278

Entry

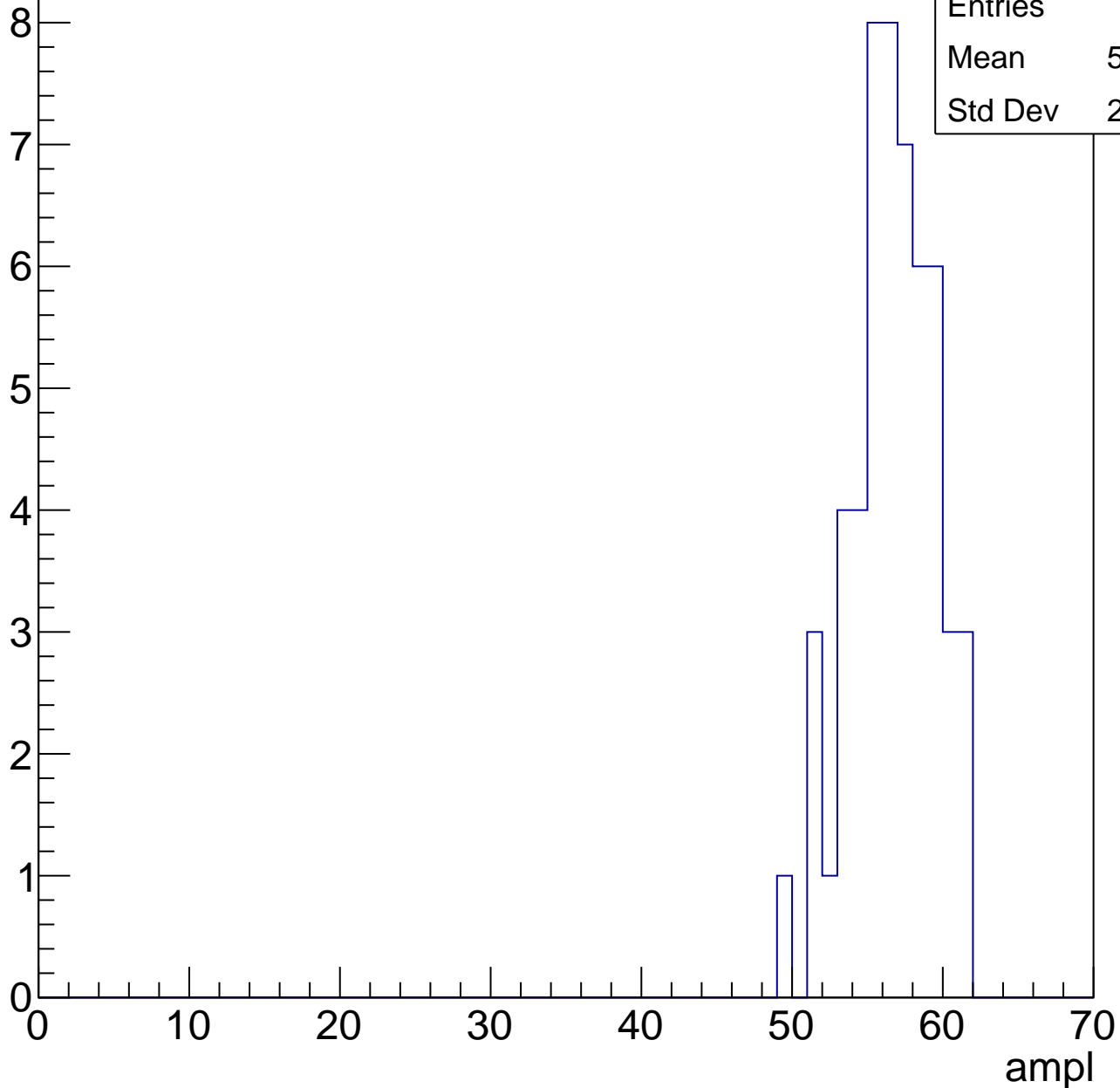


B0L001S, U23-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.19
Std Dev	2.742

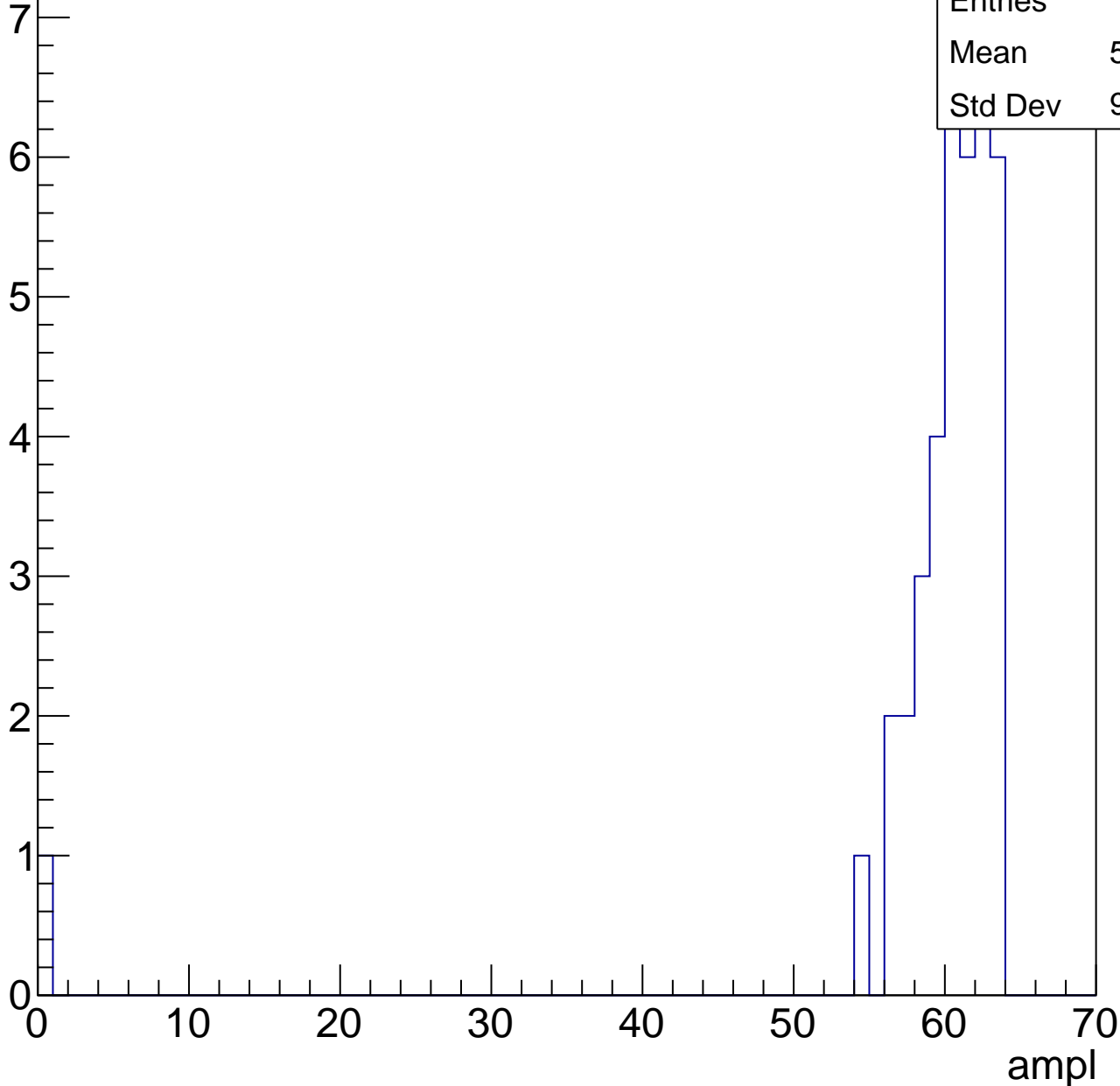


B0L001S, U23-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.67
Std Dev	9.767



B0L001S, U23-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

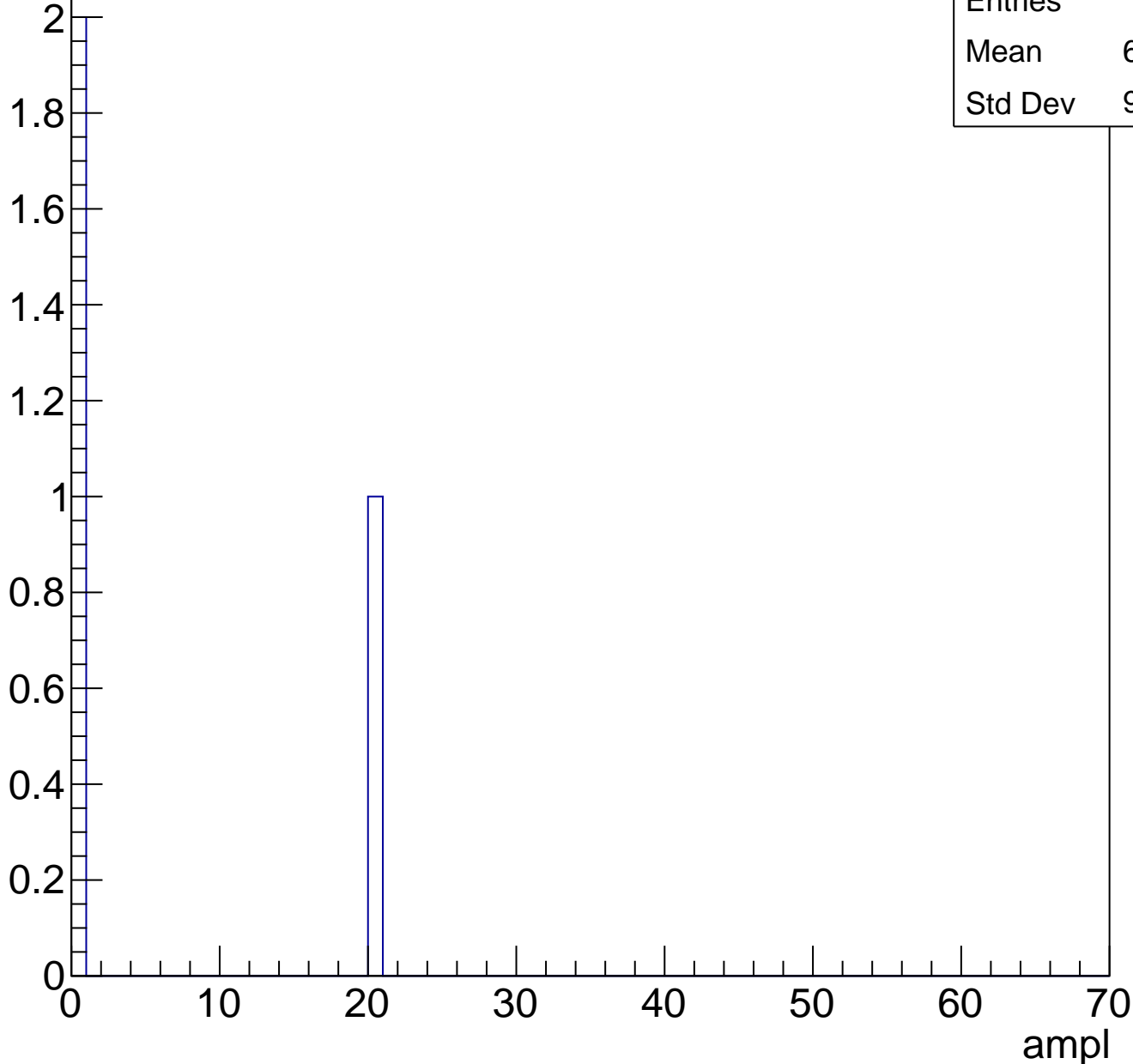
Entry



B0L001S, U23-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L001S, U23-ch30, adc0

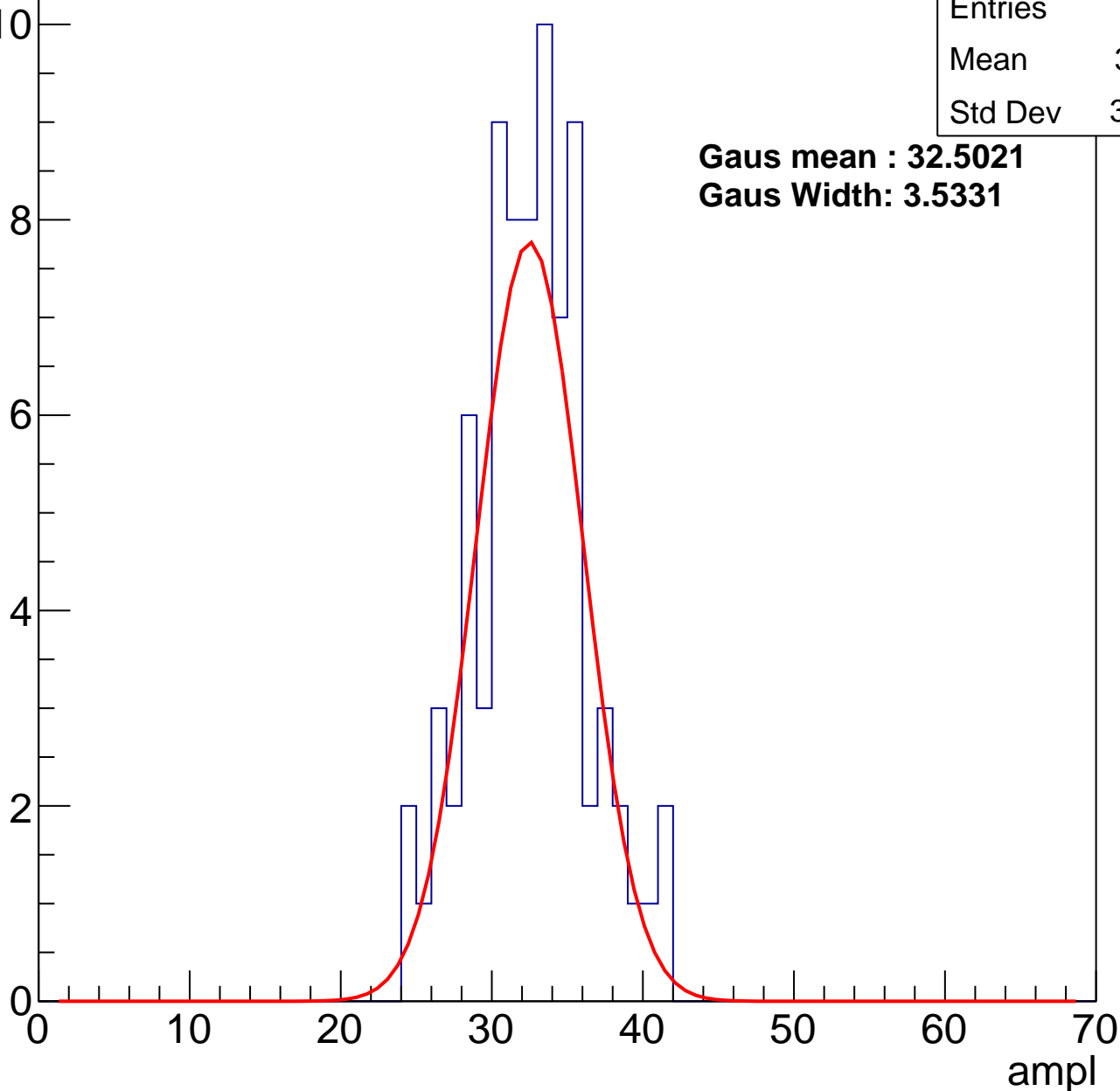
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	32.11
Std Dev	3.687

Gaus mean : 32.5021

Gaus Width: 3.5331



B0L001S, U23-ch30, adc1

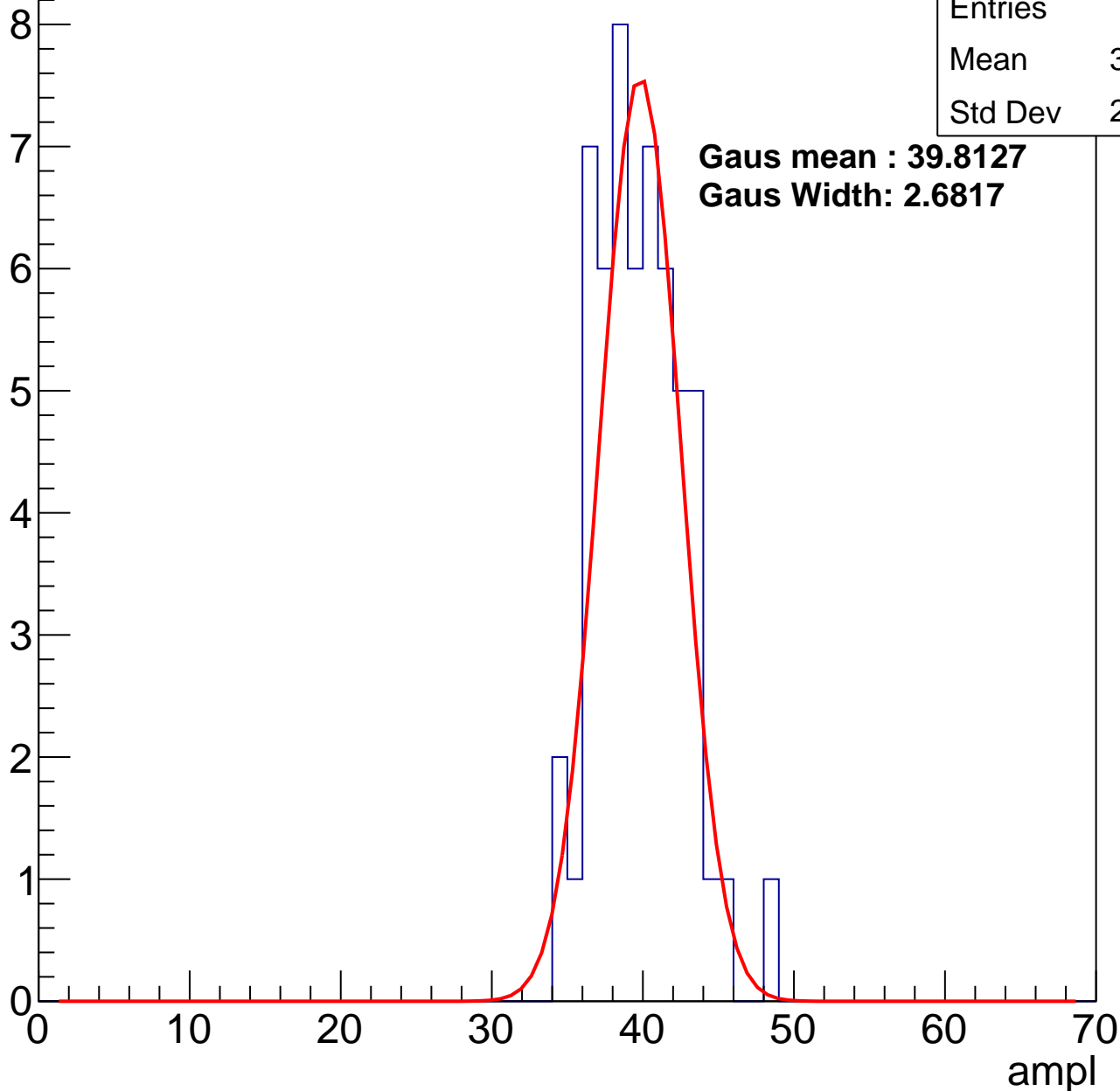
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	39.34
Std Dev	2.843

Gaus mean : 39.8127

Gaus Width: 2.6817



B0L001S, U23-ch30, adc2

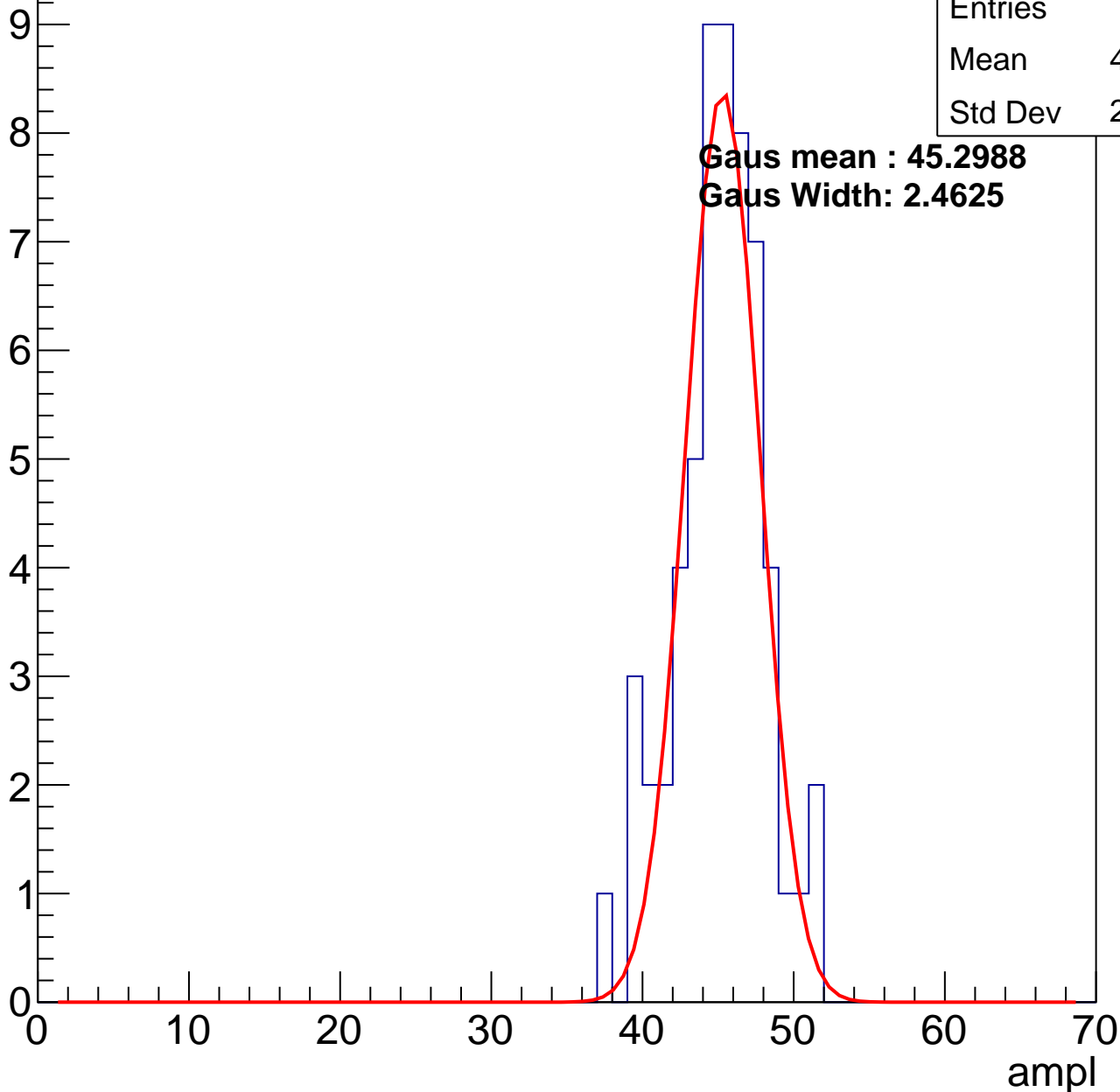
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.66
Std Dev	2.928

Gaus mean : 45.2988

Gaus Width: 2.4625

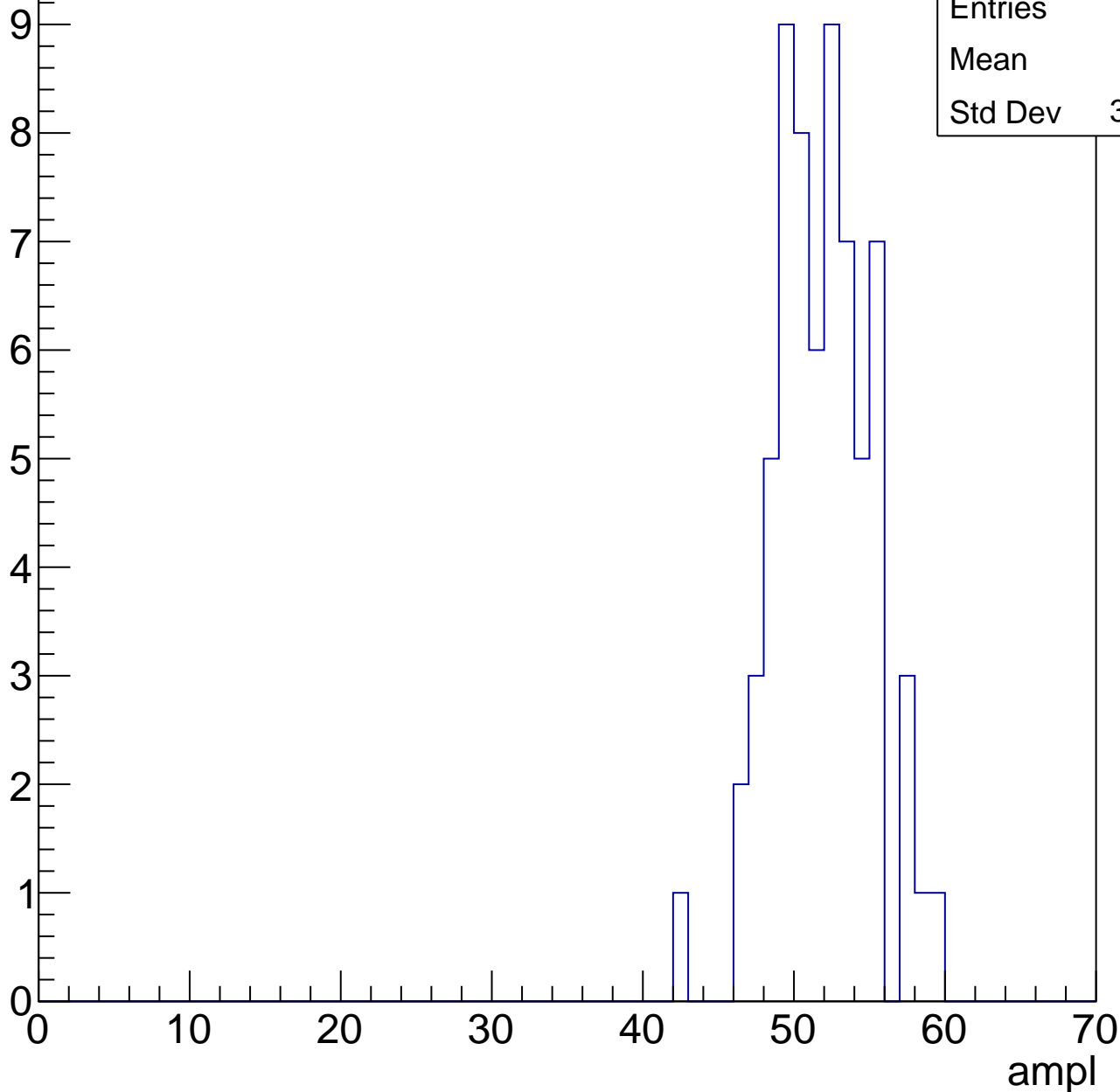


B0L001S, U23-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

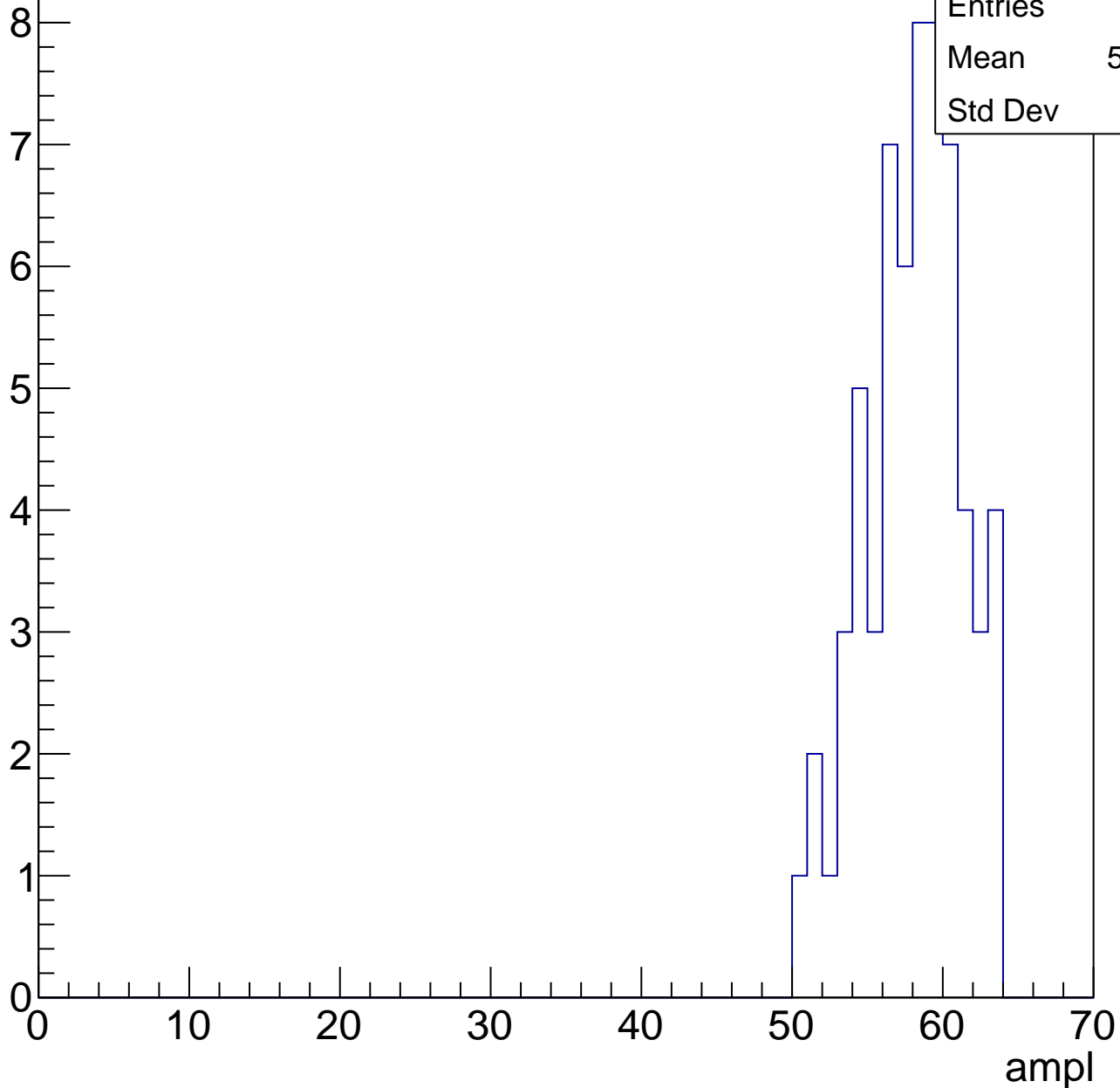
Entries	67
Mean	51.4
Std Dev	3.167



B0L001S, U23-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



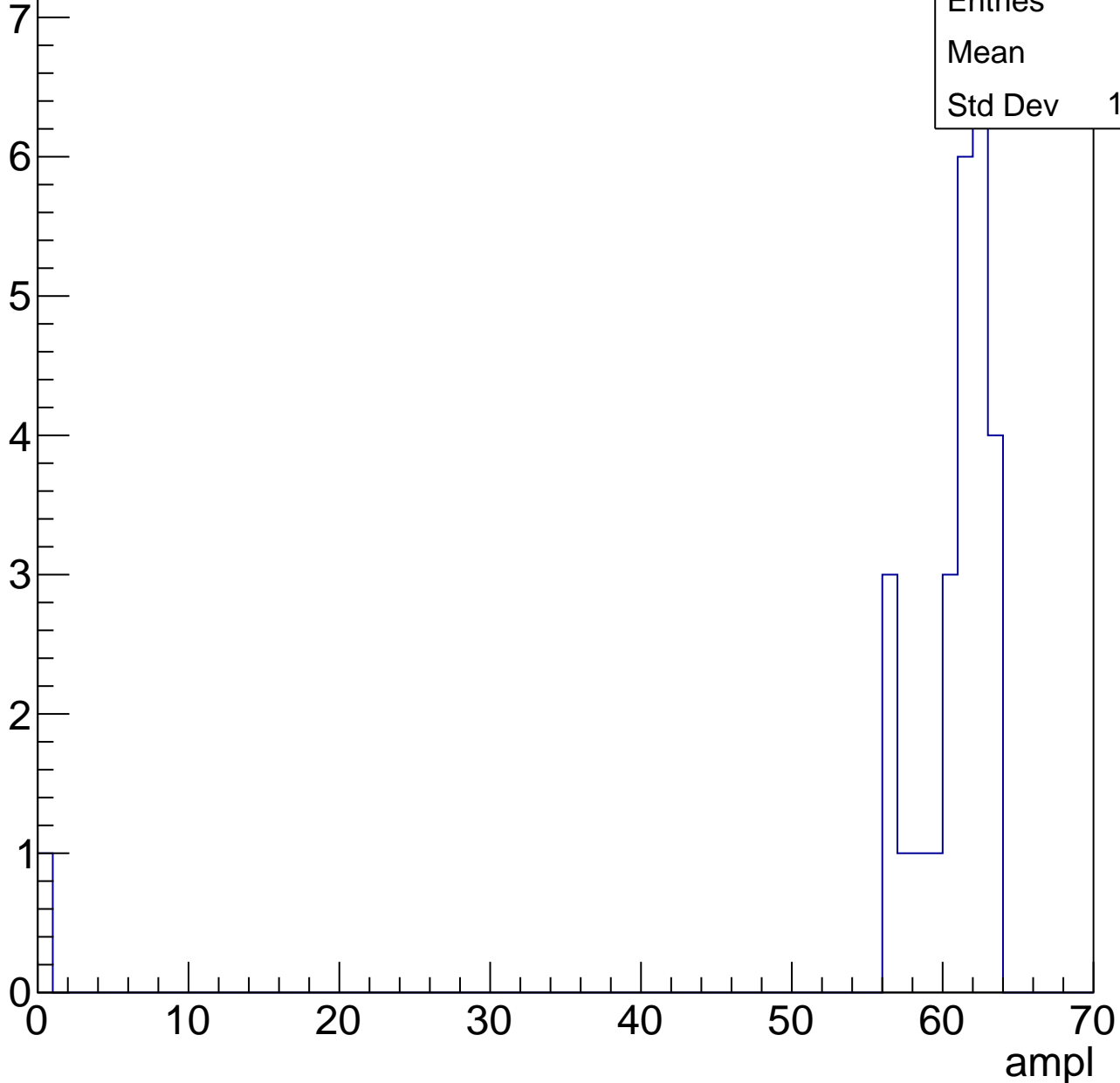
Entries	62
Mean	57.58
Std Dev	3.17

B0L001S, U23-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.3
Std Dev	11.63



B0L001S, U23-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch31, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	29.71
Std Dev	3.319

Gaus mean : 30.1465

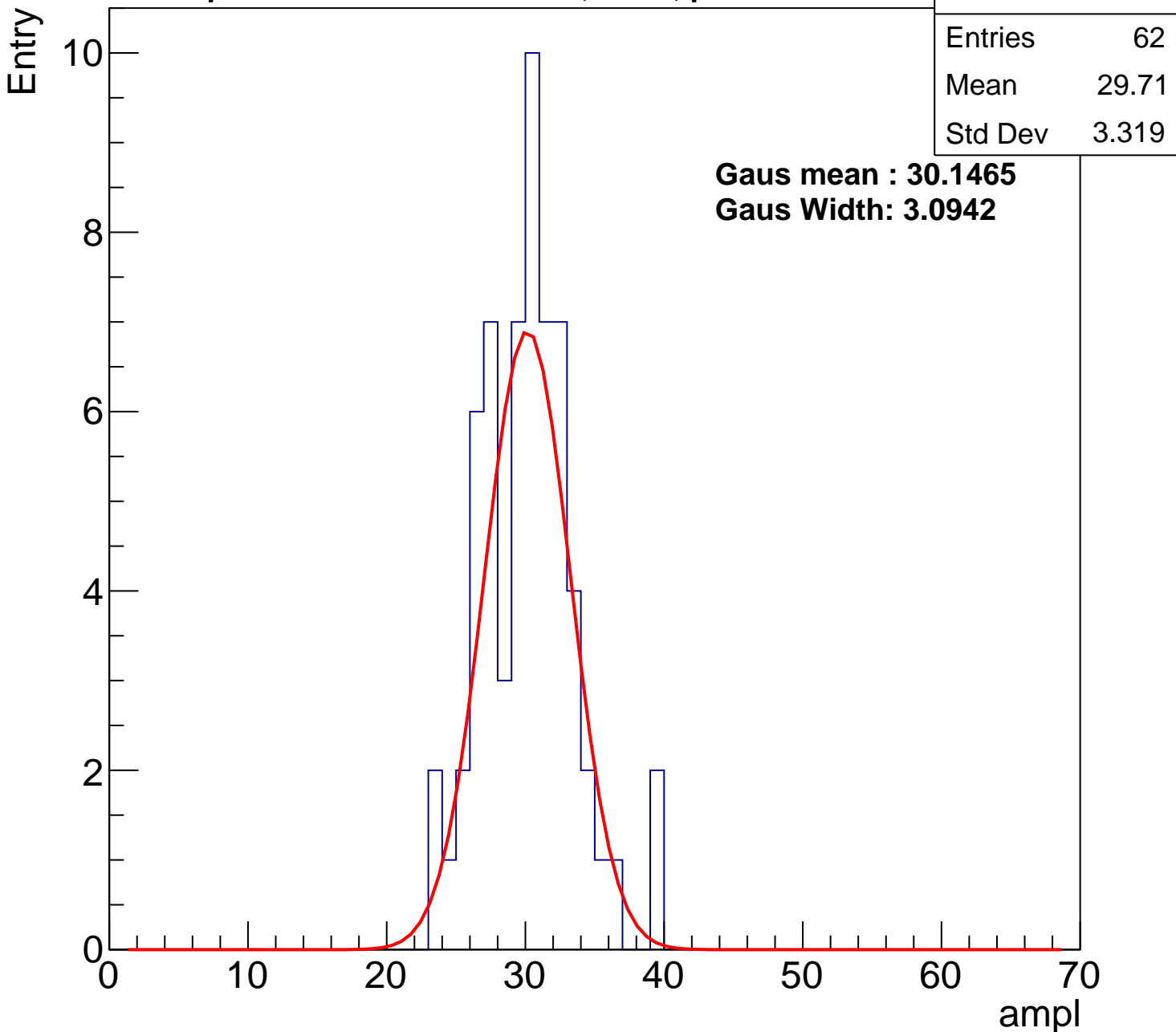
Gaus Width: 3.0942

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch31, adc1

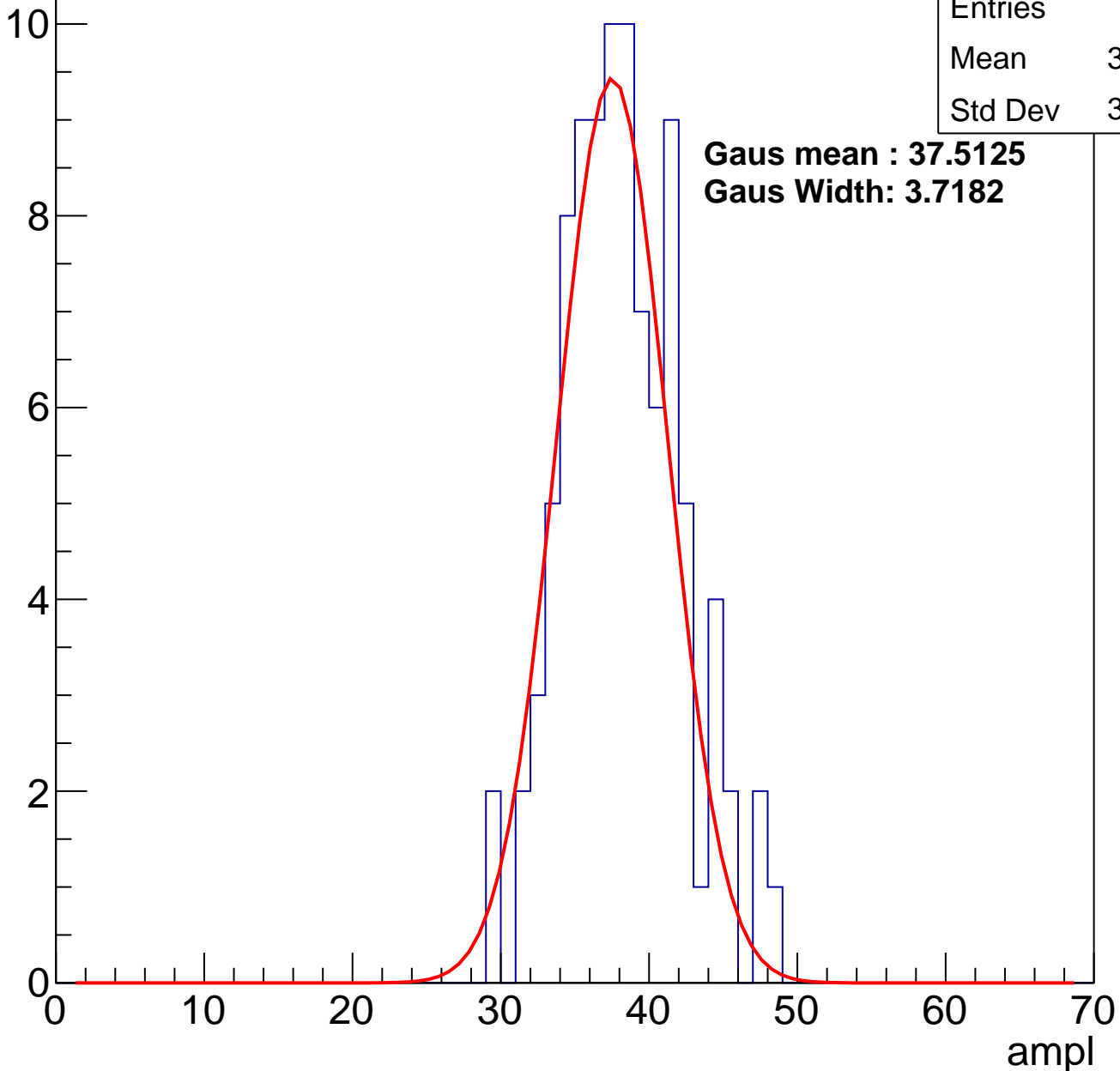
calib_packv5_042523_0143.root, FC#9, port A1

Entries	95
Mean	37.74
Std Dev	3.924

Gaus mean : 37.5125

Gaus Width: 3.7182

Entry



B0L001S, U23-ch31, adc2

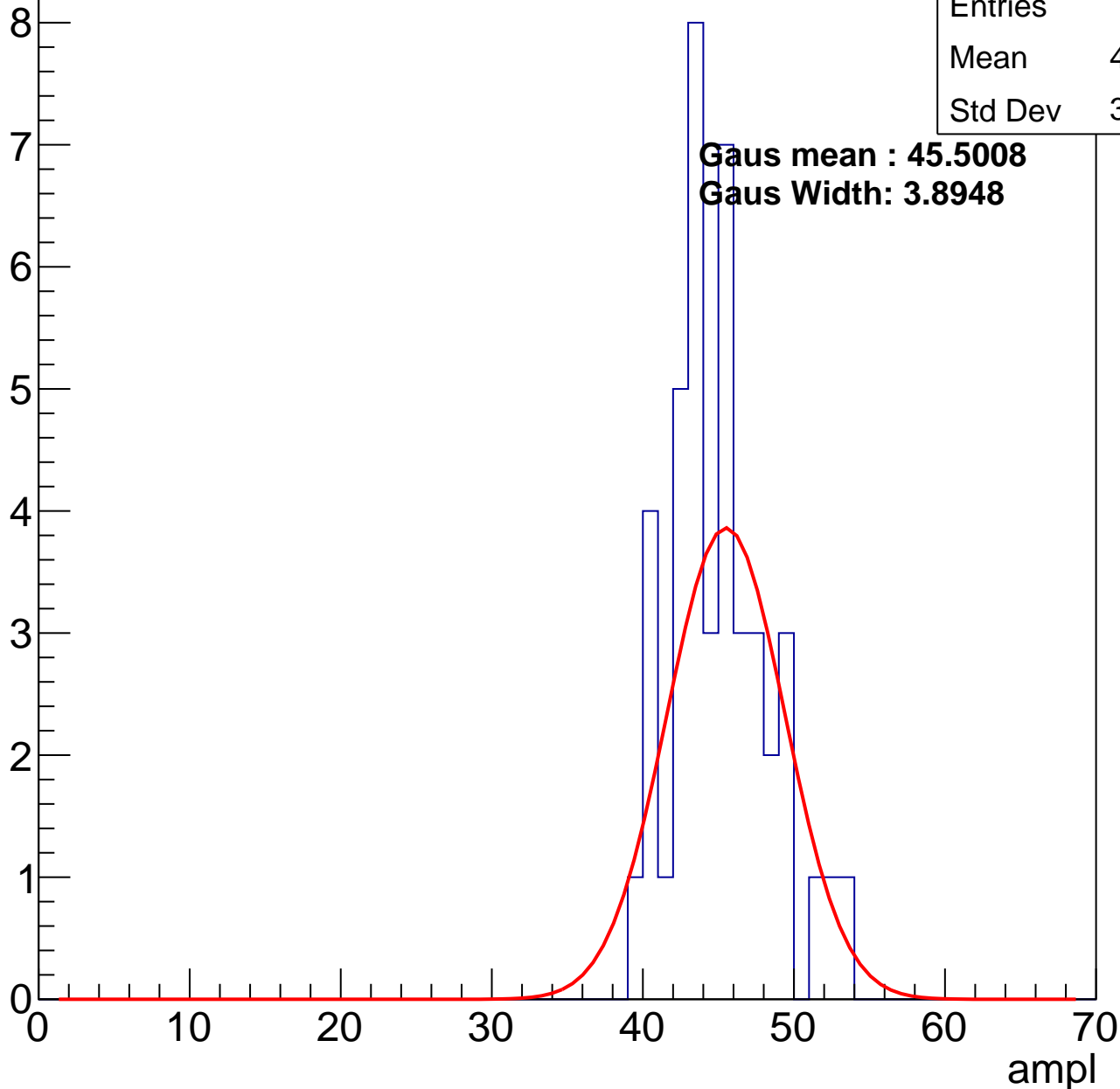
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	44.63
Std Dev	3.257

Gaus mean : 45.5008

Gaus Width: 3.8948



B0L001S, U23-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	50.84
Std Dev	3.756

Entry

10

8

6

4

2

0

0

10

20

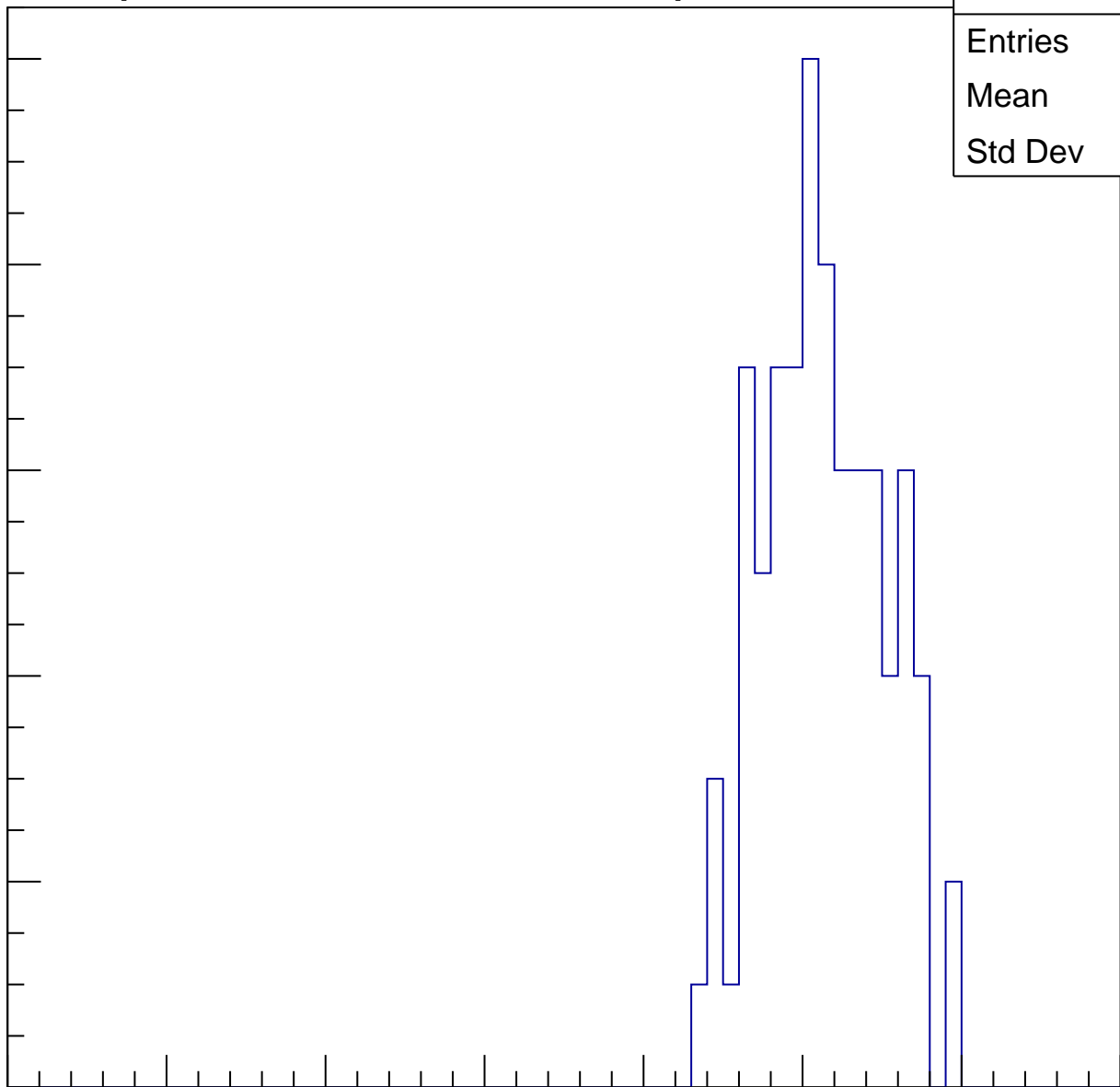
30

40

50

60

ampl

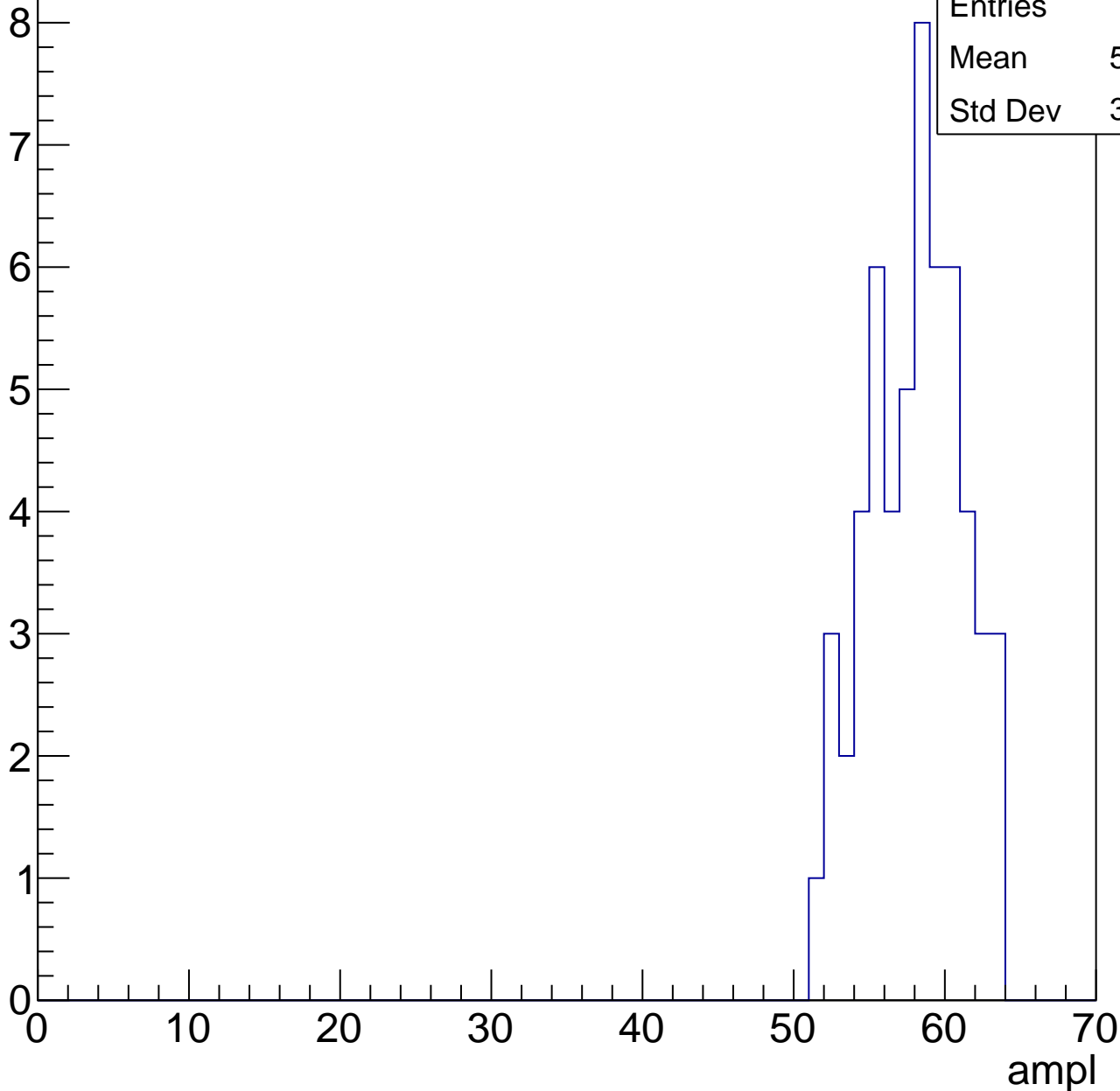


B0L001S, U23-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.55
Std Dev	3.086

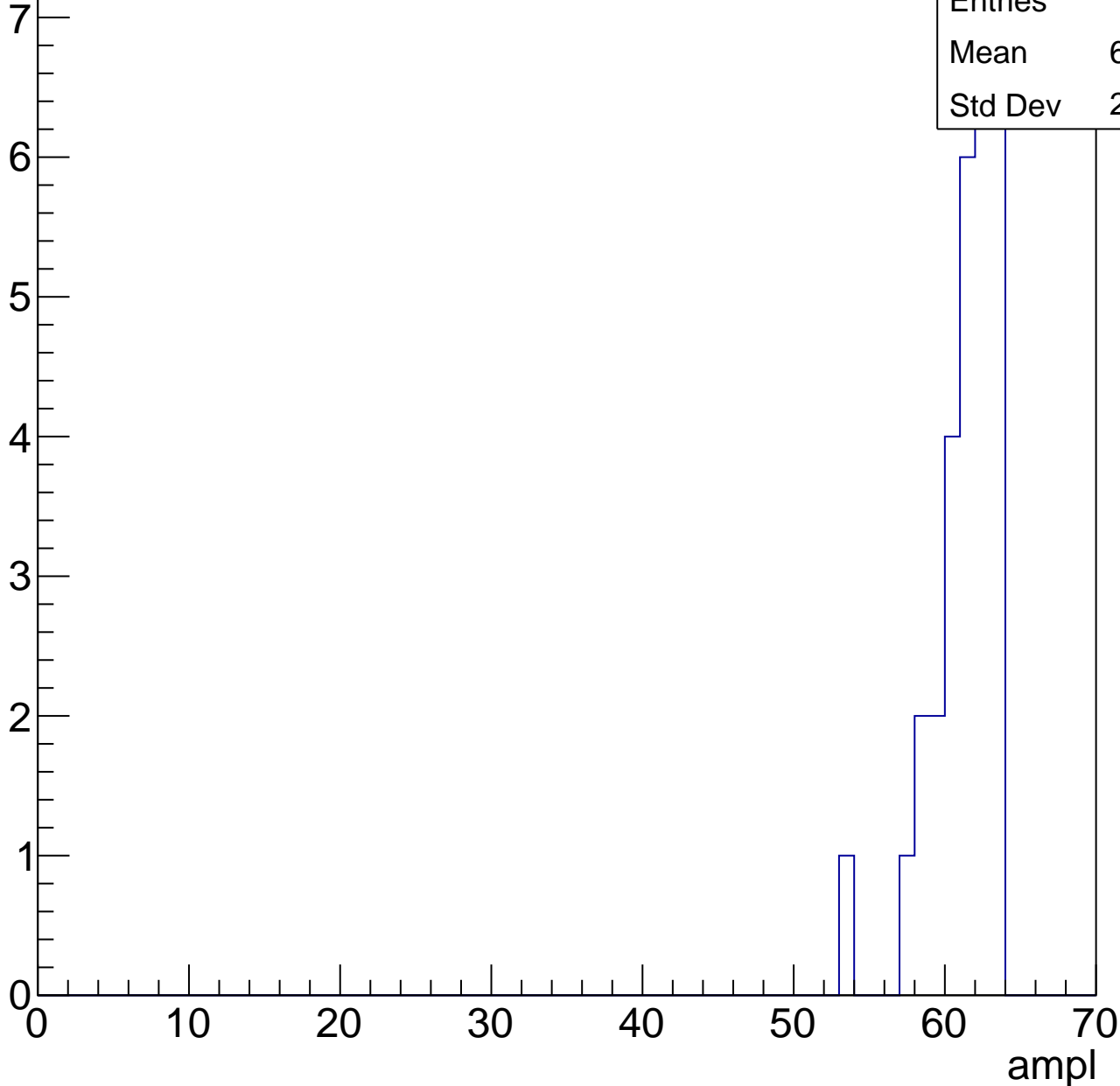


B0L001S, U23-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	60.83
Std Dev	2.192



B0L001S, U23-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch32, adc0

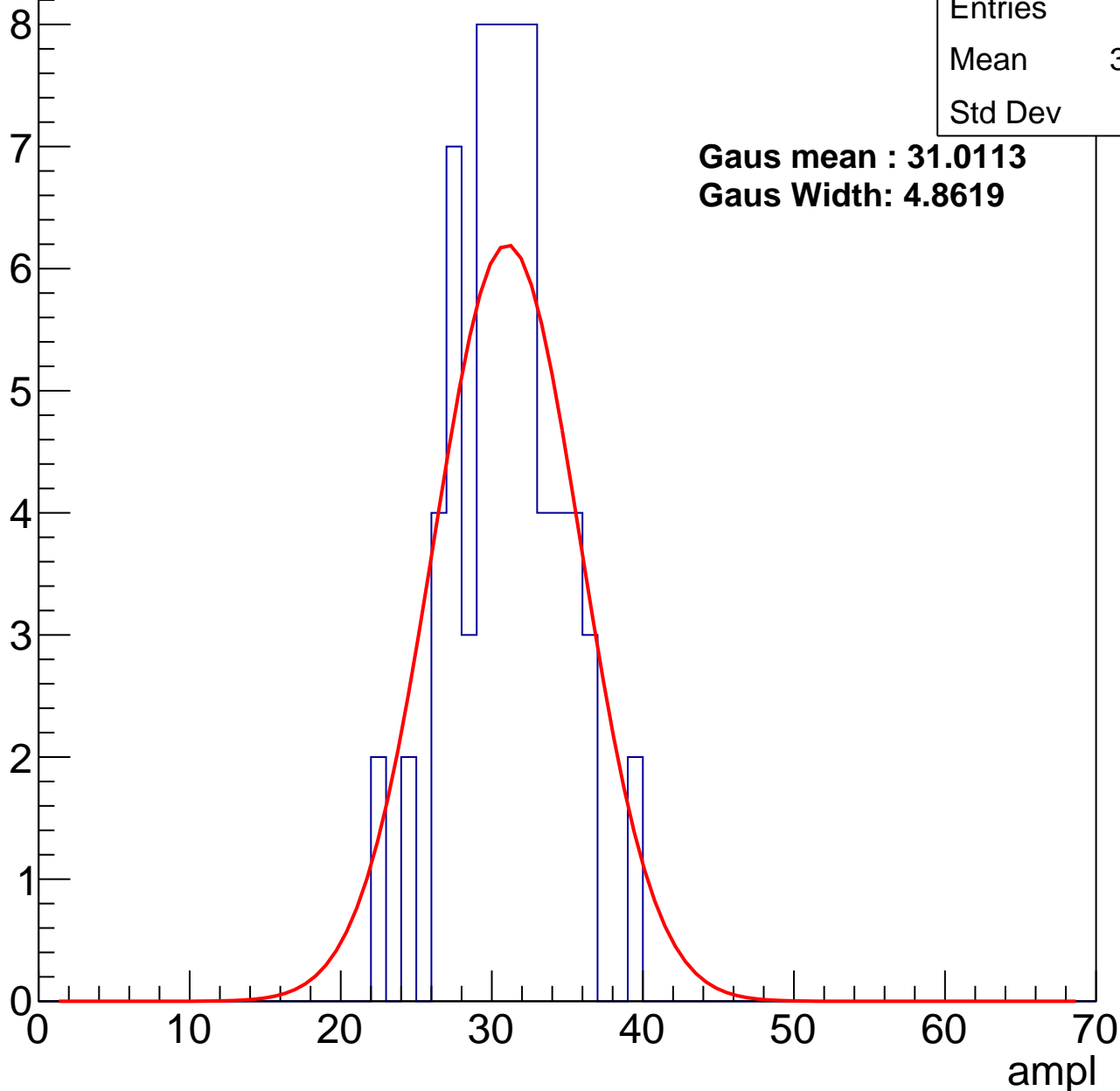
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.43
Std Dev	3.55

Gaus mean : 31.0113

Gaus Width: 4.8619



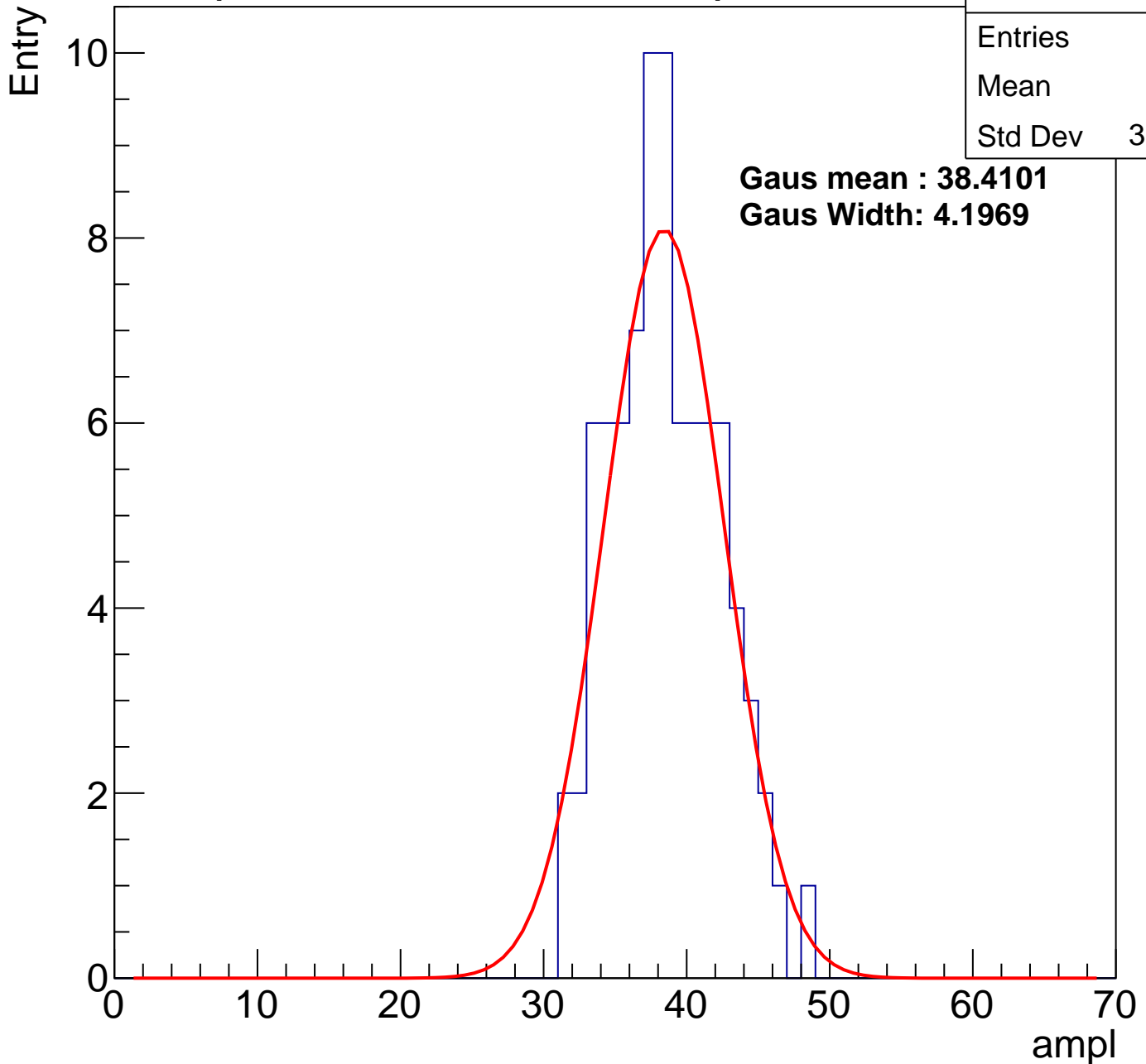
B0L001S, U23-ch32, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	38.1
Std Dev	3.712

Gaus mean : 38.4101

Gaus Width: 4.1969



B0L001S, U23-ch32, adc2

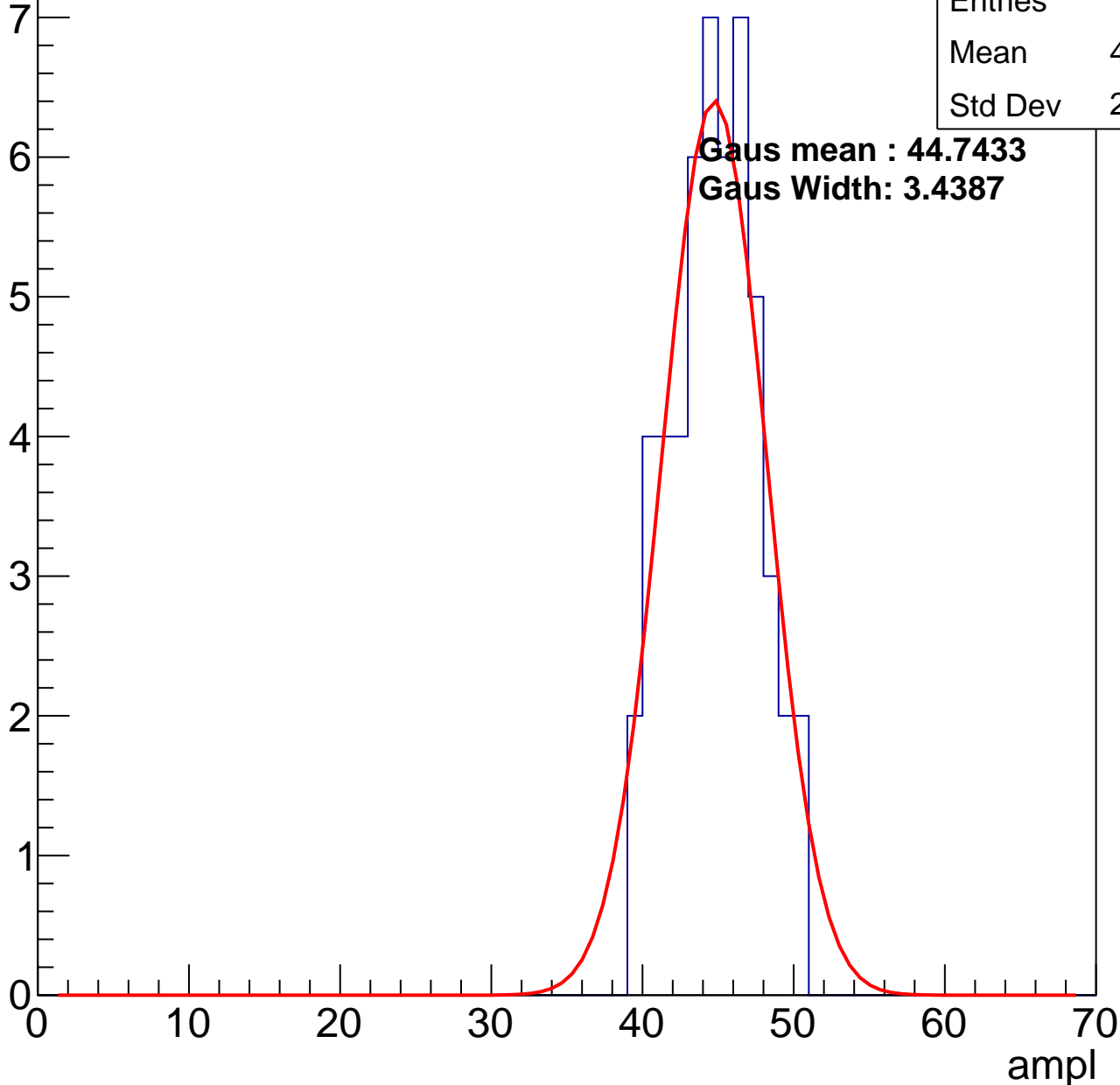
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.33
Std Dev	2.827

Gaus mean : 44.7433

Gaus Width: 3.4387

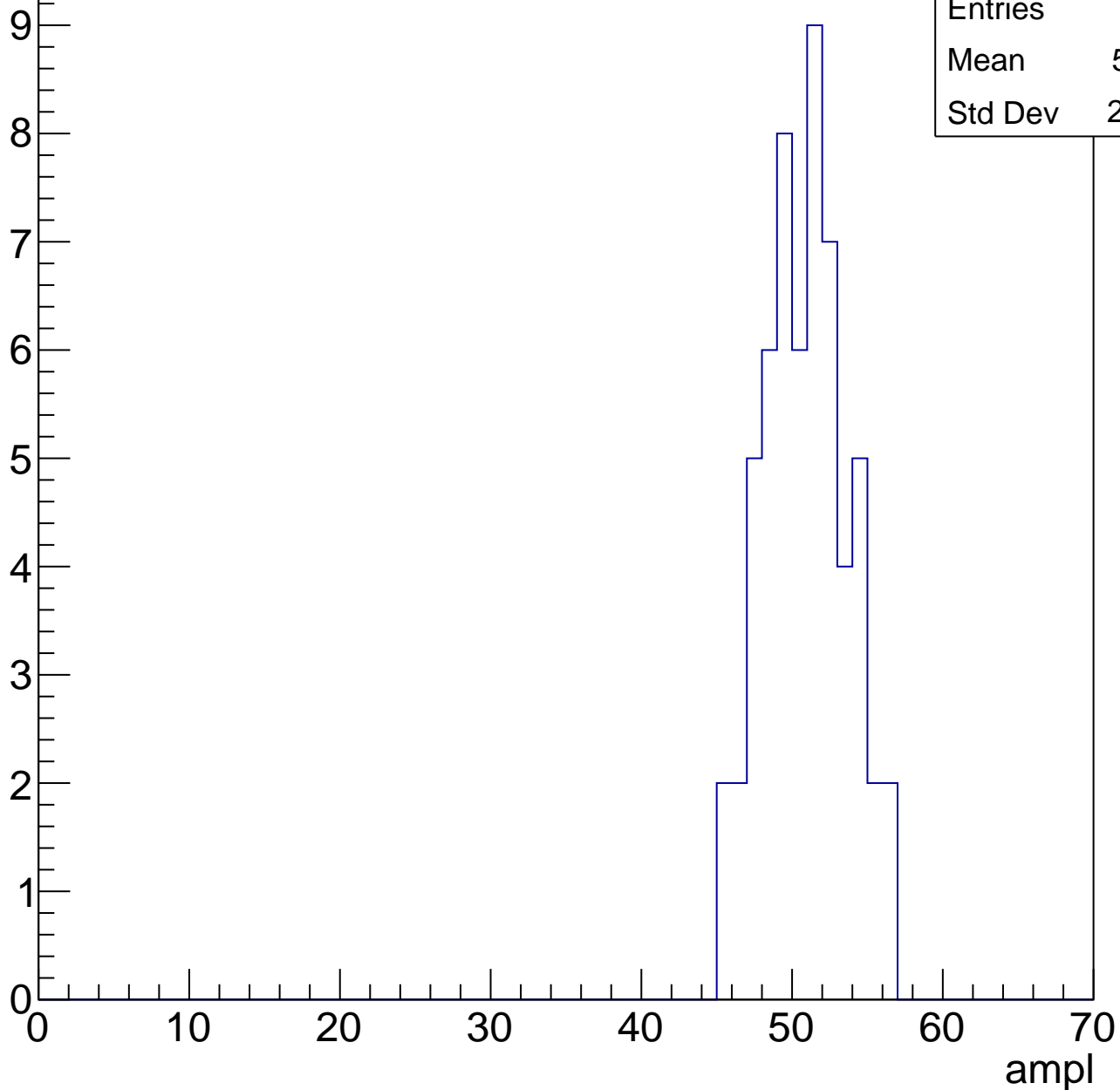


B0L001S, U23-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

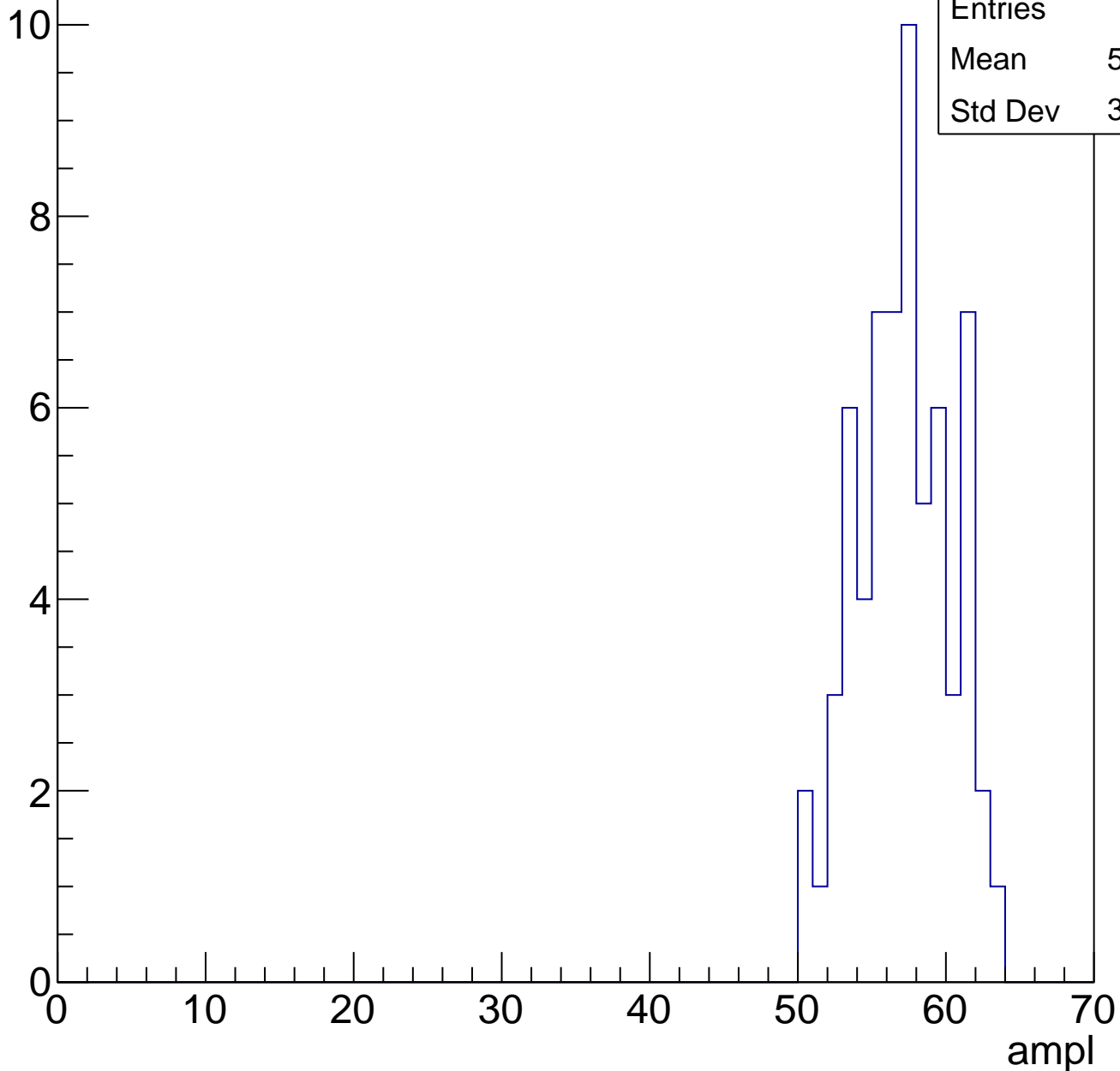
Entries	58
Mean	50.41
Std Dev	2.704



B0L001S, U23-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

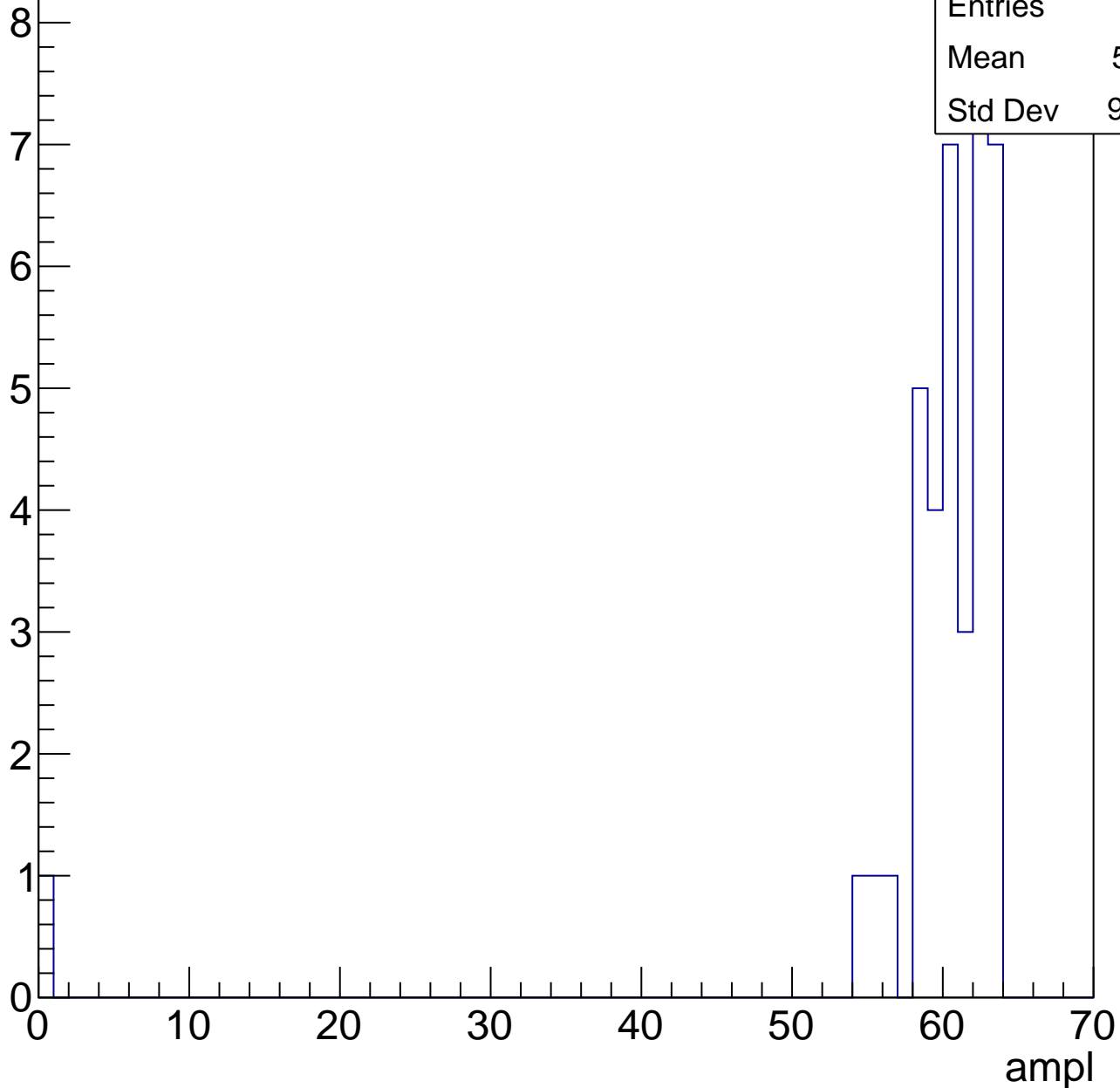


B0L001S, U23-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.71
Std Dev	9.915



B0L001S, U23-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch33, adc0

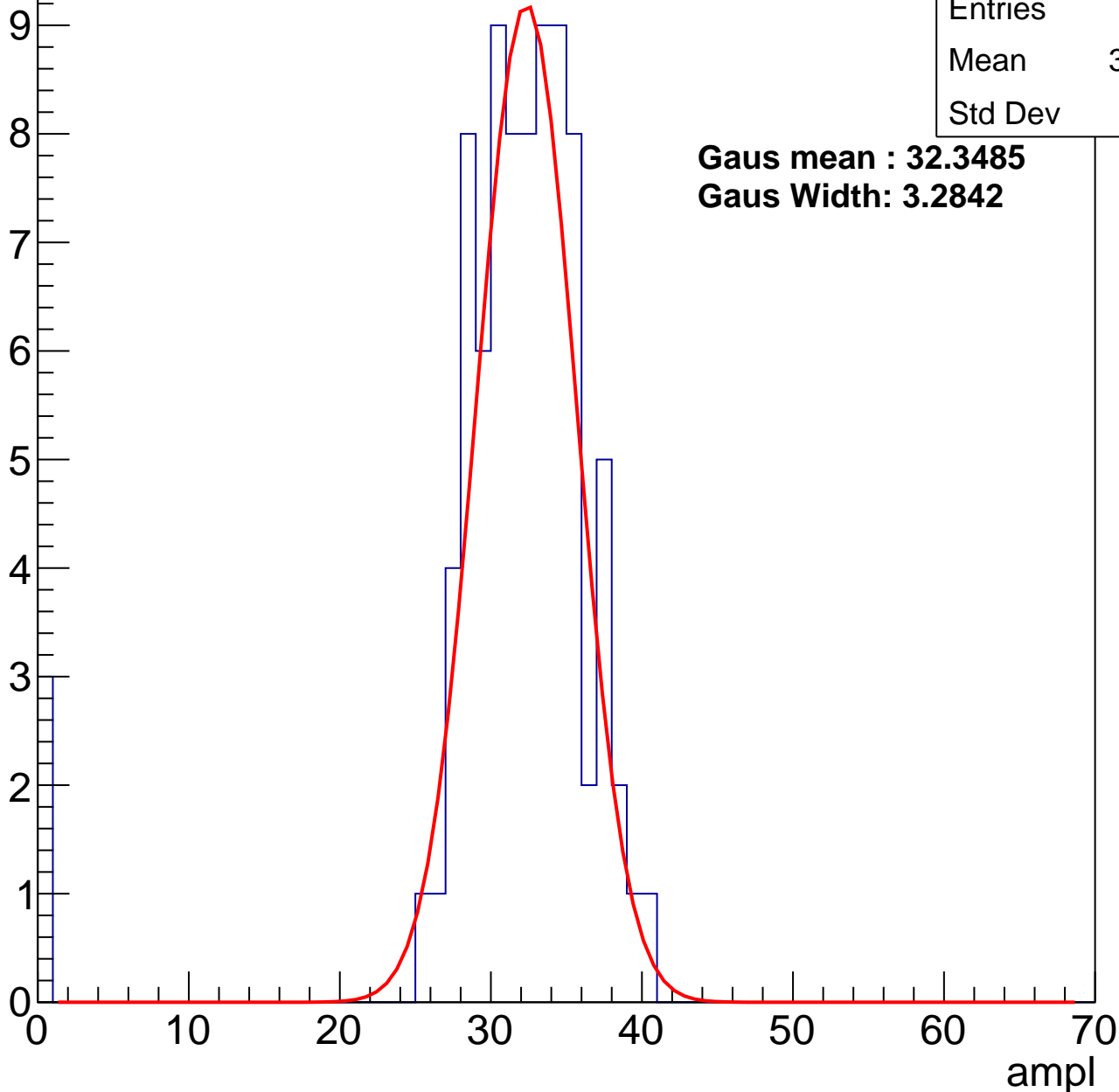
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	30.89
Std Dev	6.72

Gaus mean : 32.3485

Gaus Width: 3.2842



B0L001S, U23-ch33, adc1

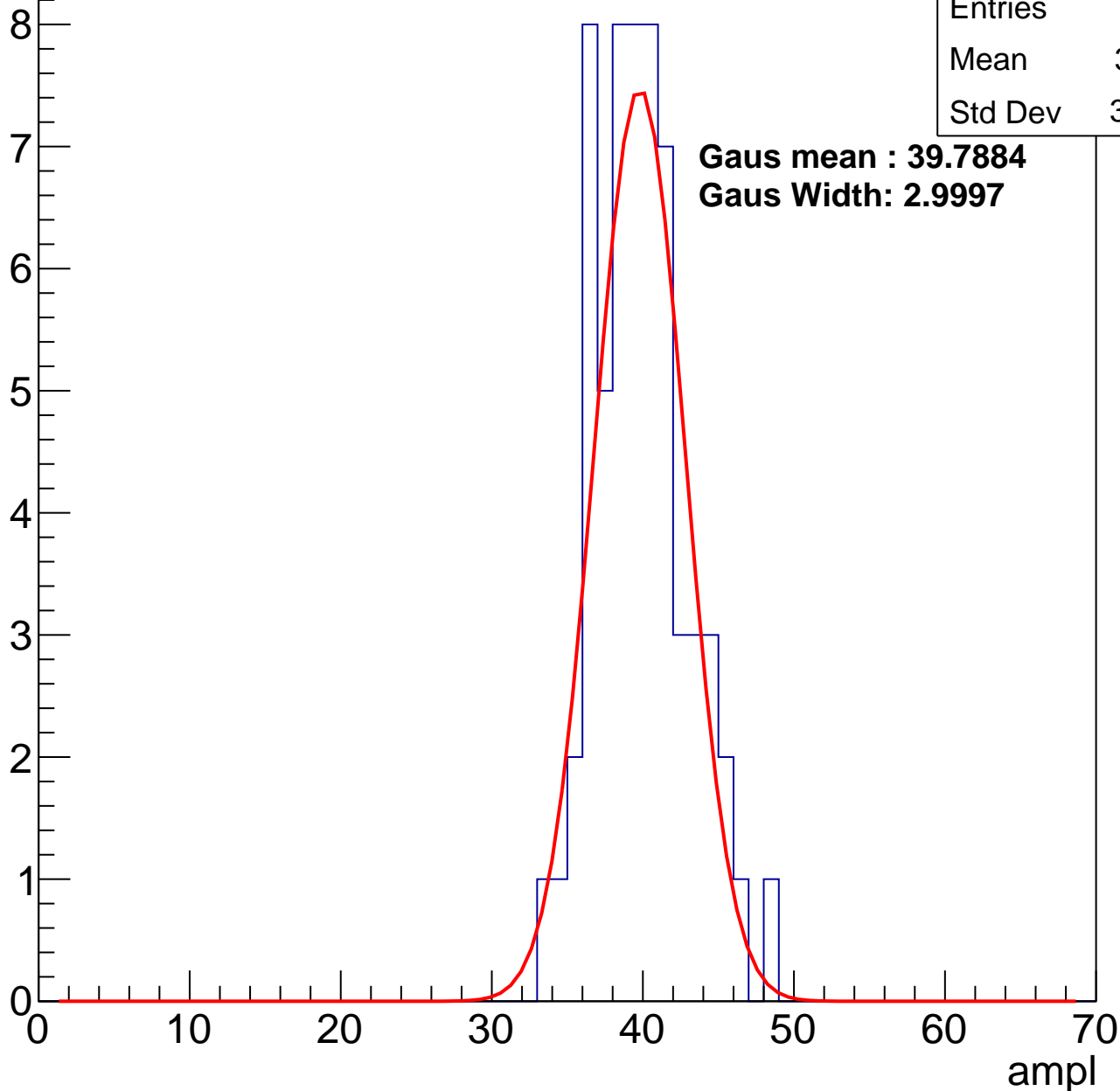
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	39.41
Std Dev	3.059

Gaus mean : 39.7884

Gaus Width: 2.9997



B0L001S, U23-ch33, adc2

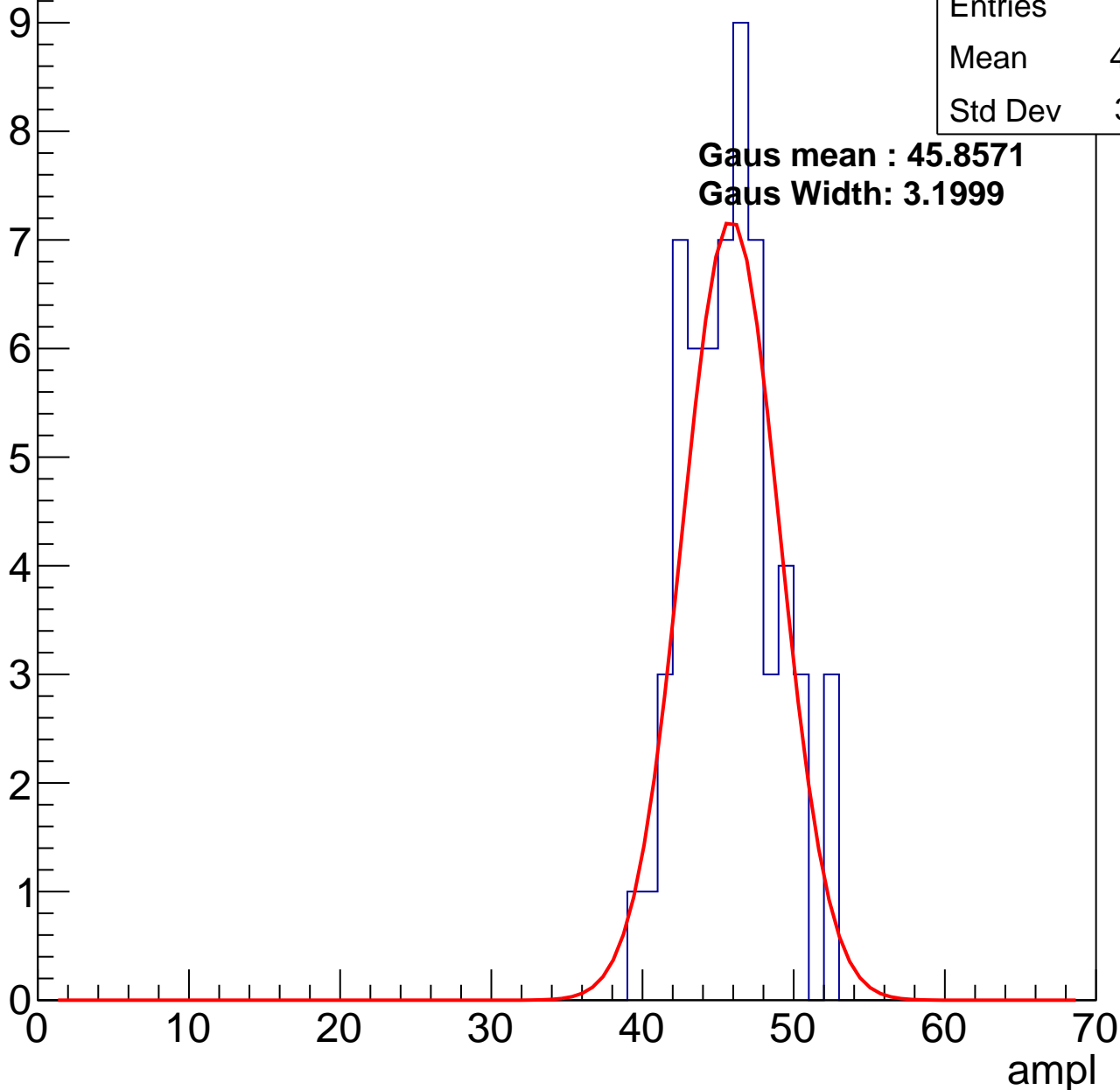
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45.37
Std Dev	3.011

Gaus mean : 45.8571

Gaus Width: 3.1999

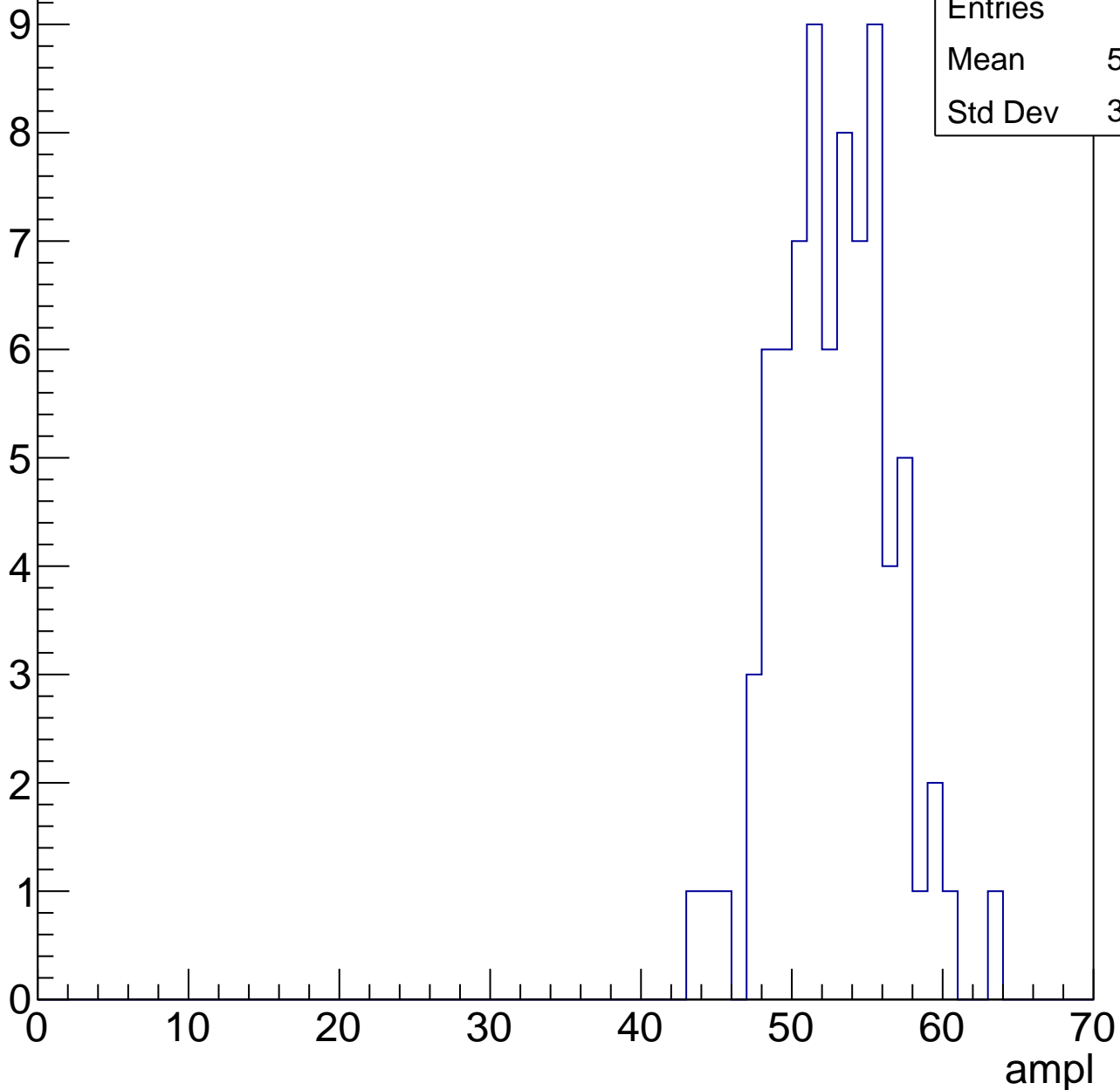


B0L001S, U23-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	52.32
Std Dev	3.709

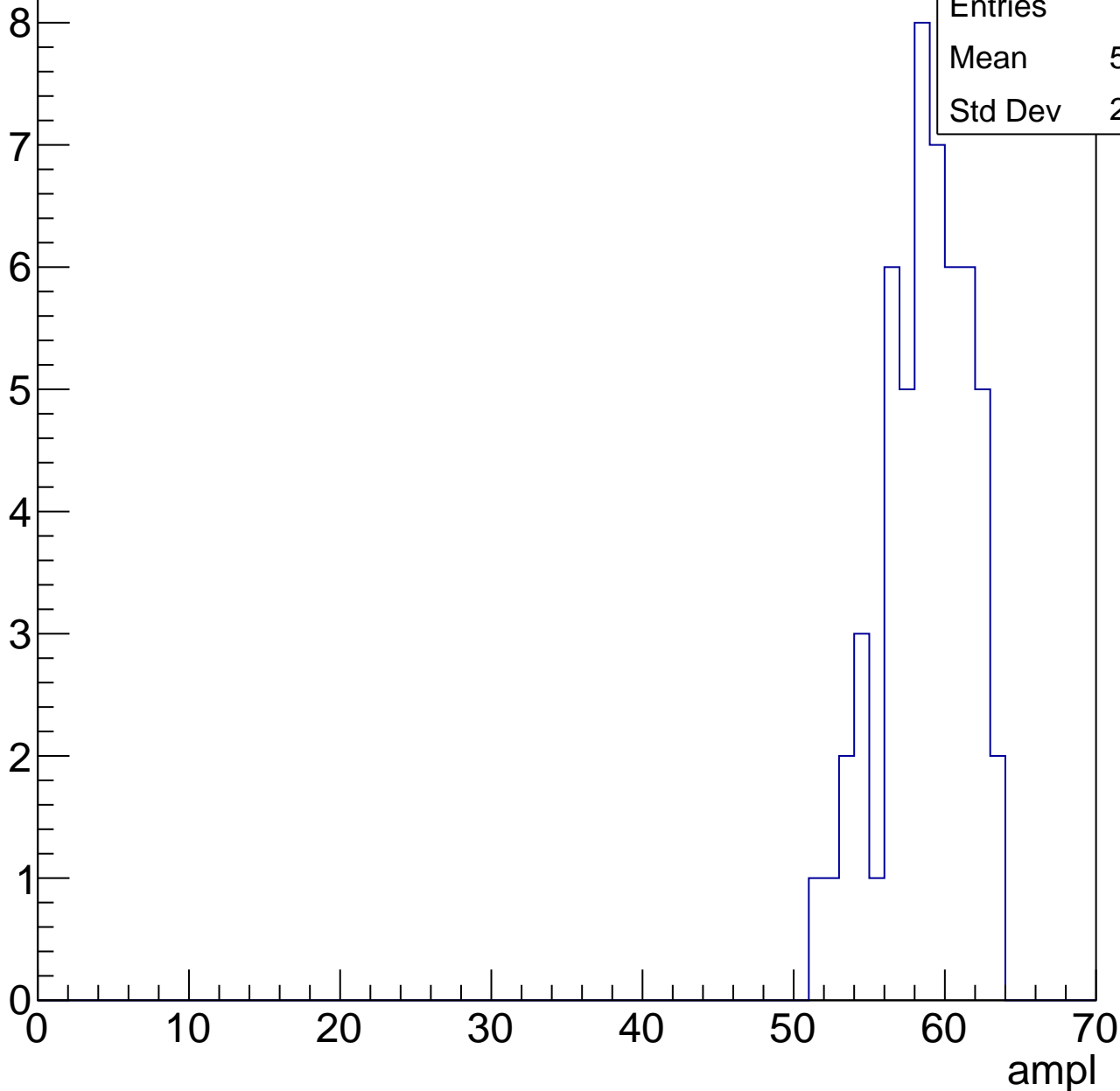


B0L001S, U23-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.23
Std Dev	2.859

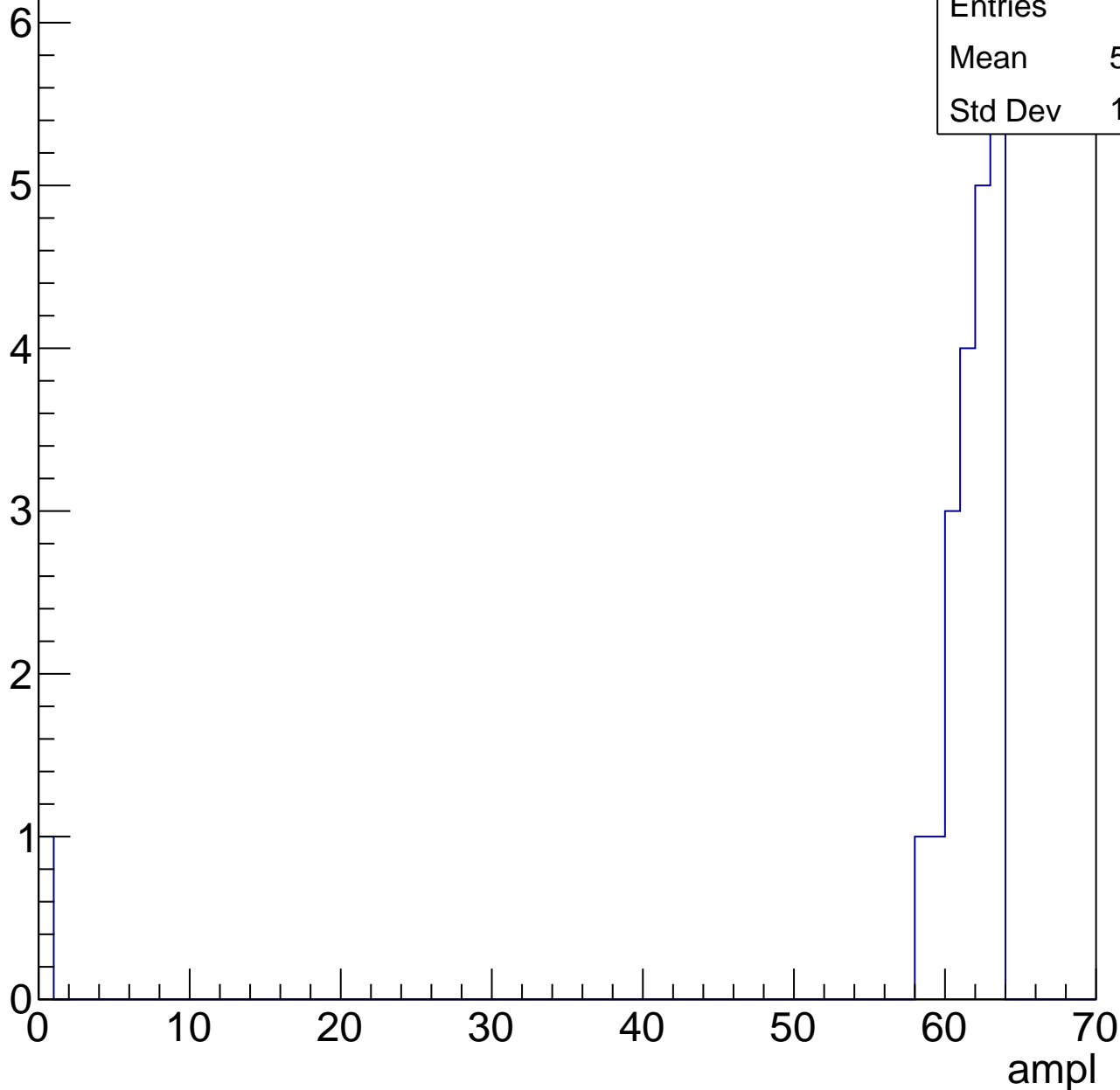


B0L001S, U23-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.52
Std Dev	13.16



B0L001S, U23-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch34, adc0

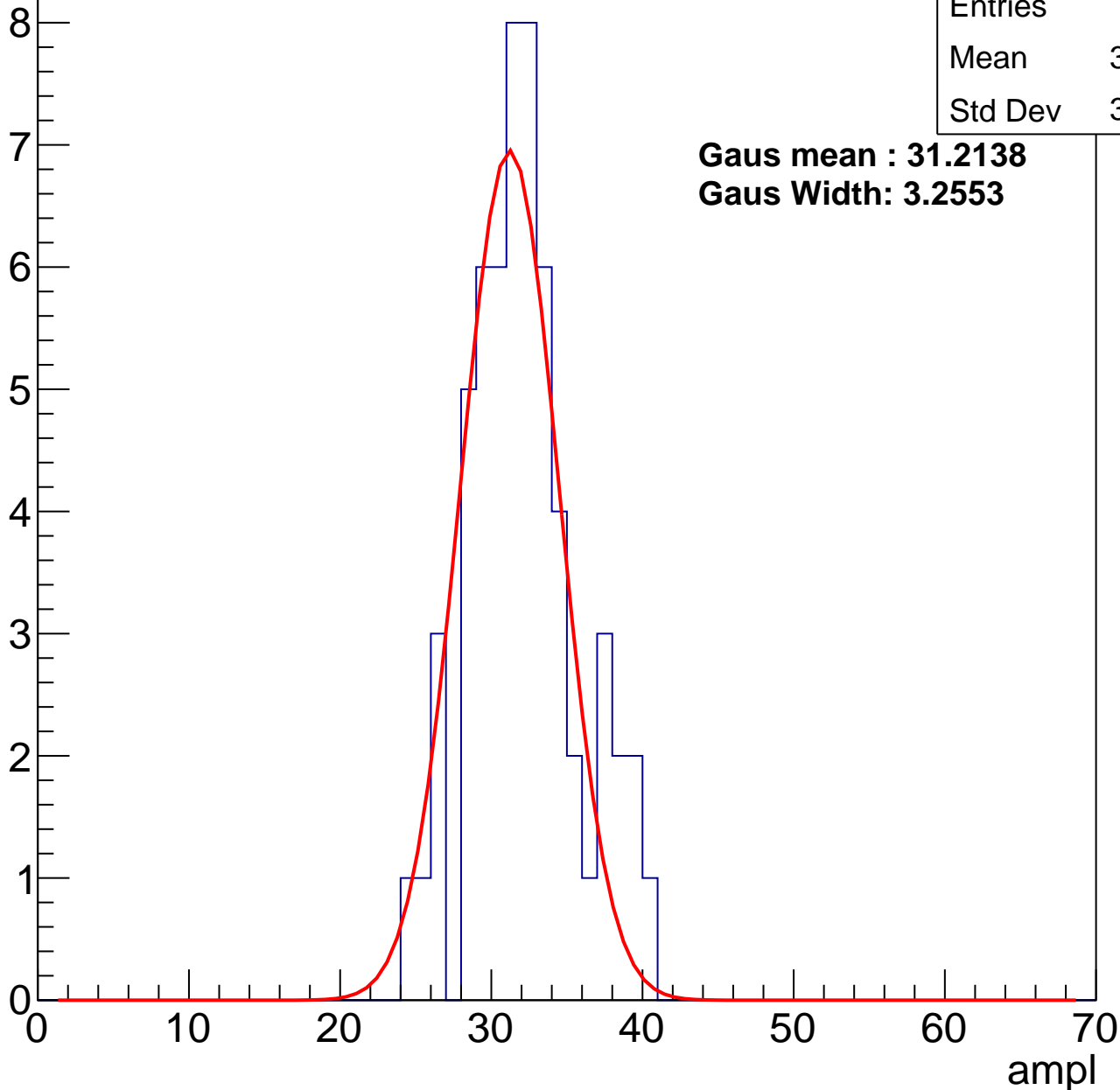
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.69
Std Dev	3.557

Gaus mean : 31.2138

Gaus Width: 3.2553



B0L001S, U23-ch34, adc1

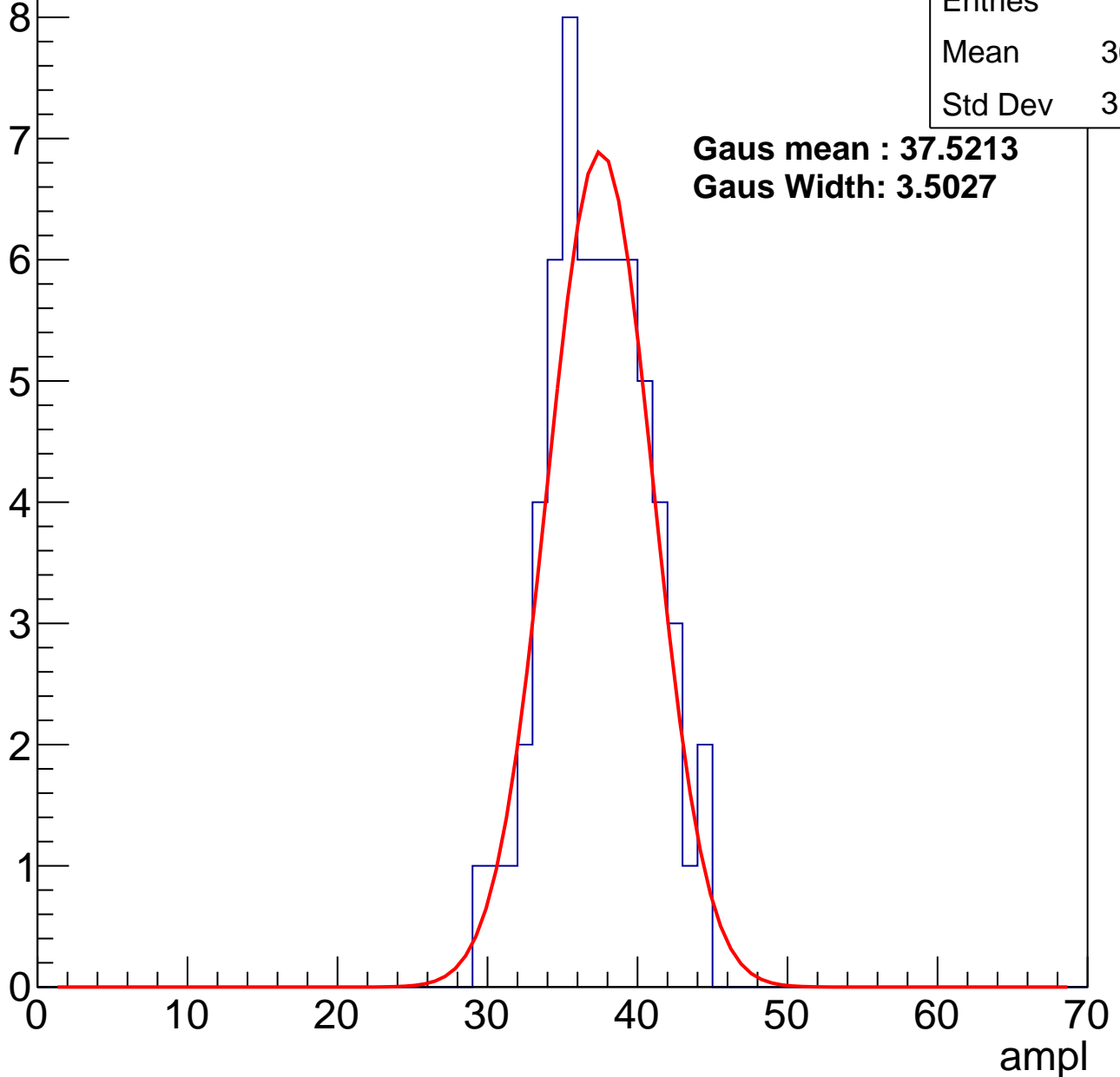
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	36.95
Std Dev	3.367

Gaus mean : 37.5213

Gaus Width: 3.5027



B0L001S, U23-ch34, adc2

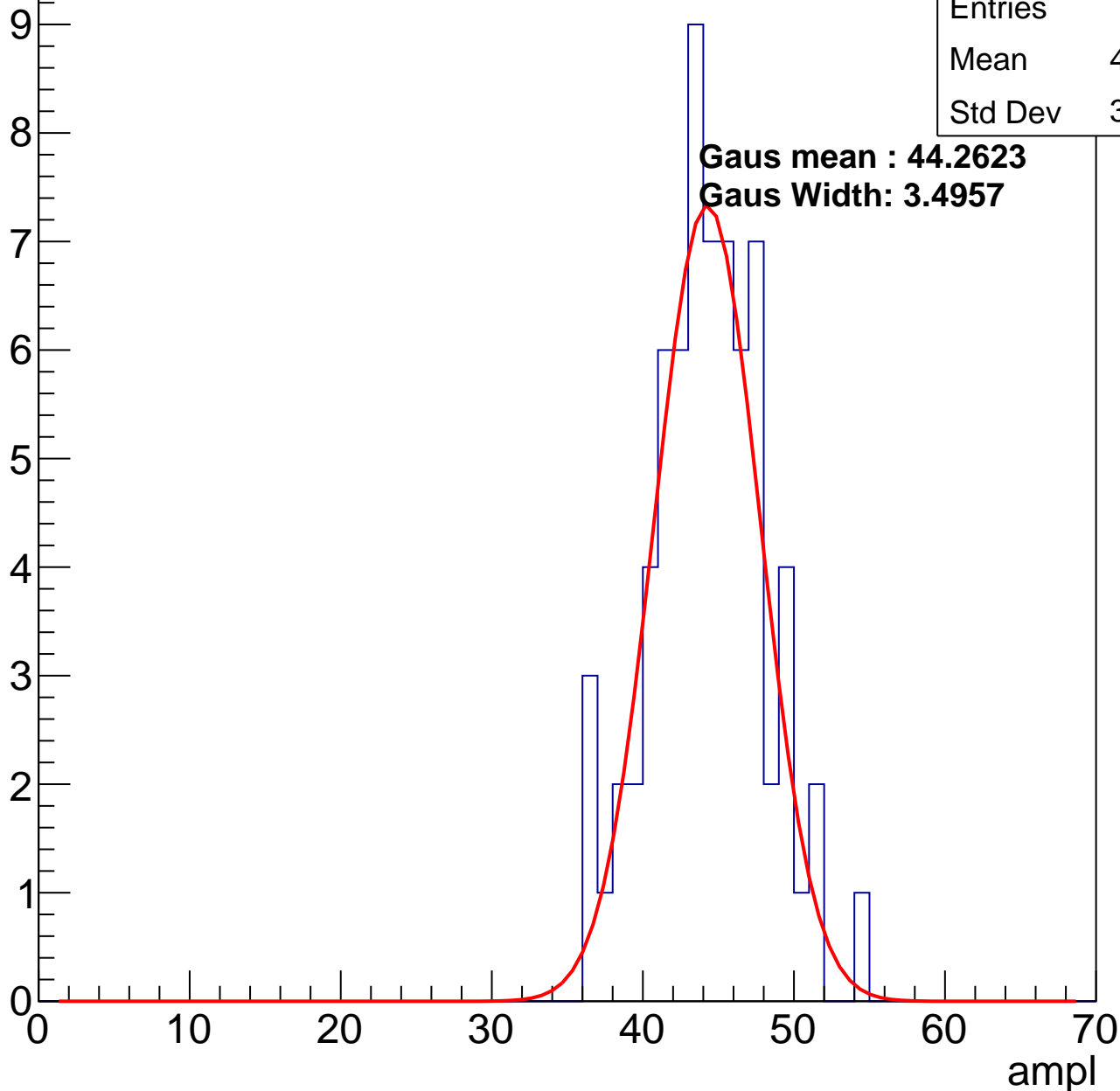
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.86
Std Dev	3.727

Gaus mean : 44.2623

Gaus Width: 3.4957

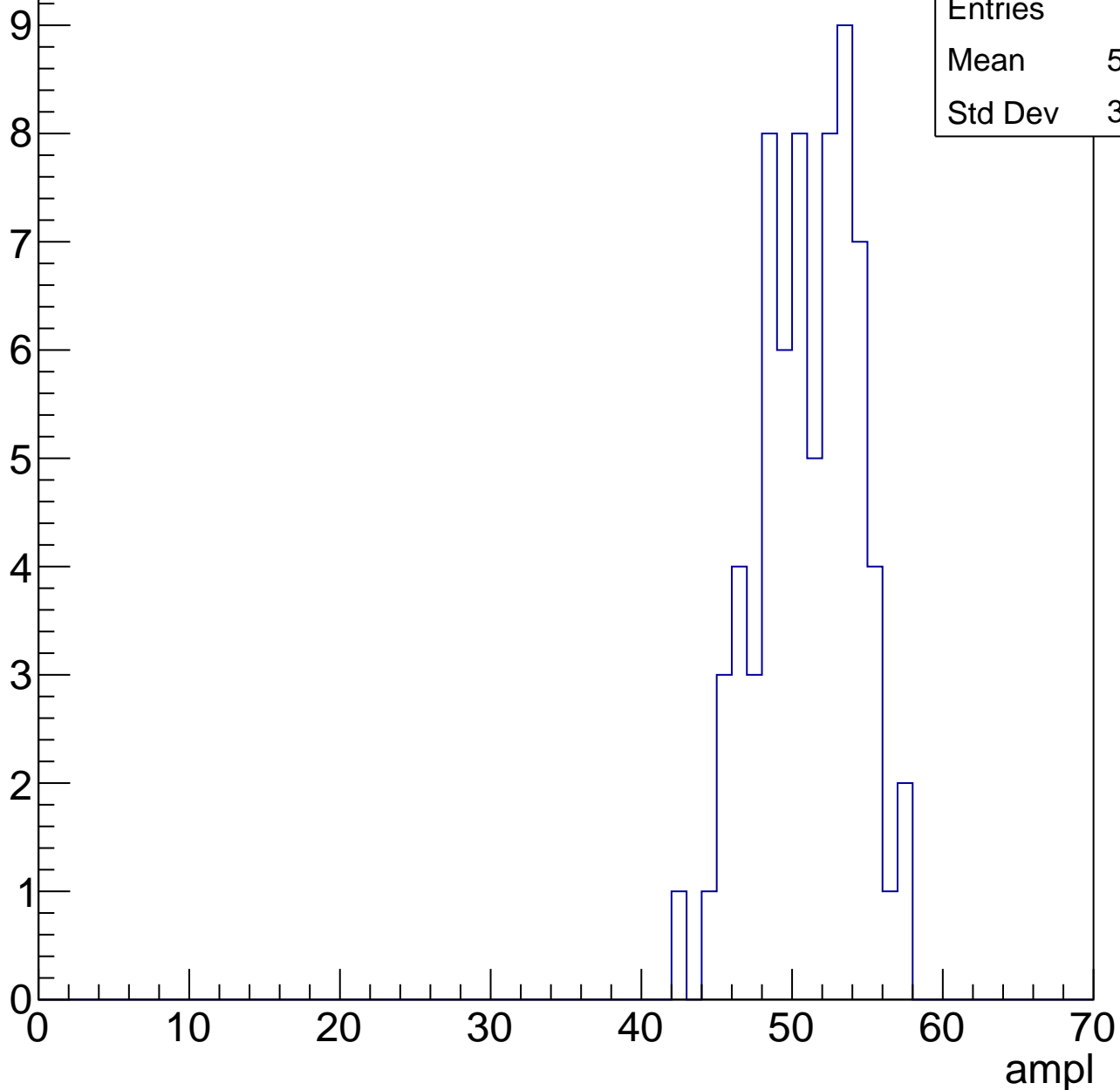


B0L001S, U23-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.57
Std Dev	3.263

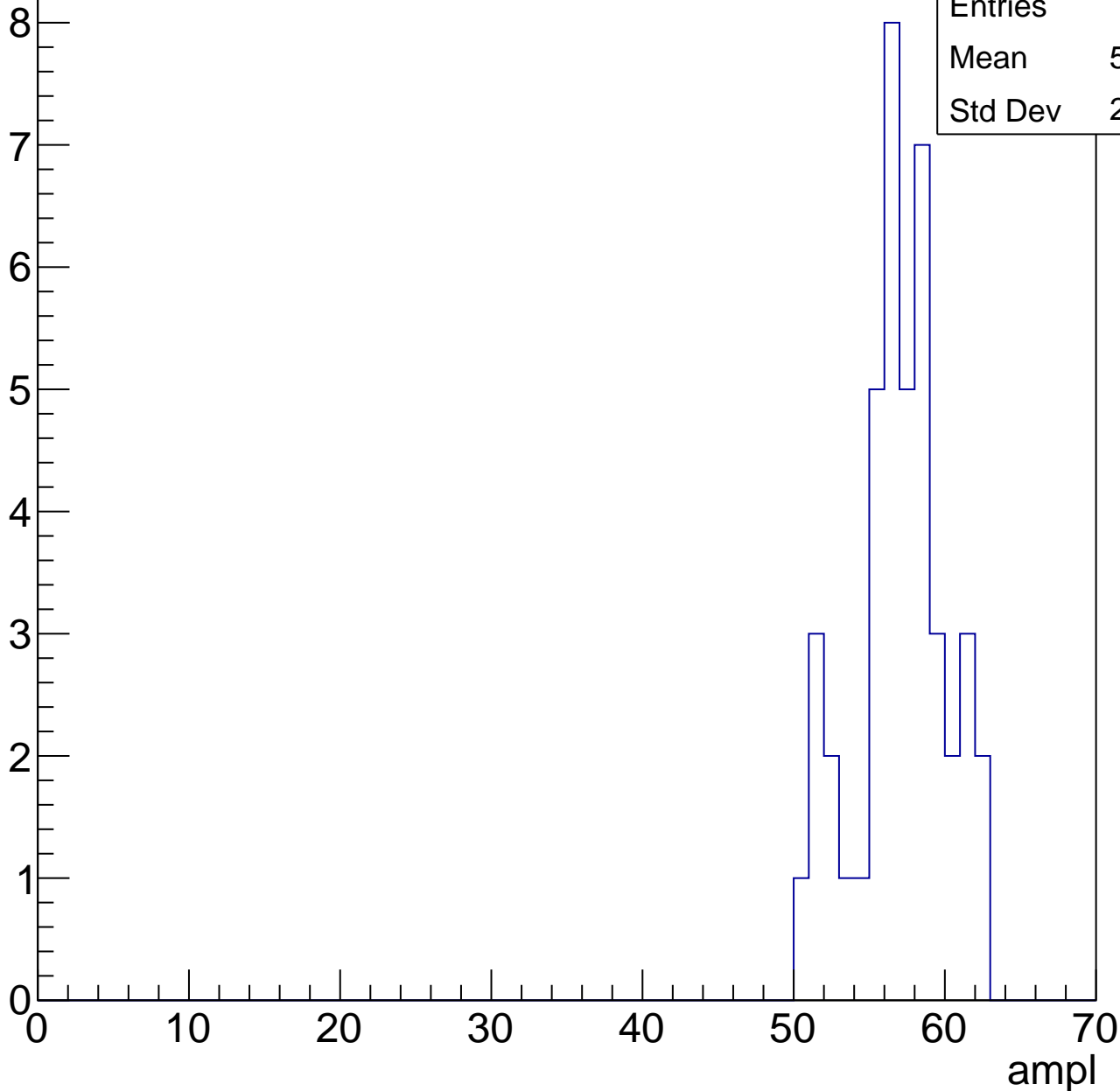


B0L001S, U23-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	56.56
Std Dev	2.998

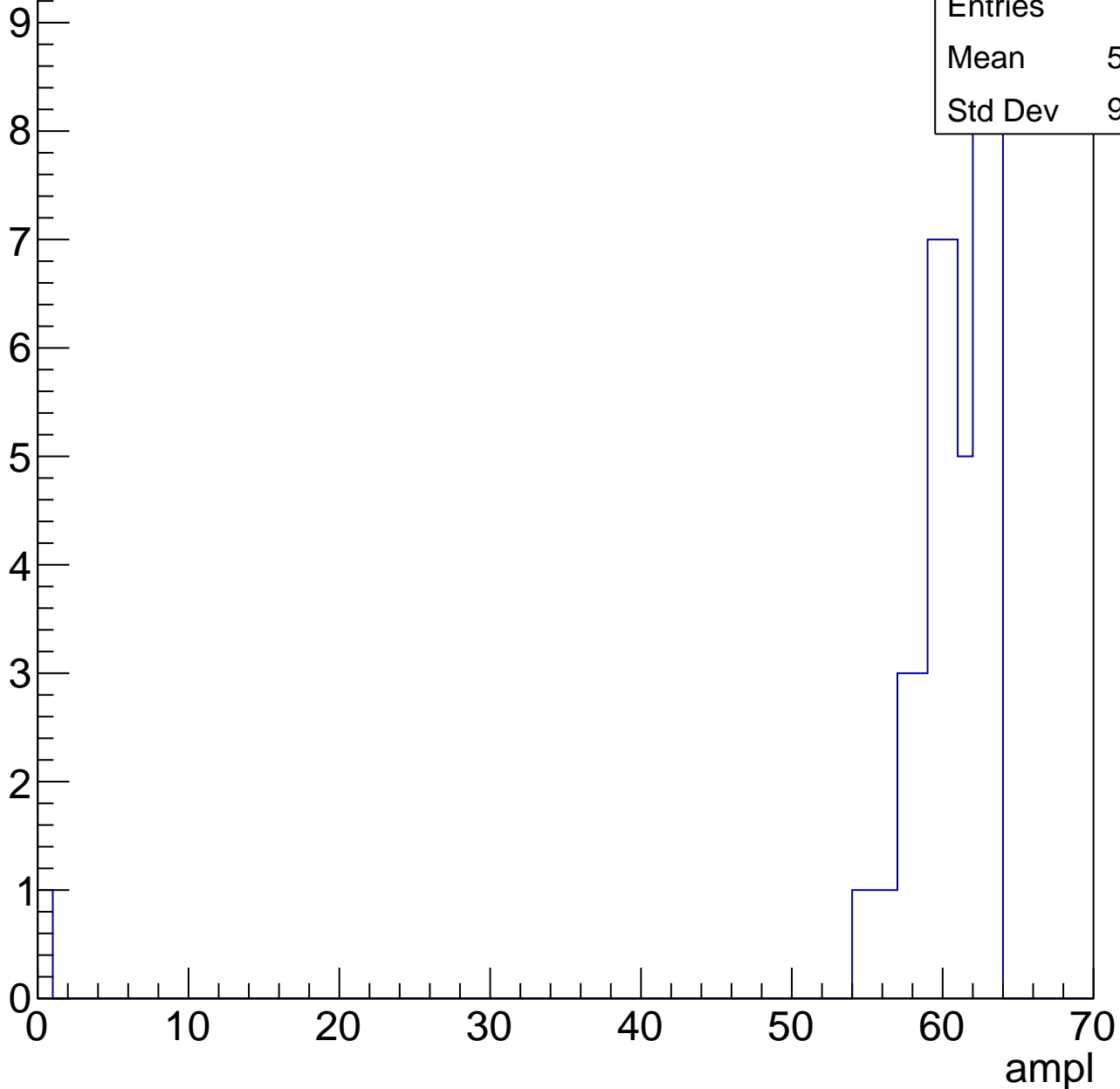


B0L001S, U23-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.93
Std Dev	9.076



B0L001S, U23-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch35, adc0

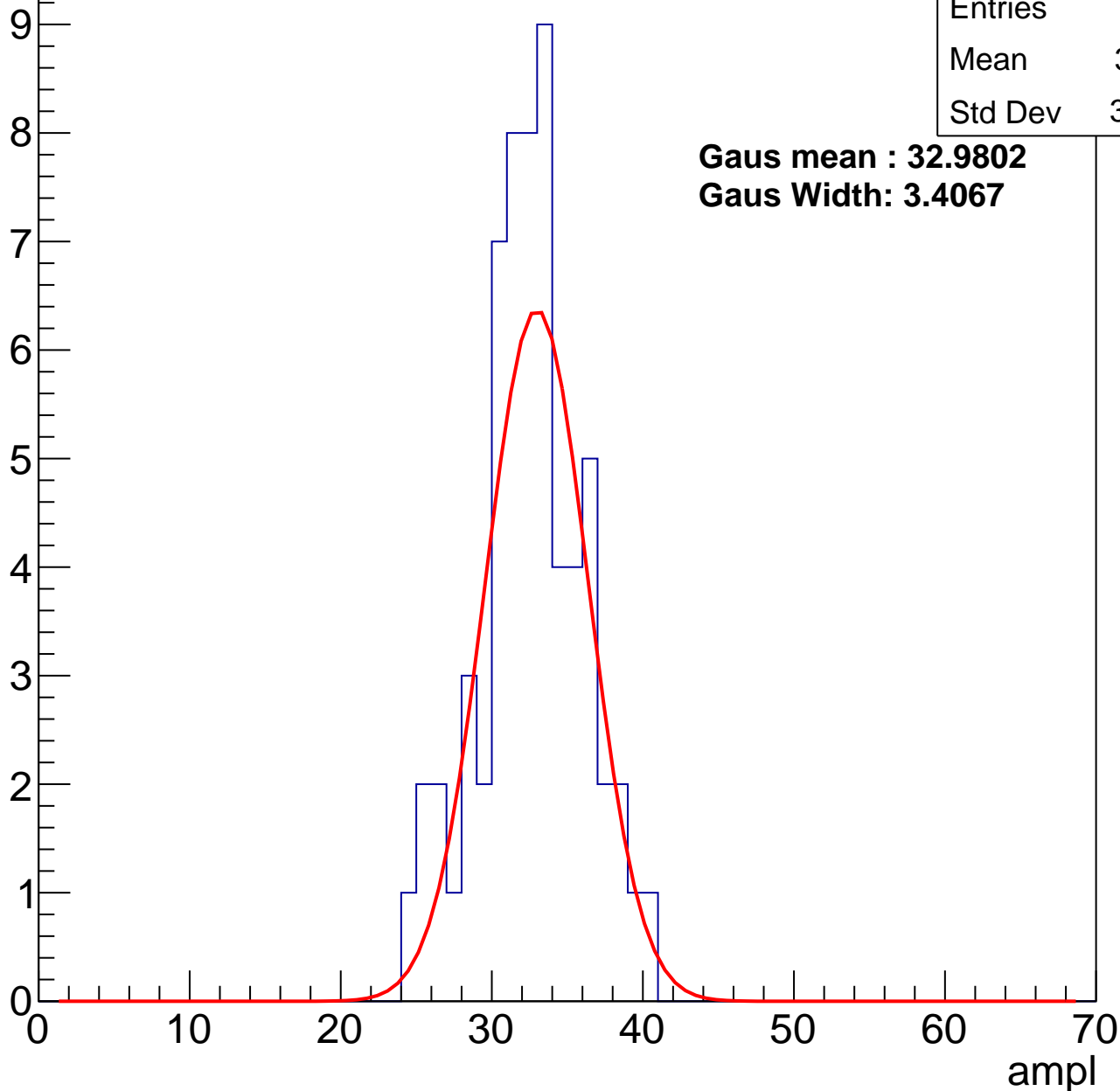
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	32.11
Std Dev	3.446

Gaus mean : 32.9802

Gaus Width: 3.4067



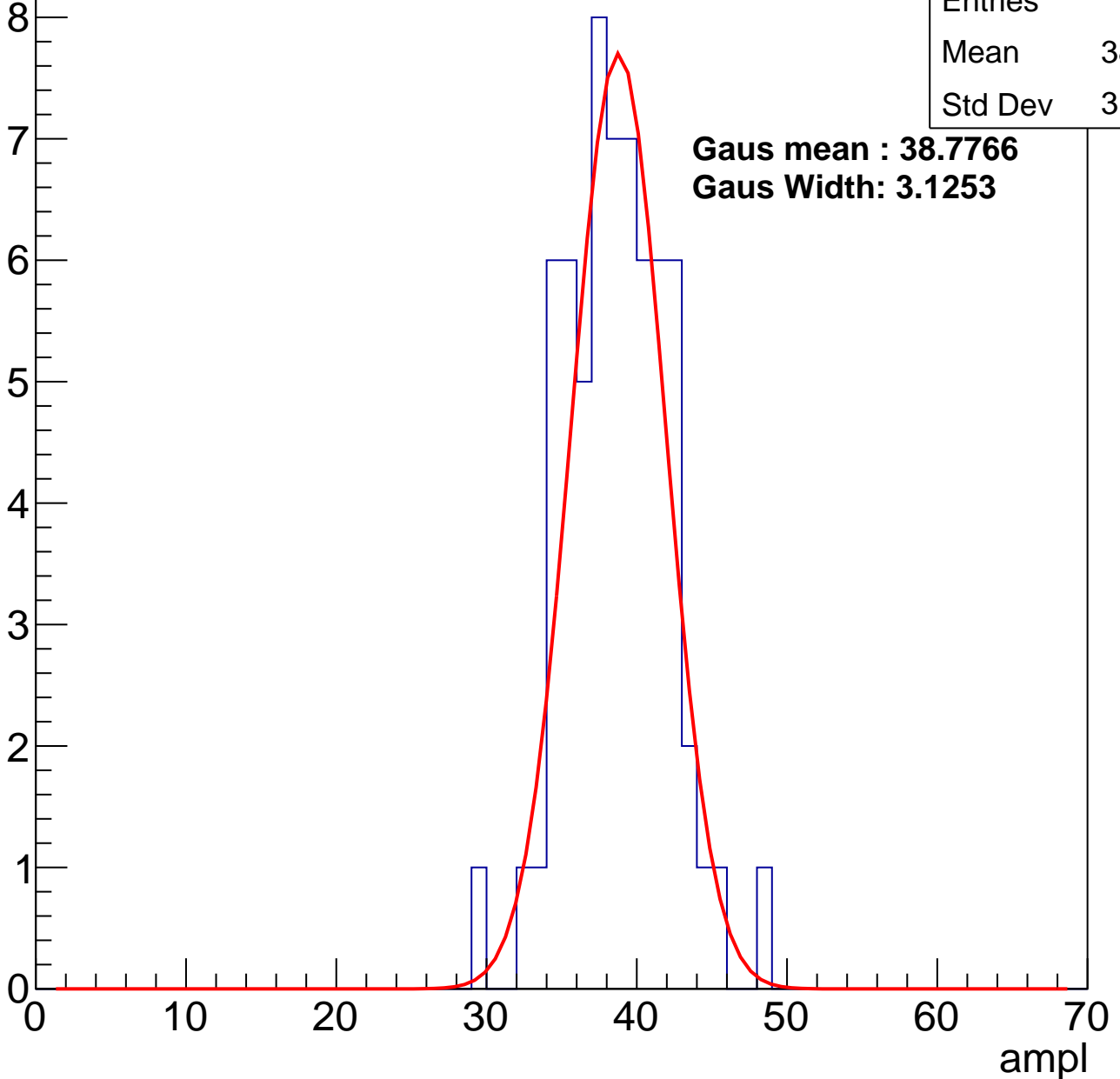
B0L001S, U23-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.22
Std Dev	3.358

Gaus mean : 38.7766
Gaus Width: 3.1253



B0L001S, U23-ch35, adc2

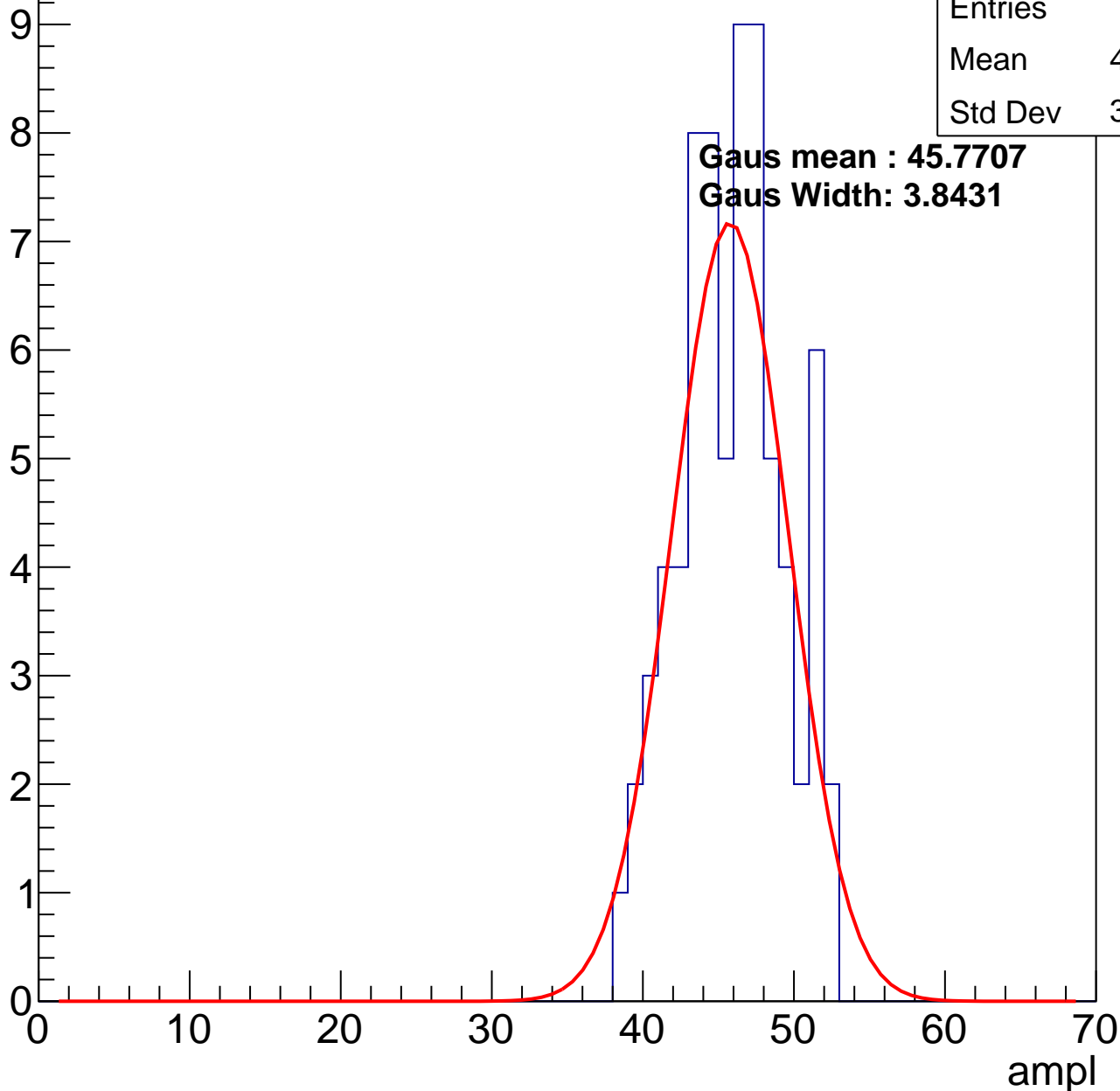
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	45.44
Std Dev	3.415

Gaus mean : 45.7707

Gaus Width: 3.8431

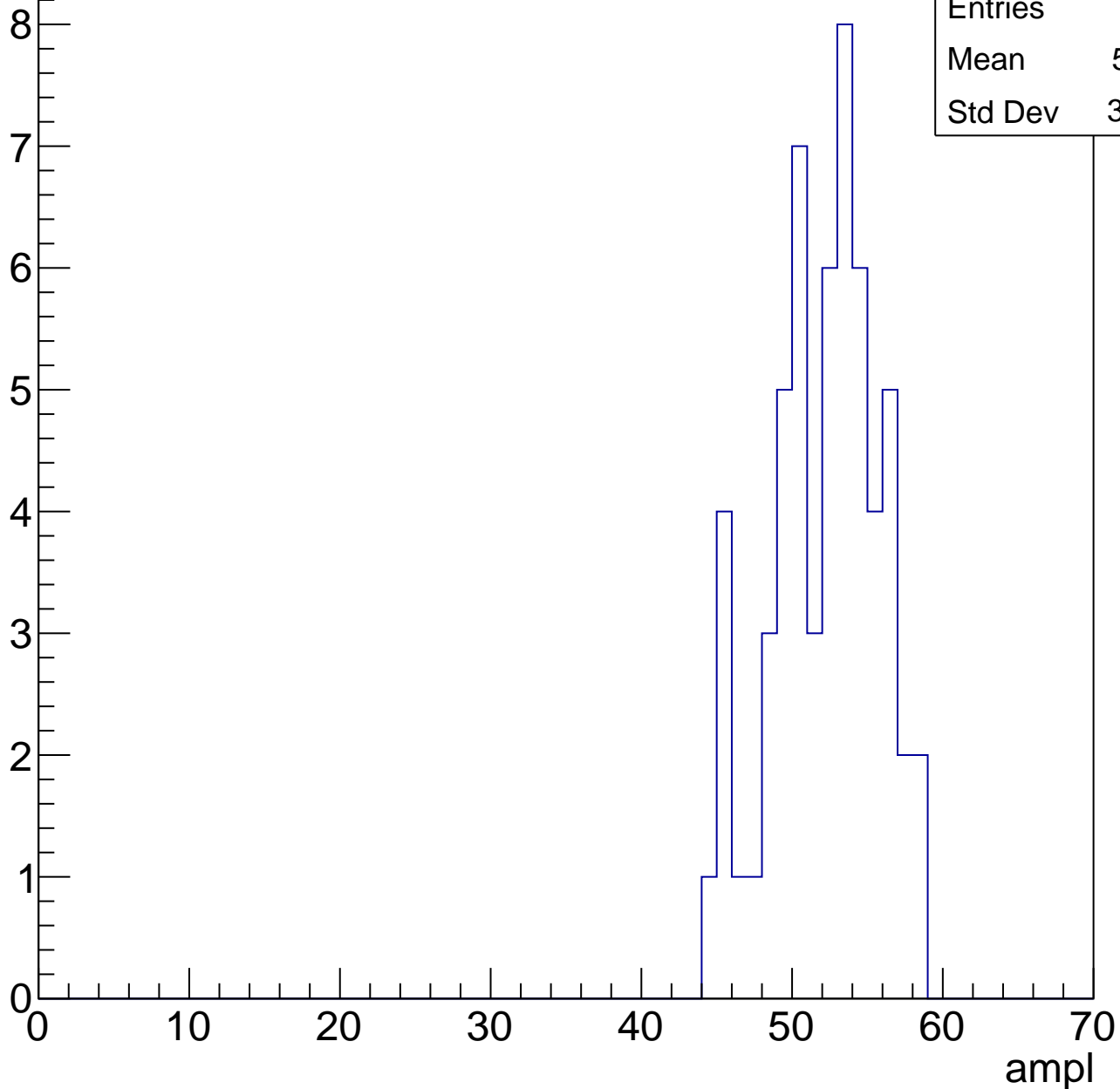


B0L001S, U23-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	51.71
Std Dev	3.499

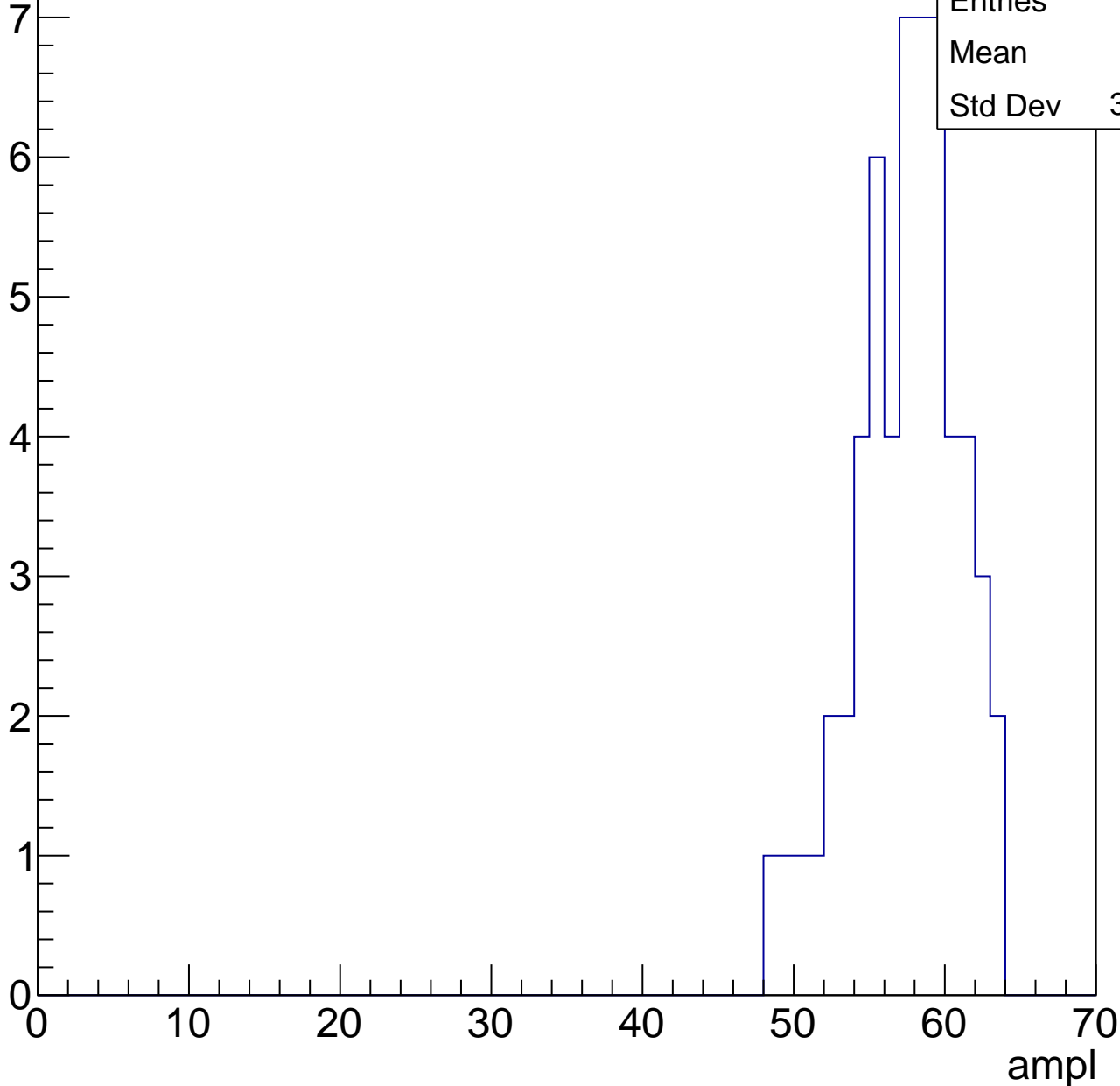


B0L001S, U23-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57
Std Dev	3.428

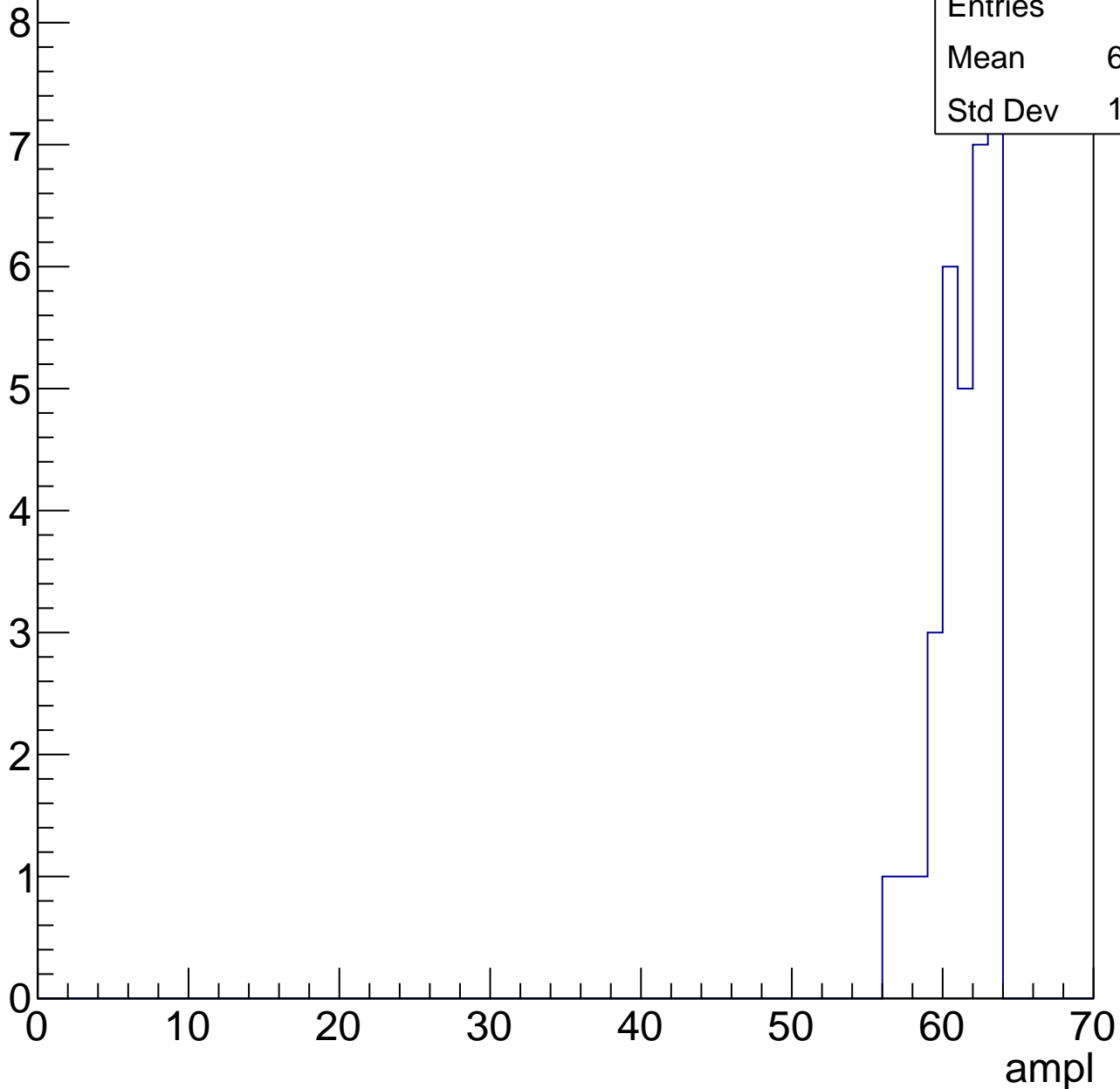


B0L001S, U23-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

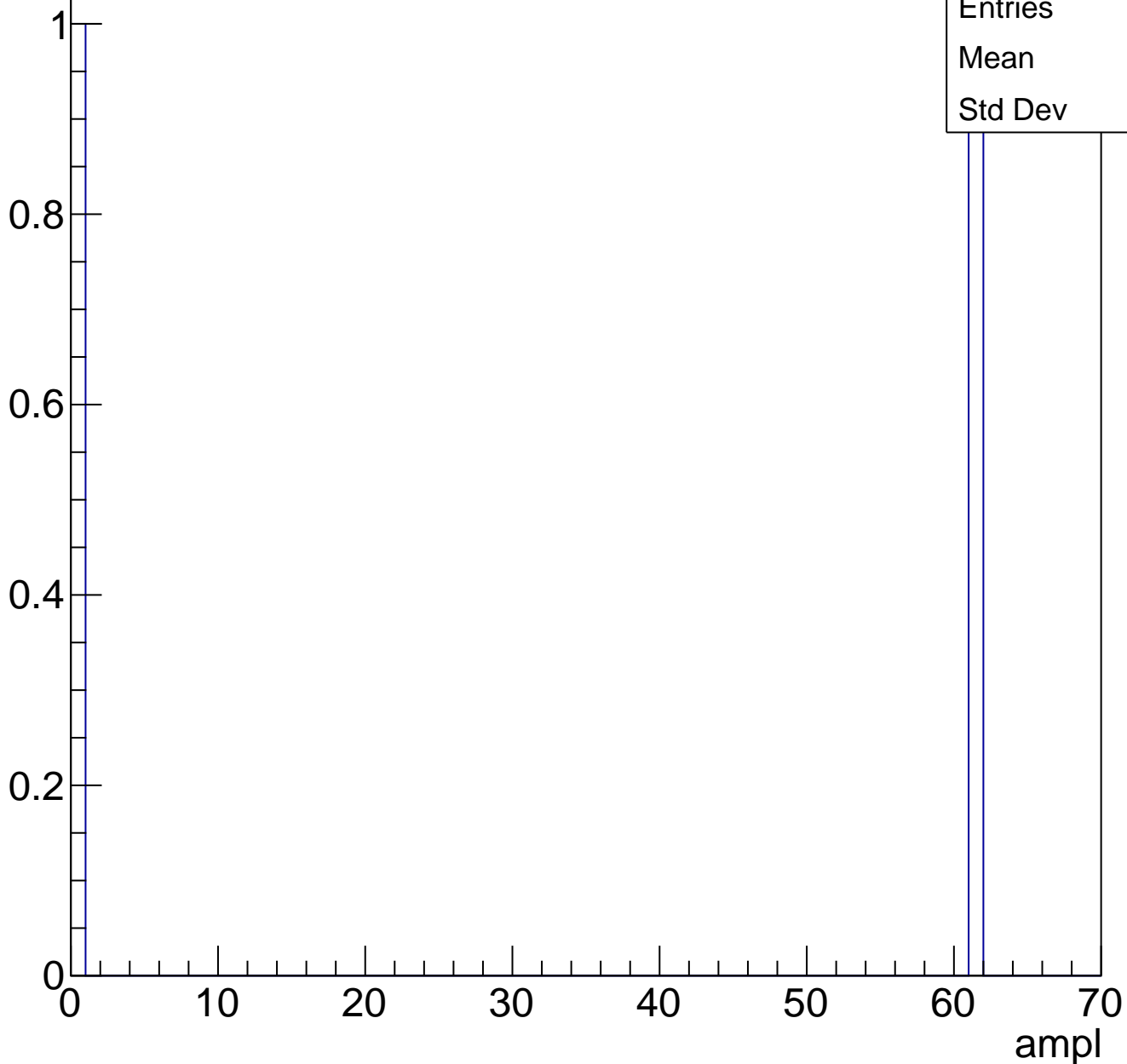
Entries	32
Mean	60.97
Std Dev	1.828



B0L001S, U23-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch36, adc0

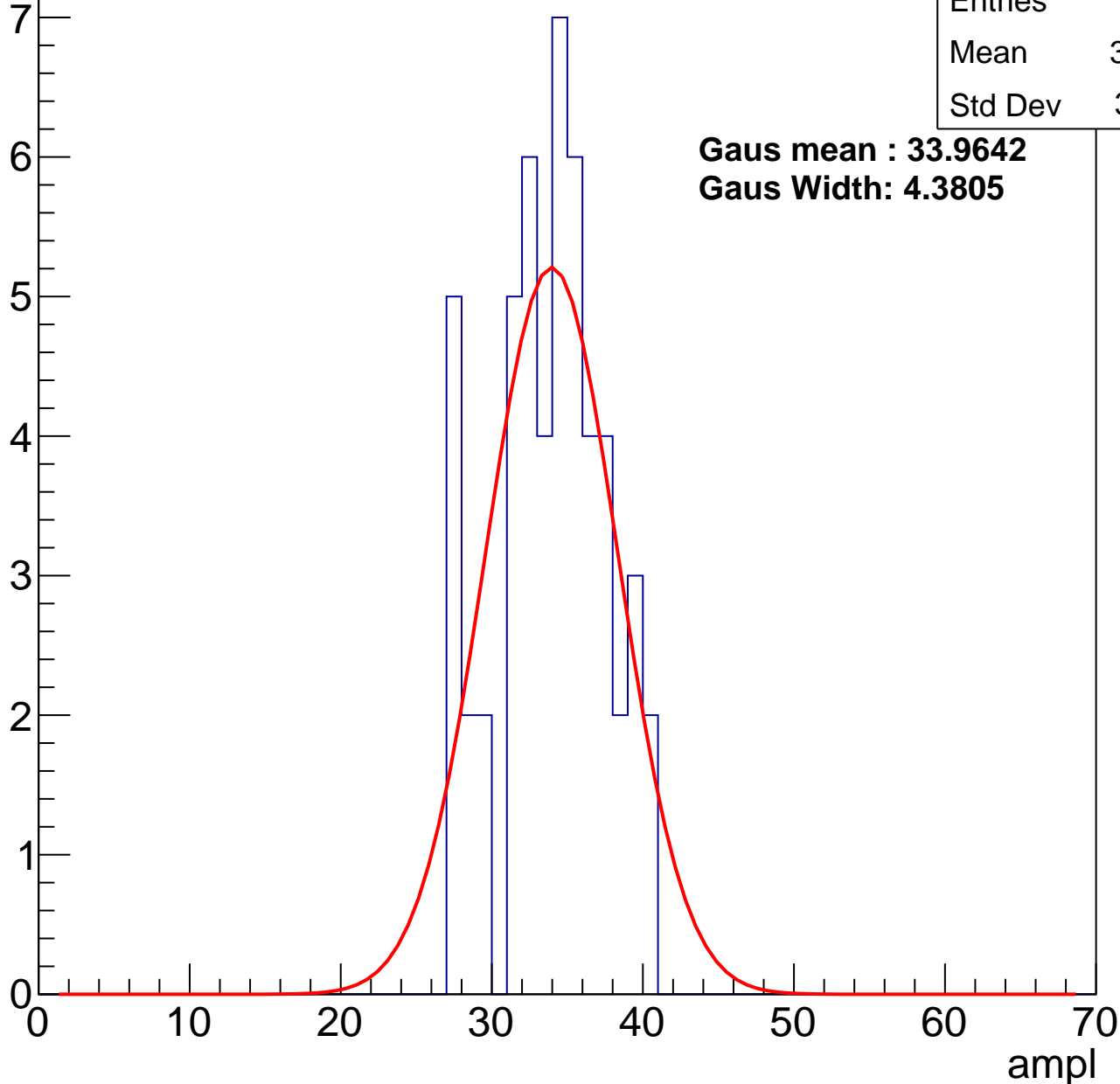
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	33.48
Std Dev	3.571

Gaus mean : 33.9642

Gaus Width: 4.3805



B0L001S, U23-ch36, adc1

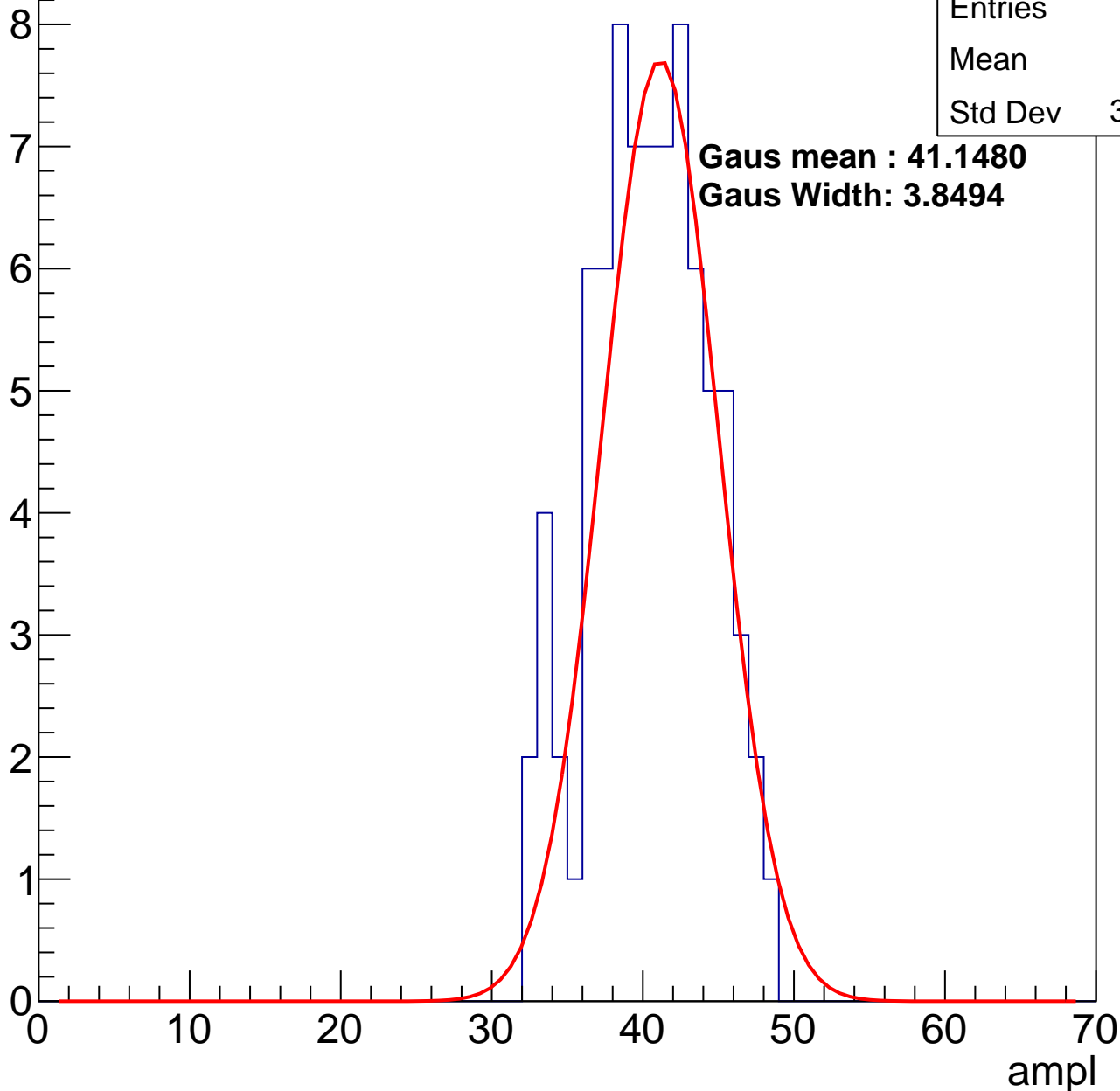
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	40
Std Dev	3.837

Gaus mean : 41.1480

Gaus Width: 3.8494



B0L001S, U23-ch36, adc2

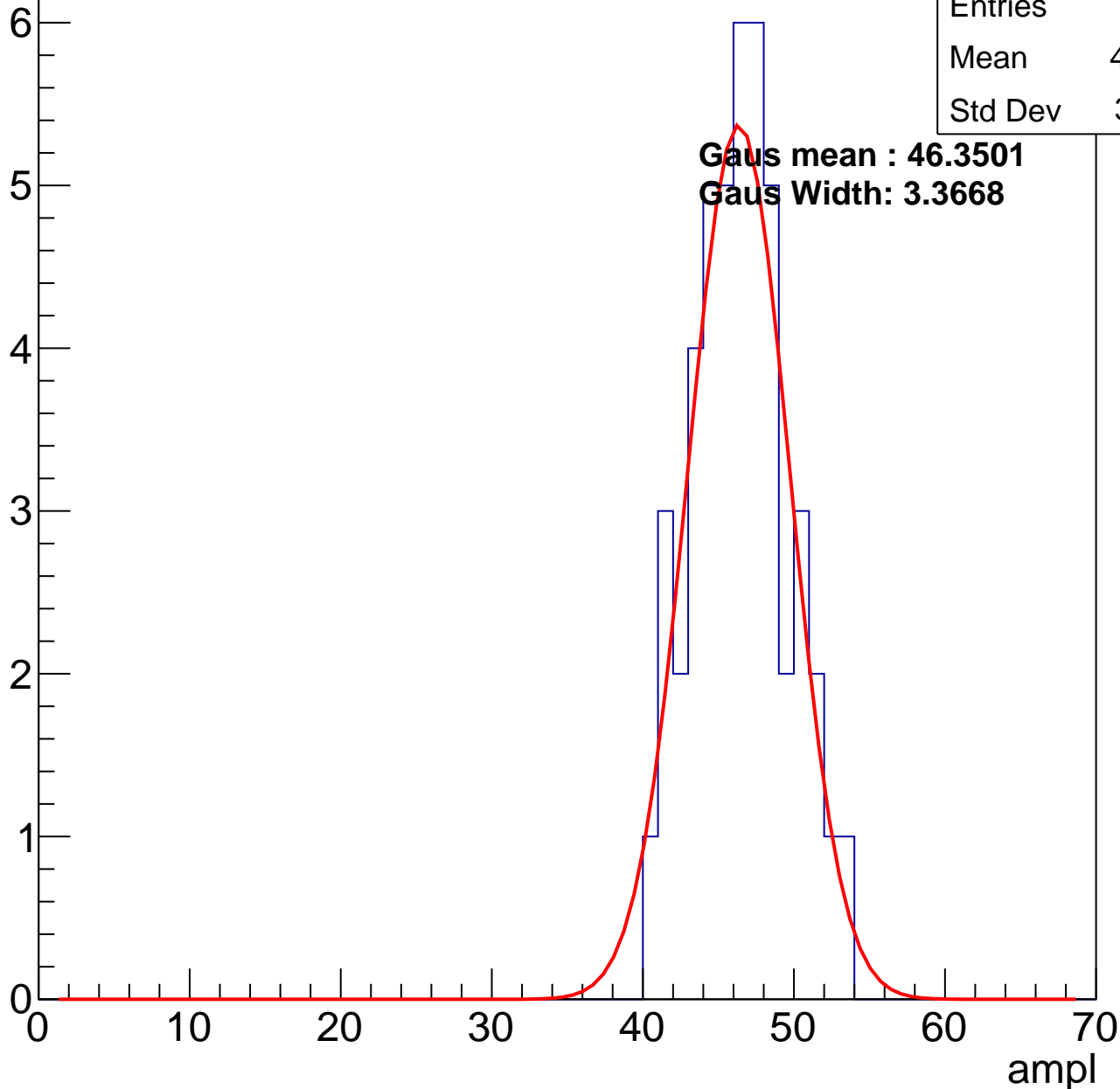
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	46.02
Std Dev	3.061

Gaus mean : 46.3501

Gaus Width: 3.3668

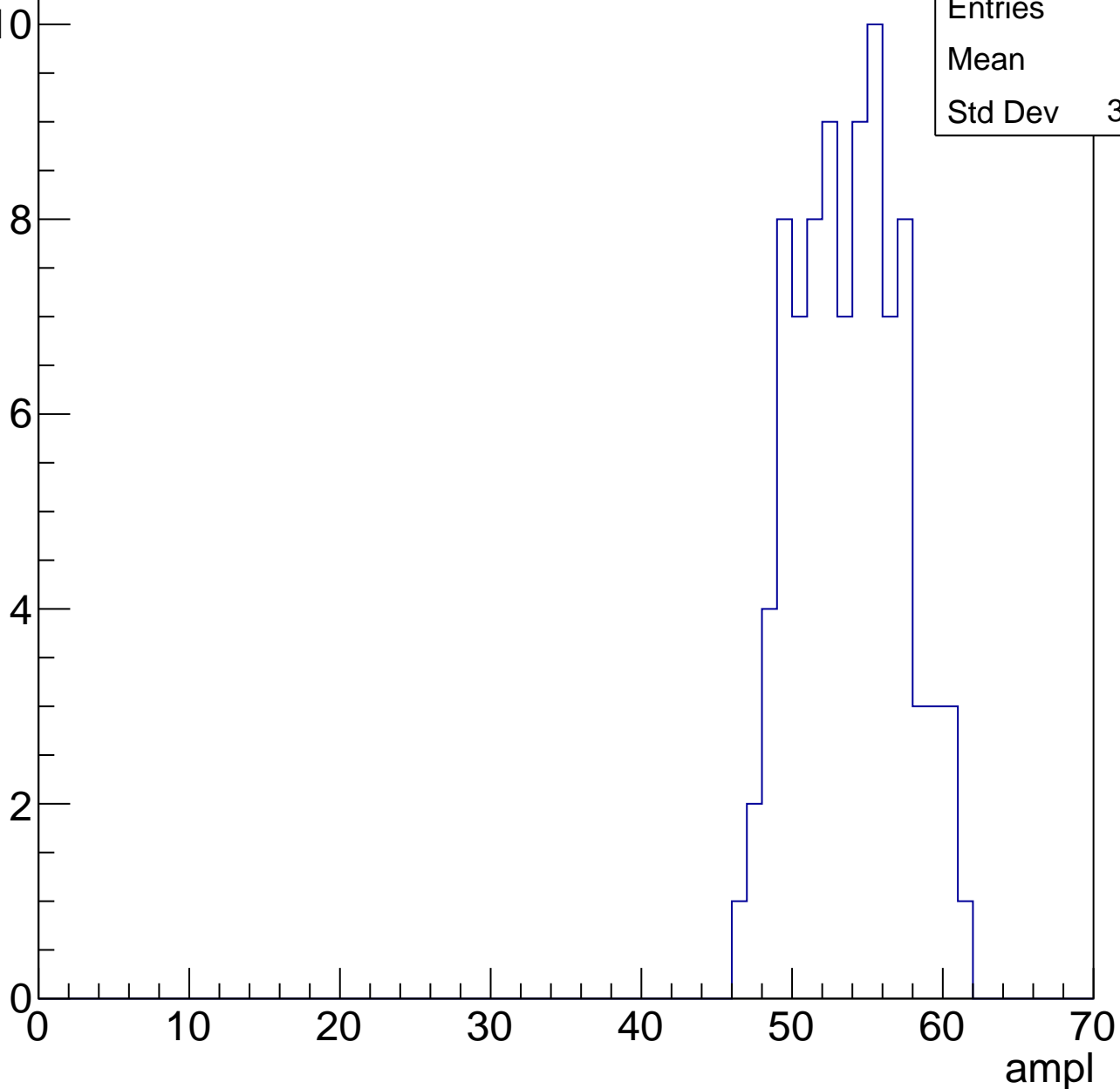


B0L001S, U23-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	90
Mean	53.3
Std Dev	3.462

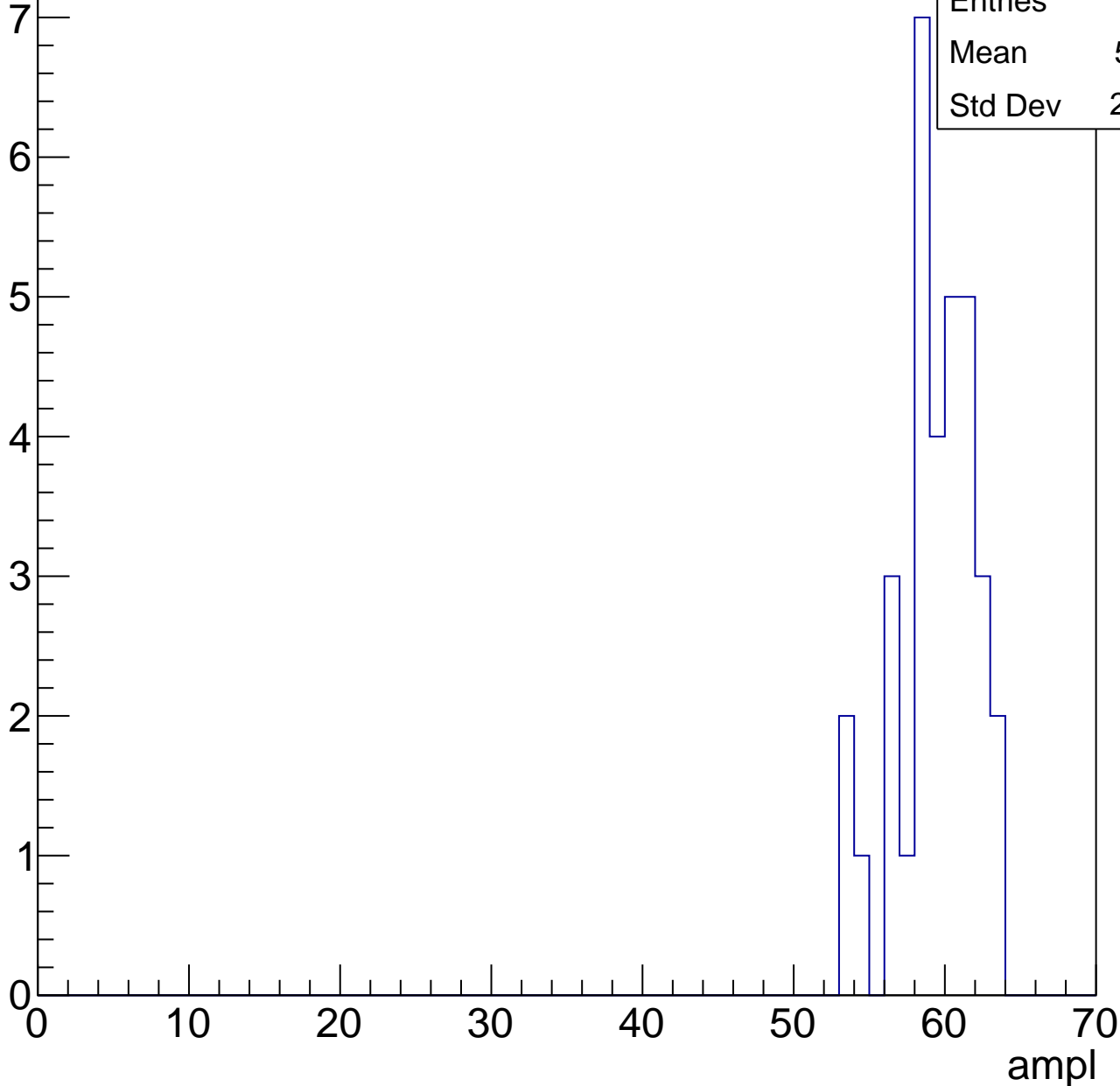


B0L001S, U23-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.91
Std Dev	2.575

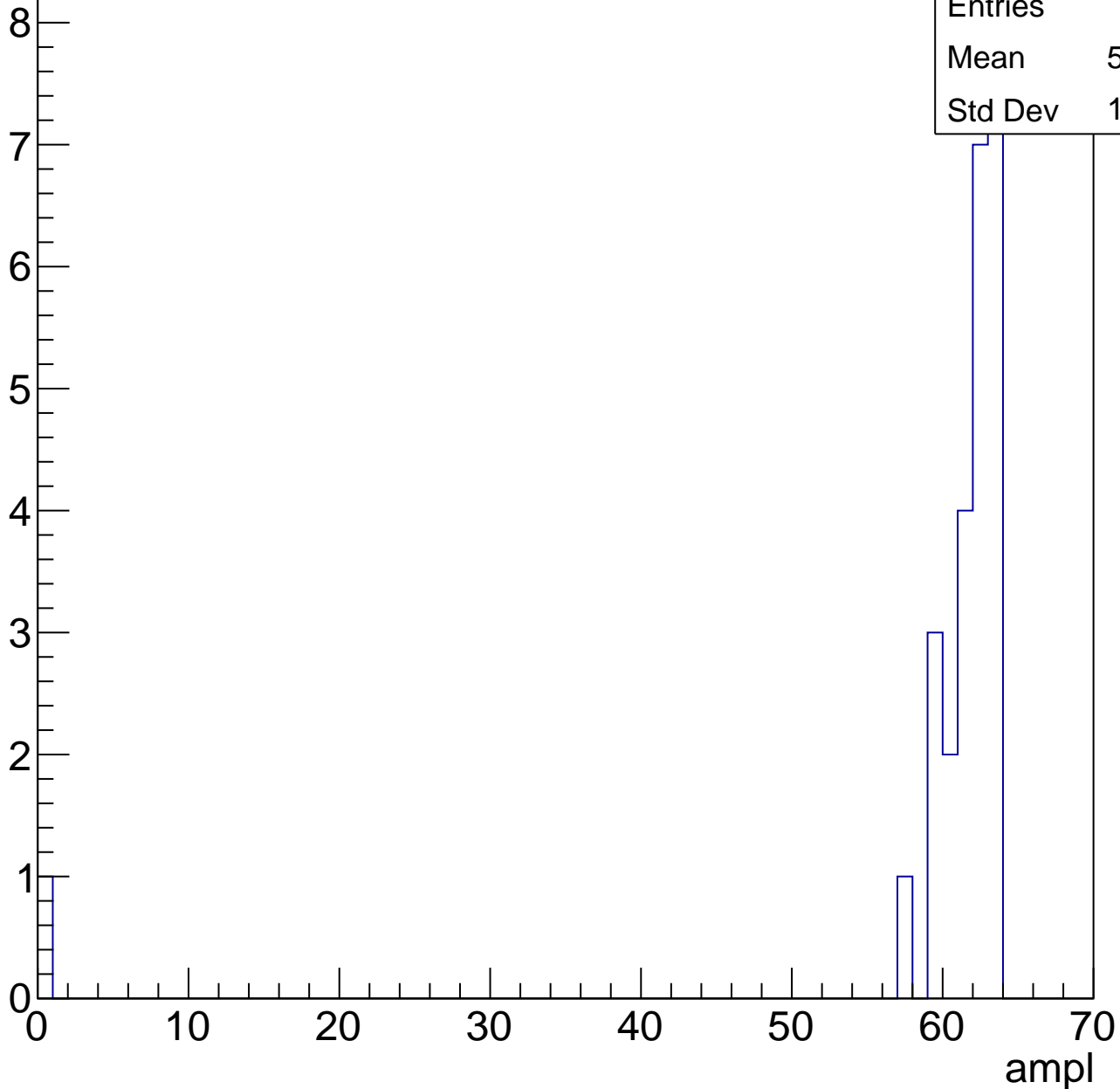


B0L001S, U23-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	59.08
Std Dev	11.92



B0L001S, U23-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch37, adc0

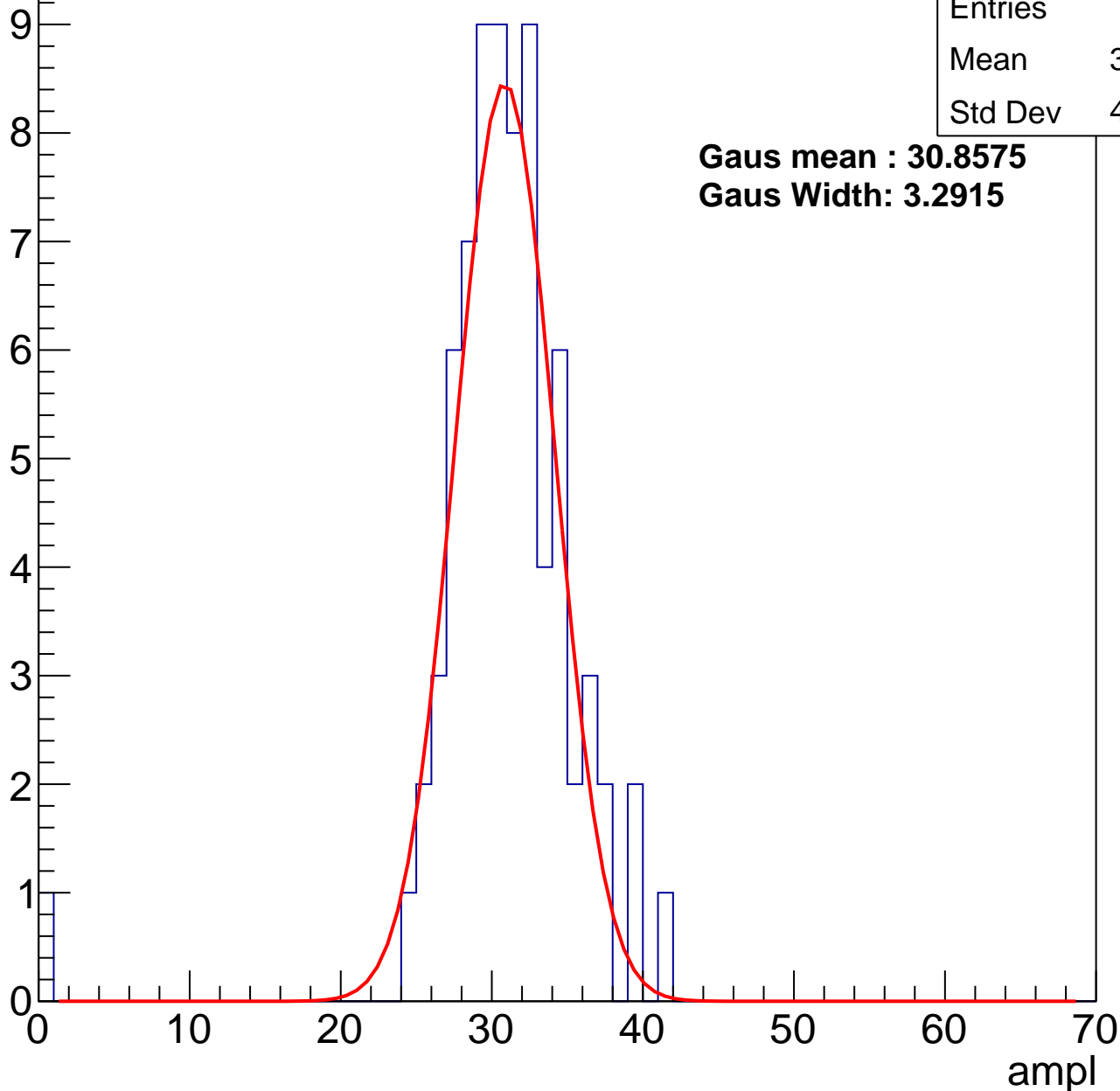
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	30.45
Std Dev	4.932

Gaus mean : 30.8575

Gaus Width: 3.2915



B0L001S, U23-ch37, adc1

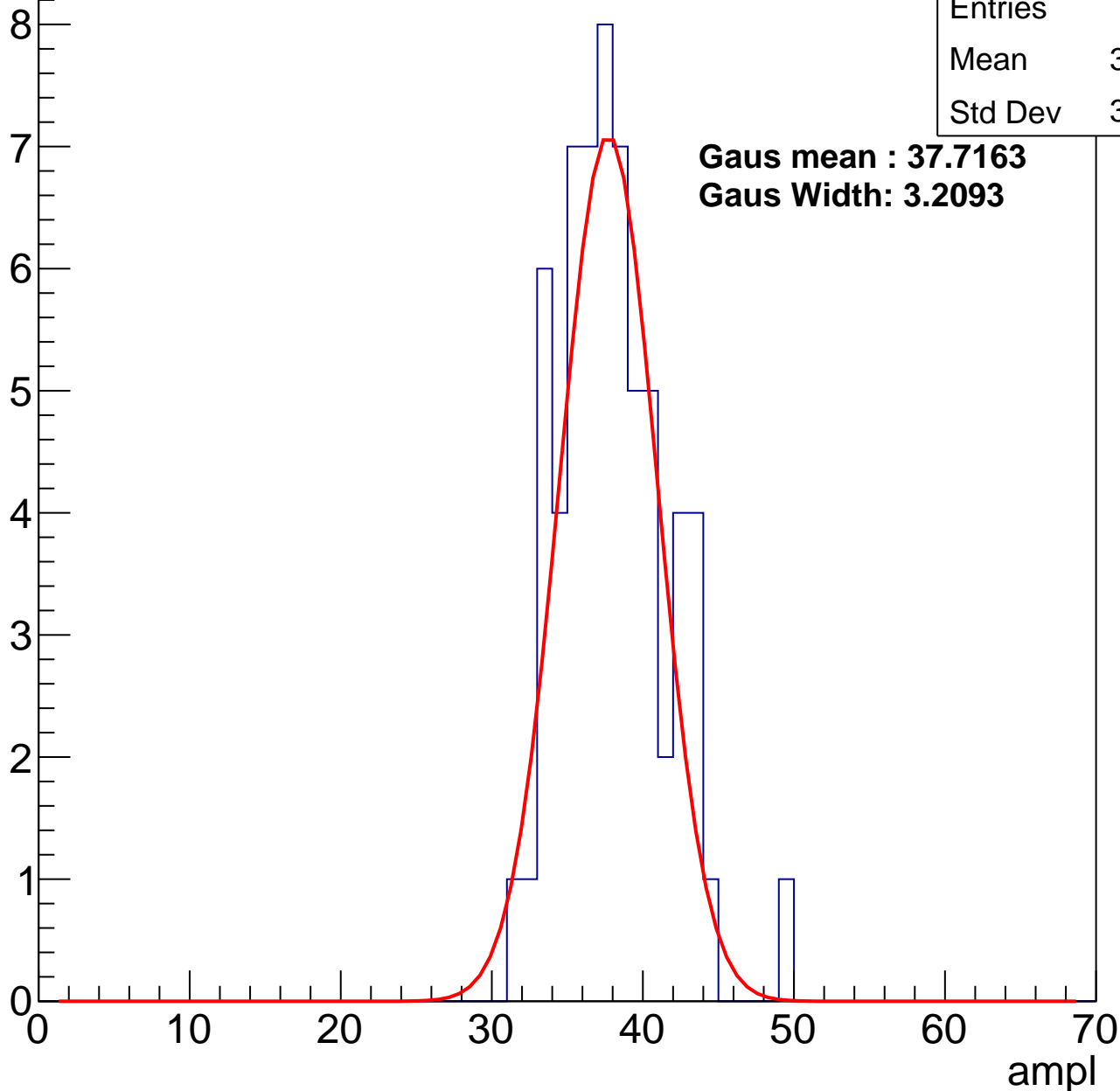
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.56
Std Dev	3.449

Gaus mean : 37.7163

Gaus Width: 3.2093



B0L001S, U23-ch37, adc2

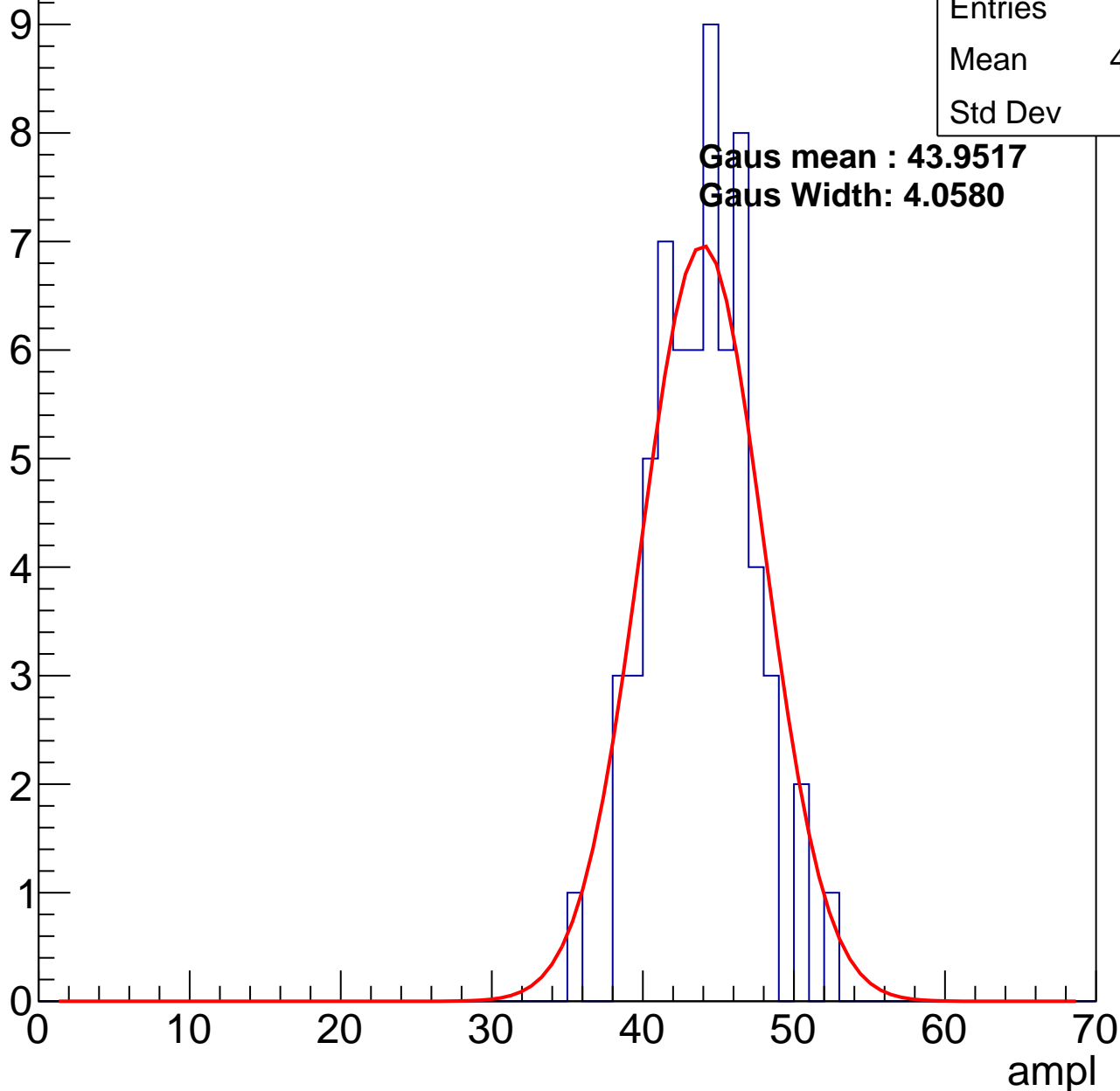
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.45
Std Dev	3.25

Gaus mean : 43.9517

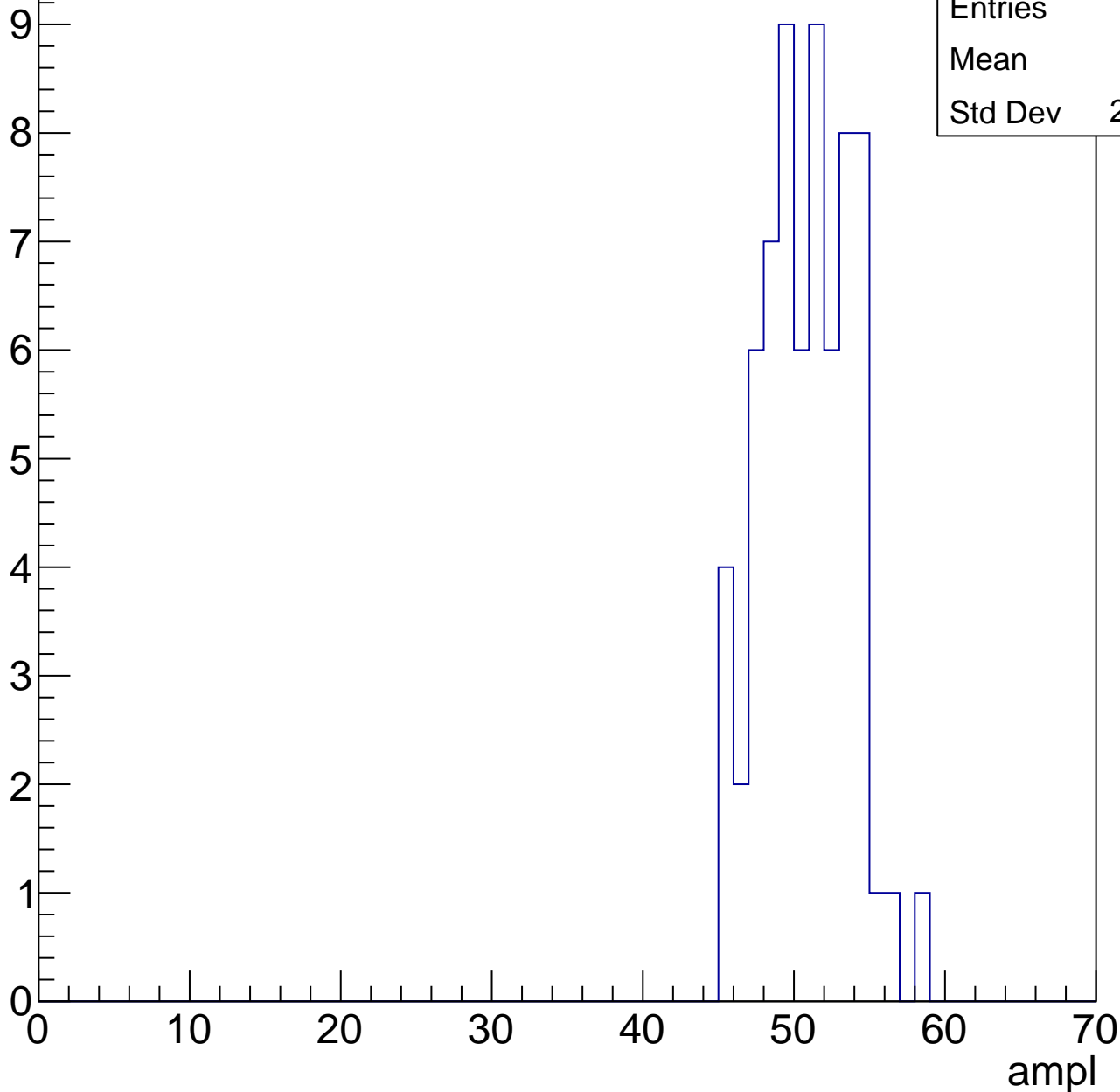
Gaus Width: 4.0580



B0L001S, U23-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



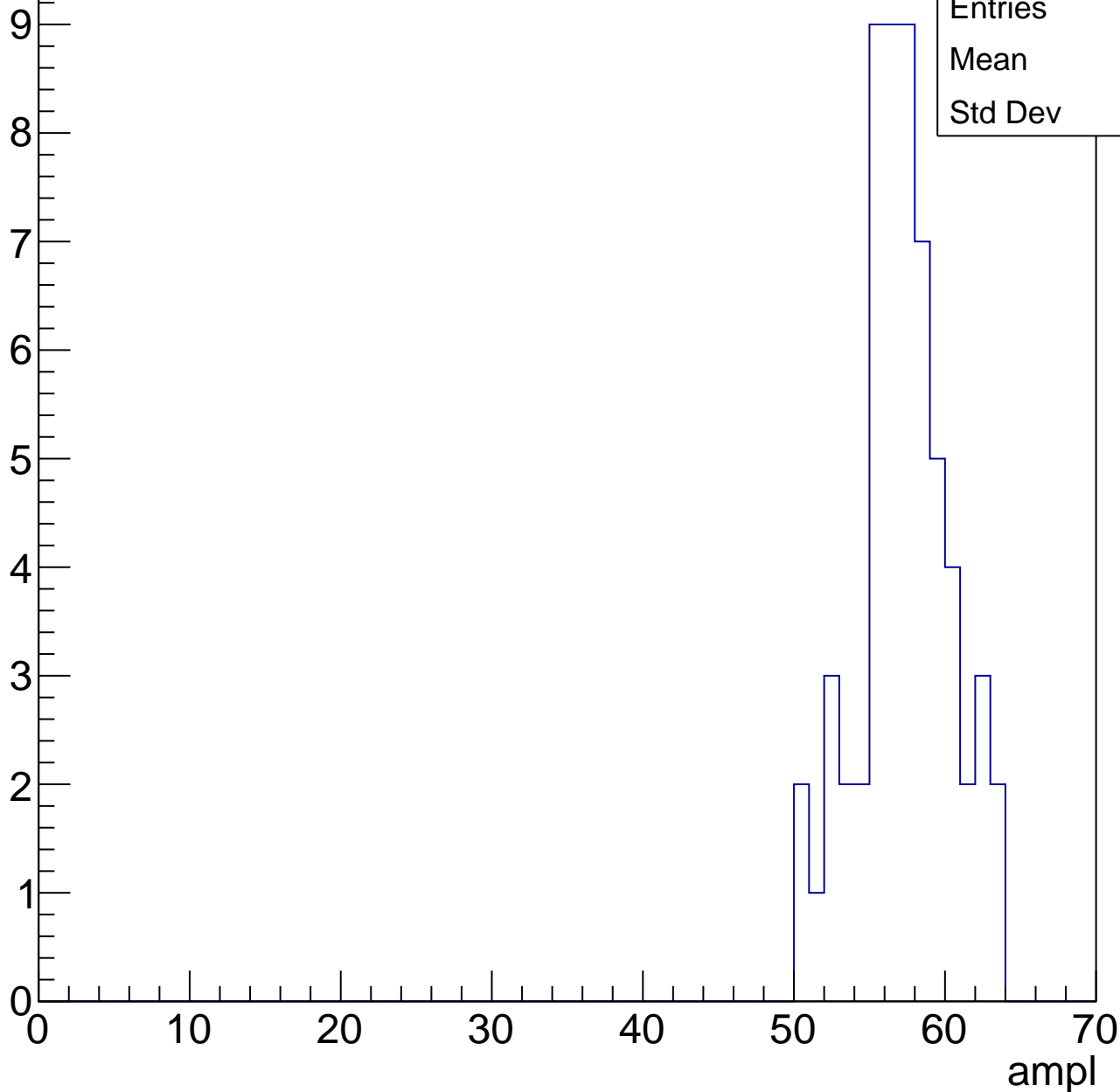
Entries	68
Mean	50.4
Std Dev	2.896

B0L001S, U23-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.8
Std Dev	3.01



B0L001S, U23-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

Entries	33
Mean	58.91
Std Dev	10.54

10

20

30

40

50

60

70

ampl

B0L001S, U23-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U23-ch38, adc0

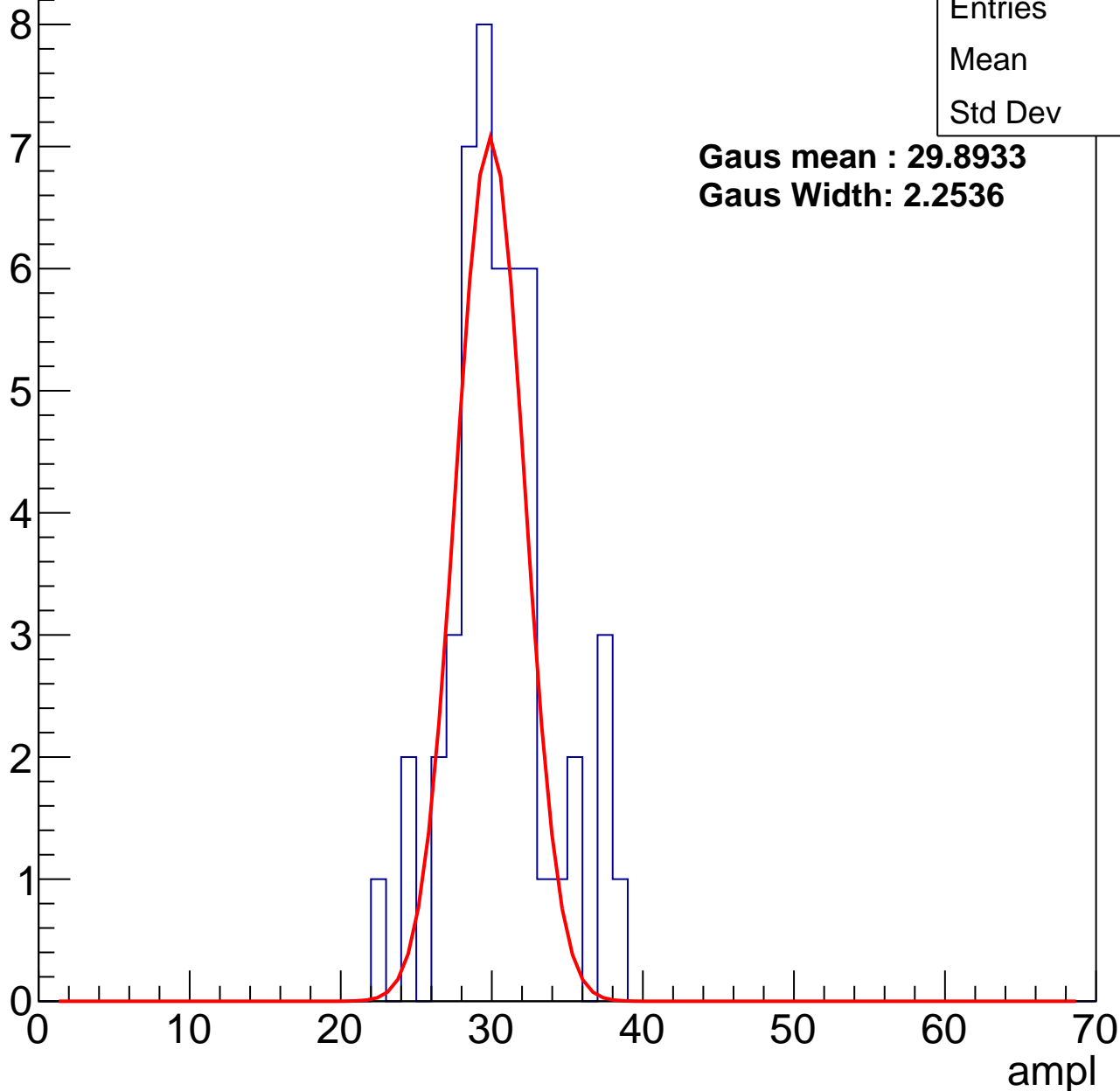
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	30.1
Std Dev	3.34

Gaus mean : 29.8933

Gaus Width: 2.2536



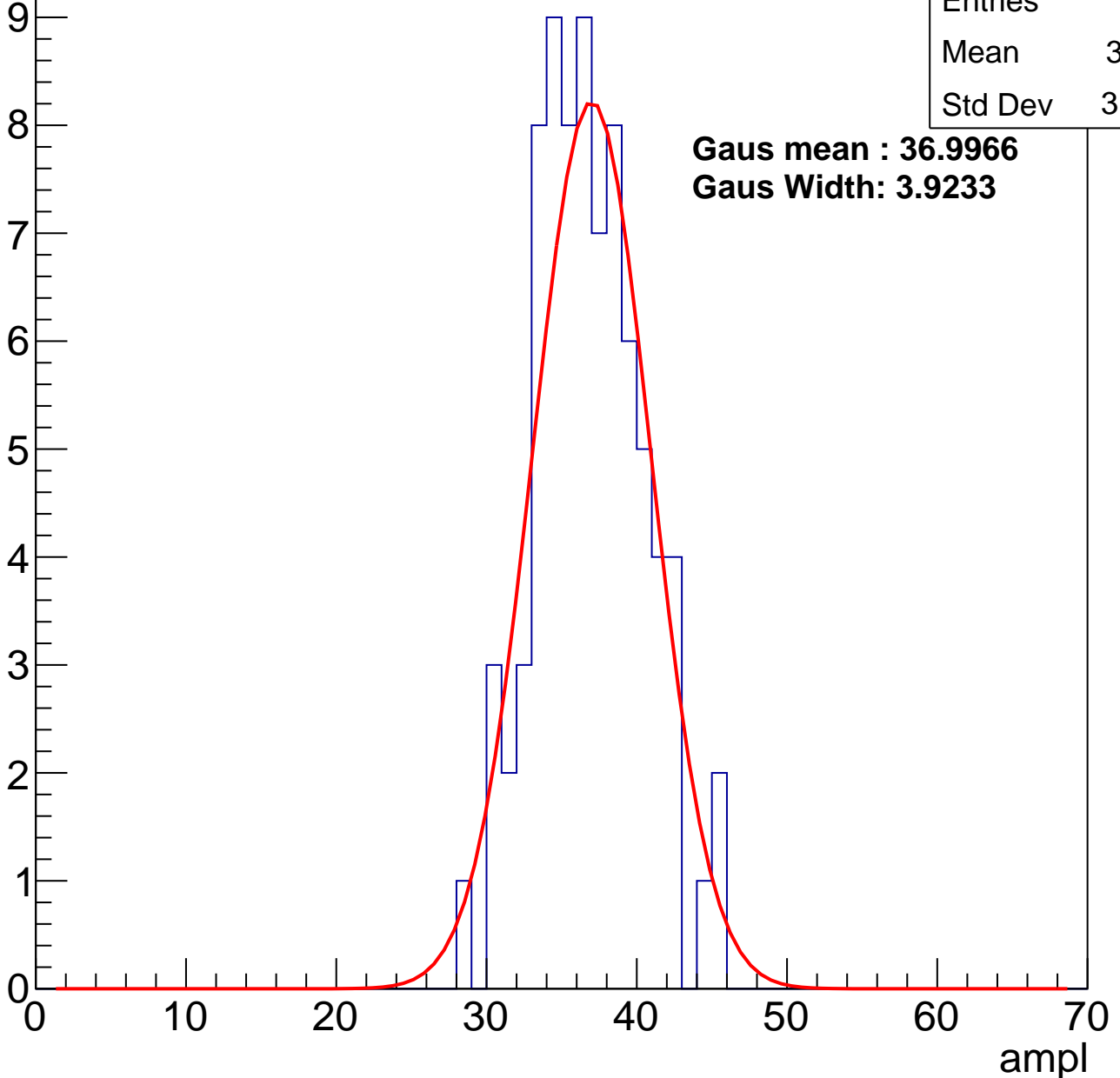
B0L001S, U23-ch38, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	36.41
Std Dev	3.563

Gaus mean : 36.9966
Gaus Width: 3.9233



B0L001S, U23-ch38, adc2

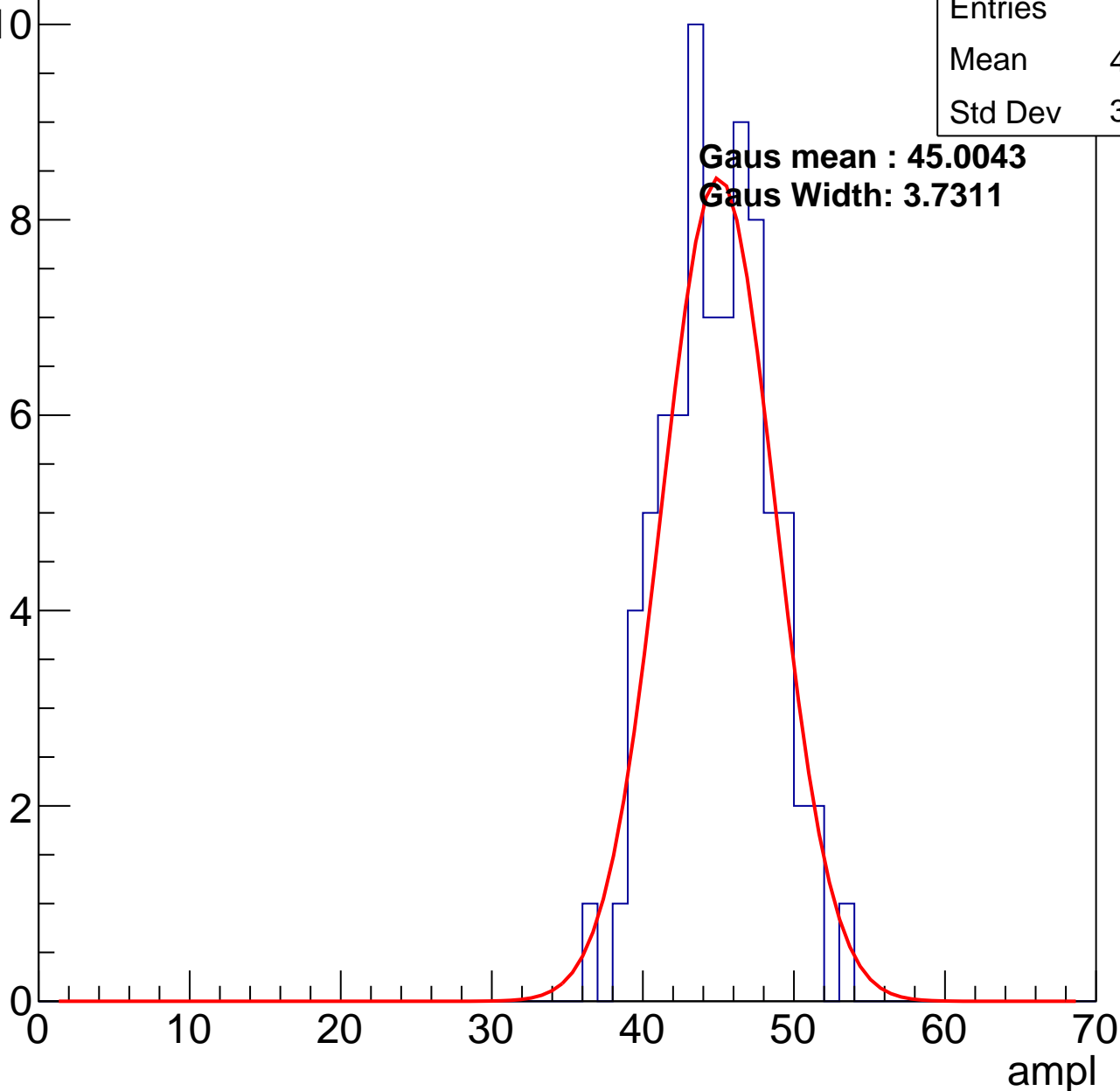
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	44.44
Std Dev	3.419

Gaus mean : 45.0043

Gaus Width: 3.7311

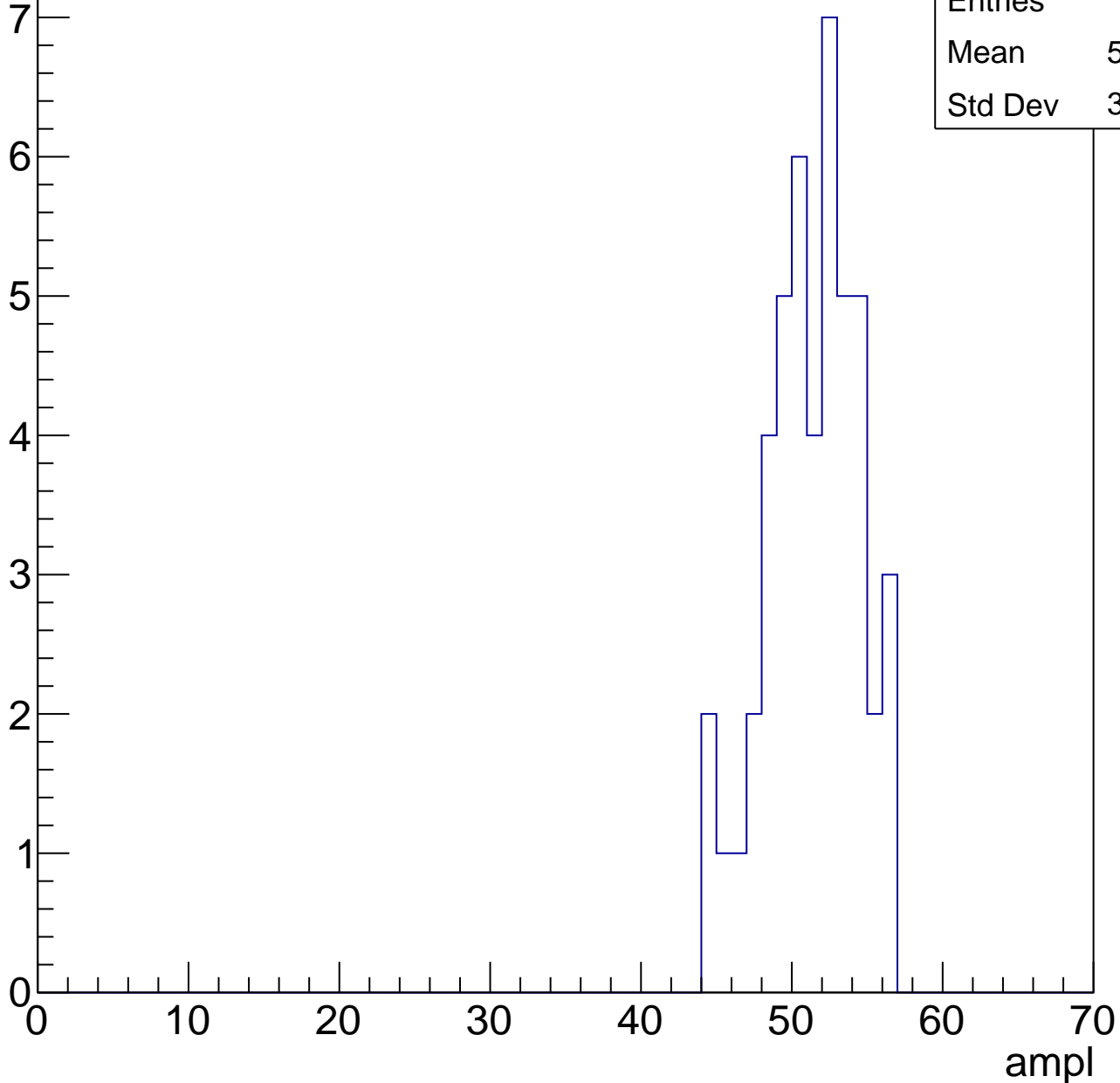


B0L001S, U23-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	50.87
Std Dev	3.029

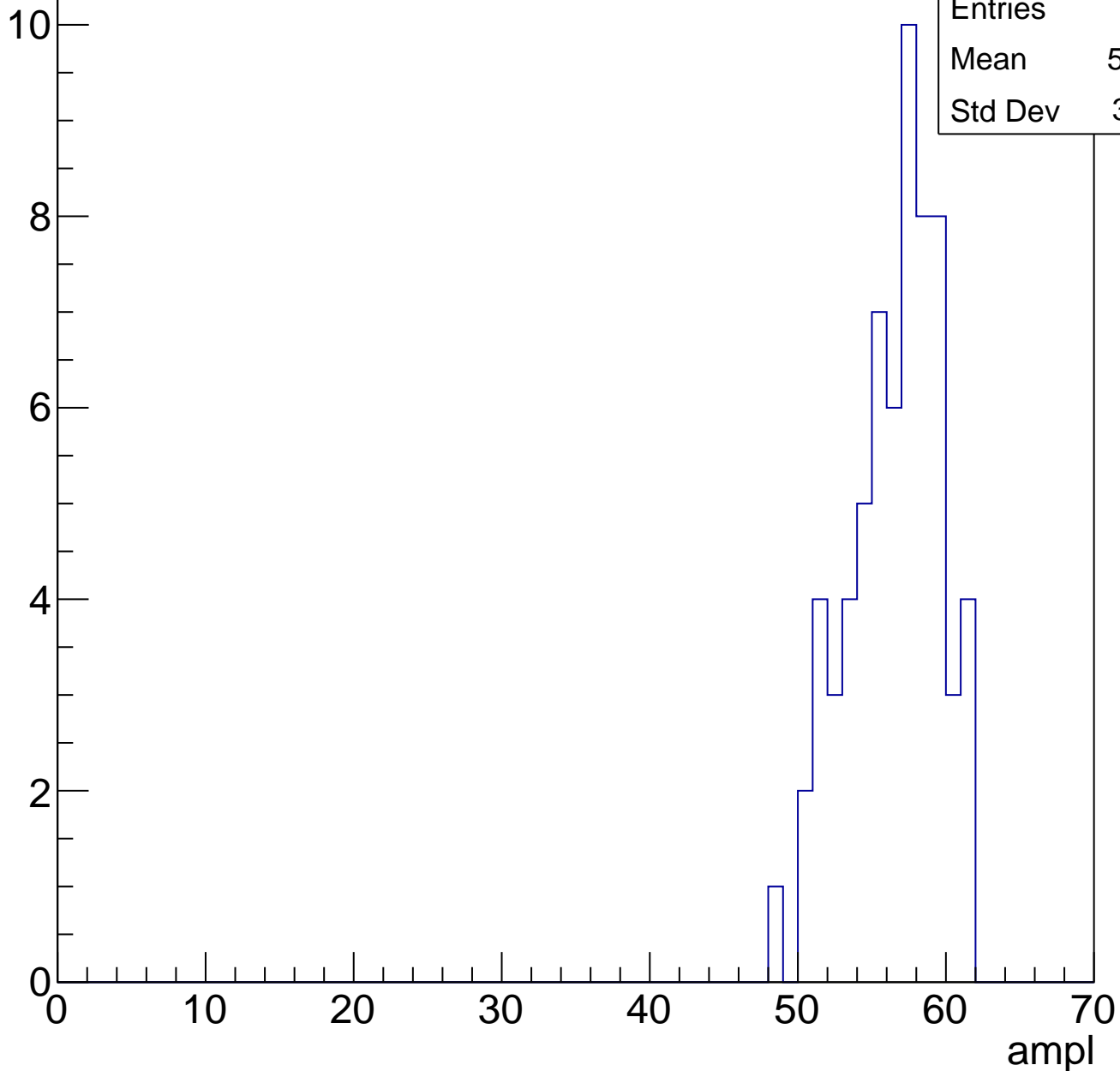


B0L001S, U23-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.02
Std Dev	3.061

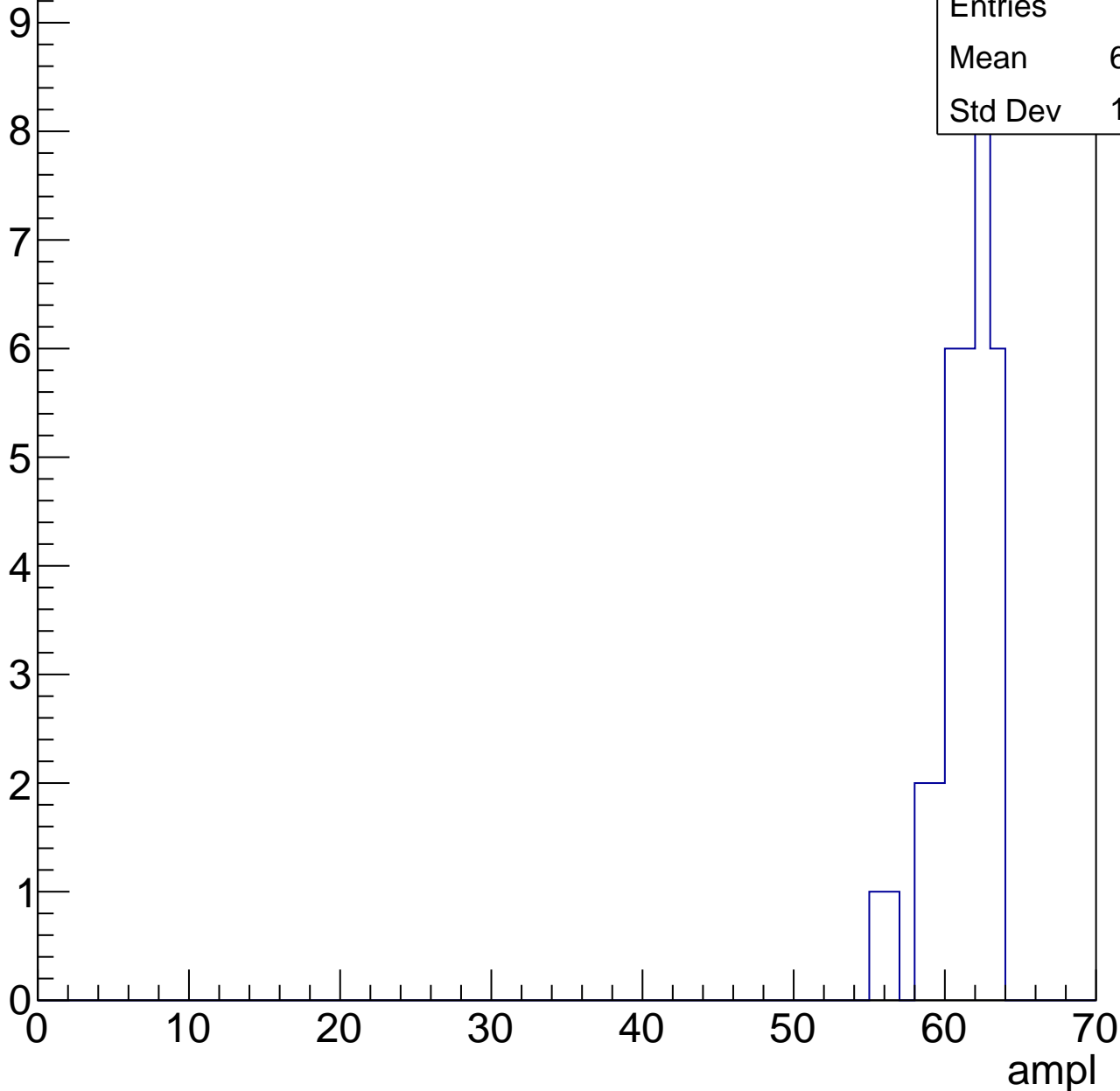


B0L001S, U23-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

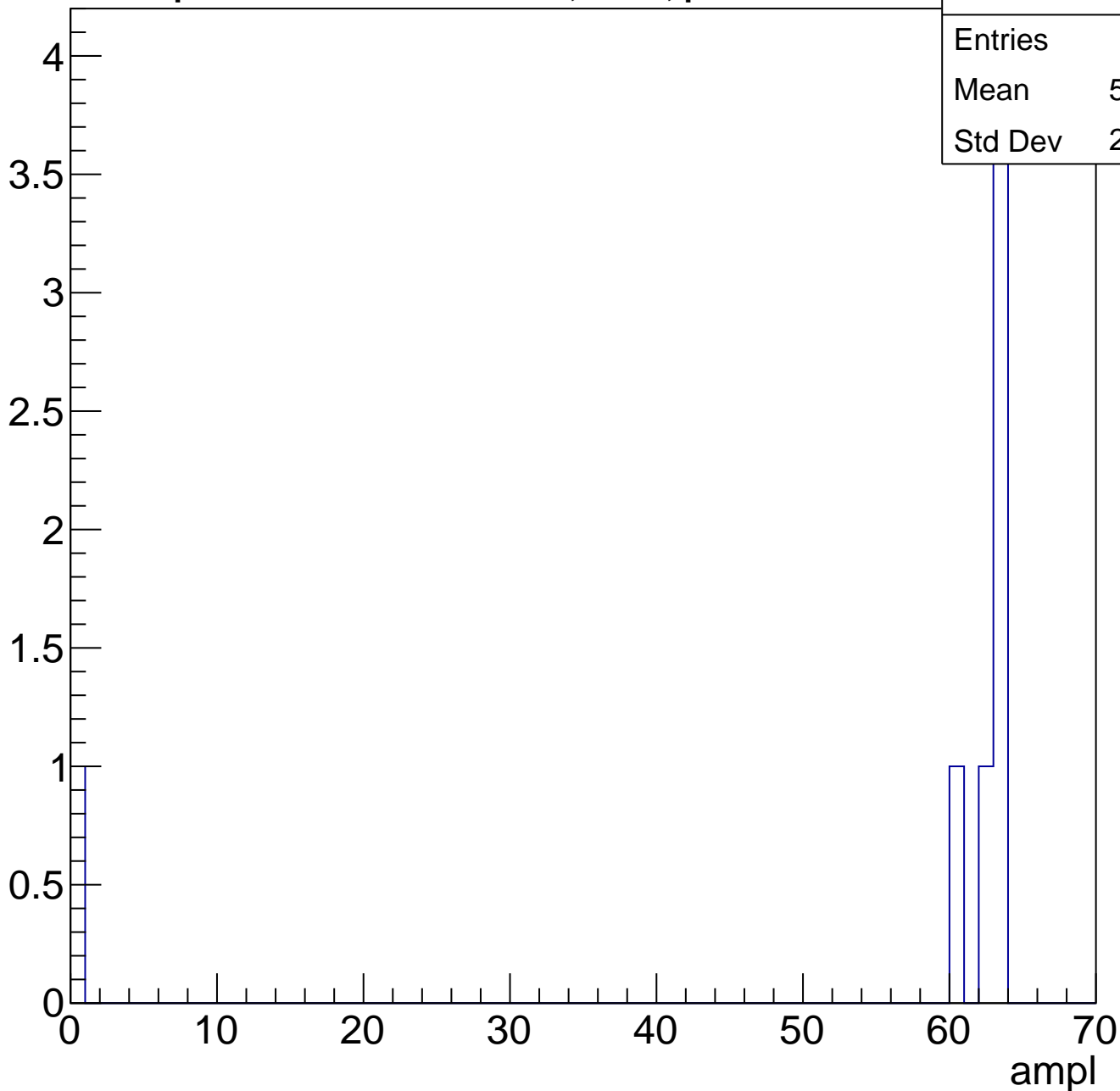
Entries	33
Mean	60.82
Std Dev	1.946



B0L001S, U23-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch39, adc0

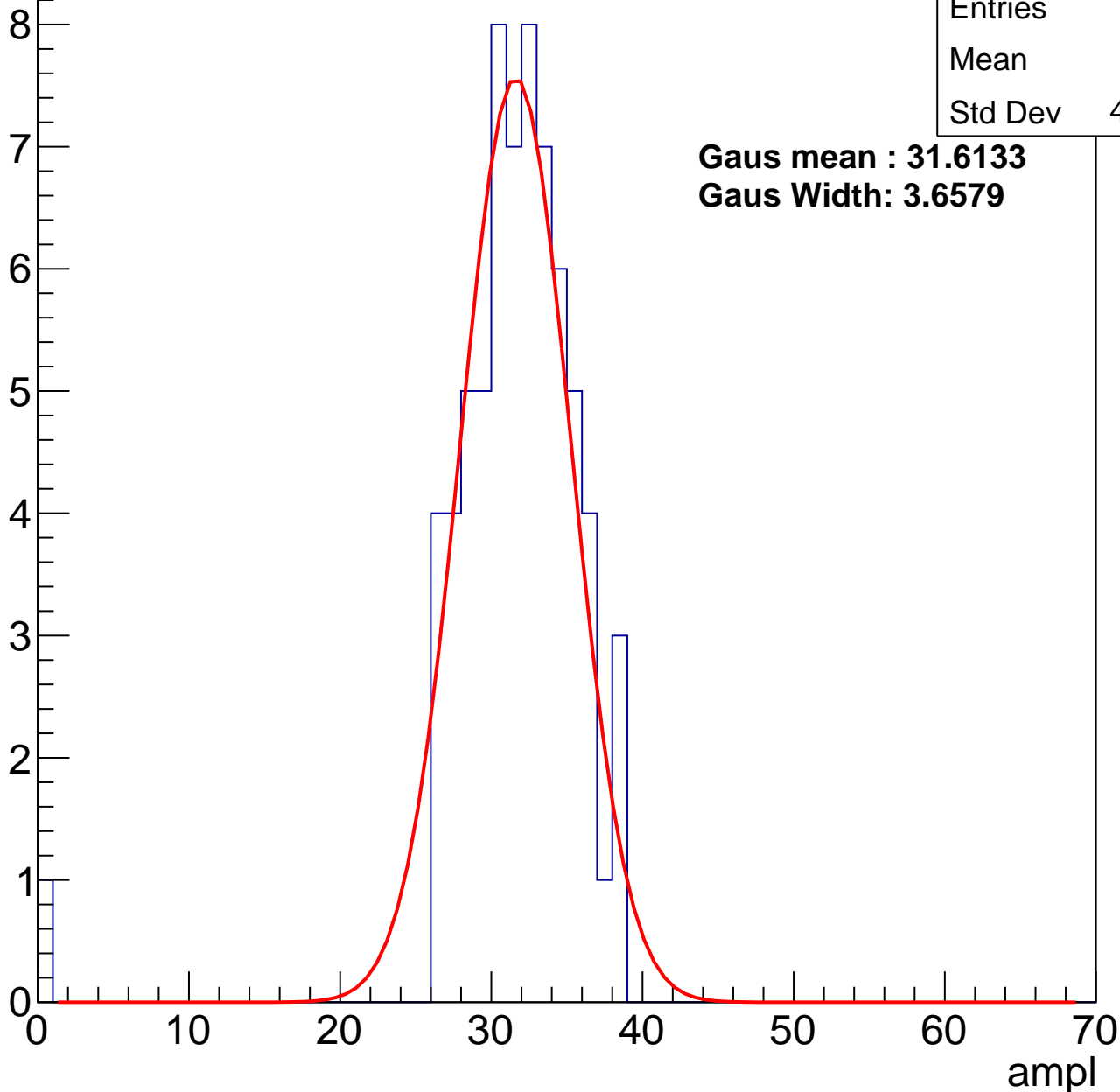
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.1
Std Dev	4.926

Gaus mean : 31.6133

Gaus Width: 3.6579



B0L001S, U23-ch39, adc1

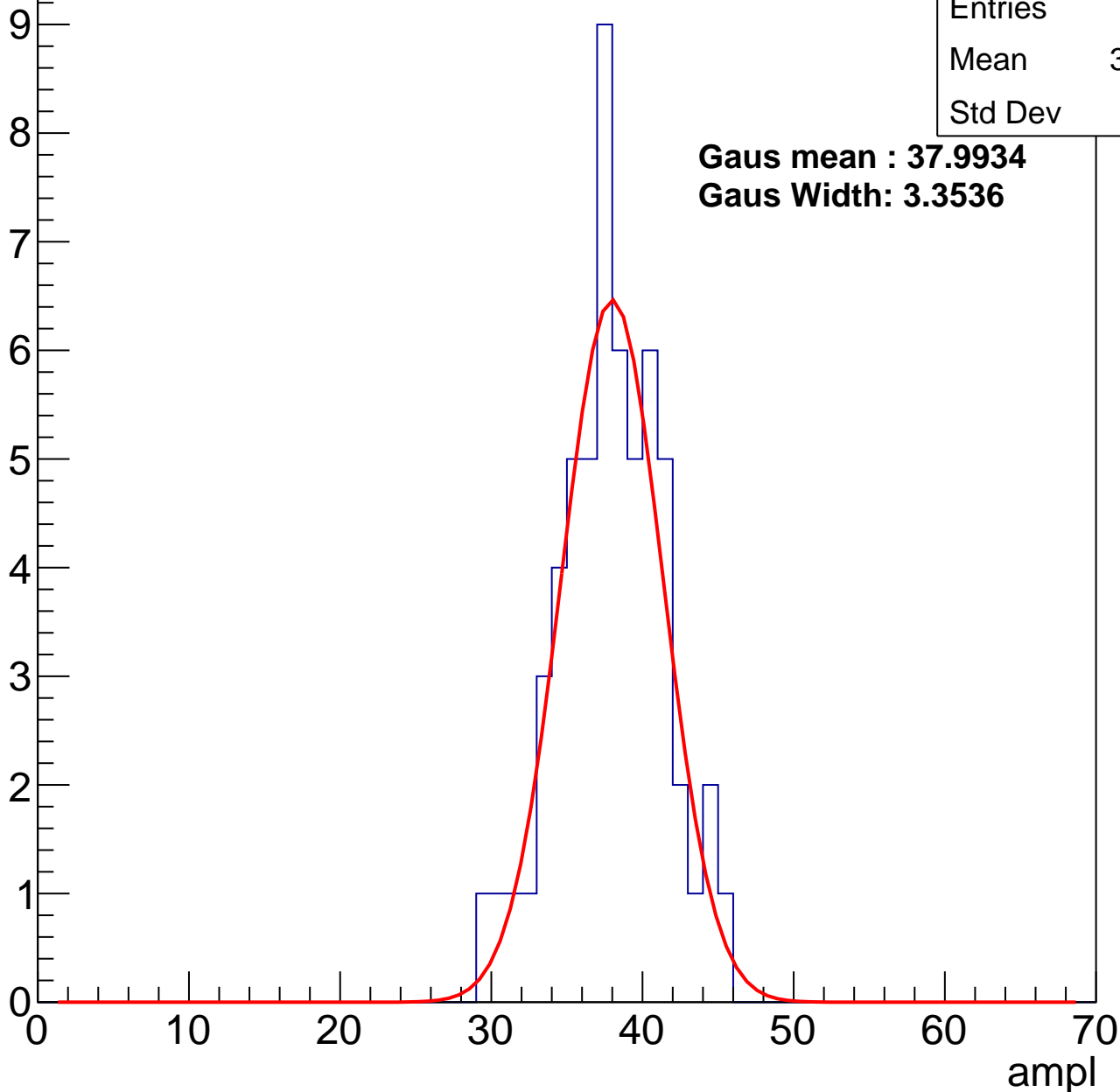
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.47
Std Dev	3.42

Gaus mean : 37.9934

Gaus Width: 3.3536



B0L001S, U23-ch39, adc2

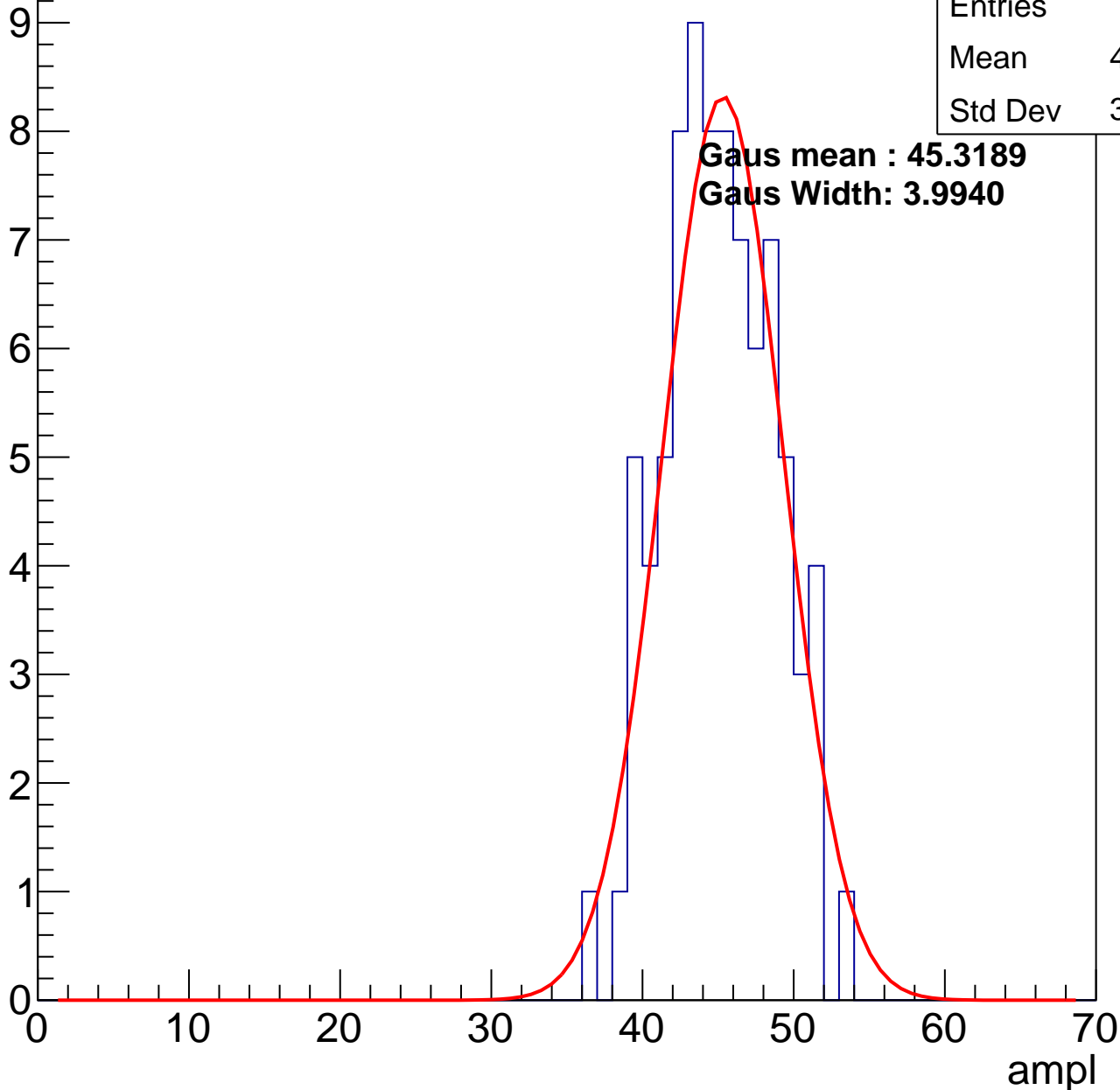
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	44.65
Std Dev	3.583

Gaus mean : 45.3189

Gaus Width: 3.9940

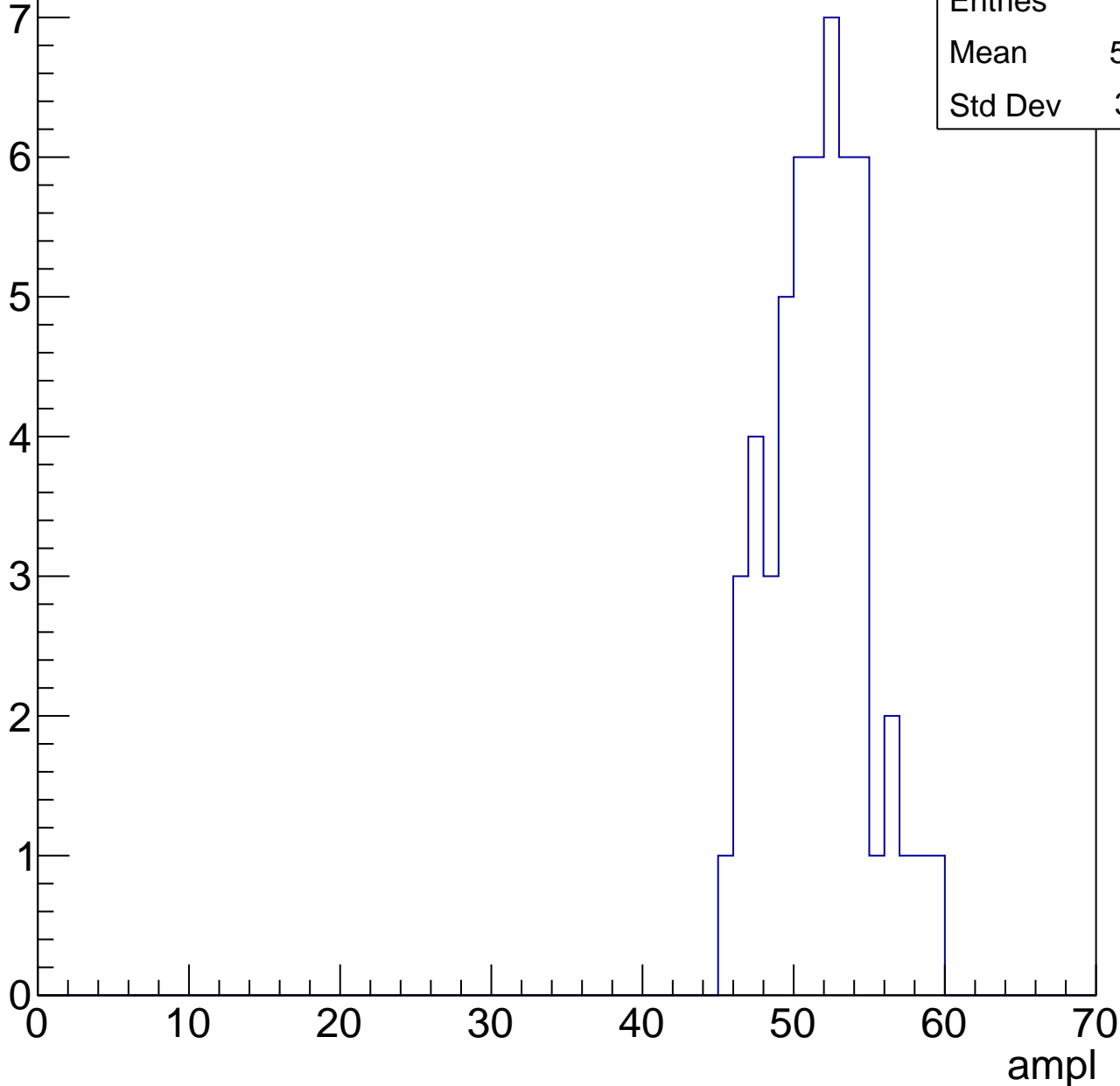


B0L001S, U23-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

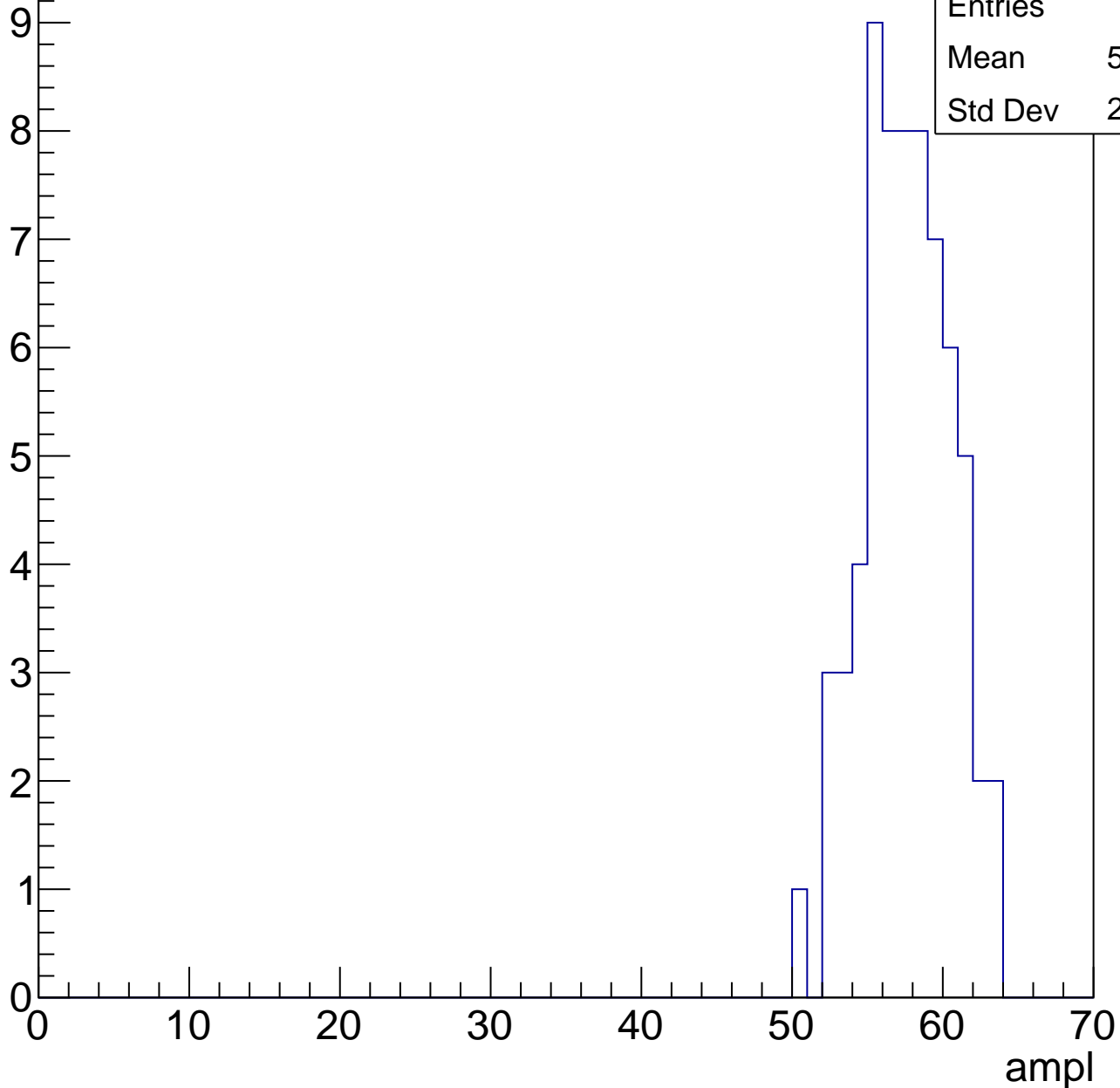
Entries	53
Mean	51.19
Std Dev	3.151



B0L001S, U23-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

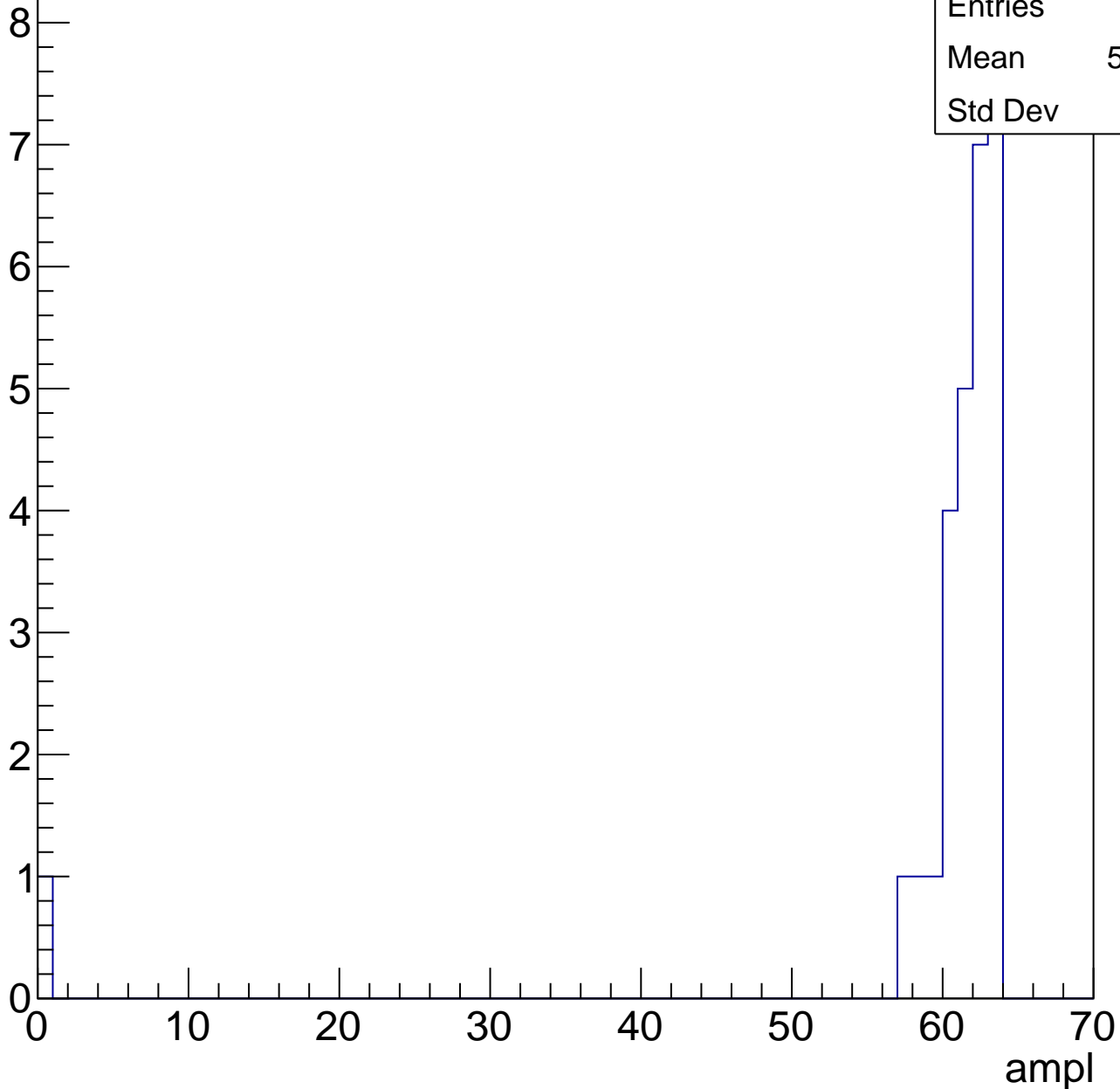


B0L001S, U23-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

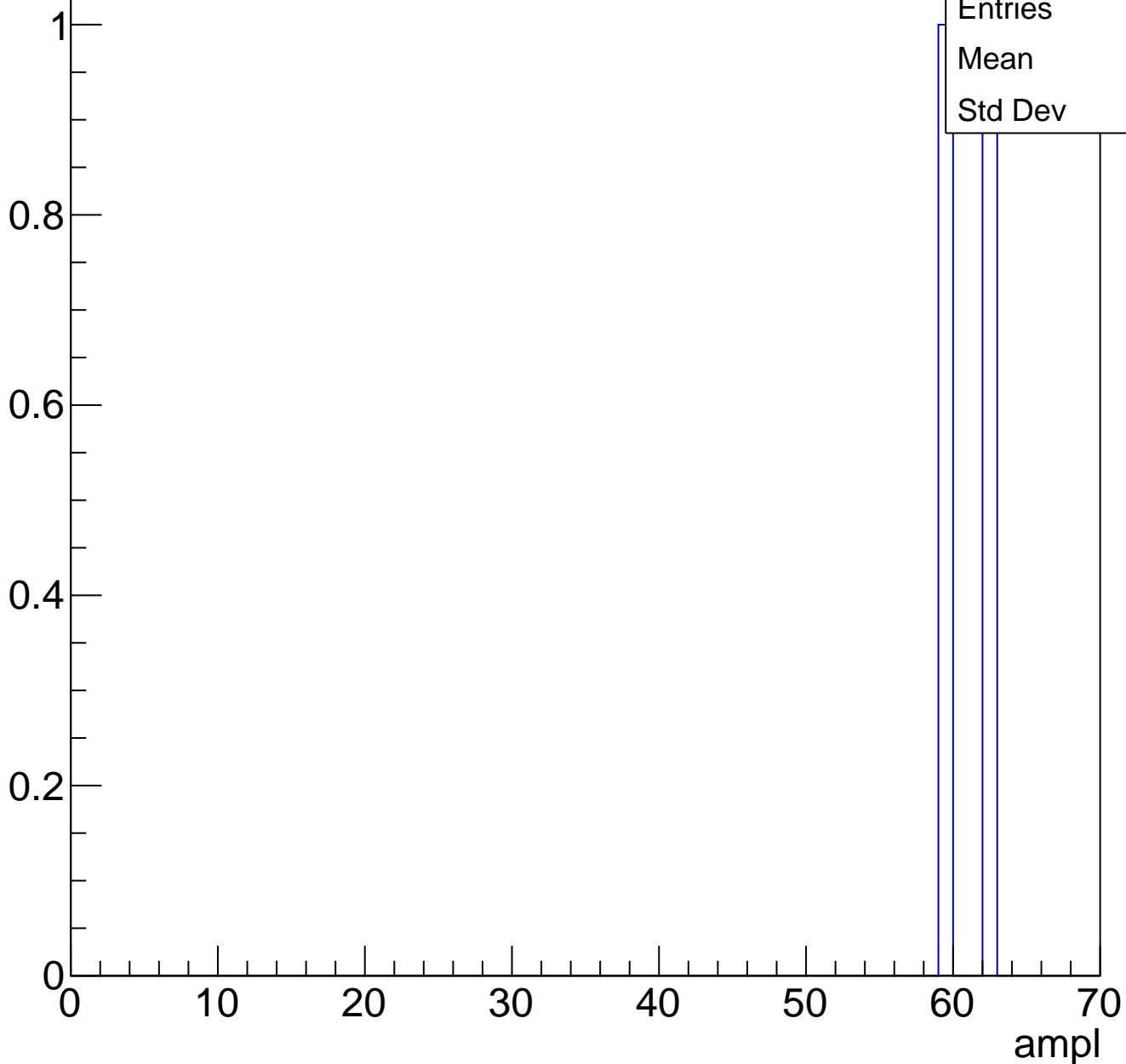
Entries	28
Mean	59.18
Std Dev	11.5



B0L001S, U23-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch39, adc7

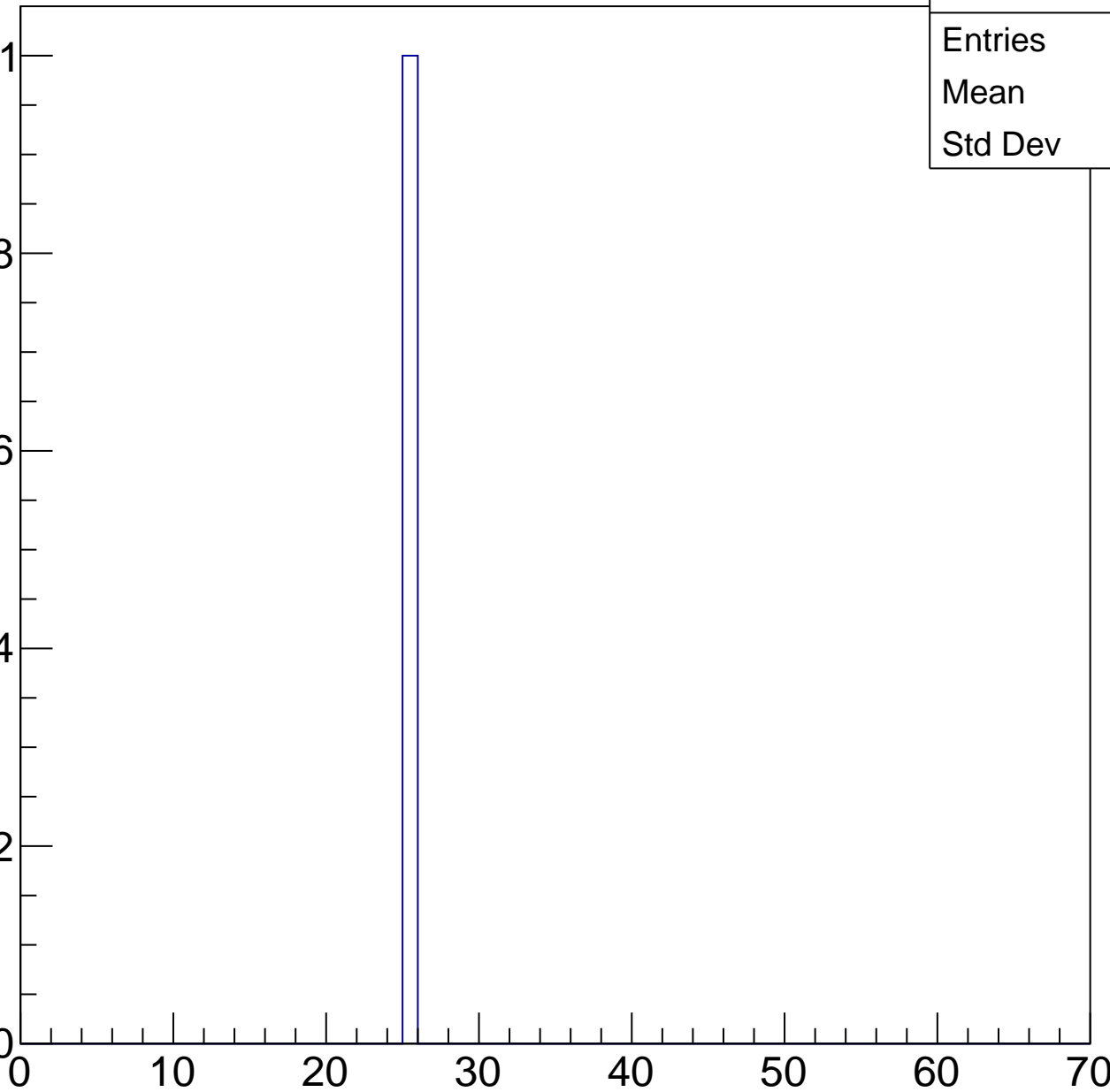
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L001S, U23-ch40, adc0

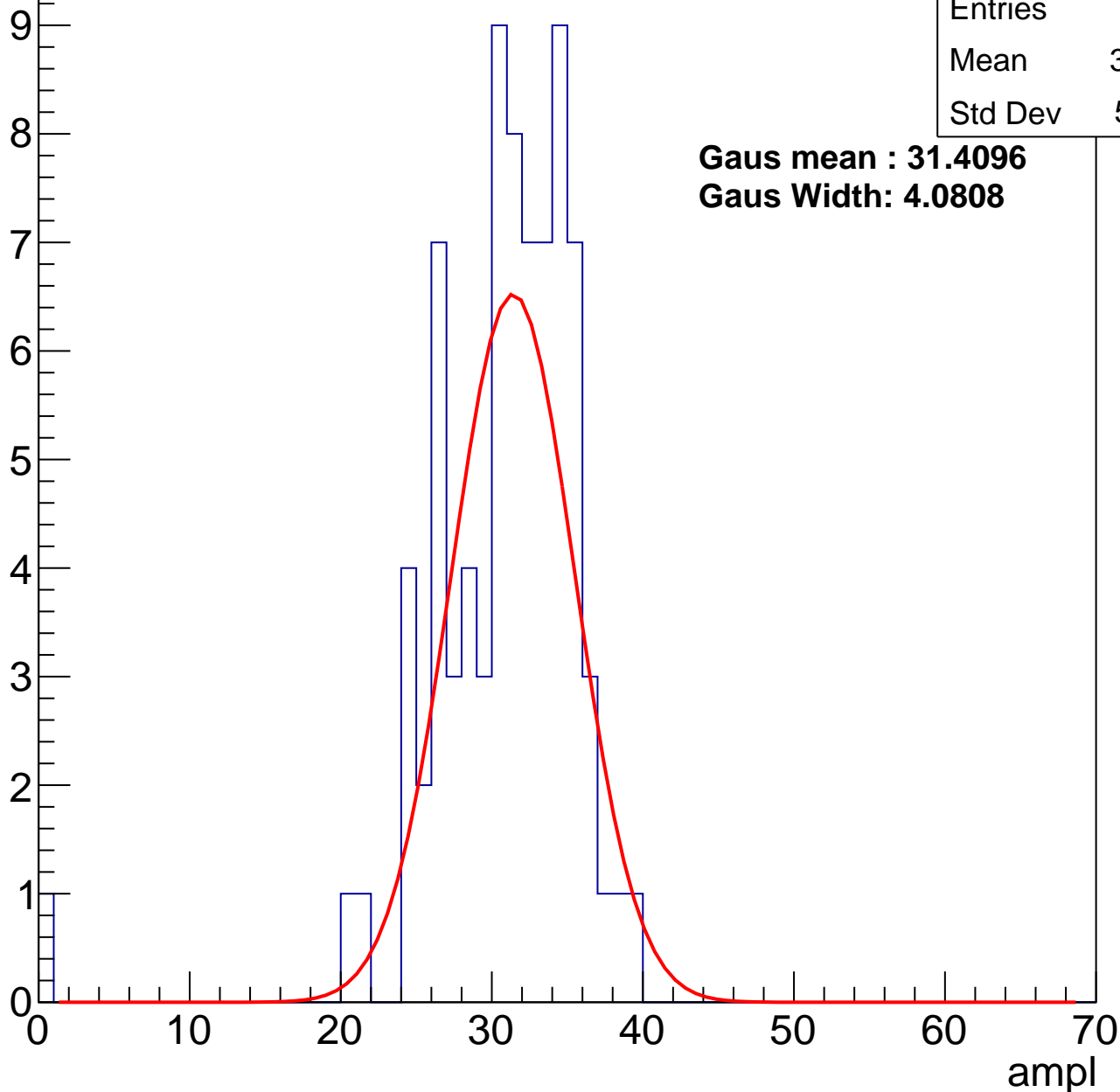
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.32
Std Dev	5.211

Gaus mean : 31.4096

Gaus Width: 4.0808



B0L001S, U23-ch40, adc1

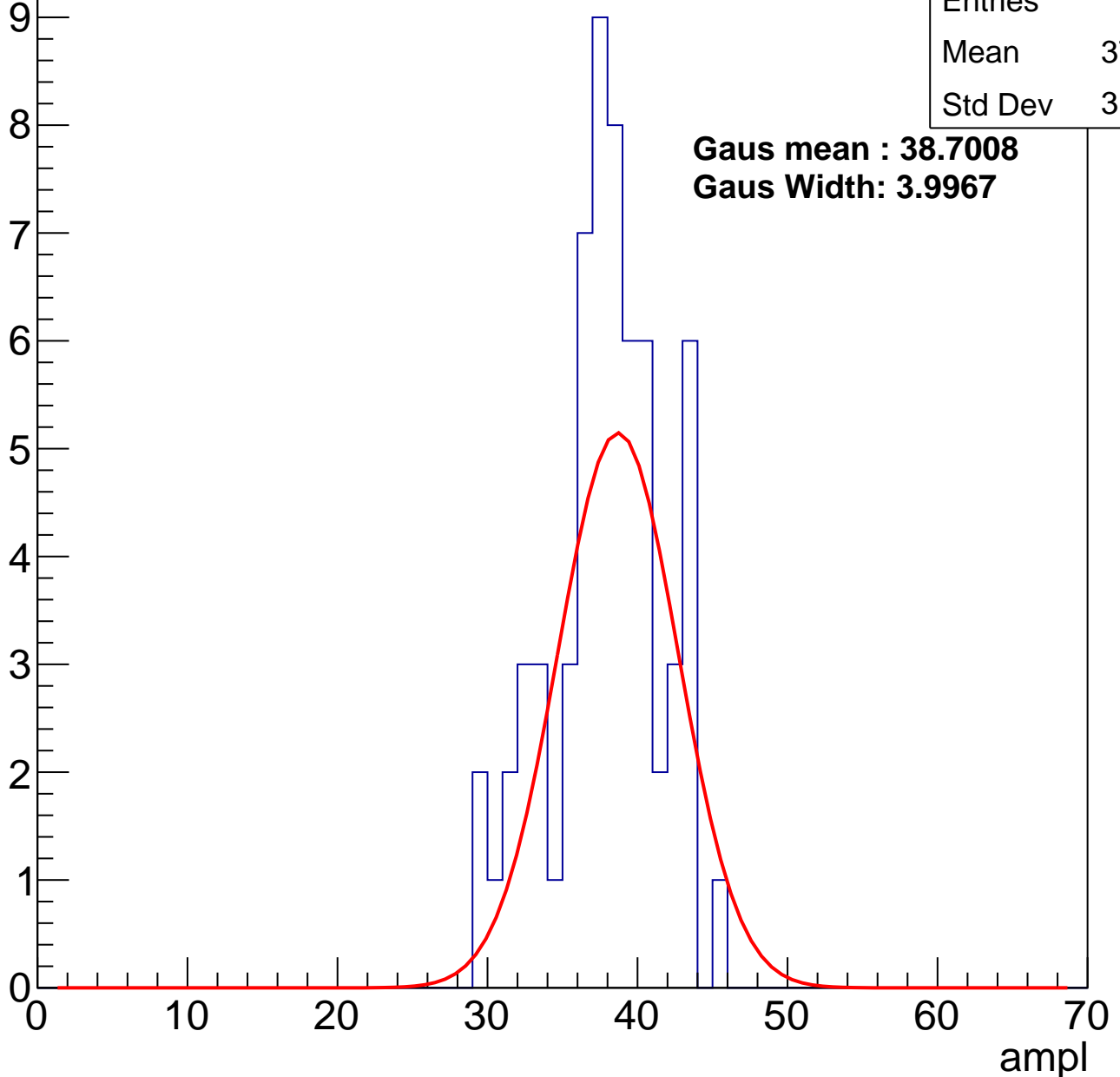
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.43
Std Dev	3.698

Gaus mean : 38.7008

Gaus Width: 3.9967



B0L001S, U23-ch40, adc2

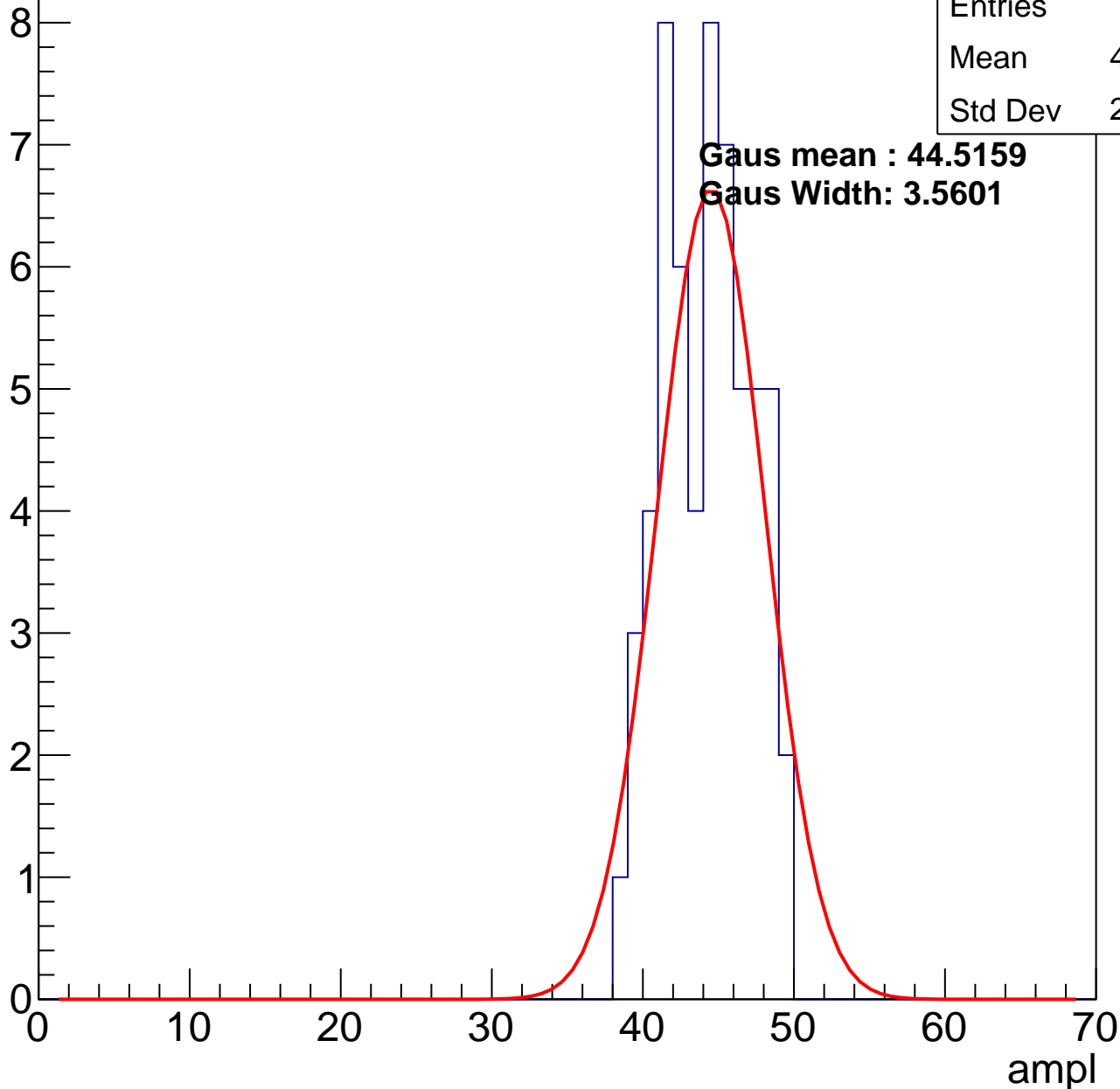
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.74
Std Dev	2.856

Gaus mean : 44.5159

Gaus Width: 3.5601

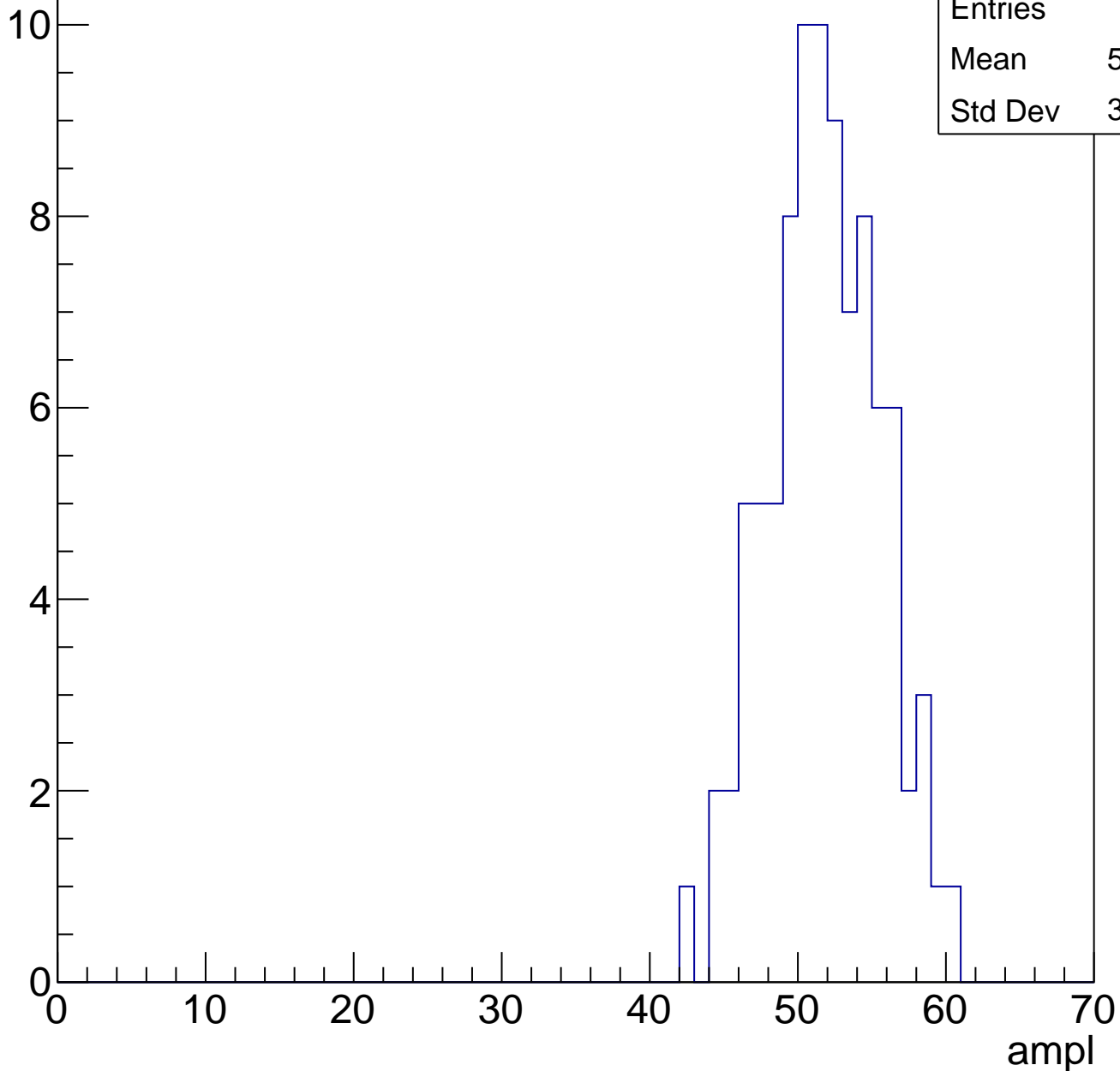


B0L001S, U23-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	91
Mean	51.33
Std Dev	3.709

Entry



B0L001S, U23-ch40, adc4

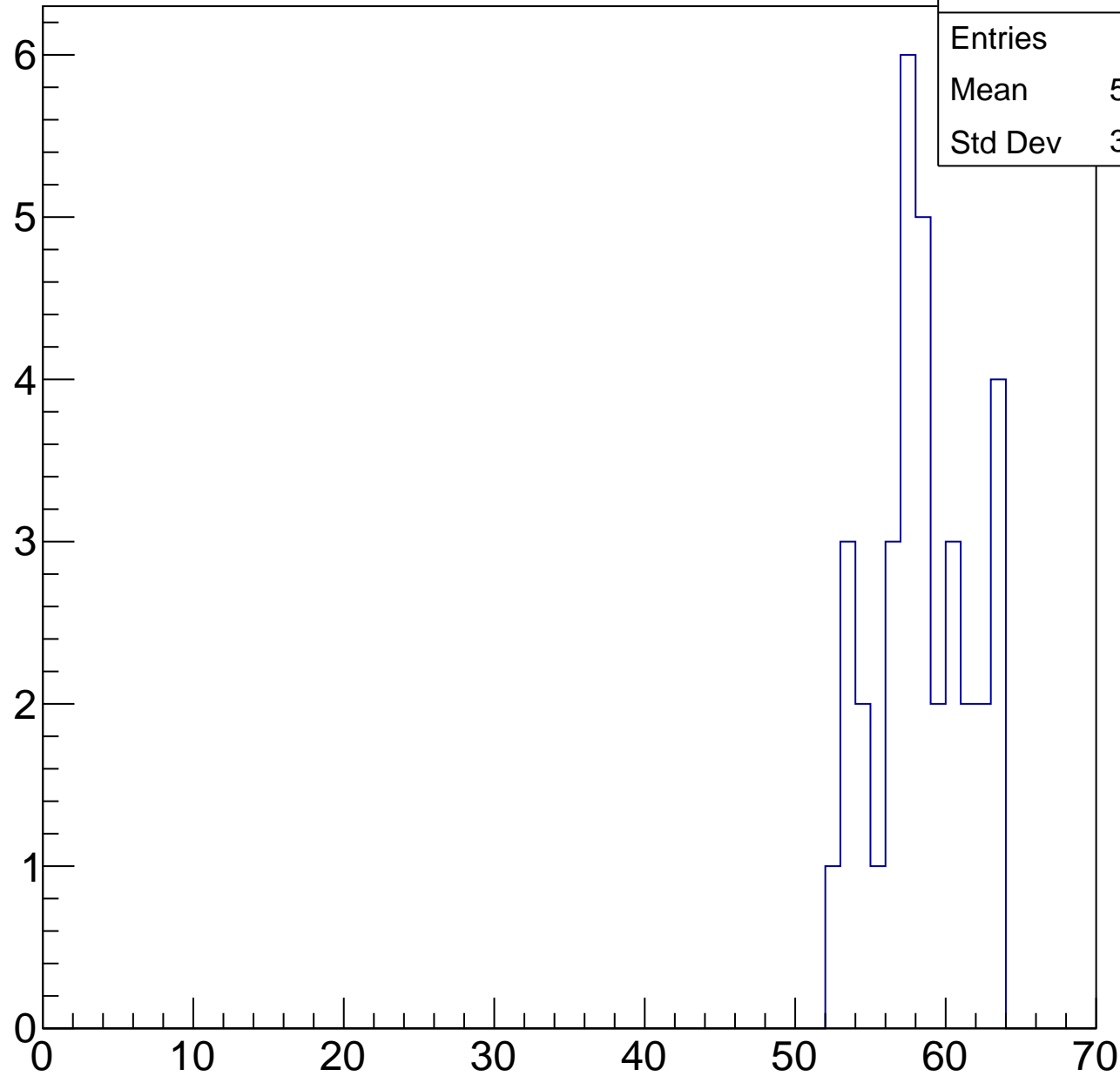
calib_packv5_042523_0143.root, FC#9, port A1

Entry

6
5
4
3
2
1
0

Entries	34
Mean	57.94
Std Dev	3.134

ampl

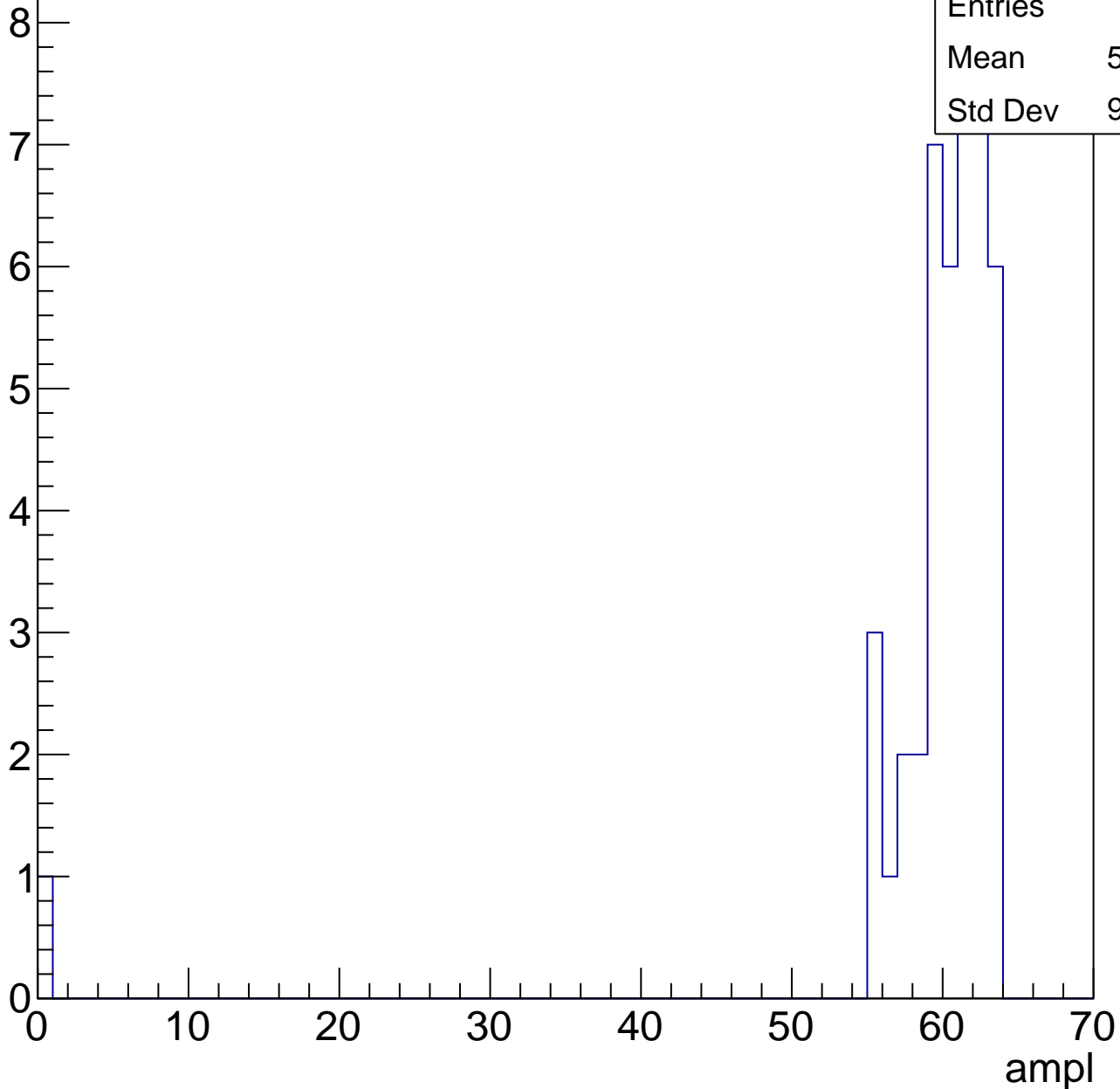


B0L001S, U23-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.77
Std Dev	9.234



B0L001S, U23-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch41, adc0

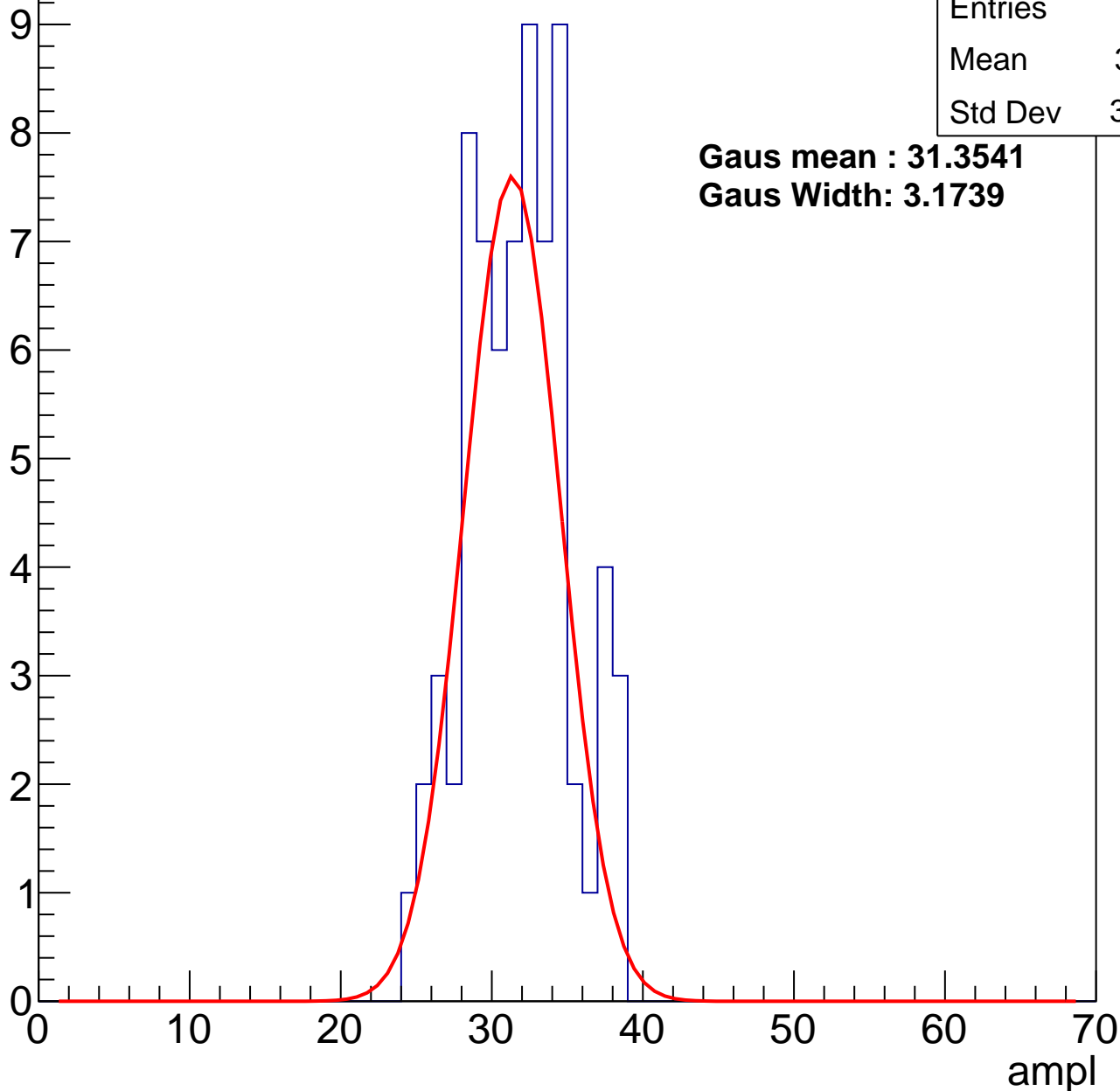
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	31.31
Std Dev	3.342

Gaus mean : 31.3541

Gaus Width: 3.1739



B0L001S, U23-ch41, adc1

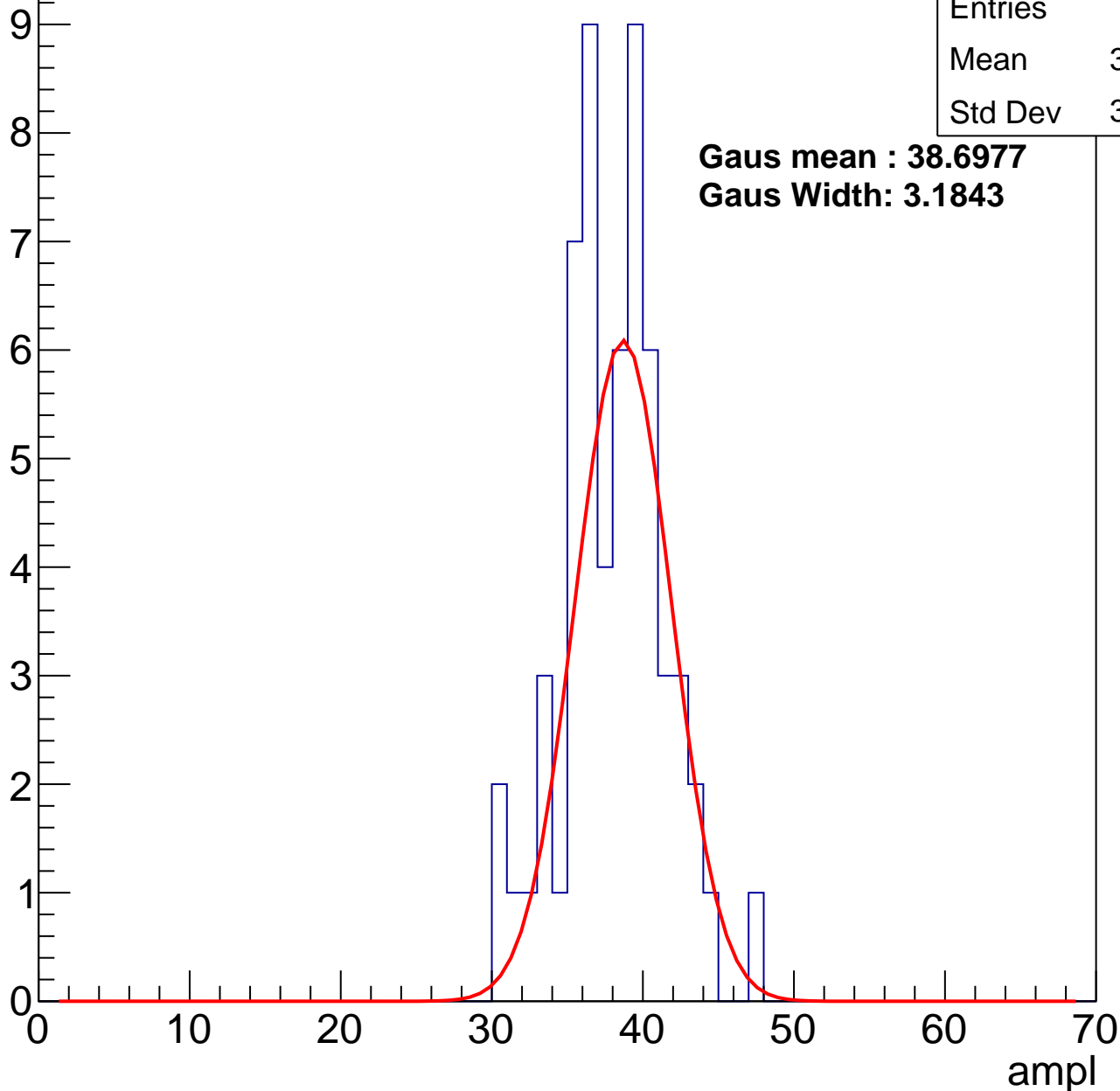
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.59
Std Dev	3.375

Gaus mean : 38.6977

Gaus Width: 3.1843



B0L001S, U23-ch41, adc2

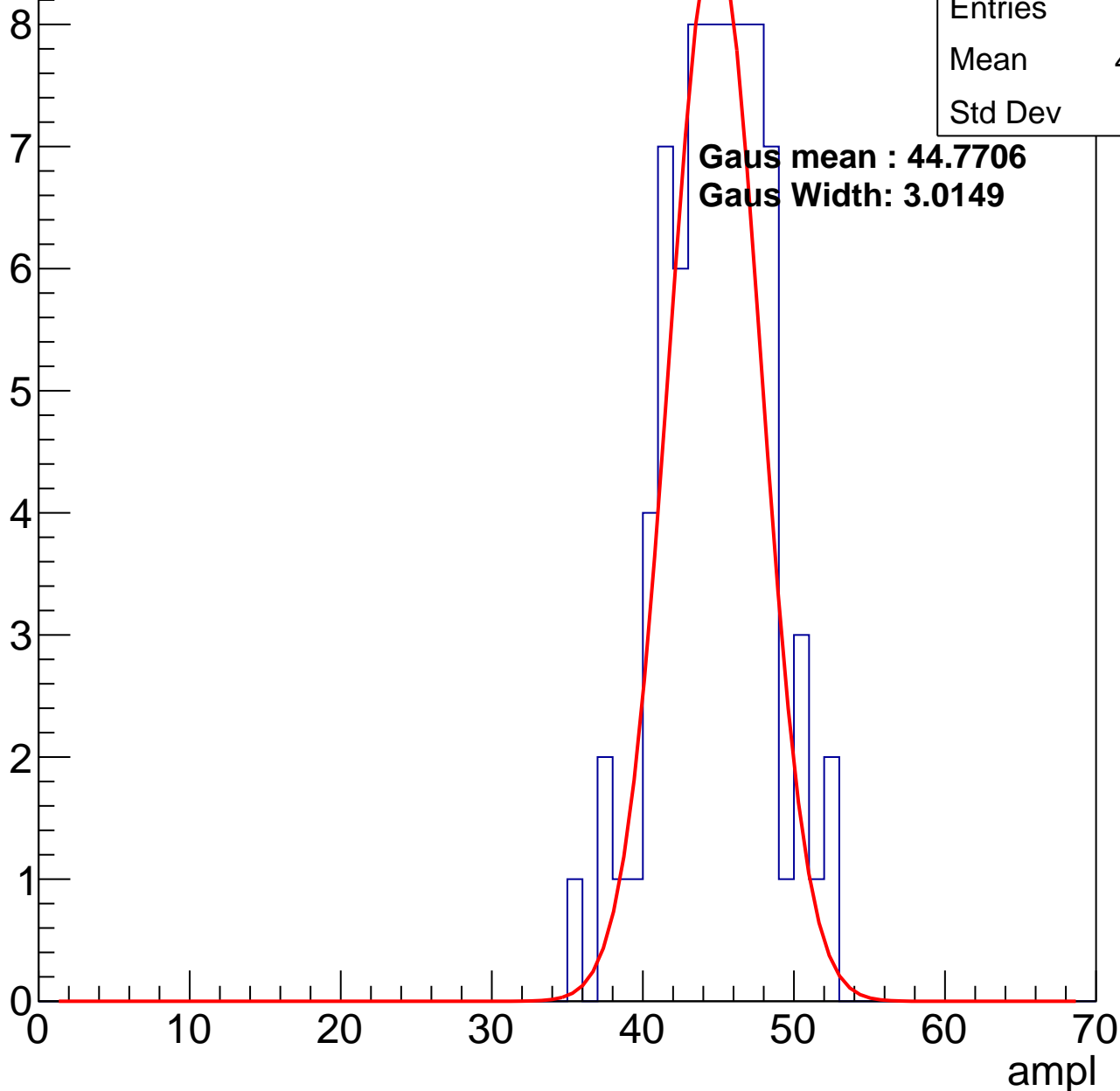
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.41
Std Dev	3.48

Gaus mean : 44.7706

Gaus Width: 3.0149

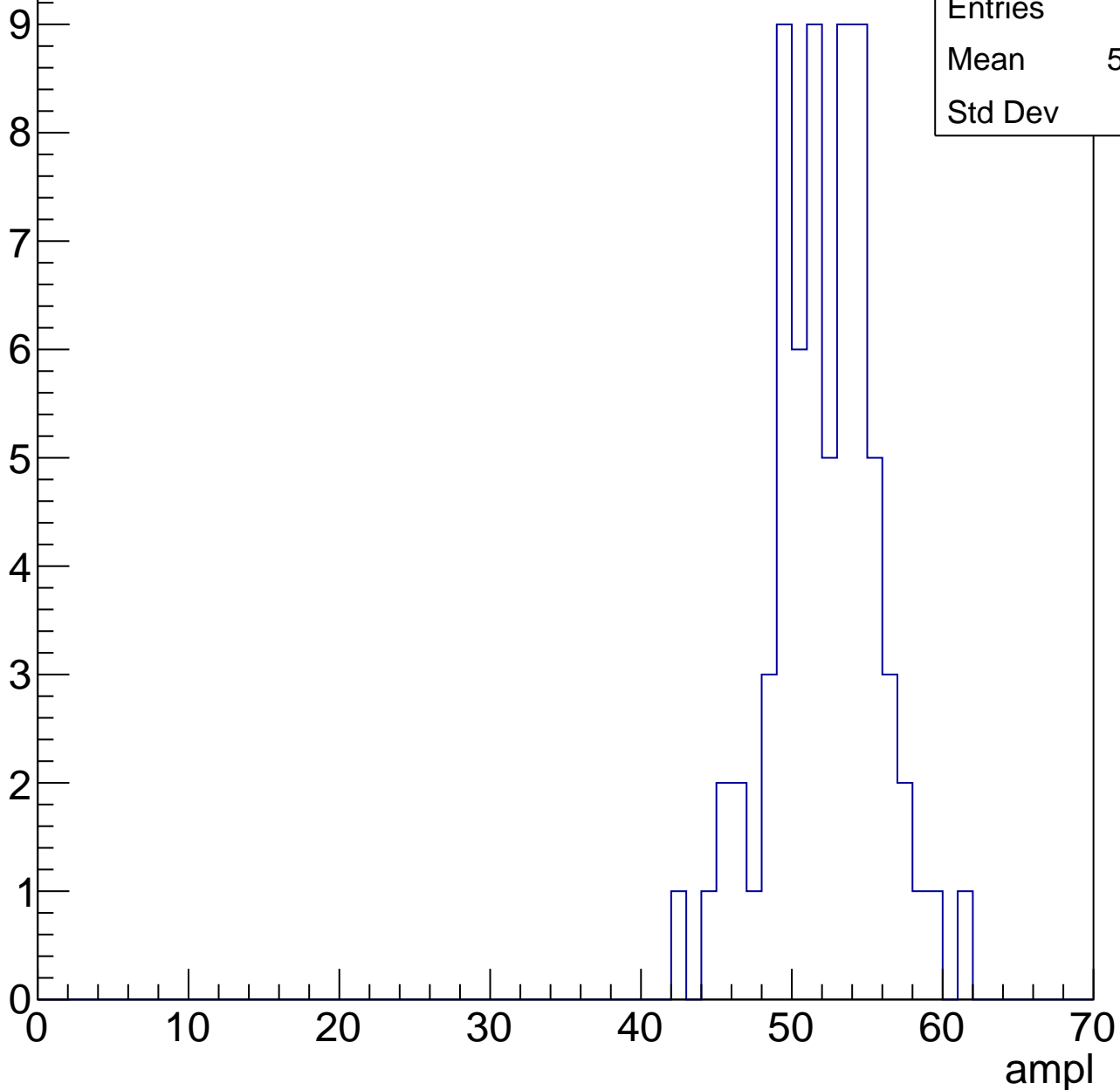


B0L001S, U23-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	51.67
Std Dev	3.52



B0L001S, U23-ch41, adc4

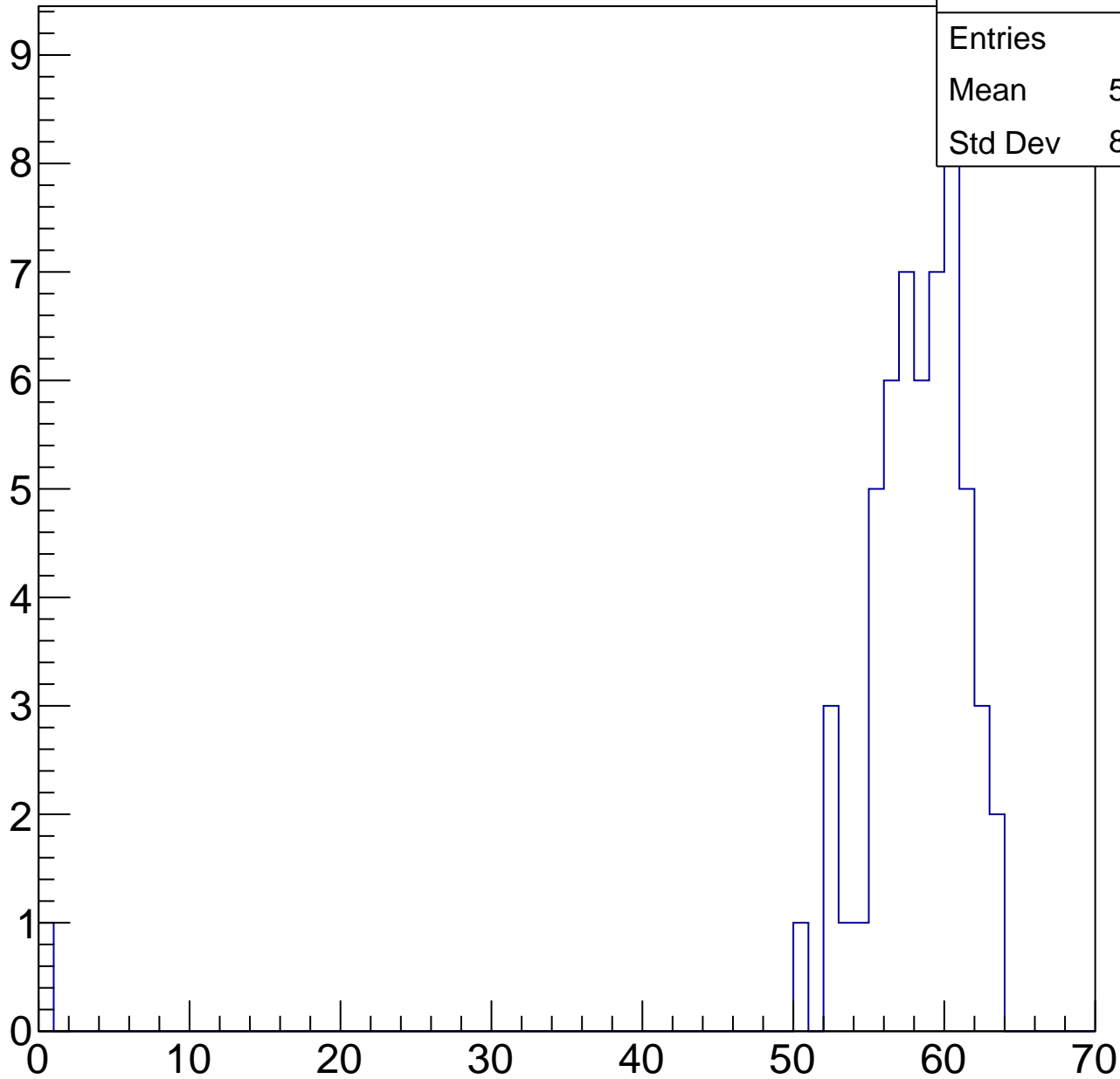
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	56.86
Std Dev	8.127

ampl

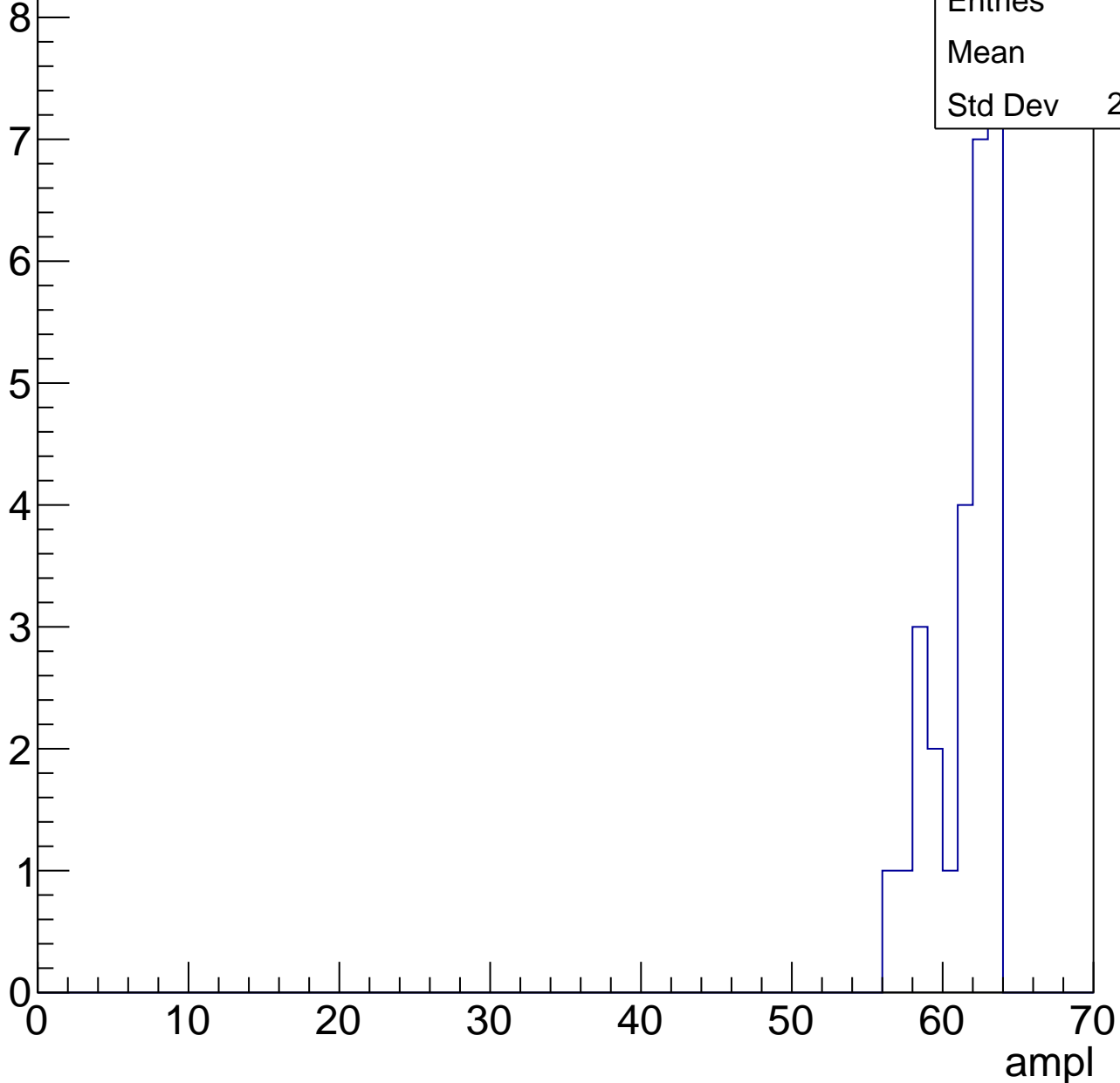


B0L001S, U23-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	61
Std Dev	2.073



B0L001S, U23-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch42, adc0

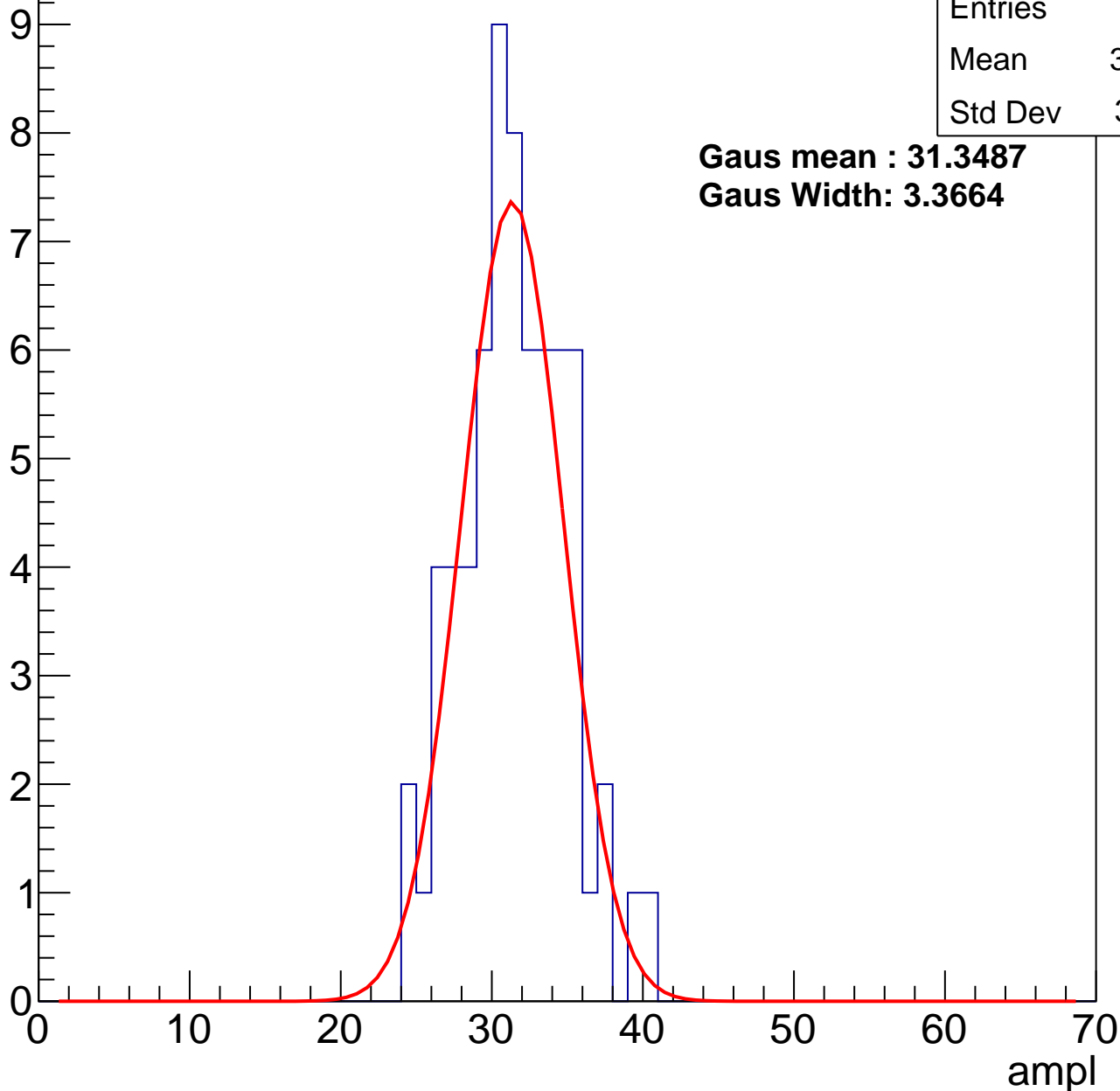
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	31.07
Std Dev	3.431

Gaus mean : 31.3487

Gaus Width: 3.3664



B0L001S, U23-ch42, adc1

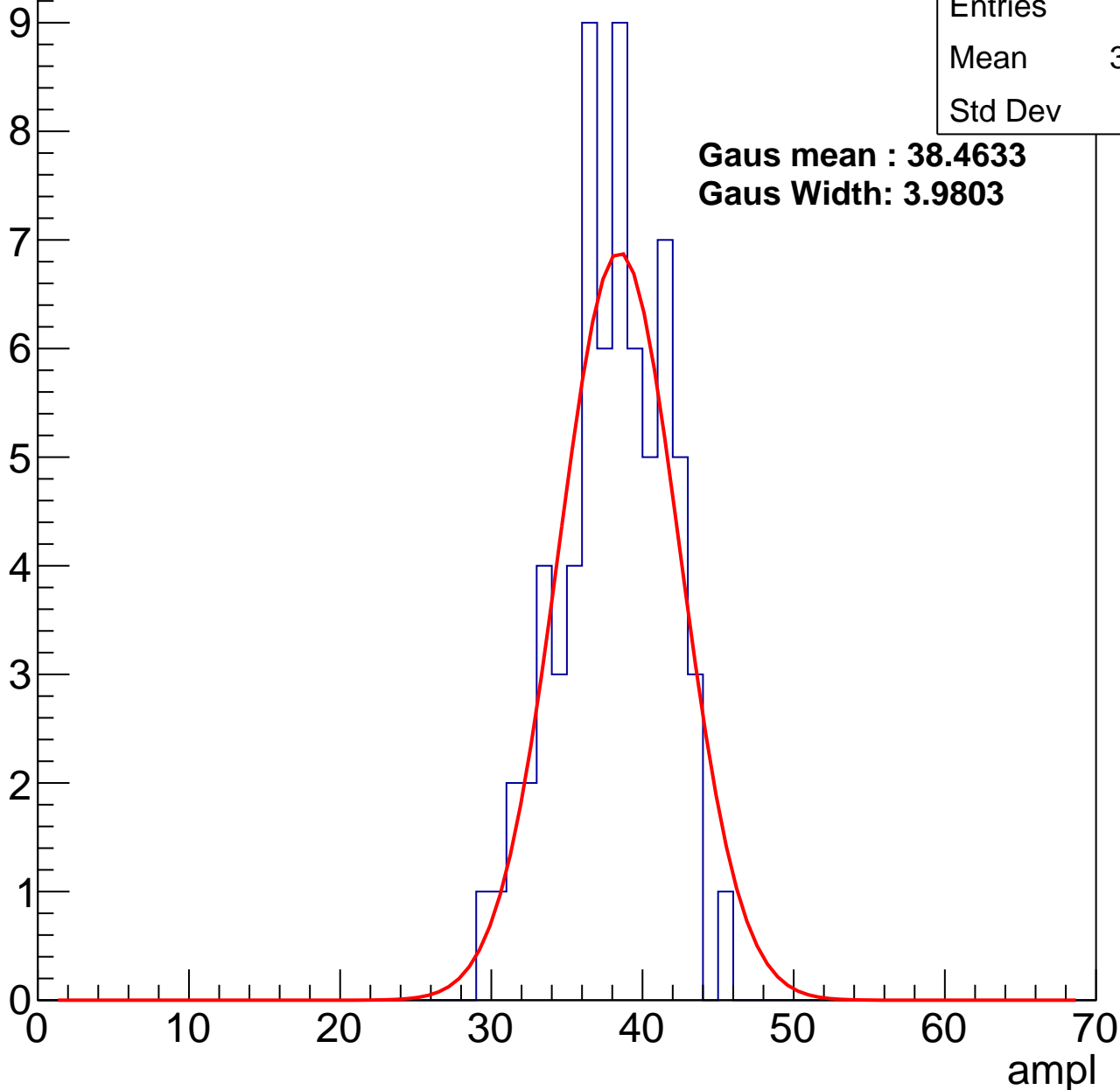
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.53
Std Dev	3.47

Gaus mean : 38.4633

Gaus Width: 3.9803



B0L001S, U23-ch42, adc2

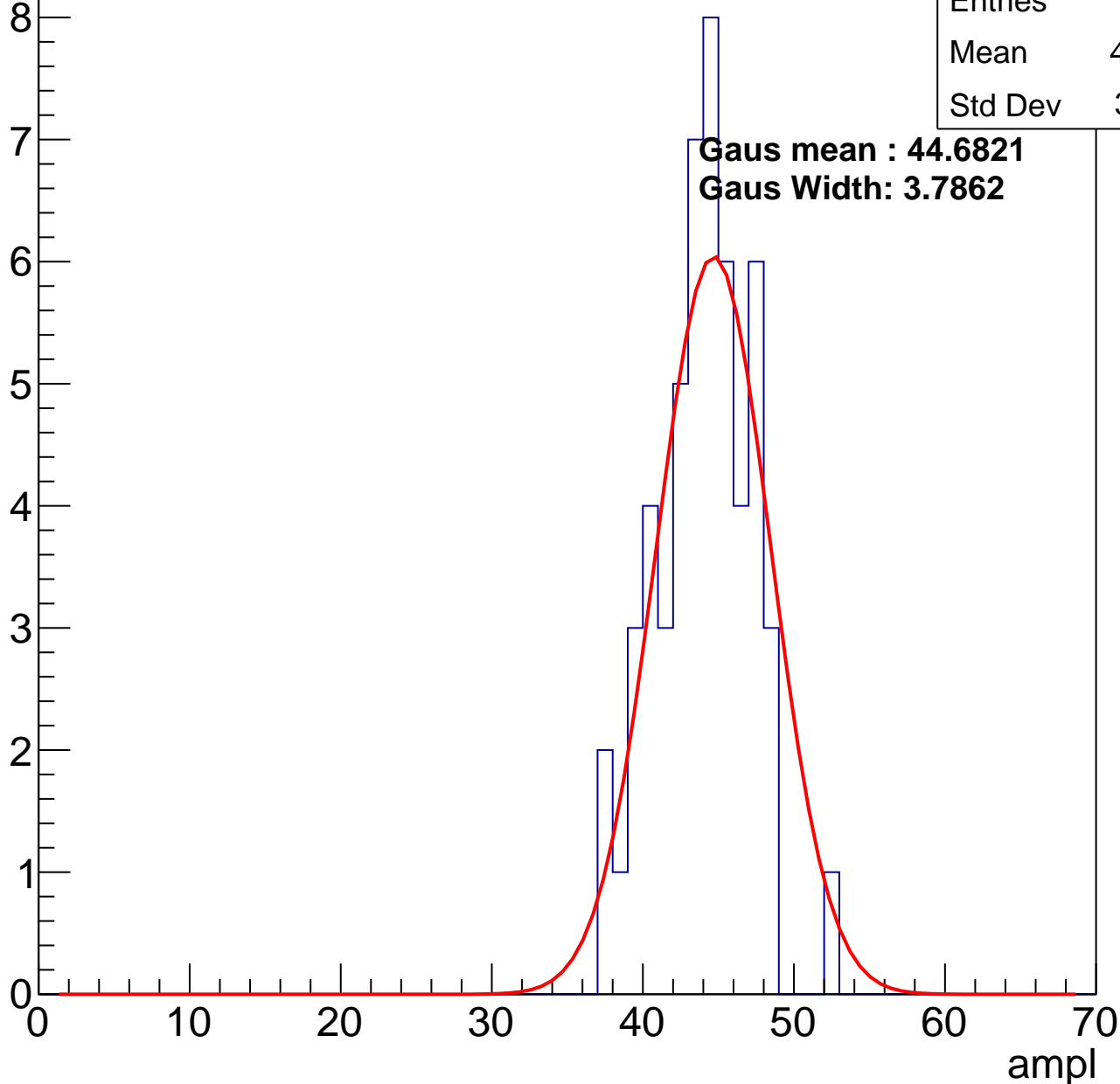
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	43.53
Std Dev	3.081

Gaus mean : 44.6821

Gaus Width: 3.7862

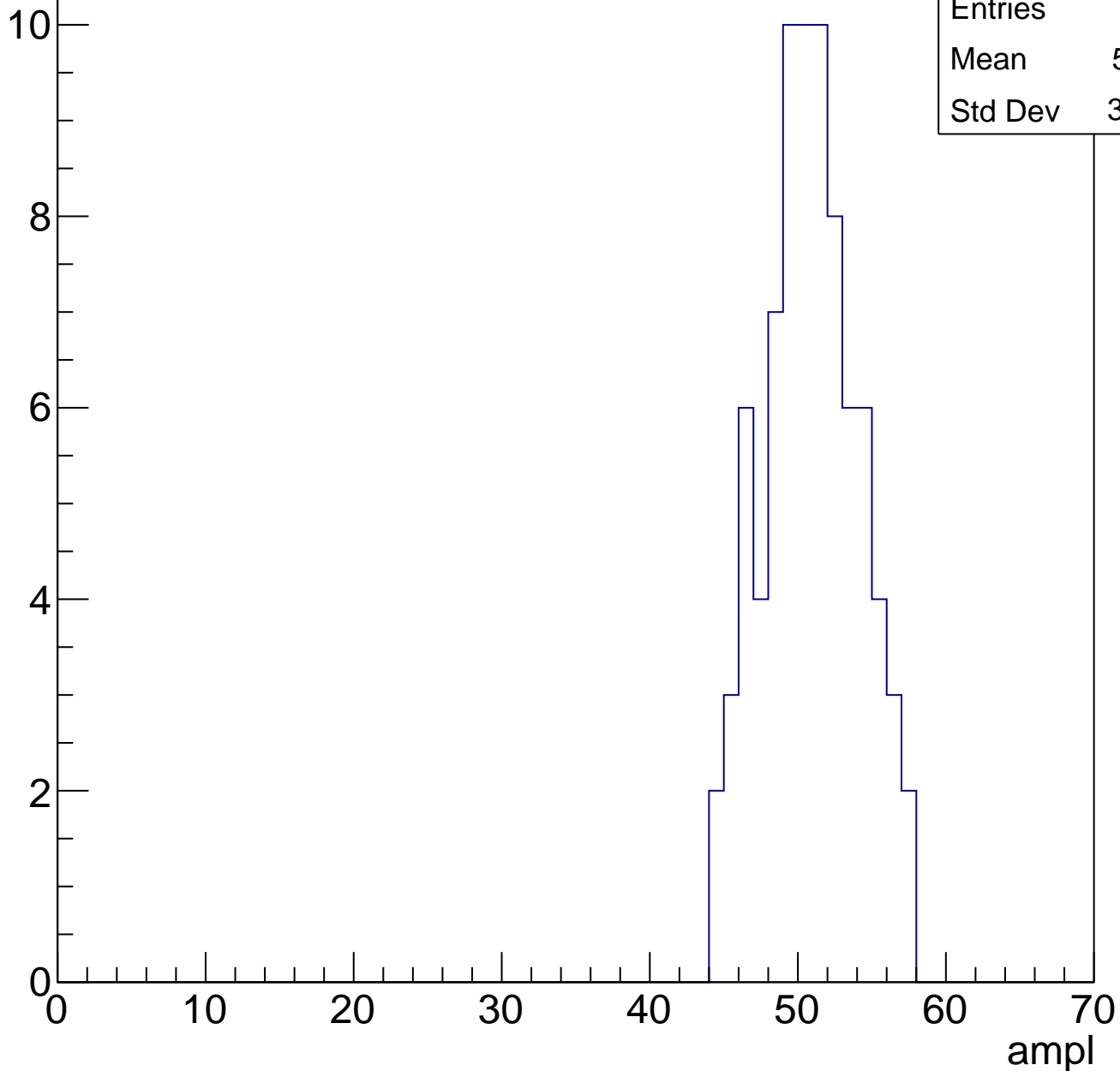


B0L001S, U23-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	50.41
Std Dev	3.146

Entry

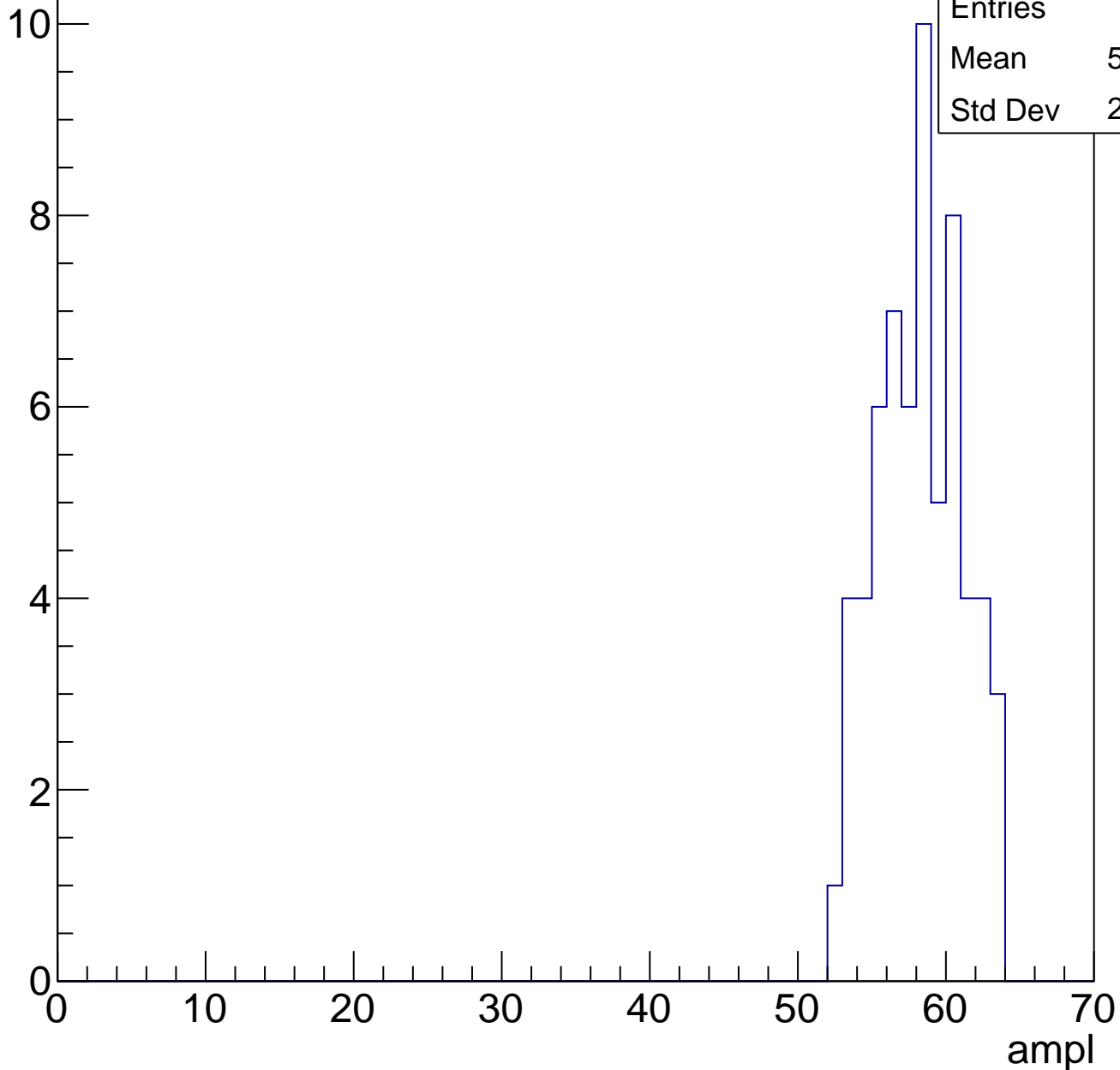


B0L001S, U23-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	57.74
Std Dev	2.828

Entry

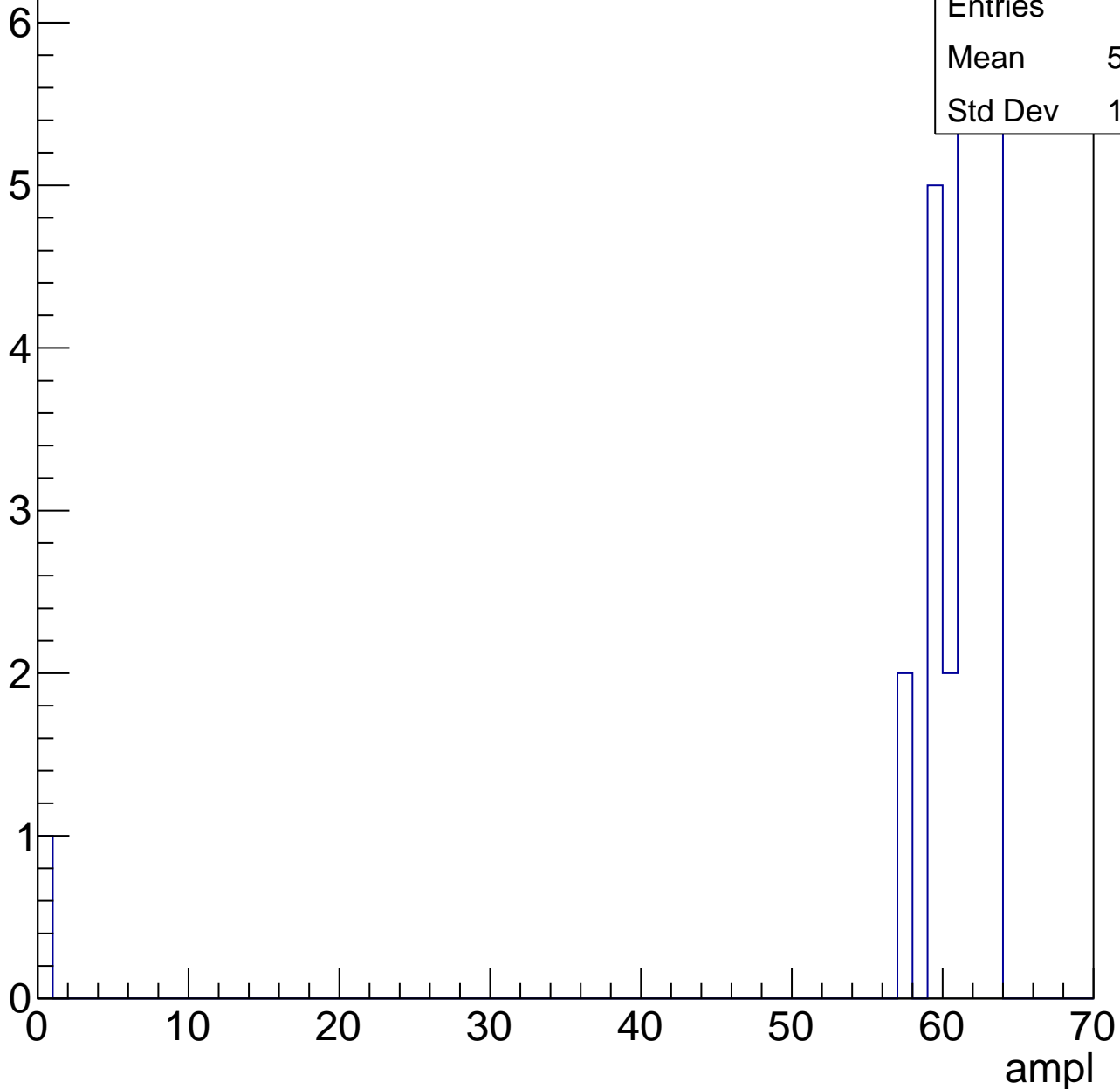


B0L001S, U23-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.75
Std Dev	11.44



B0L001S, U23-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	22
Std Dev	0

B0L001S, U23-ch43, adc0

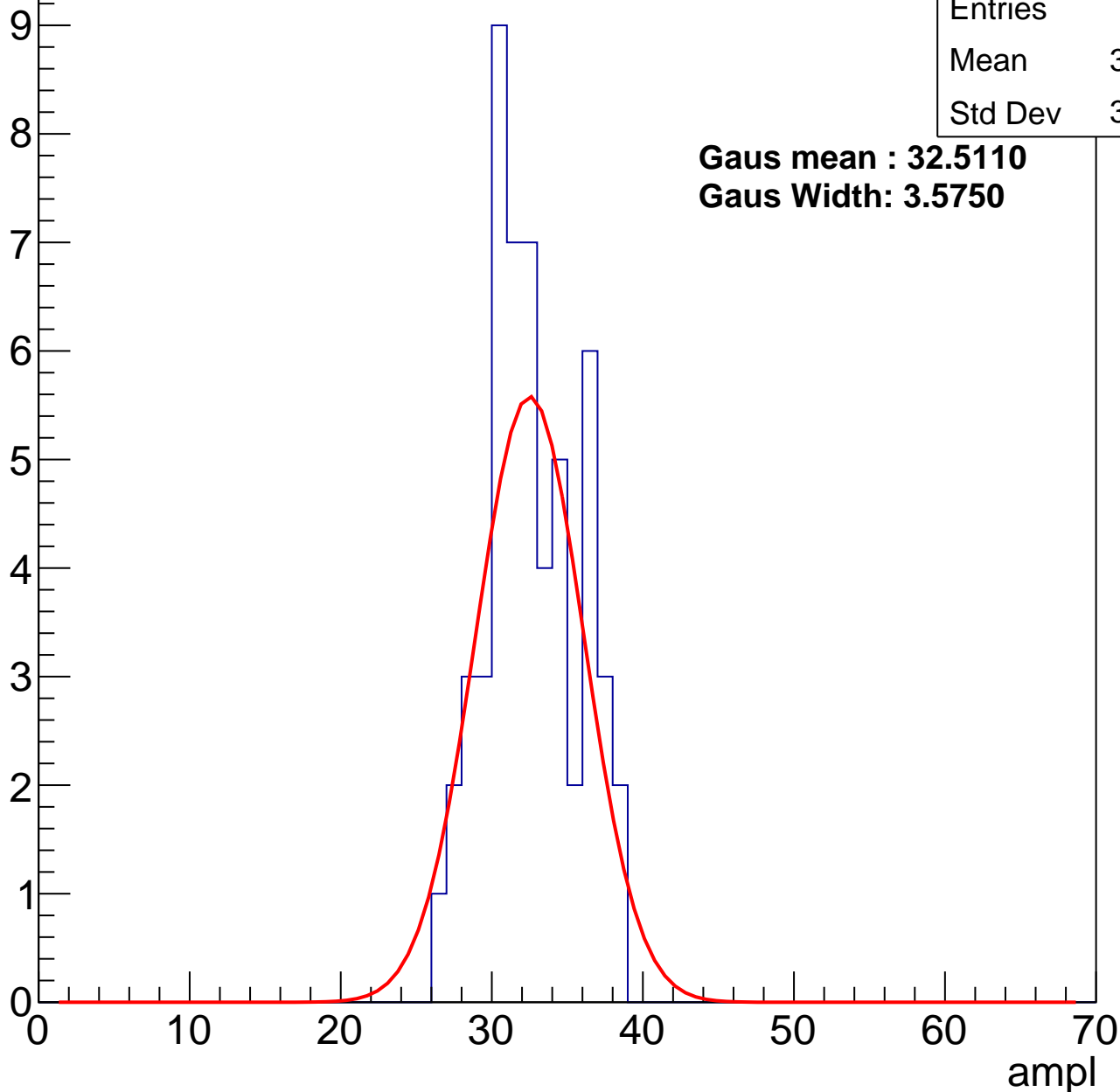
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	32.17
Std Dev	3.005

Gaus mean : 32.5110

Gaus Width: 3.5750



B0L001S, U23-ch43, adc1

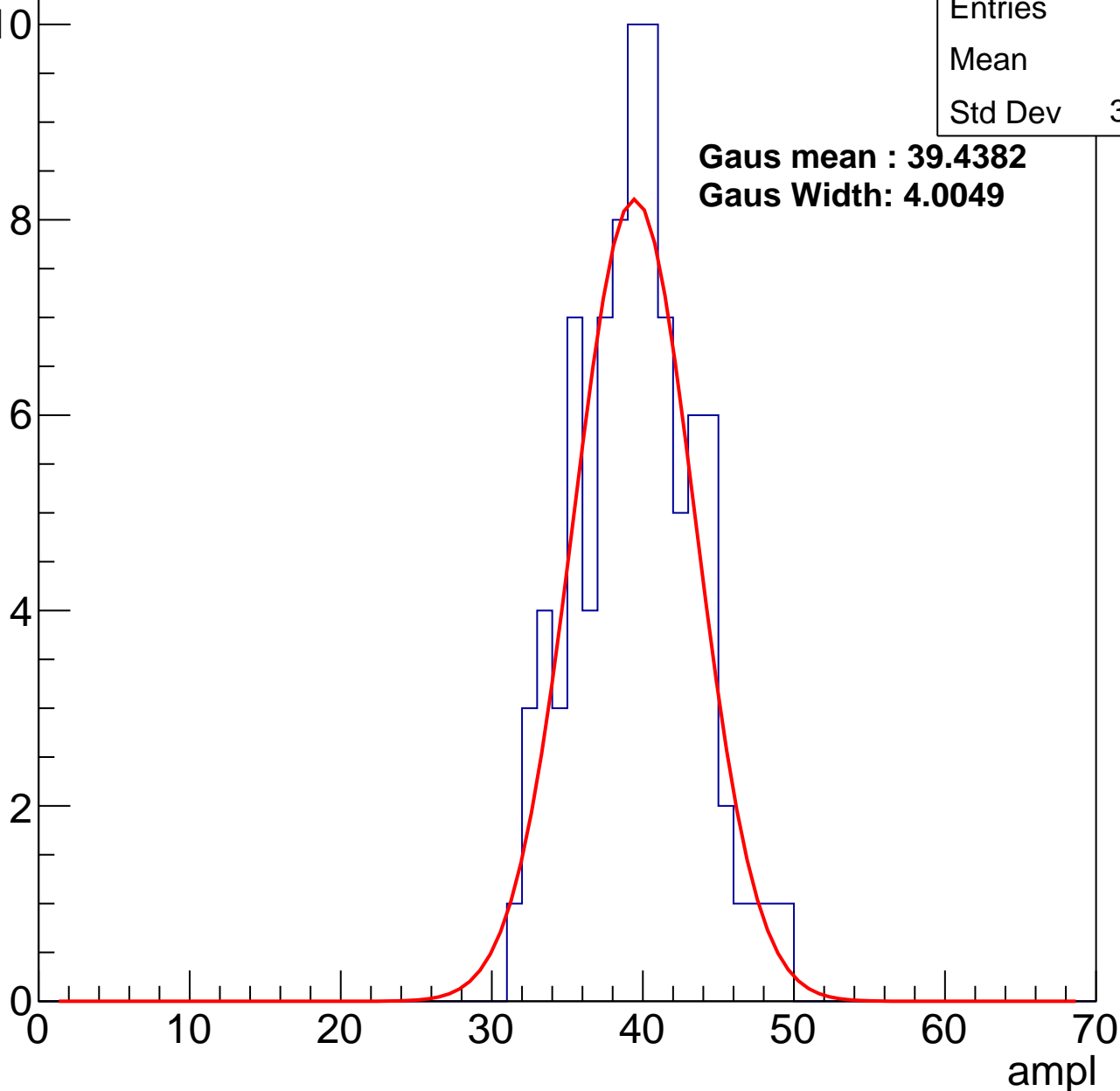
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	39.1
Std Dev	3.878

Gaus mean : 39.4382

Gaus Width: 4.0049



B0L001S, U23-ch43, adc2

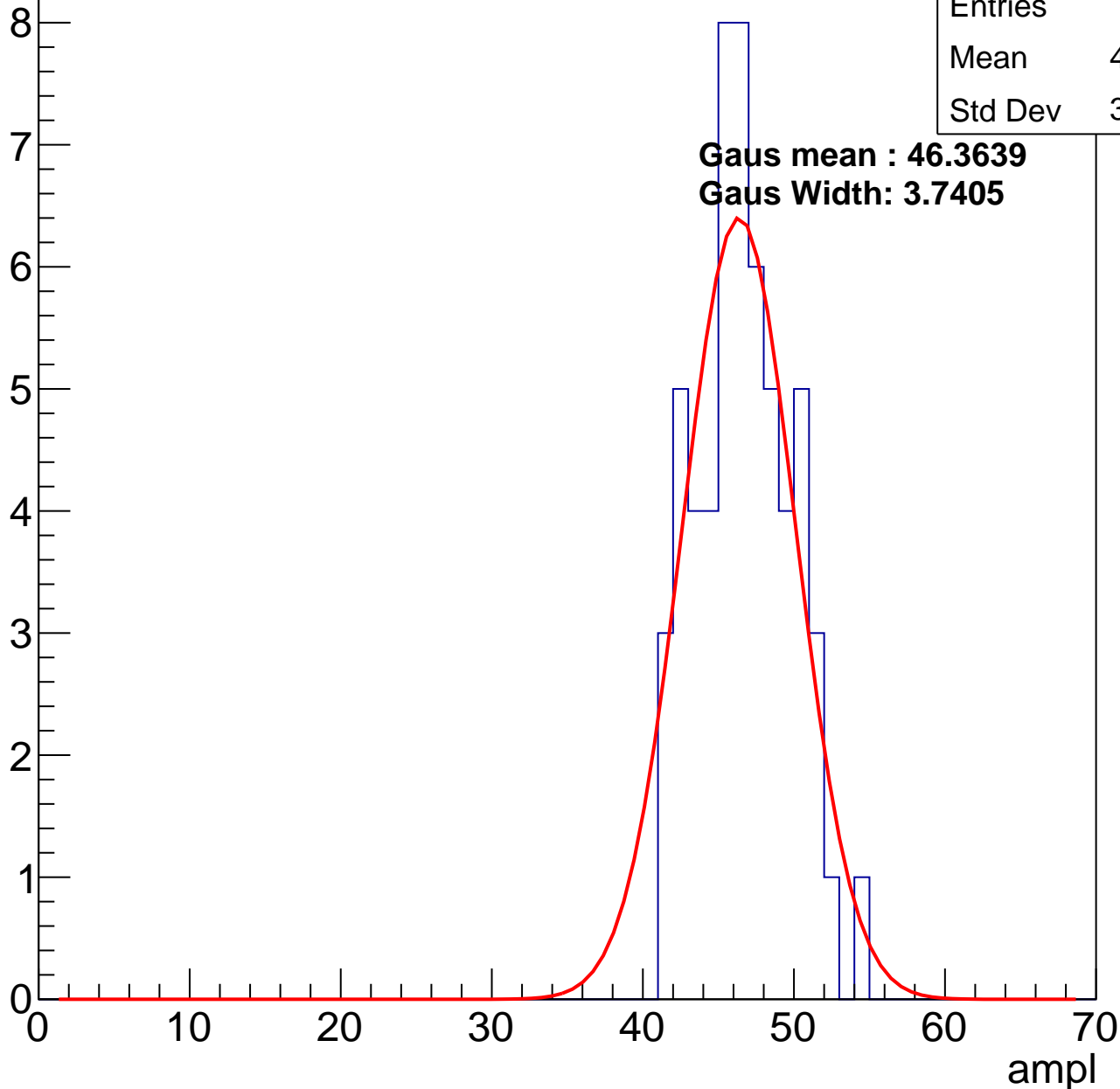
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	46.25
Std Dev	3.045

Gaus mean : 46.3639

Gaus Width: 3.7405

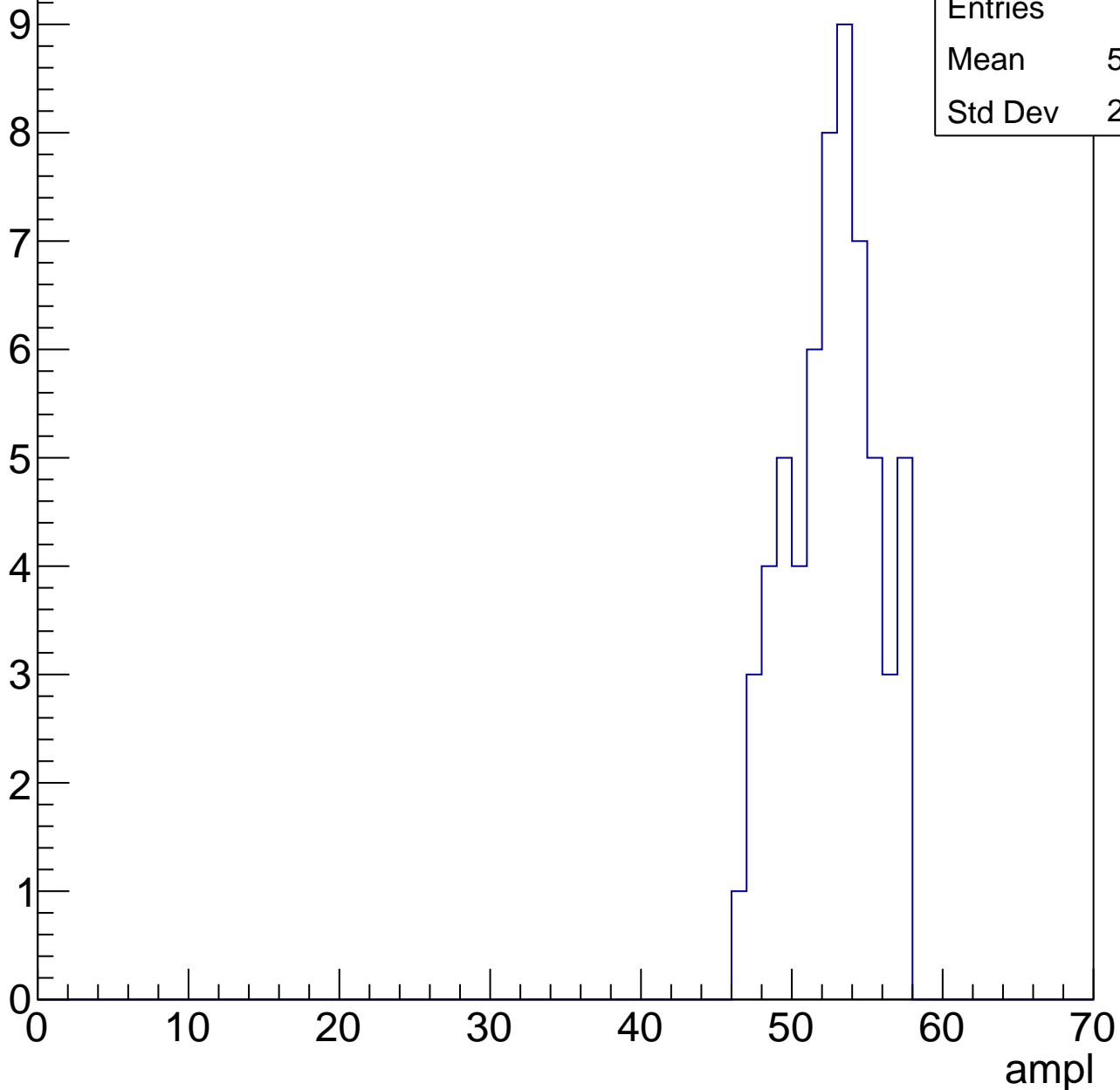


B0L001S, U23-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

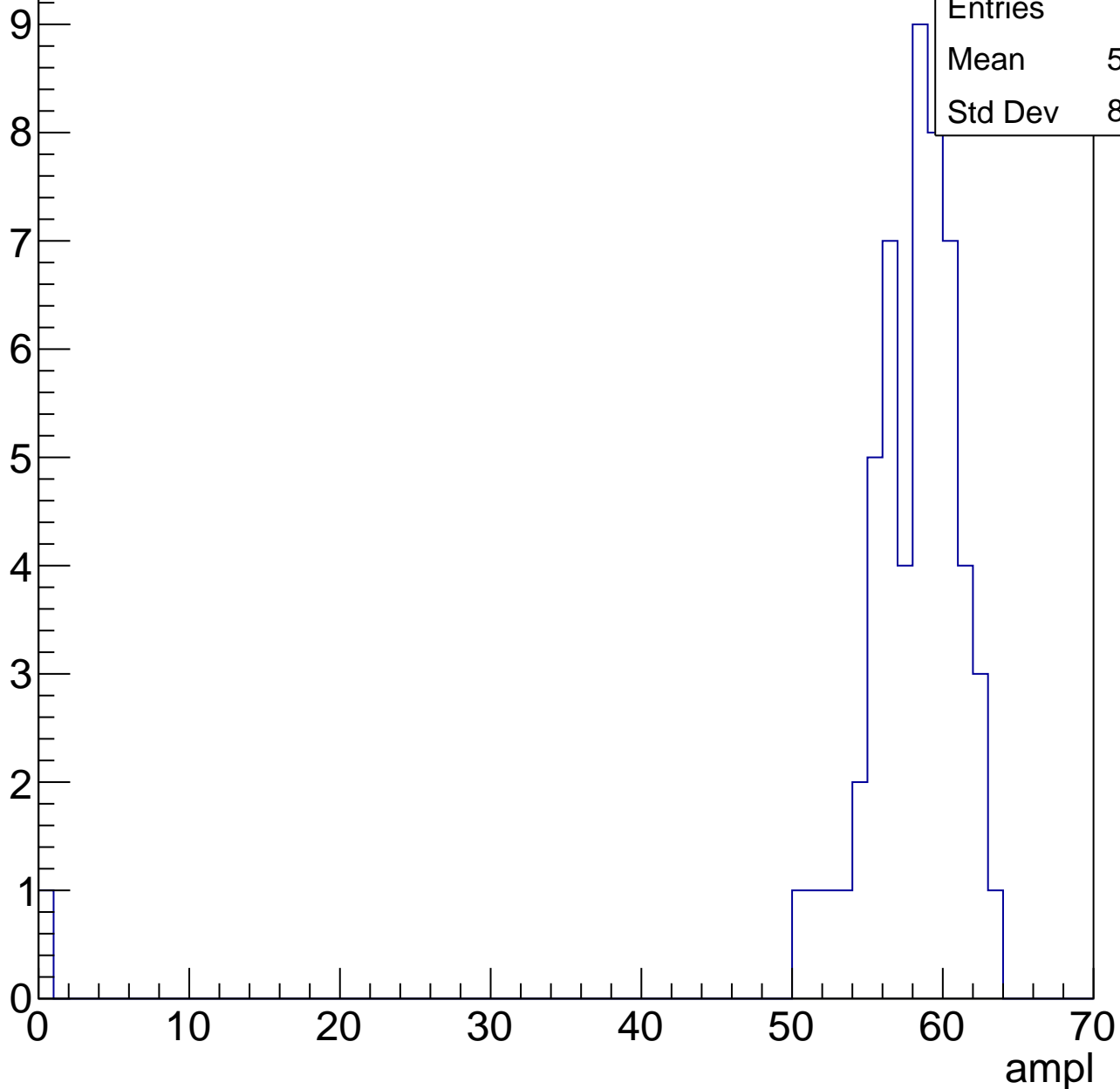
Entries	60
Mean	52.15
Std Dev	2.874



B0L001S, U23-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

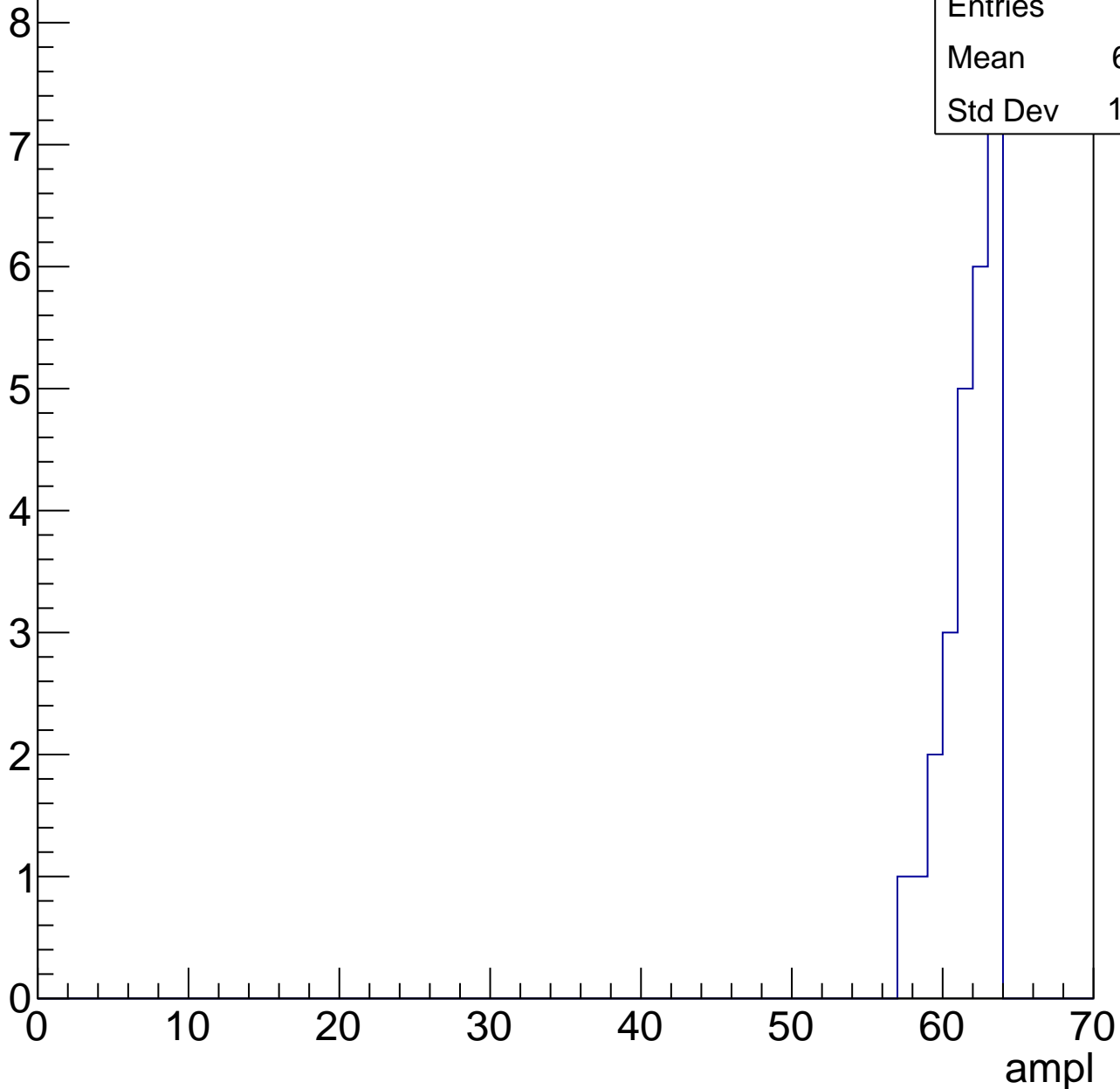


B0L001S, U23-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.31
Std Dev	1.659



B0L001S, U23-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch44, adc0

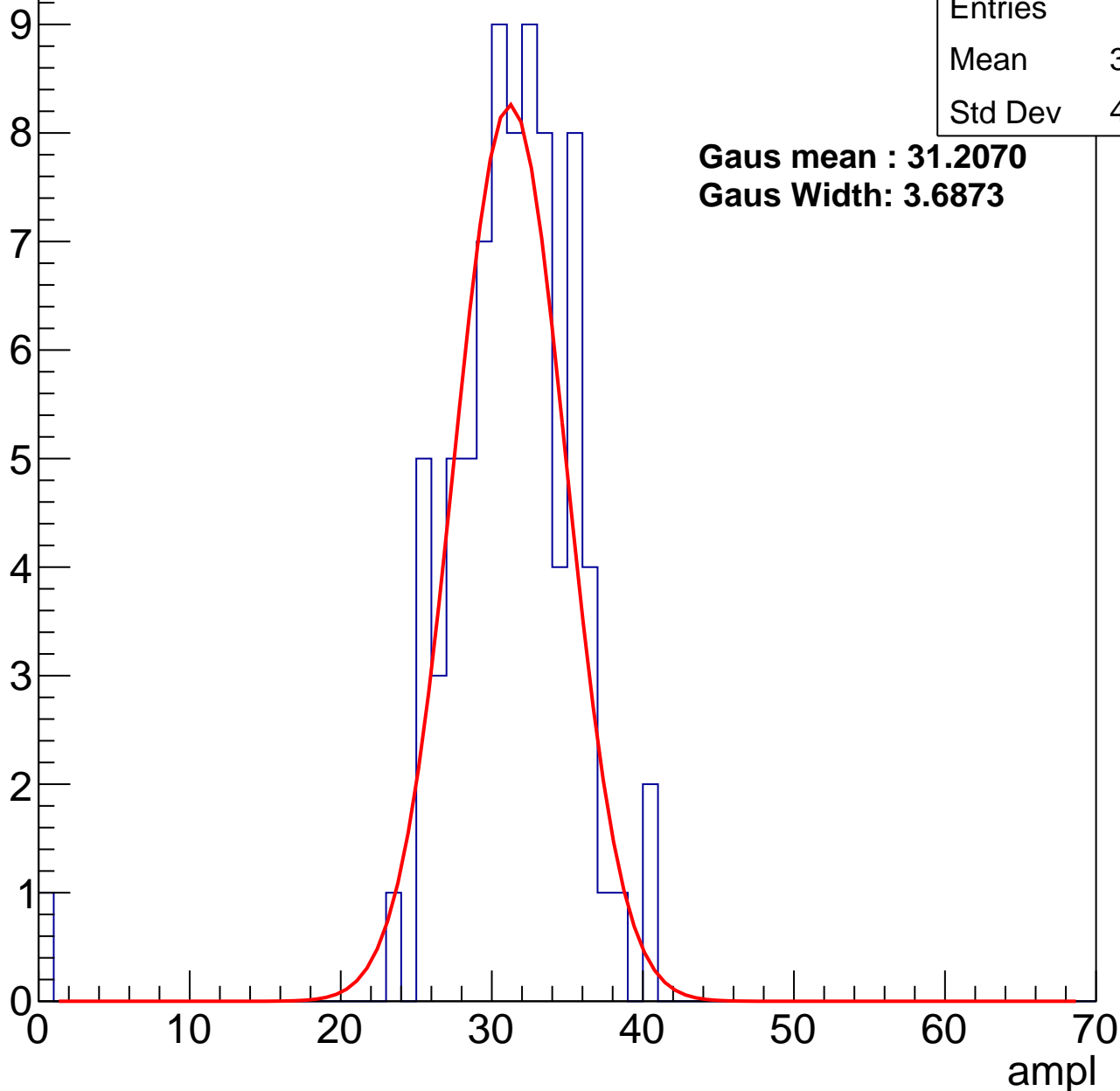
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.73
Std Dev	4.954

Gaus mean : 31.2070

Gaus Width: 3.6873



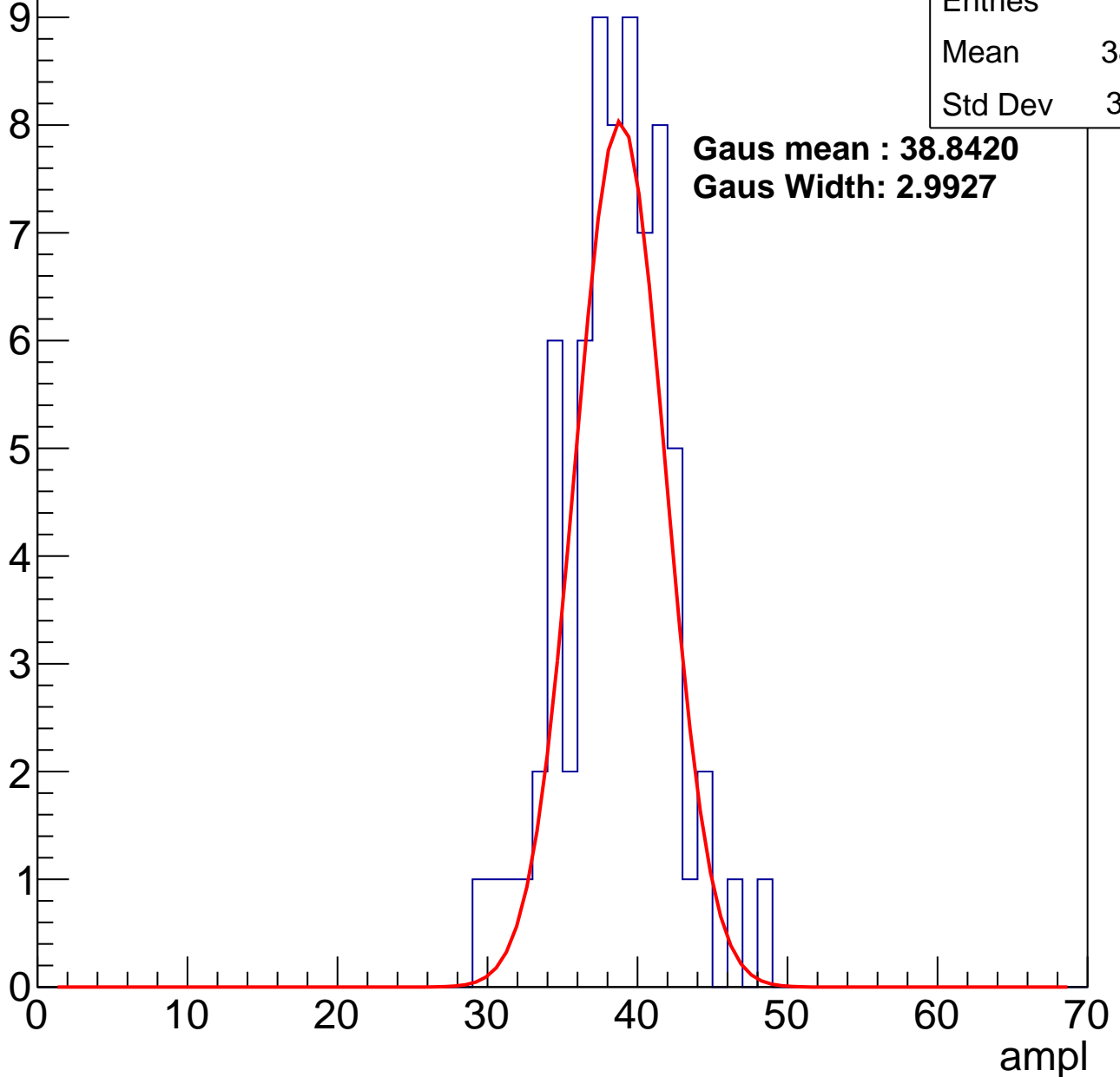
B0L001S, U23-ch44, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.15
Std Dev	3.511

Gaus mean : 38.8420
Gaus Width: 2.9927



B0L001S, U23-ch44, adc2

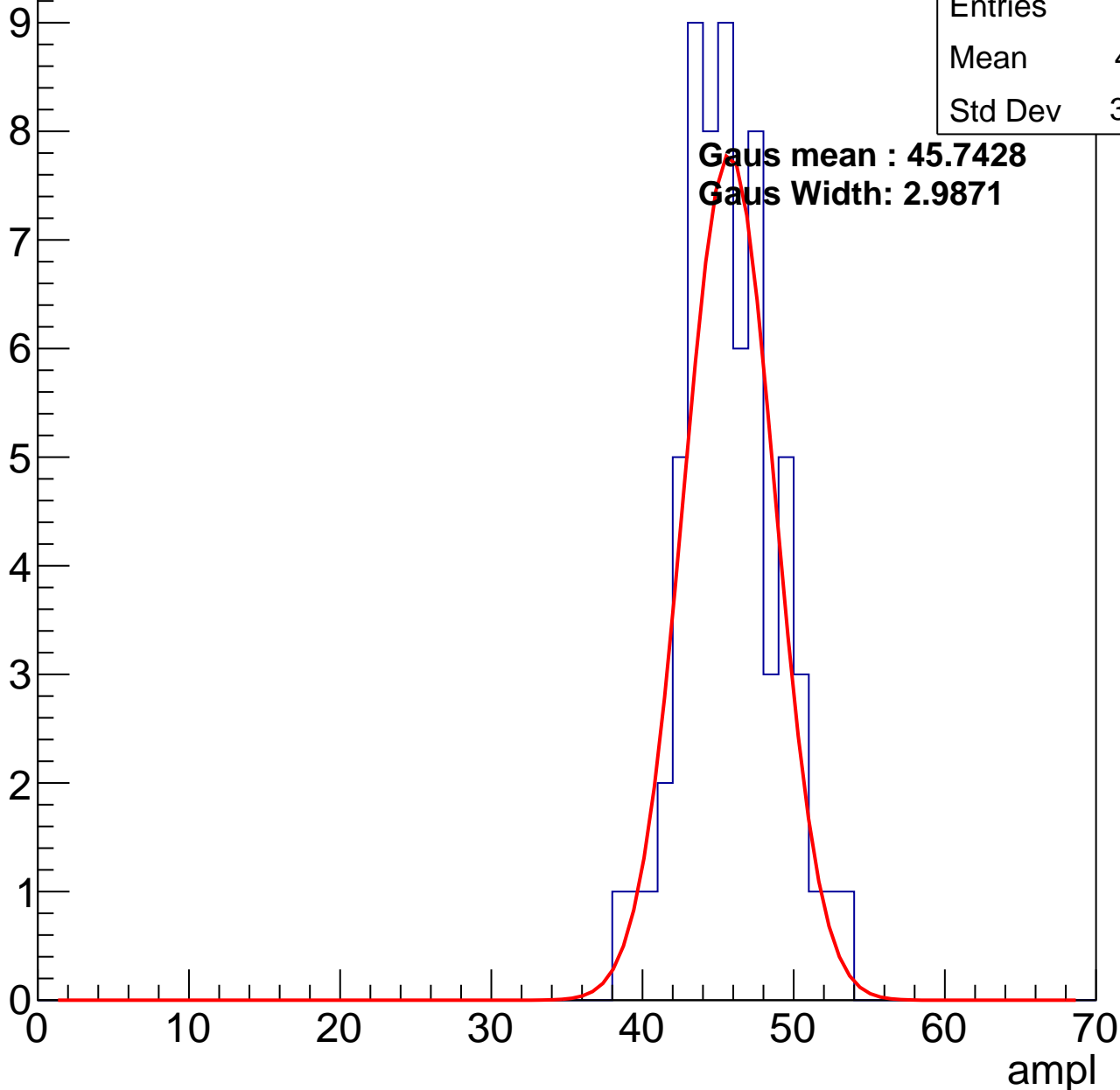
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.31
Std Dev	3.046

Gaus mean : 45.7428

Gaus Width: 2.9871

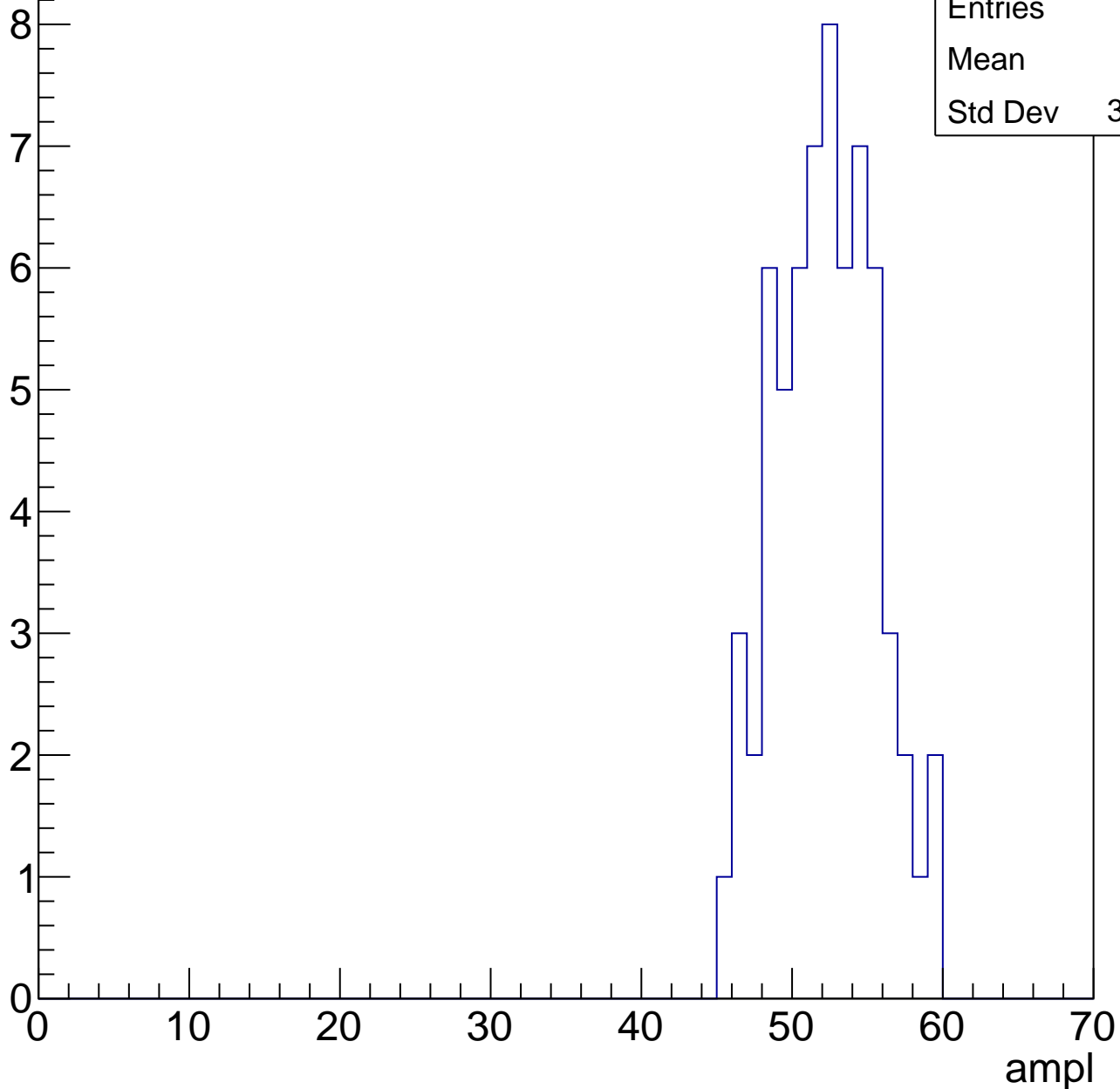


B0L001S, U23-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.8
Std Dev	3.273

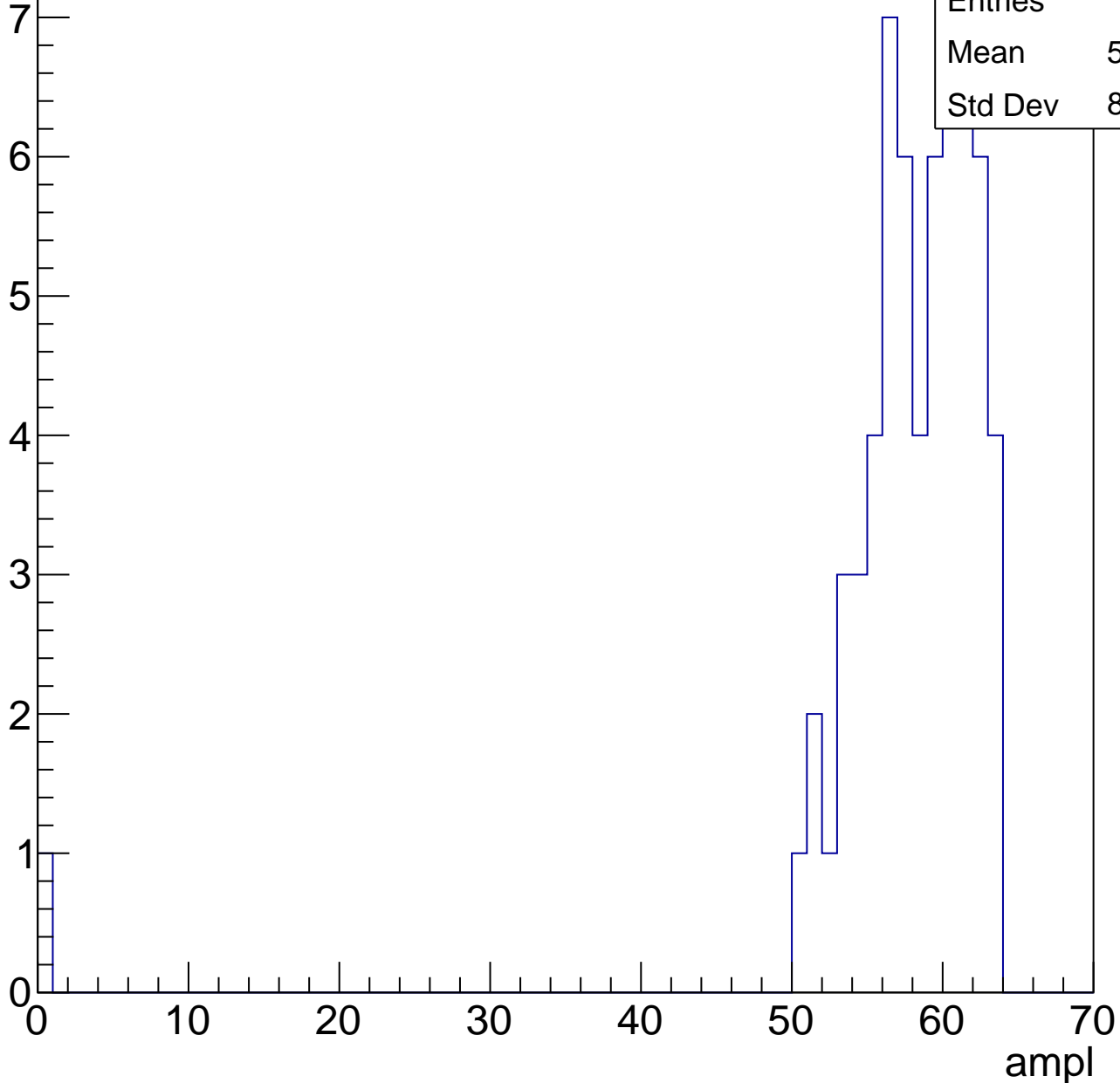


B0L001S, U23-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	57.03
Std Dev	8.022

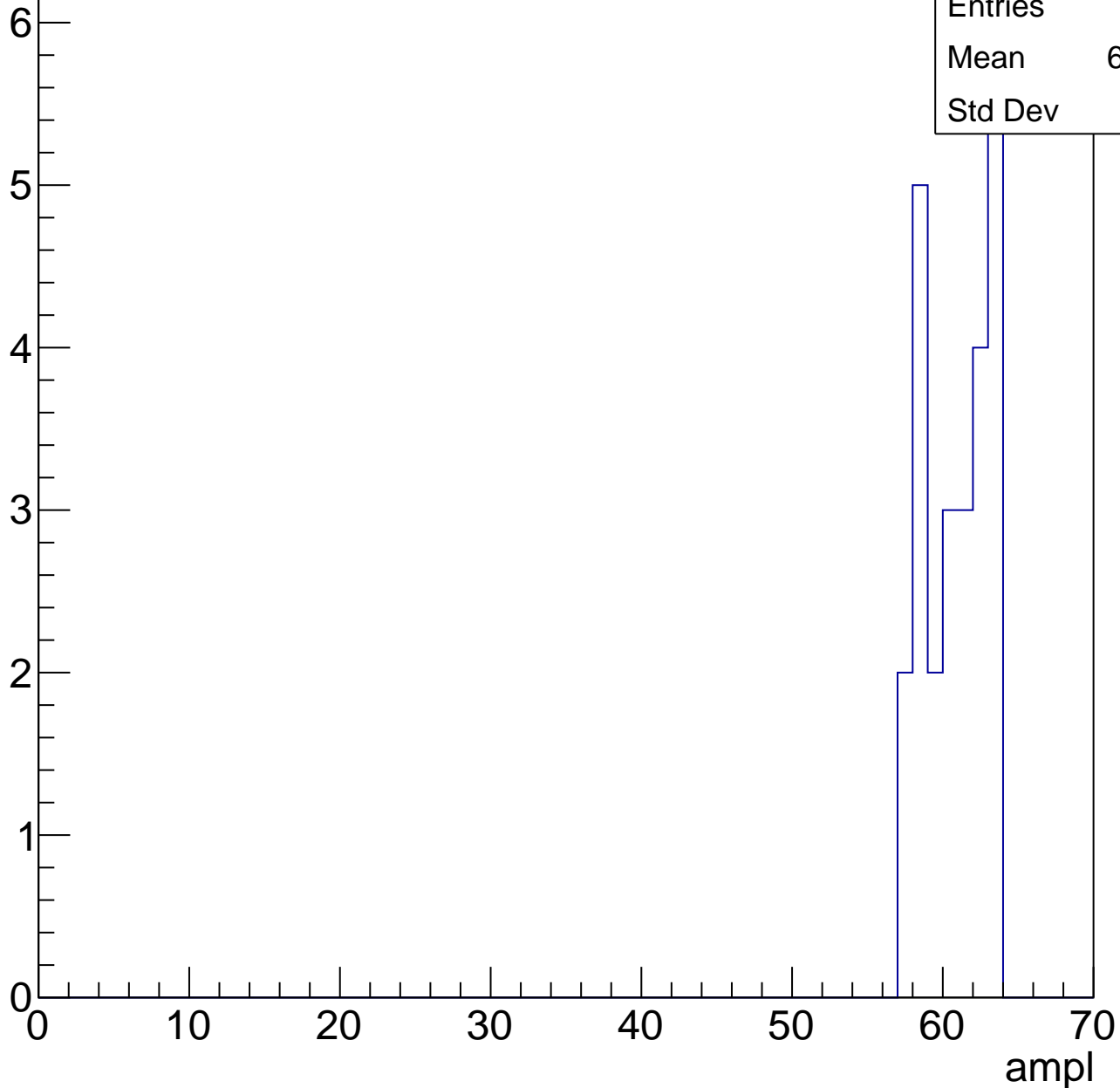


B0L001S, U23-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	60.44
Std Dev	2.08



B0L001S, U23-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch45, adc0

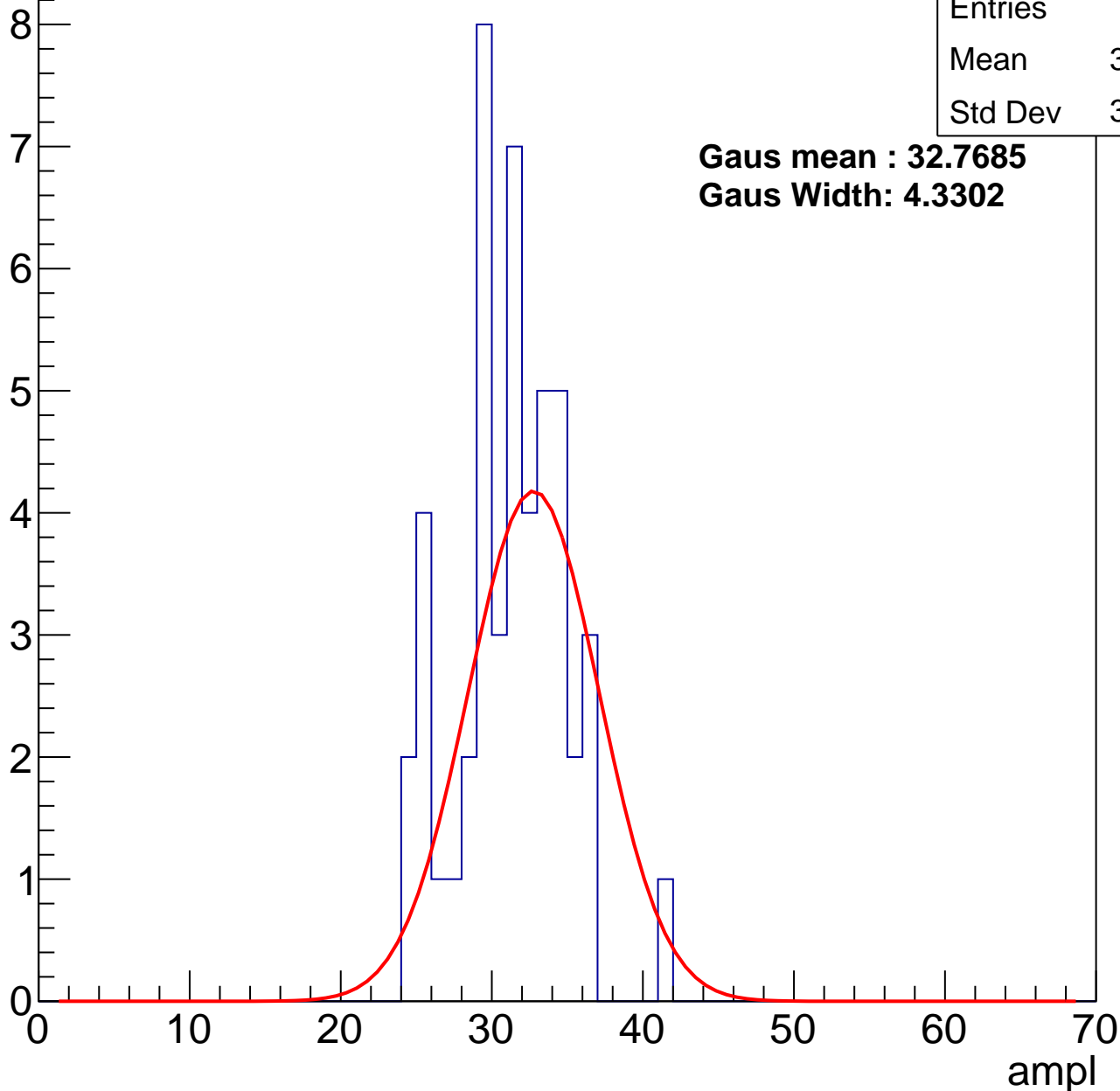
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	30.79
Std Dev	3.565

Gaus mean : 32.7685

Gaus Width: 4.3302



B0L001S, U23-ch45, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	37.14
Std Dev	3.337

Gaus mean : 37.6885

Gaus Width: 3.4278

Entry

10

8

6

4

2

0

0

10

20

30

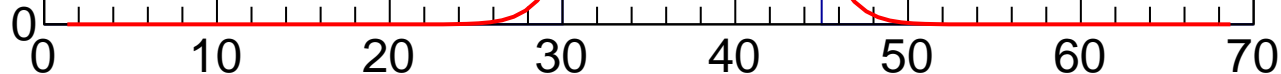
40

50

60

70

ampl



B0L001S, U23-ch45, adc2

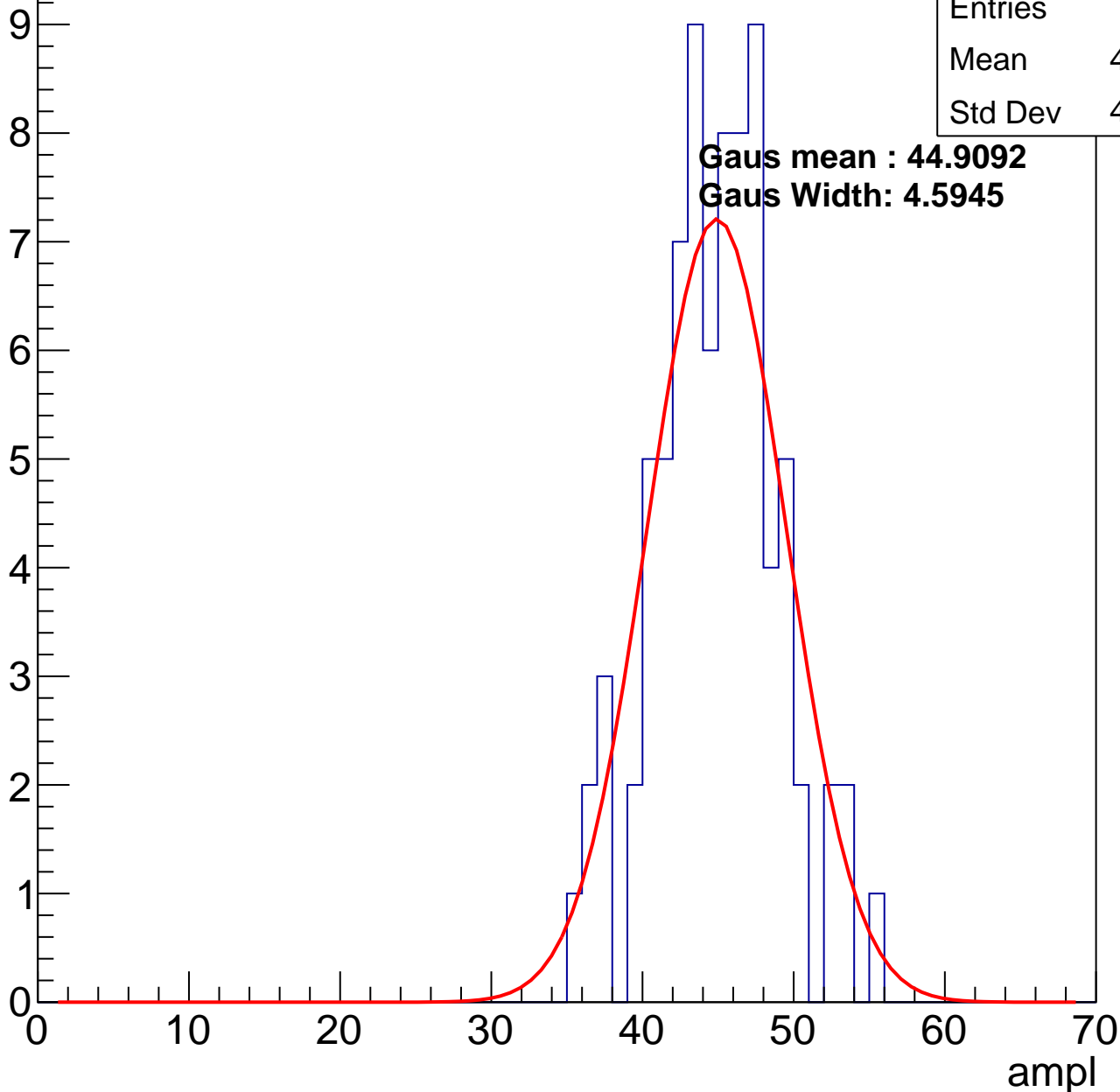
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	44.43
Std Dev	4.076

Gaus mean : 44.9092

Gaus Width: 4.5945

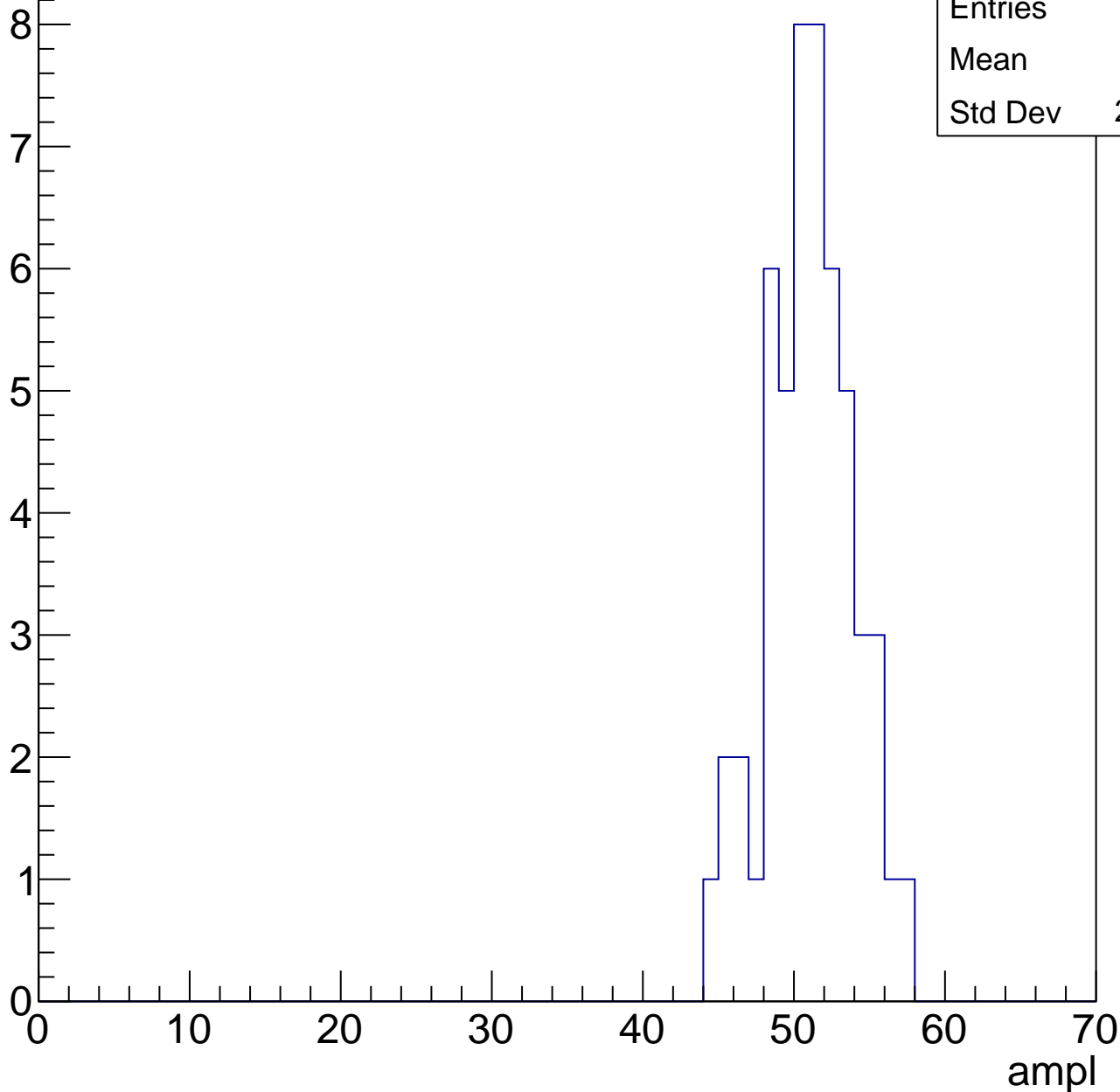


B0L001S, U23-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

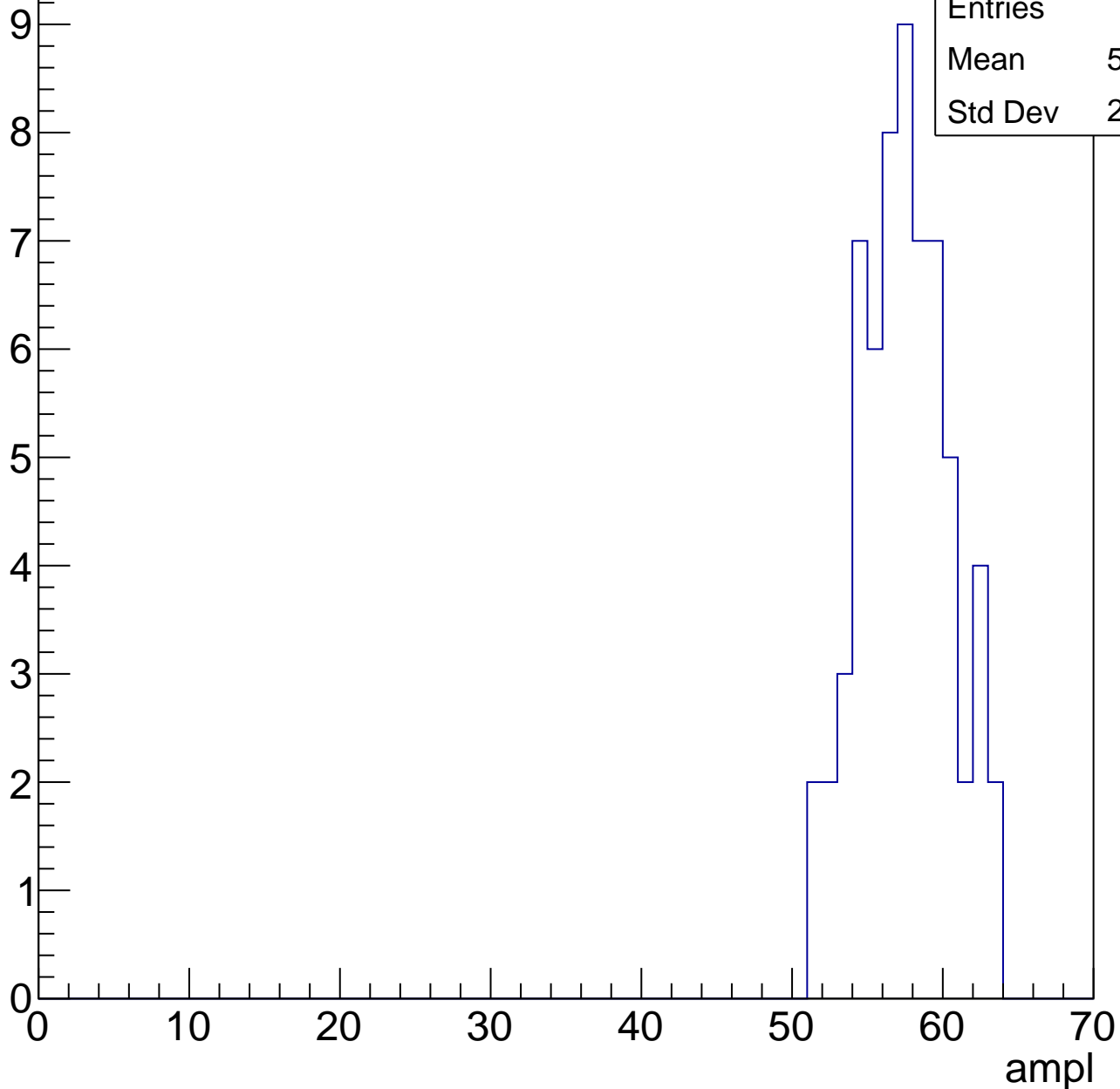
Entries	52
Mean	50.6
Std Dev	2.851



B0L001S, U23-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



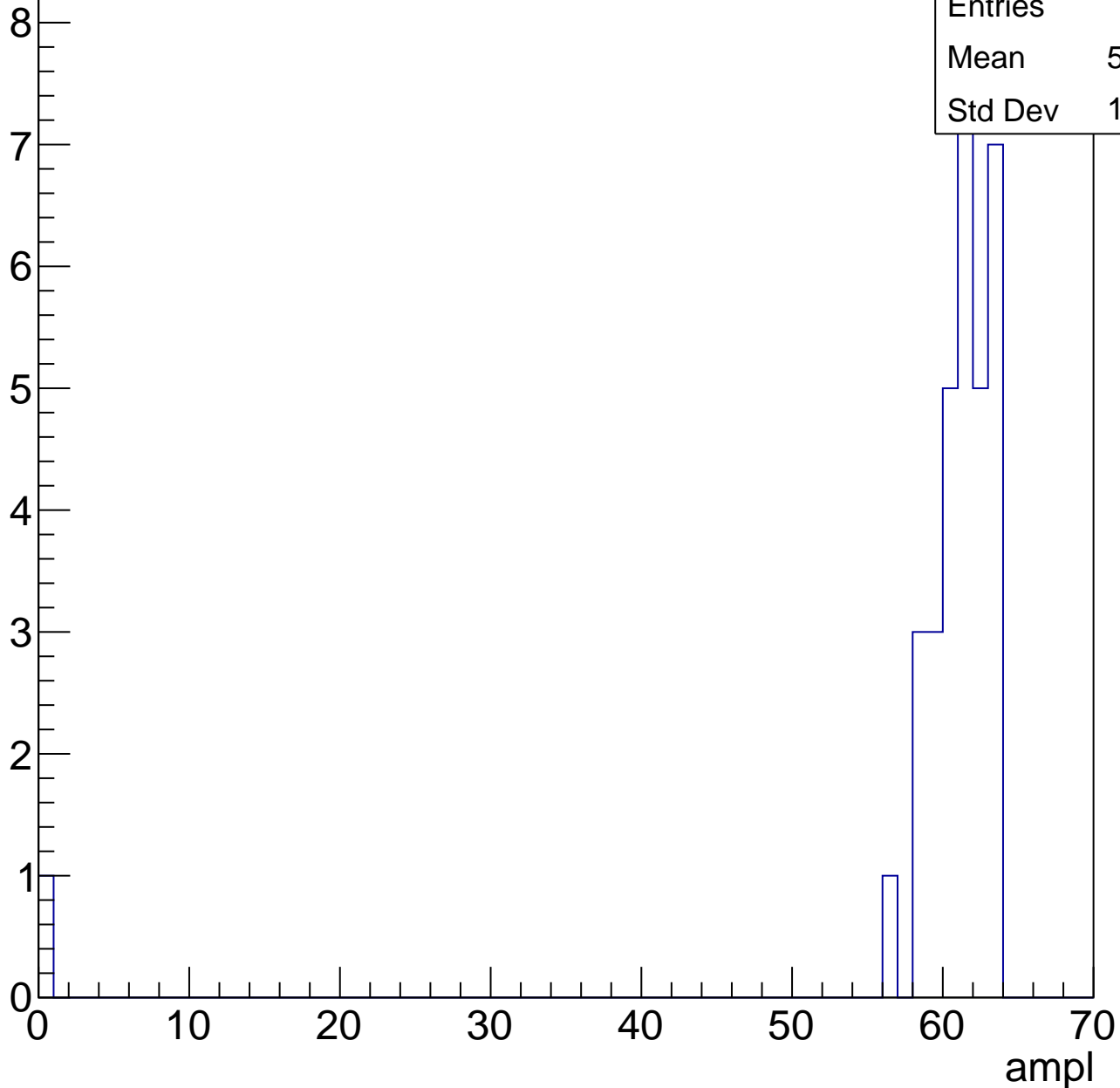
Entries	64
Mean	57.02
Std Dev	2.929

B0L001S, U23-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.97
Std Dev	10.57



B0L001S, U23-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

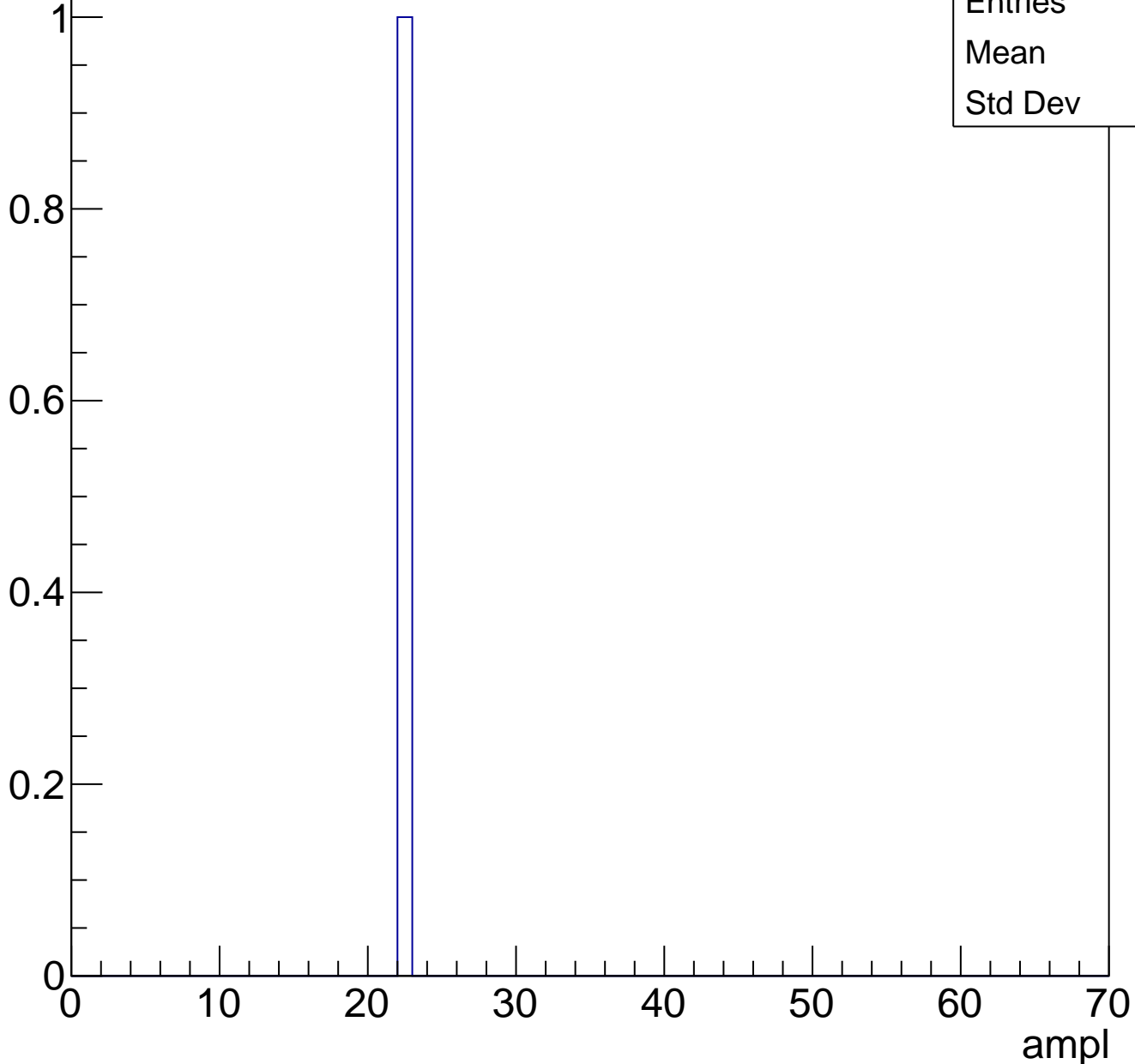
Entry



B0L001S, U23-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	22
Std Dev	0

B0L001S, U23-ch46, adc0

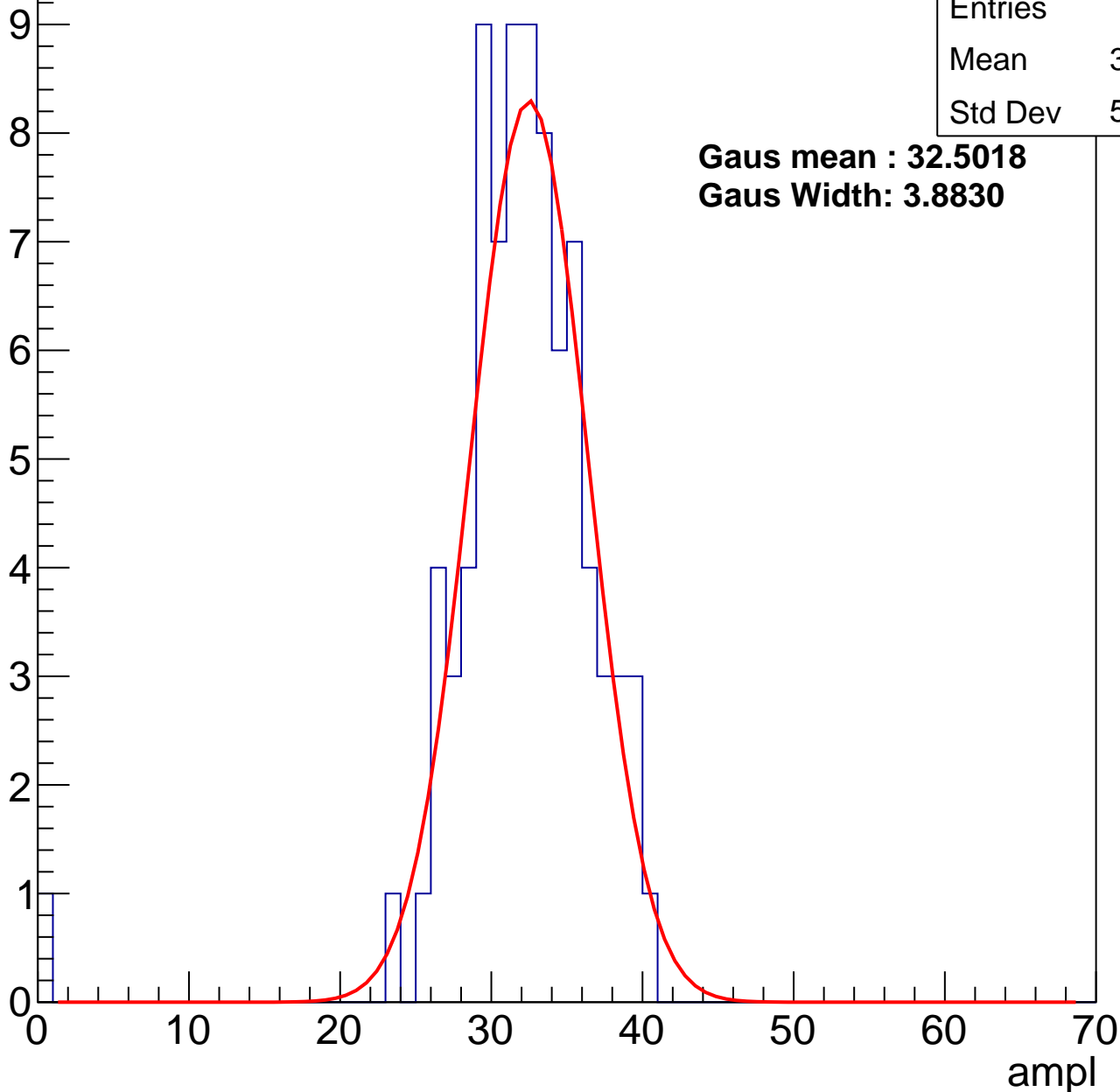
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	31.59
Std Dev	5.023

Gaus mean : 32.5018

Gaus Width: 3.8830



B0L001S, U23-ch46, adc1

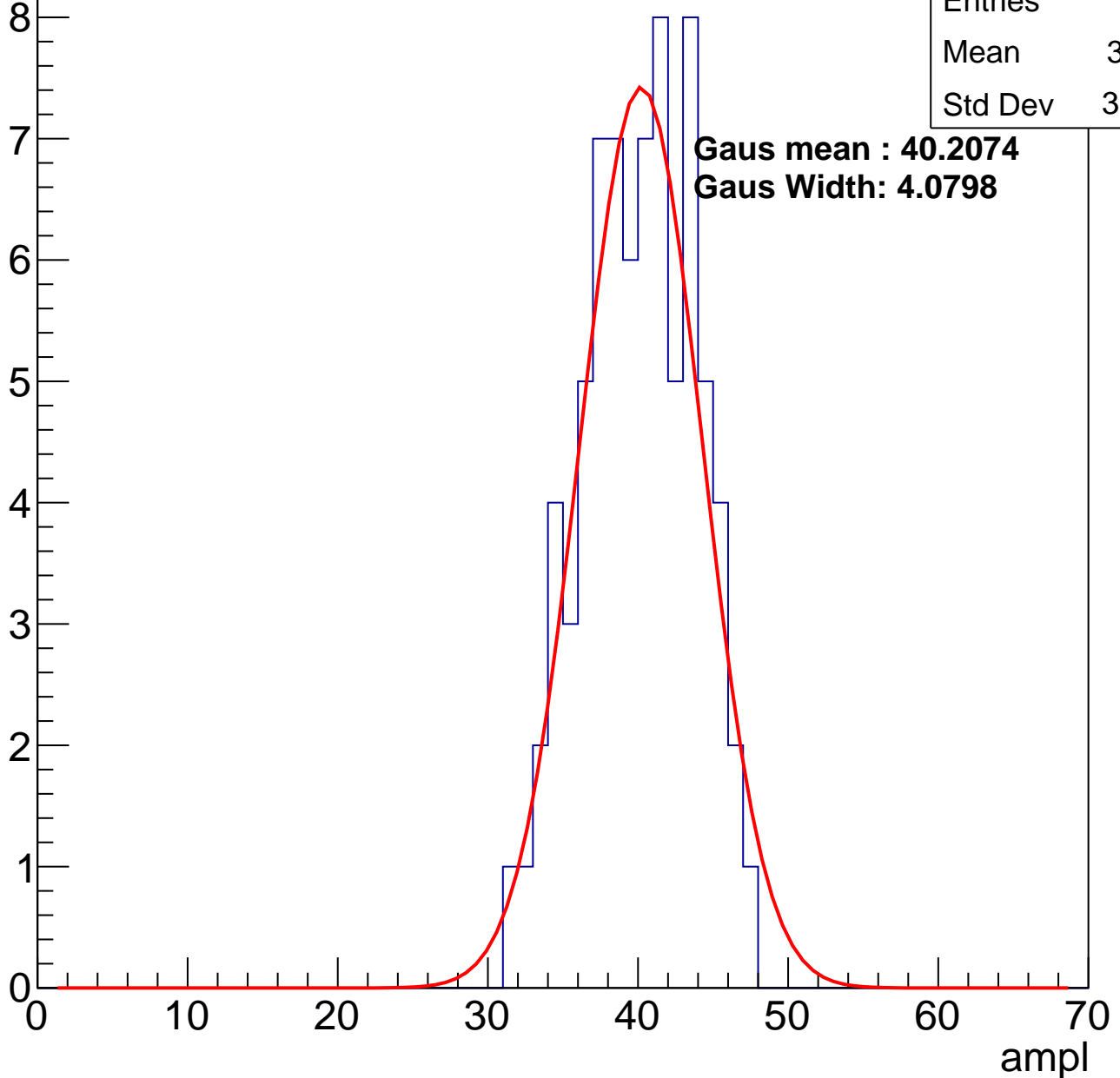
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	39.61
Std Dev	3.678

Gaus mean : 40.2074

Gaus Width: 4.0798



B0L001S, U23-ch46, adc2

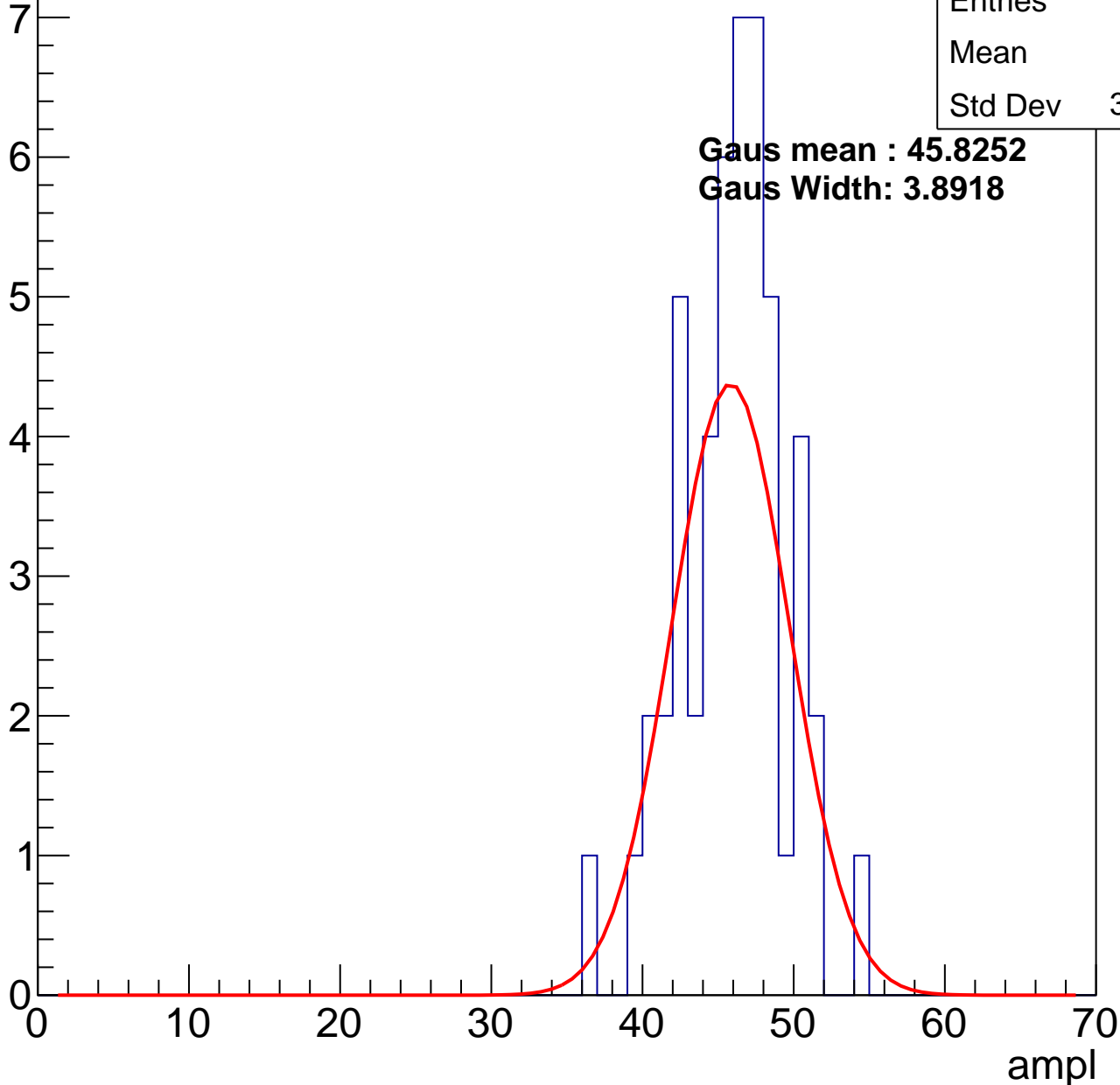
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	45.5
Std Dev	3.442

Gaus mean : 45.8252

Gaus Width: 3.8918

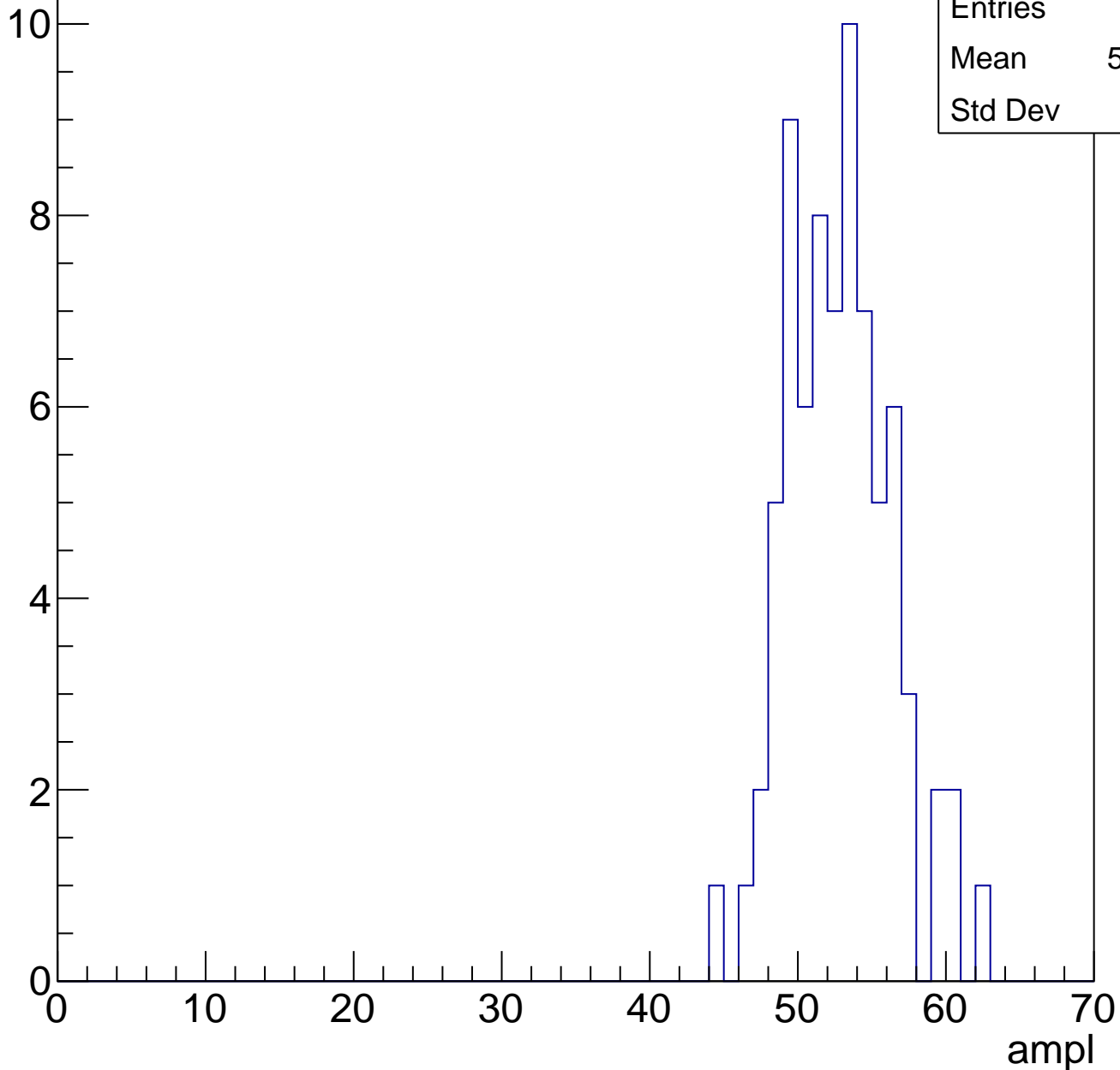


B0L001S, U23-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	52.36
Std Dev	3.49

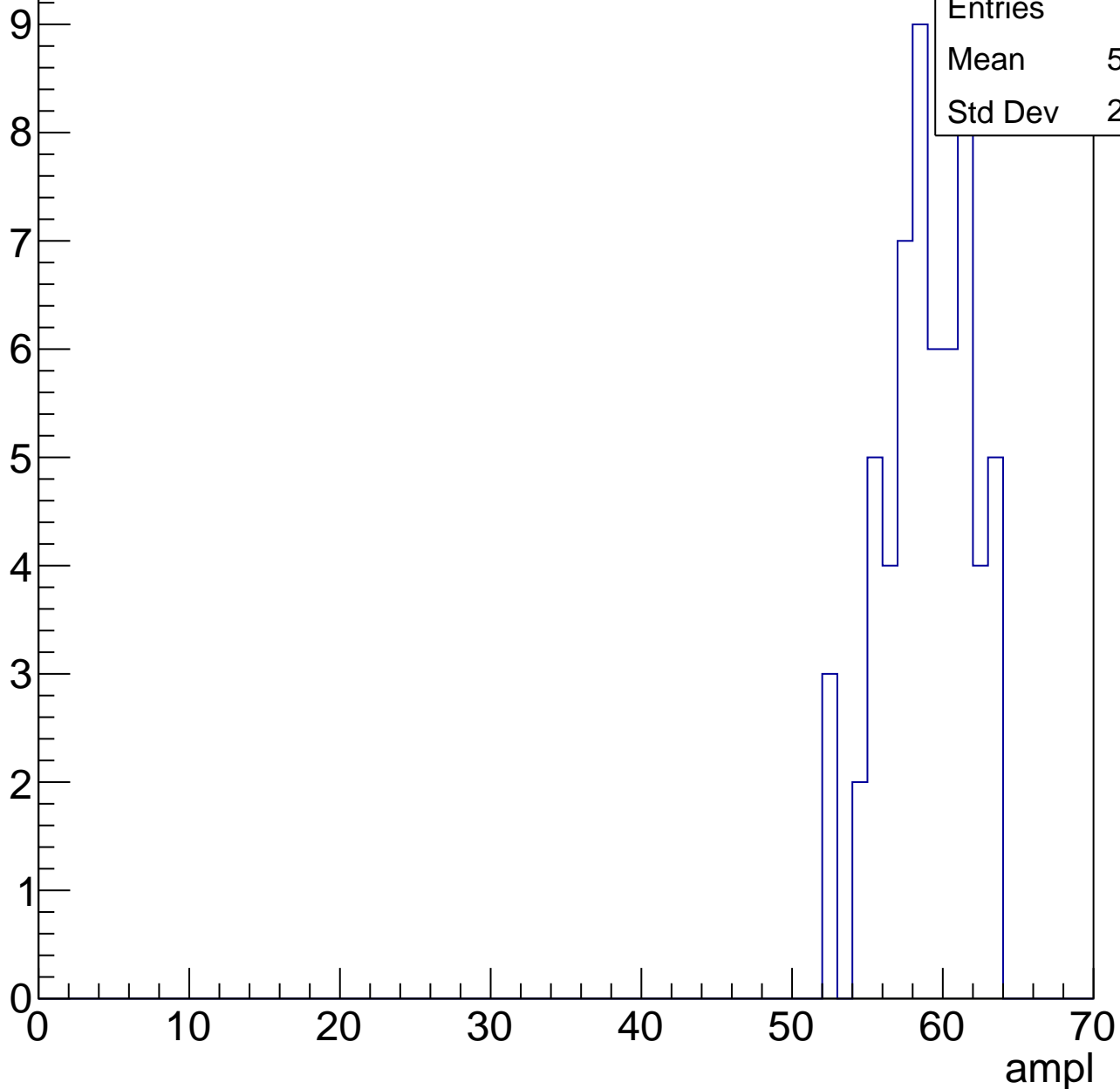
Entry



B0L001S, U23-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



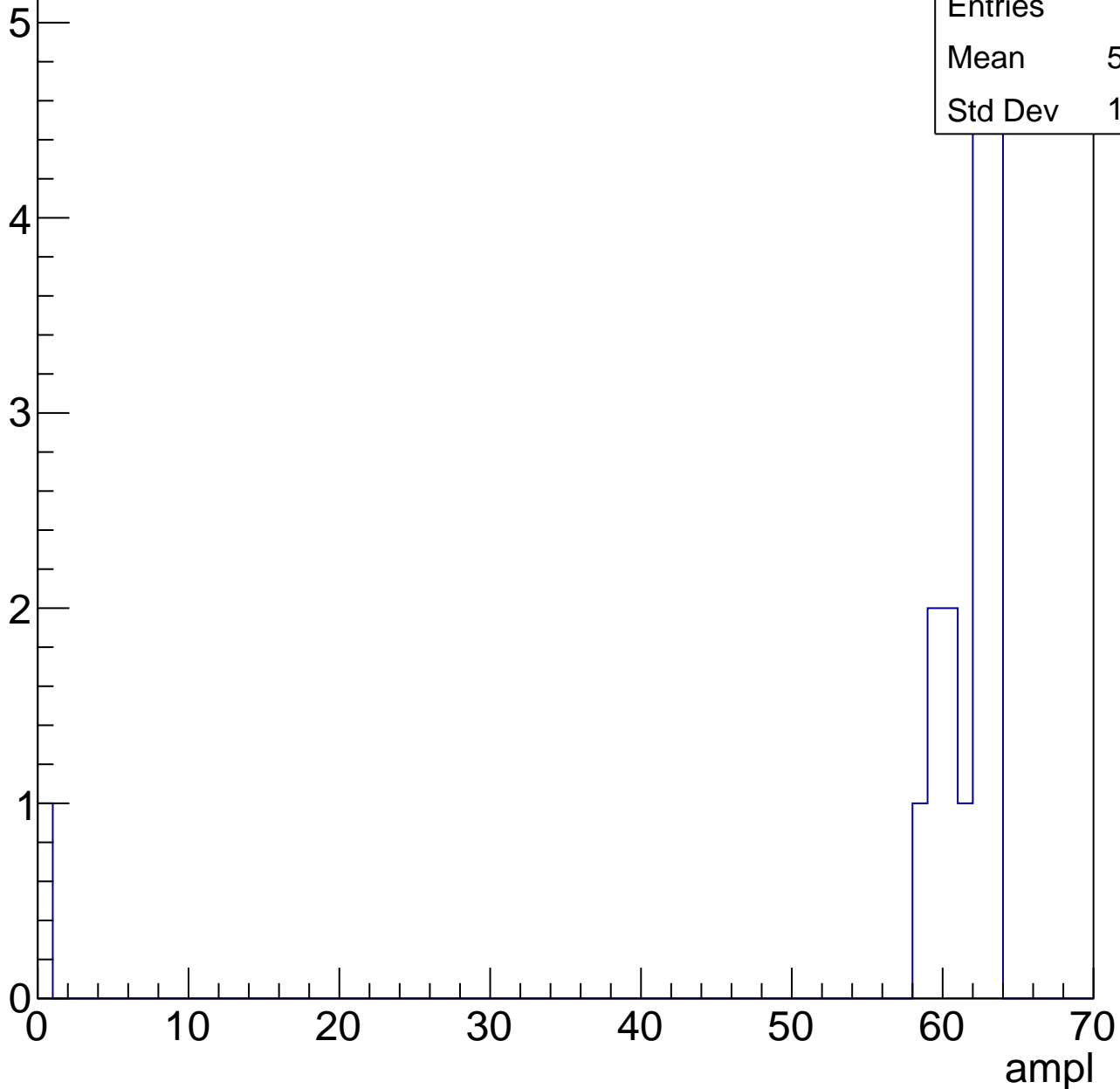
Entries	59
Mean	58.46
Std Dev	2.872

B0L001S, U23-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	17
Mean	57.76
Std Dev	14.53



B0L001S, U23-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch47, adc0

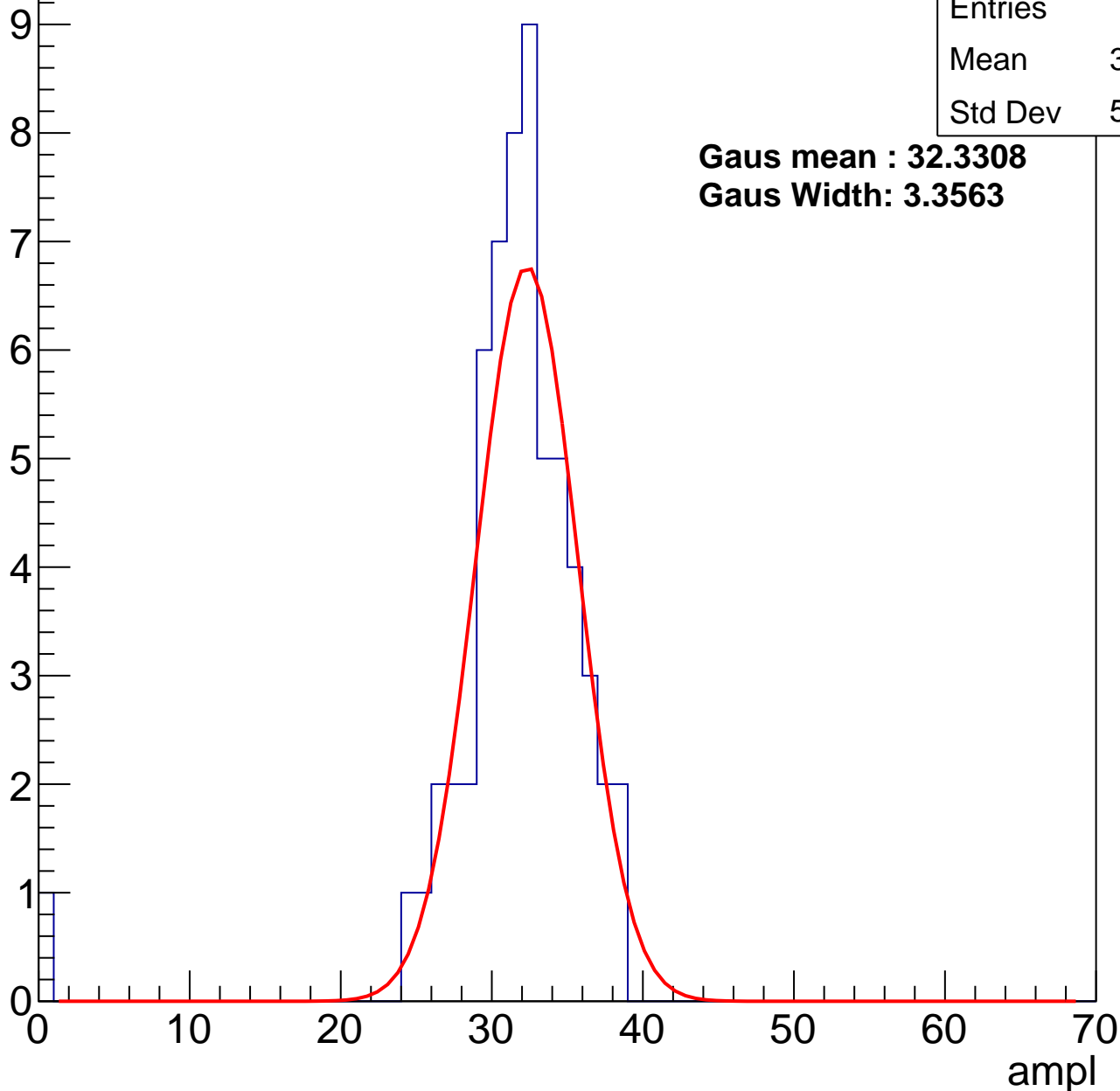
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.07
Std Dev	5.099

Gaus mean : 32.3308

Gaus Width: 3.3563



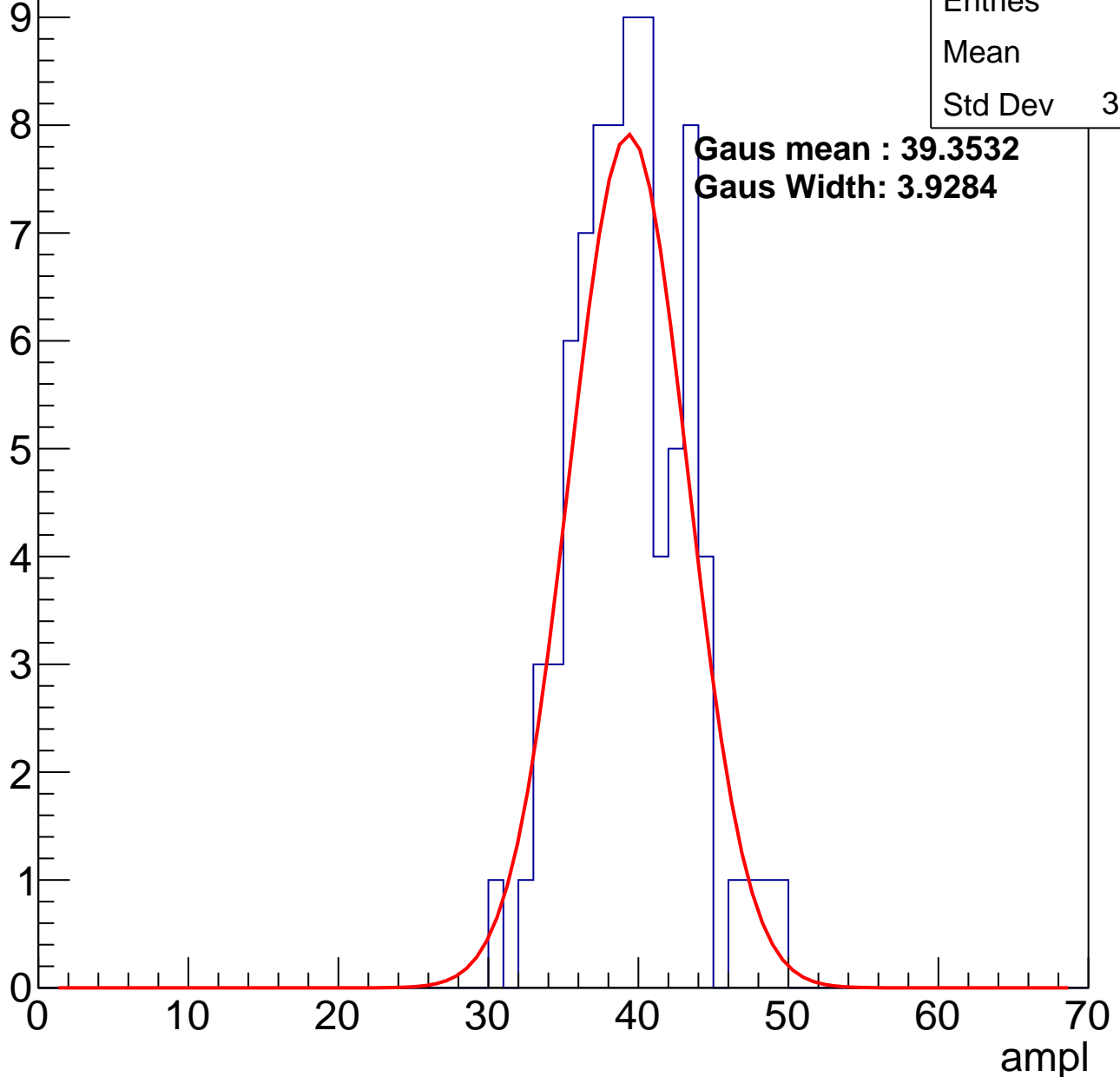
B0L001S, U23-ch47, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	39
Std Dev	3.715

Gaus mean : 39.3532
Gaus Width: 3.9284



B0L001S, U23-ch47, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	46.43
Std Dev	3.511

Gaus mean : 47.3110

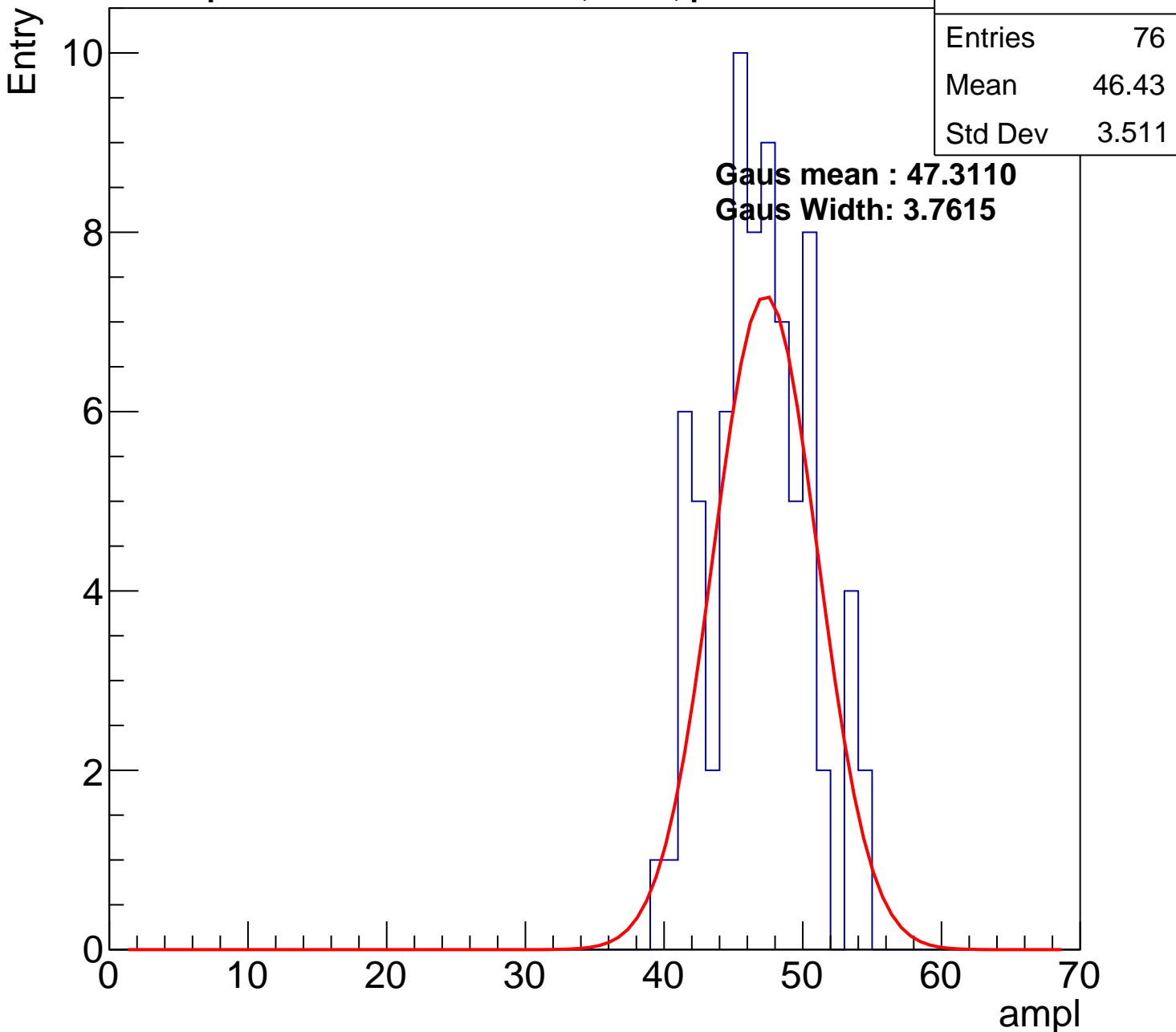
Gaus Width: 3.7615

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

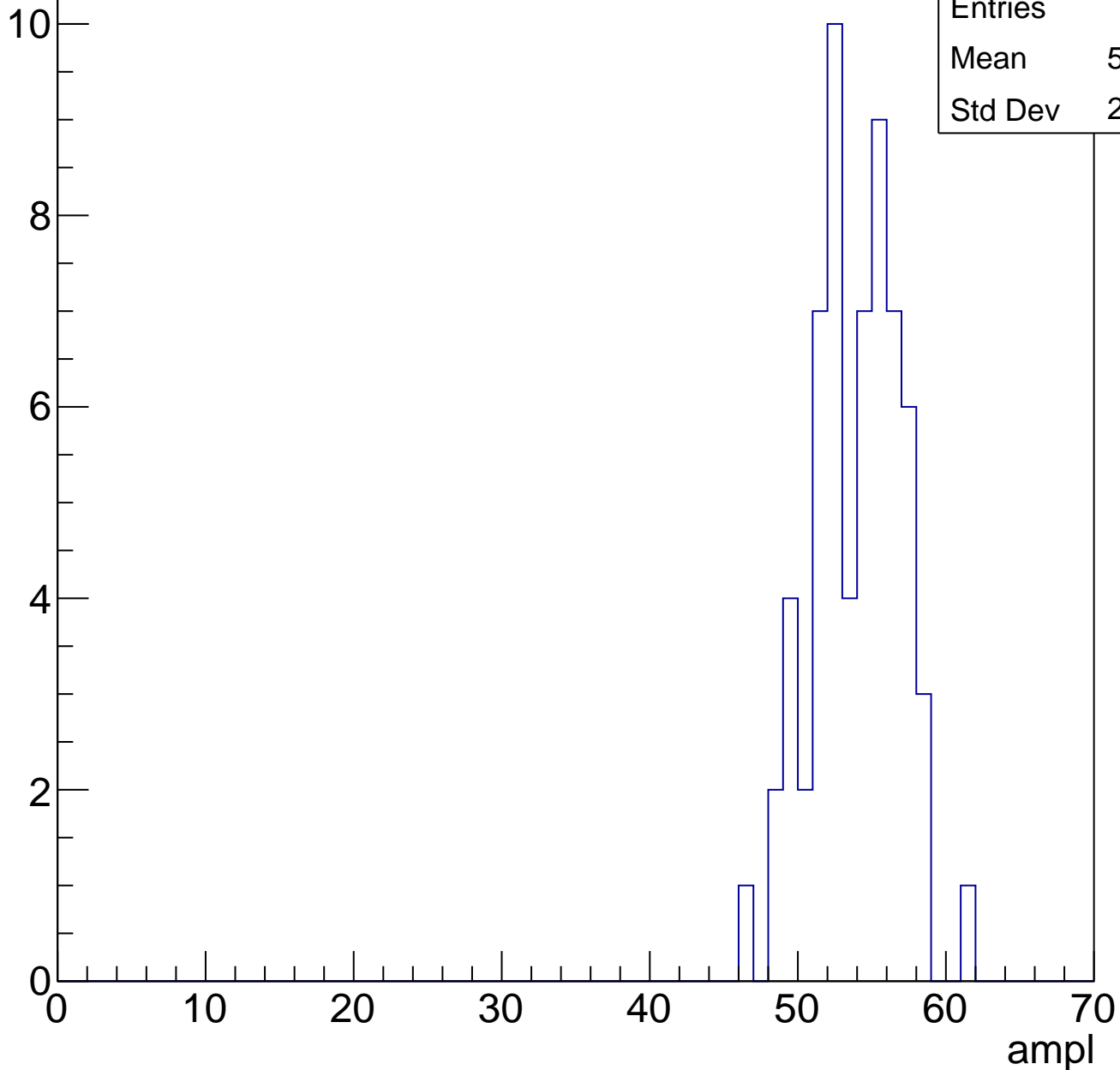


B0L001S, U23-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	53.48
Std Dev	2.943

Entry



B0L001S, U23-ch47, adc4

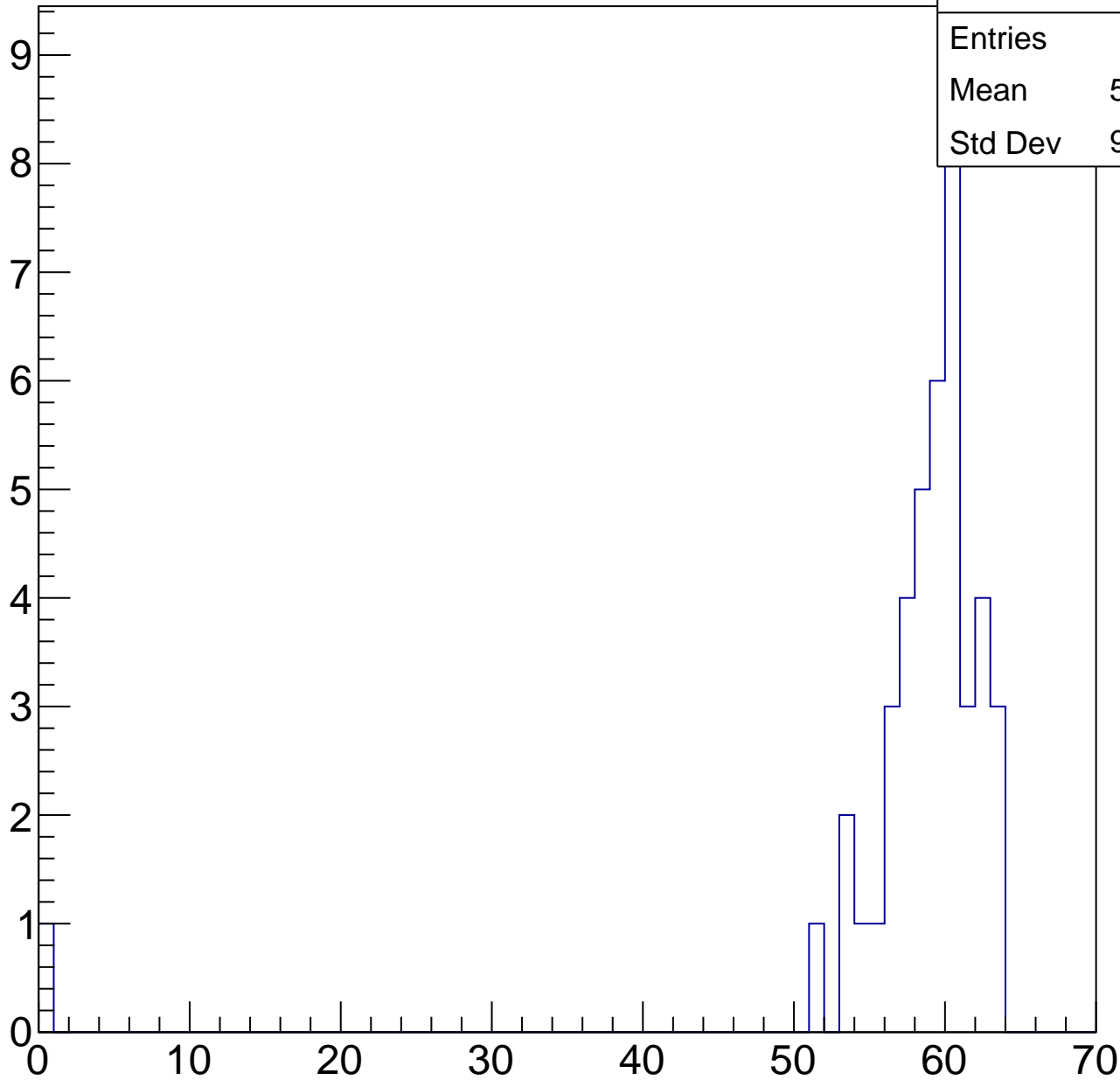
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	57.35
Std Dev	9.268

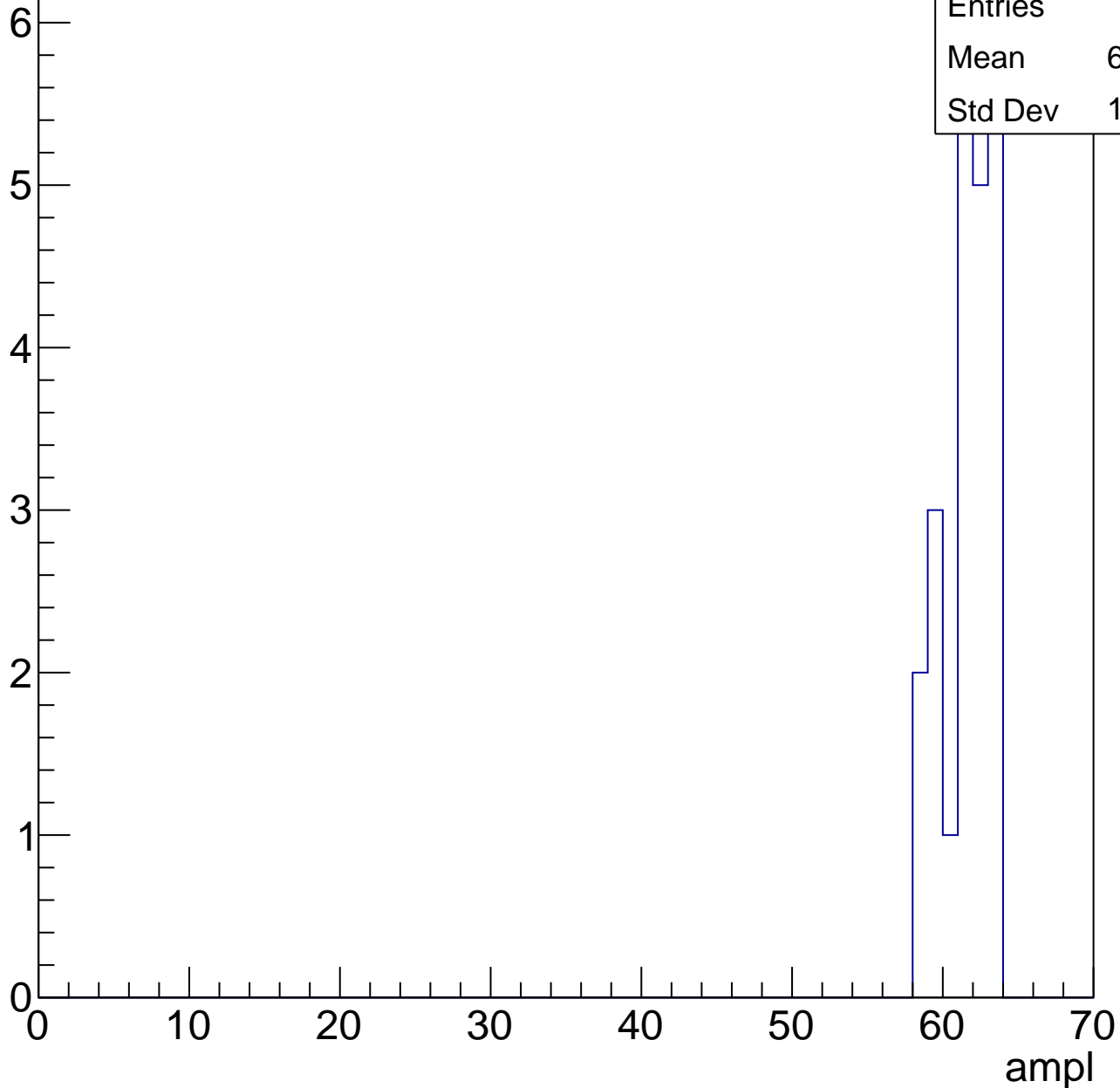
ampl



B0L001S, U23-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch48, adc0

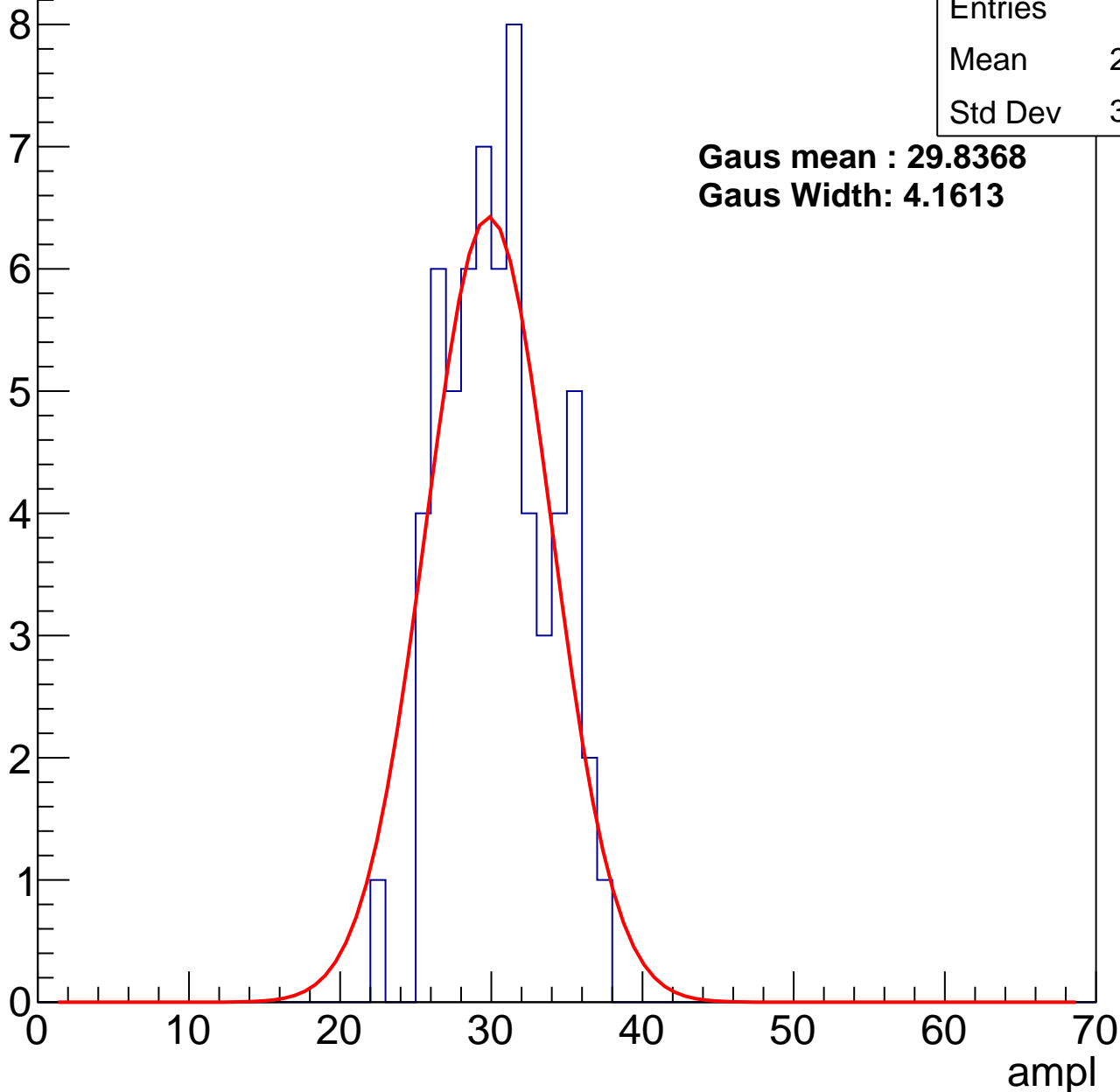
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.98
Std Dev	3.353

Gaus mean : 29.8368

Gaus Width: 4.1613



B0L001S, U23-ch48, adc1

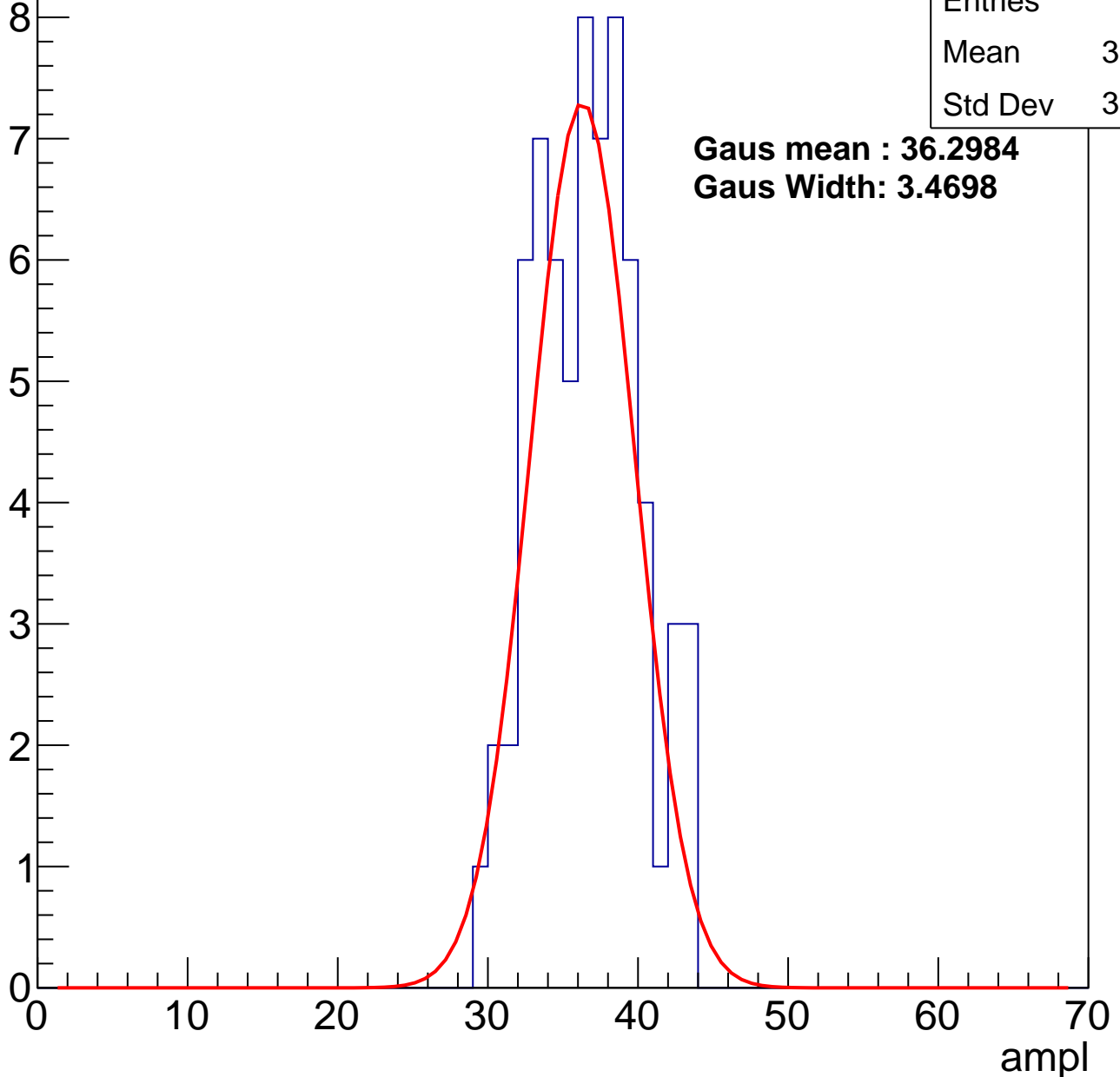
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36.14
Std Dev	3.393

Gaus mean : 36.2984

Gaus Width: 3.4698



B0L001S, U23-ch48, adc2

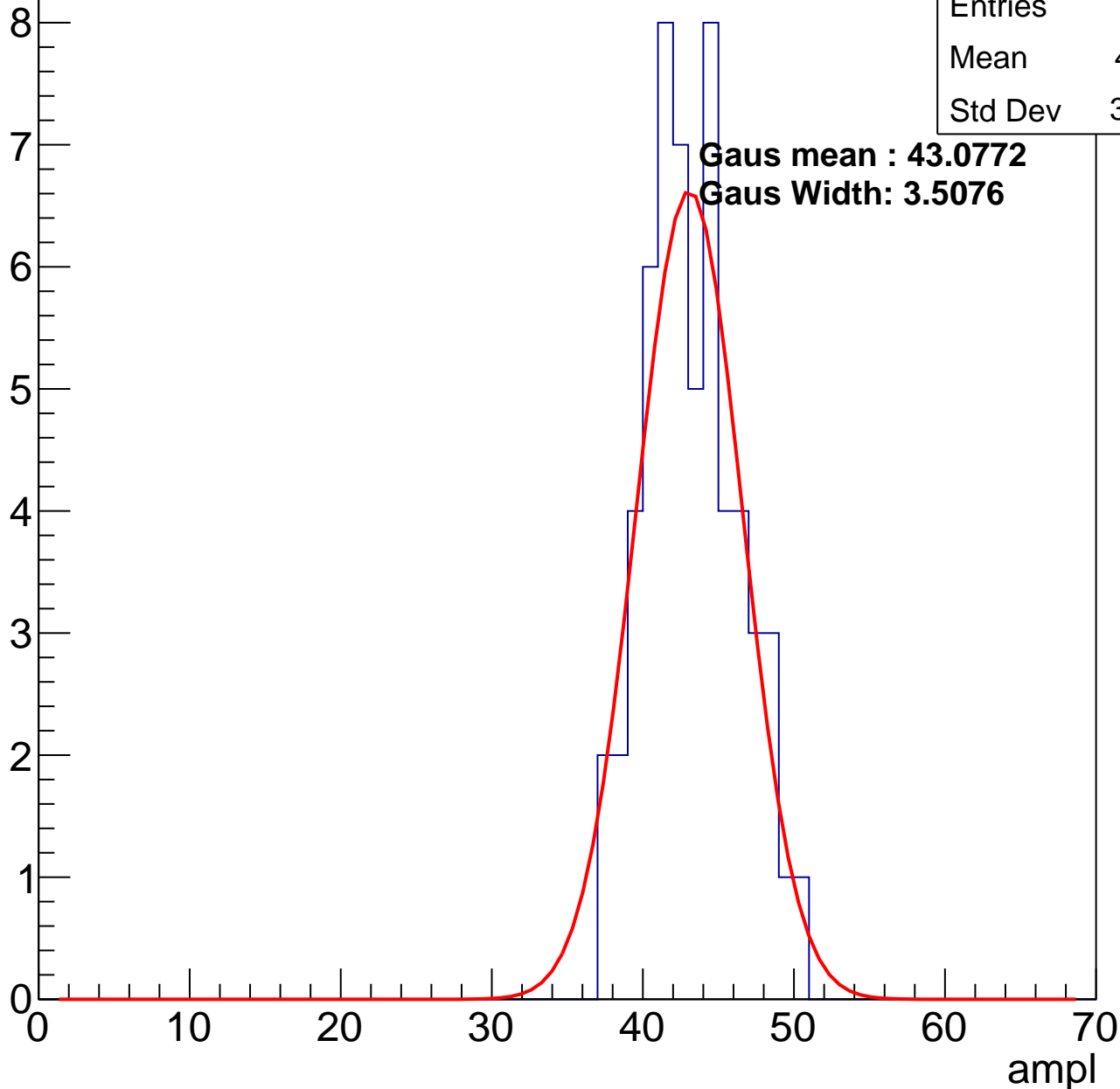
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	42.81
Std Dev	3.065

Gaus mean : 43.0772

Gaus Width: 3.5076

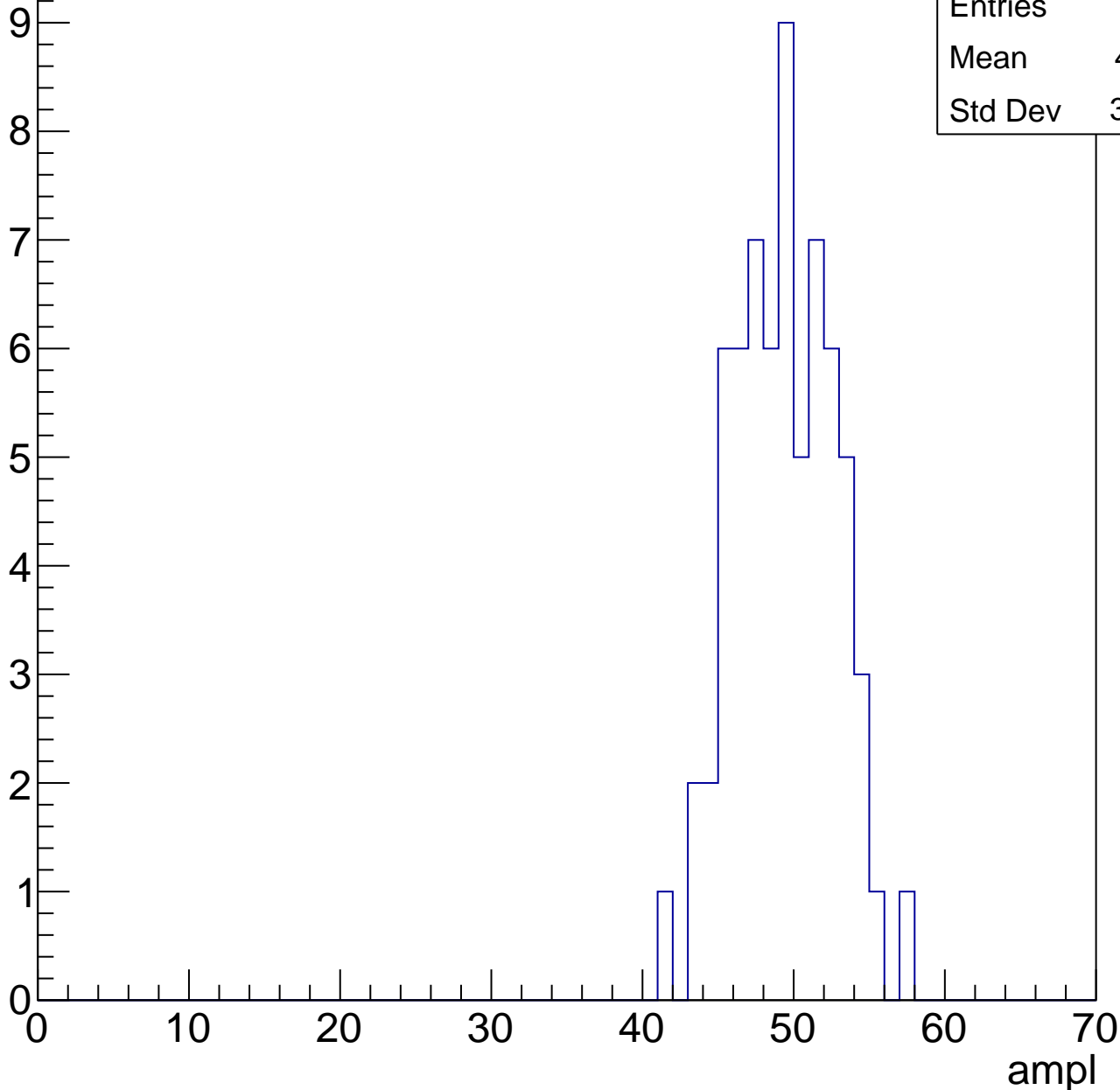


B0L001S, U23-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

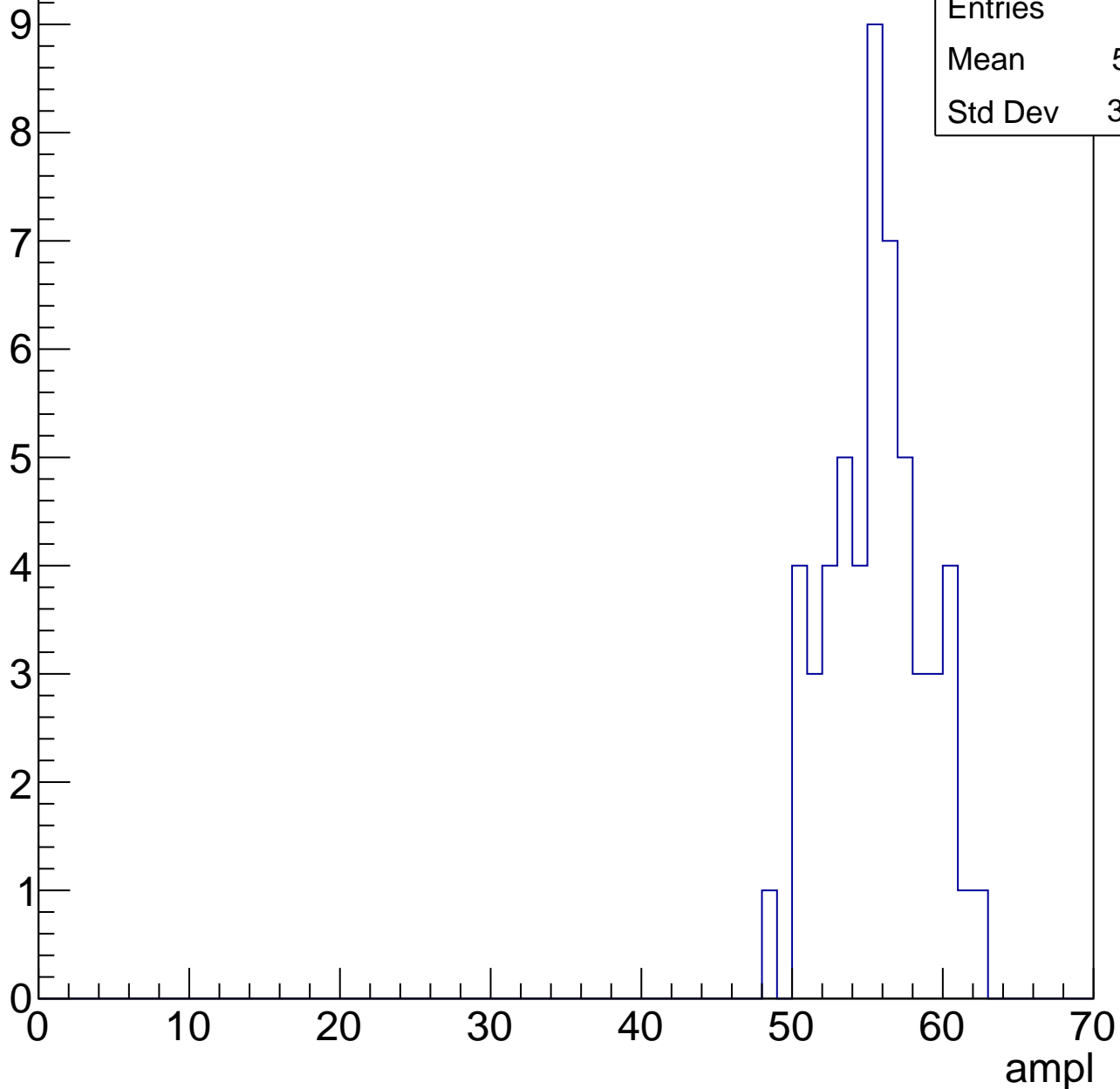
Entries	67
Mean	48.91
Std Dev	3.259



B0L001S, U23-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



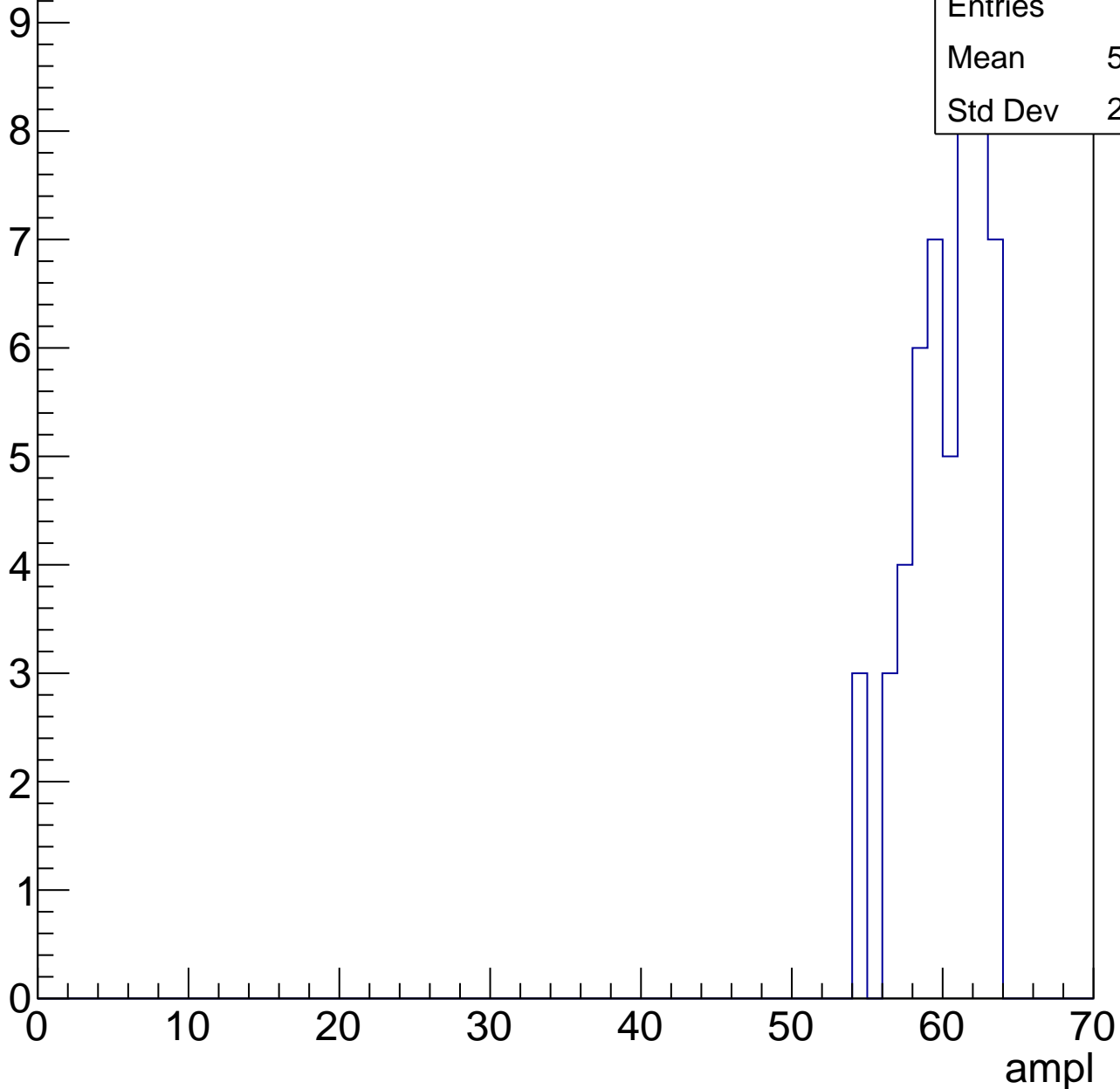
Entries	54
Mean	55.11
Std Dev	3.172

B0L001S, U23-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

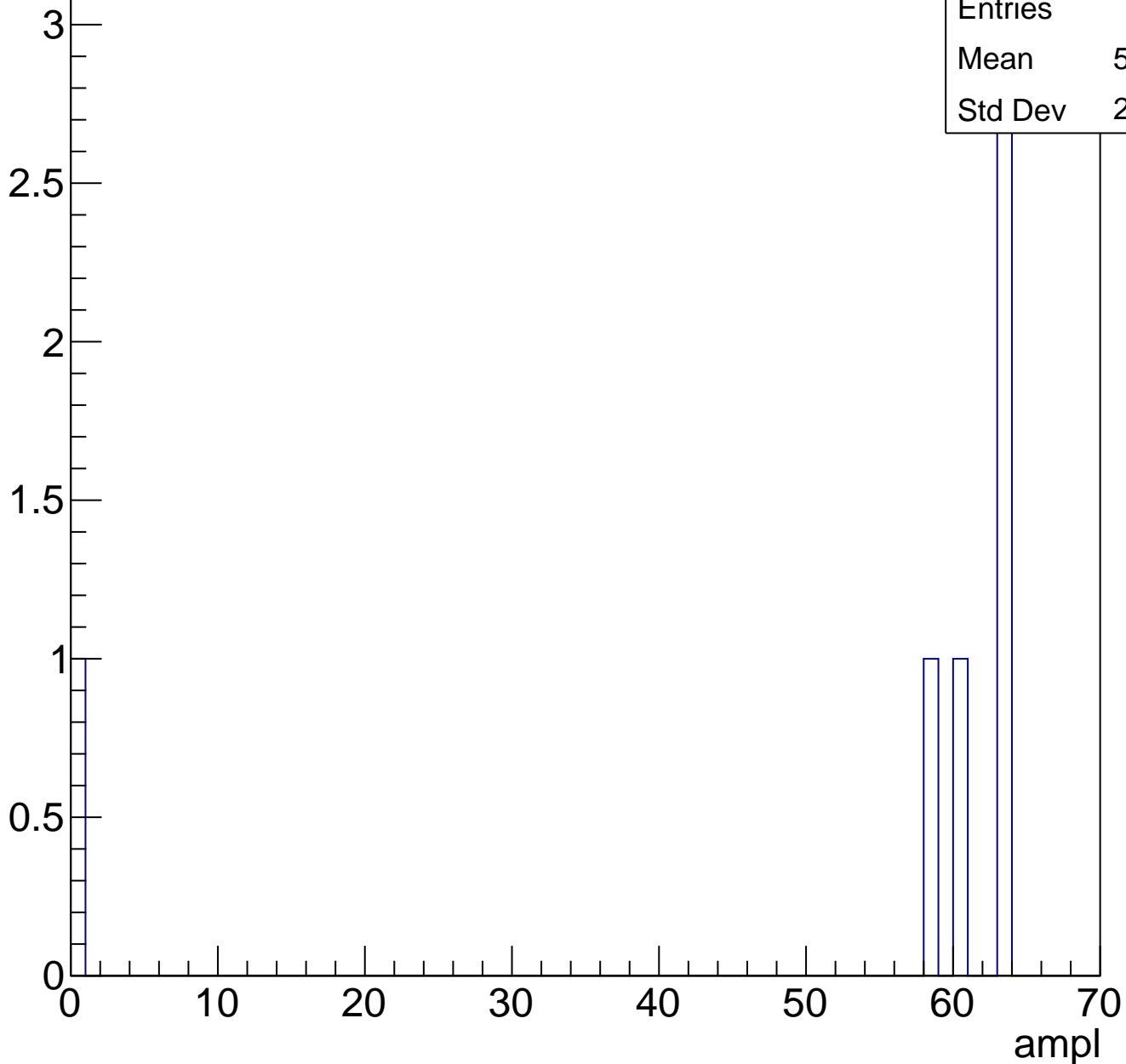
Entries	53
Mean	59.75
Std Dev	2.487



B0L001S, U23-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch49, adc0

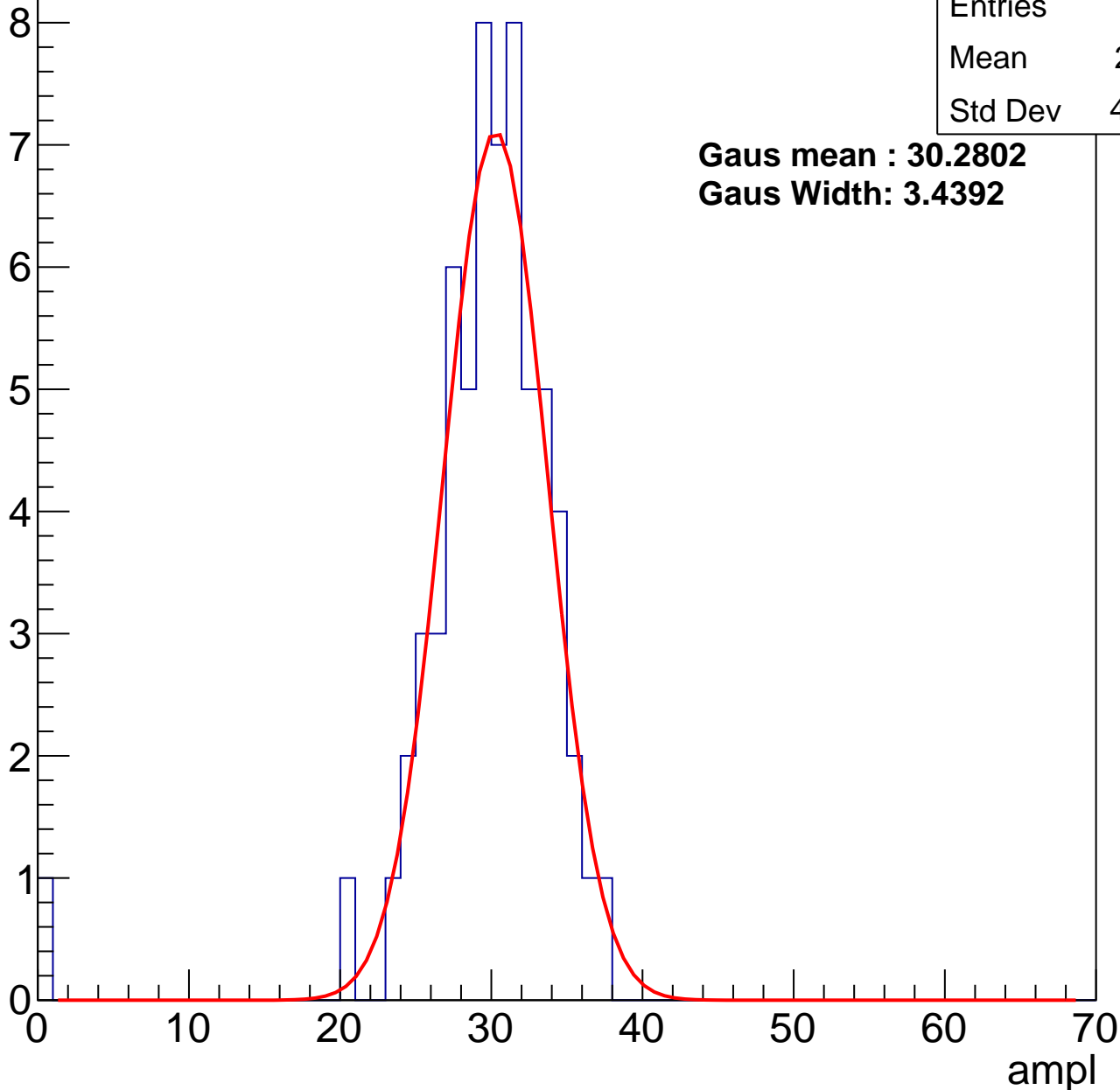
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.21
Std Dev	4.973

Gaus mean : 30.2802

Gaus Width: 3.4392



B0L001S, U23-ch49, adc1

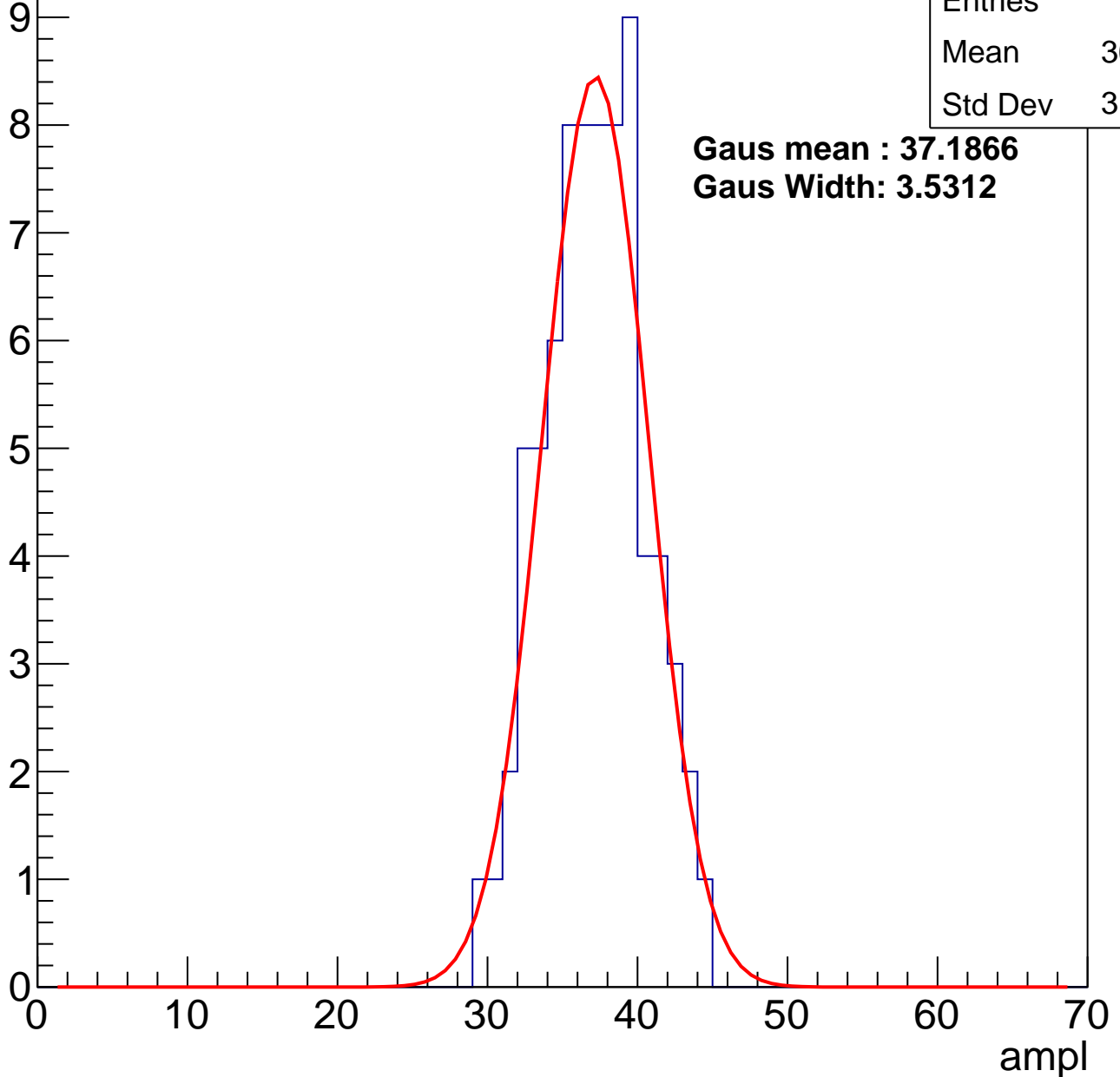
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.65
Std Dev	3.296

Gaus mean : 37.1866

Gaus Width: 3.5312



B0L001S, U23-ch49, adc2

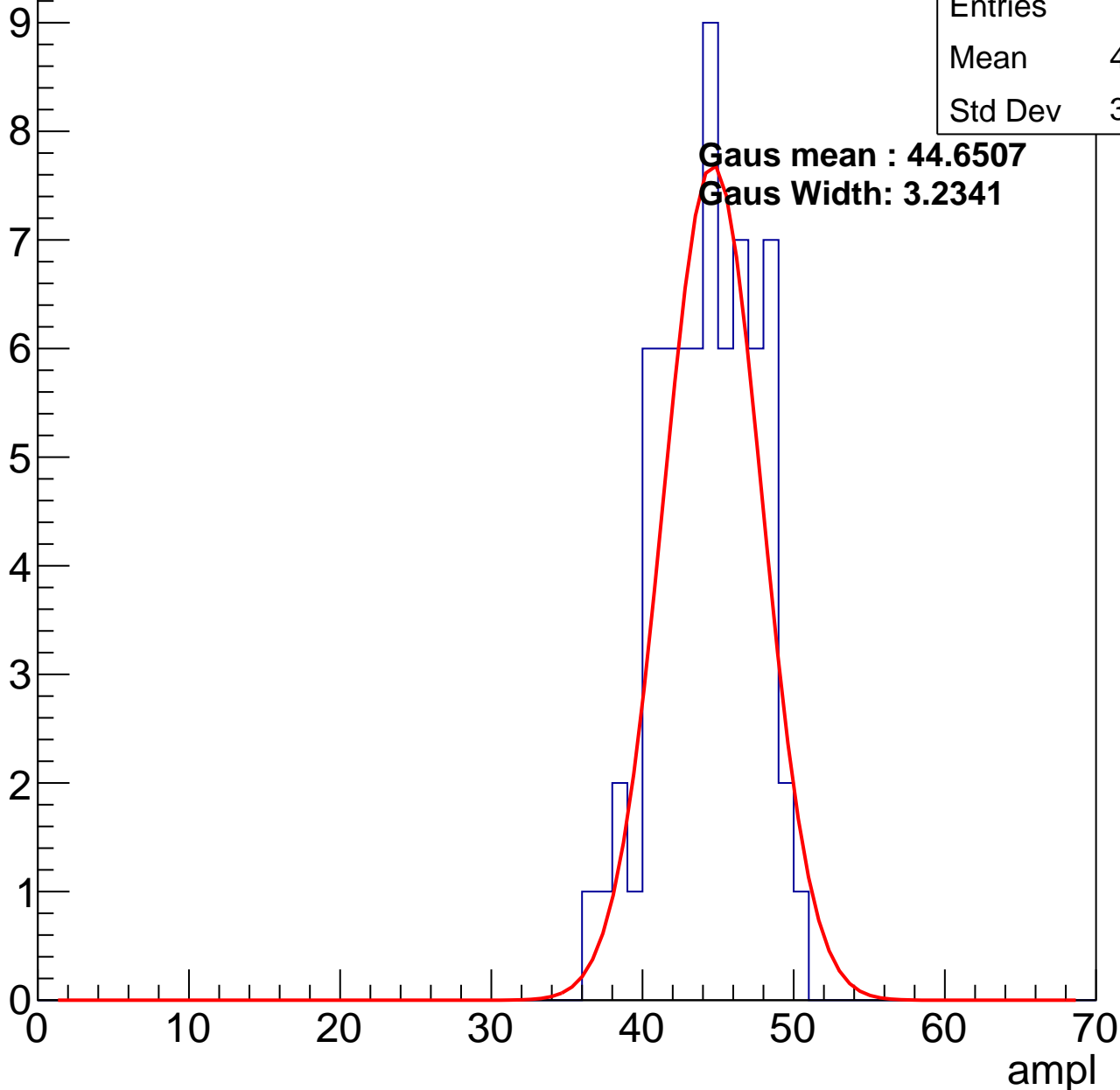
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.85
Std Dev	3.173

Gaus mean : 44.6507

Gaus Width: 3.2341

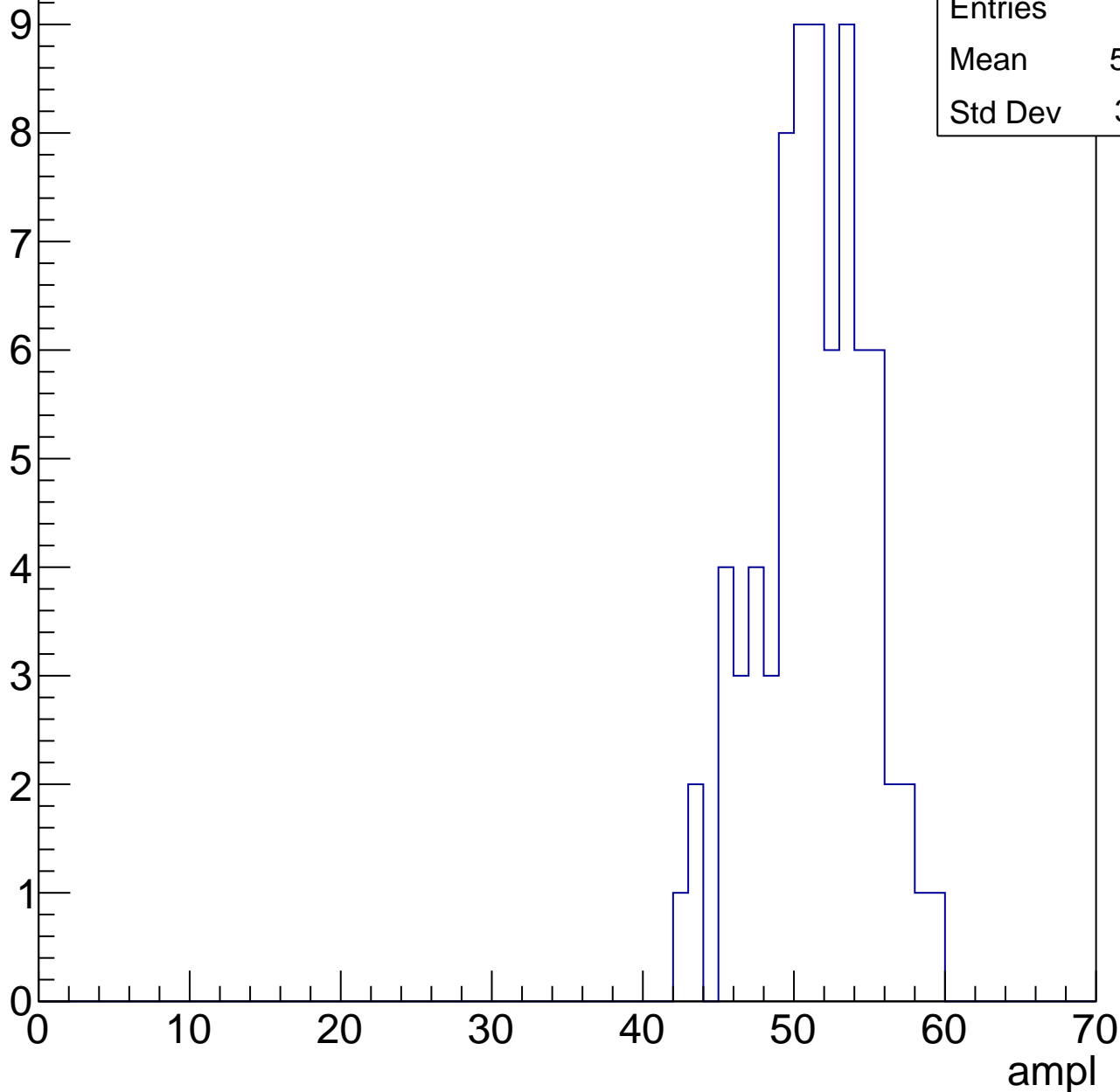


B0L001S, U23-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	50.86
Std Dev	3.601

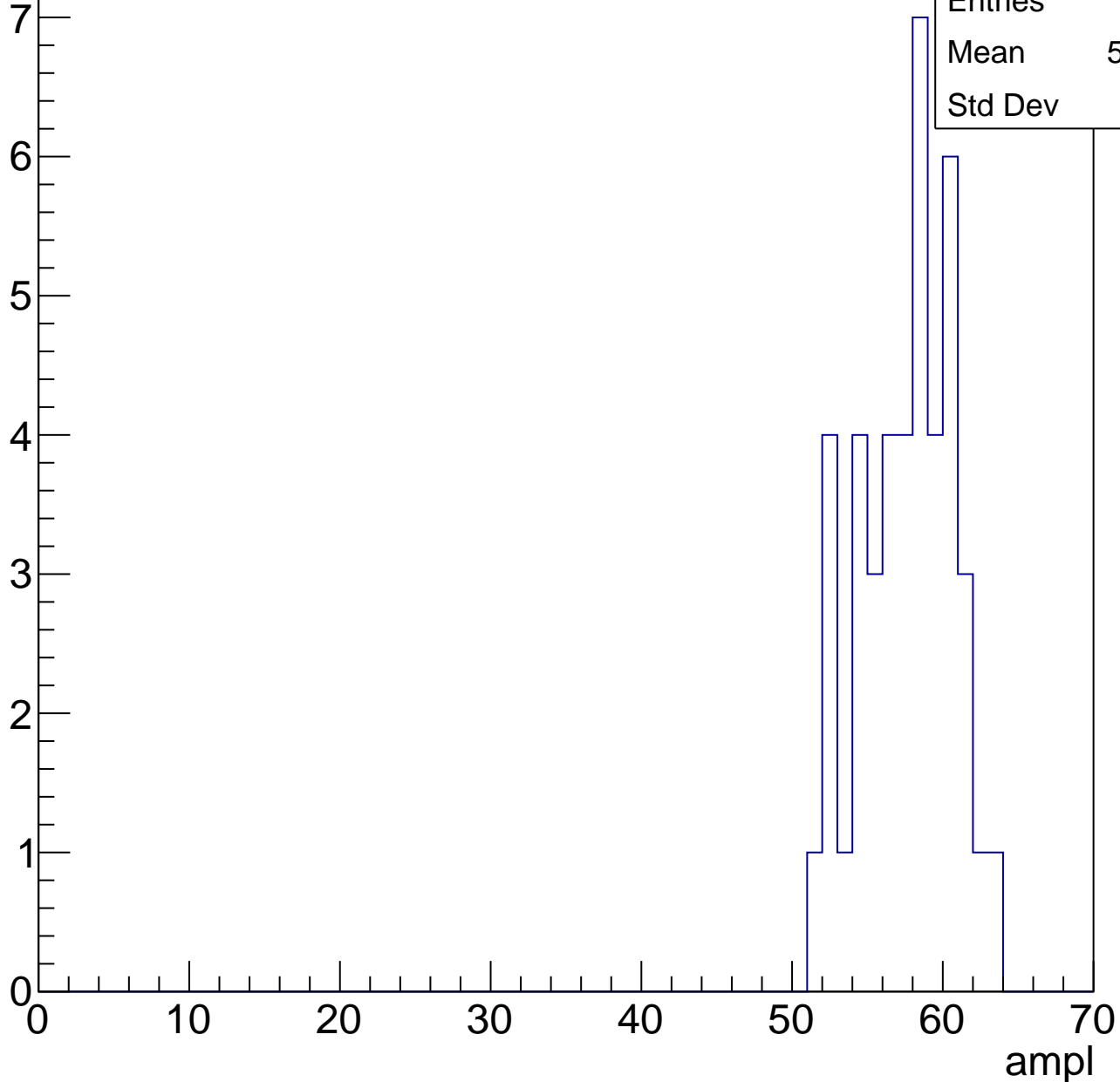


B0L001S, U23-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	57.09
Std Dev	3.01

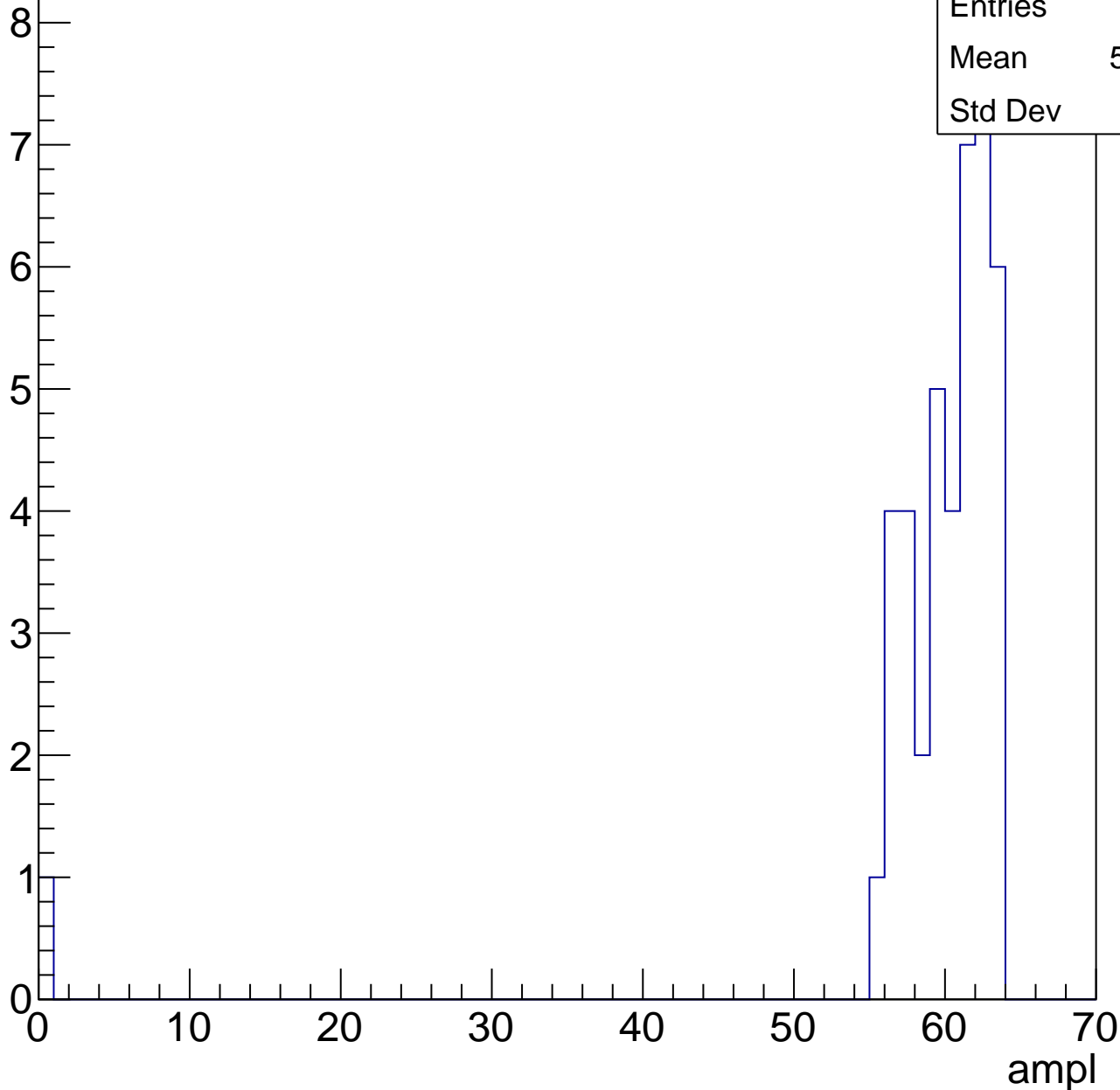


B0L001S, U23-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.55
Std Dev	9.44



B0L001S, U23-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch50, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.93
Std Dev	3.286

Gaus mean : 31.3186

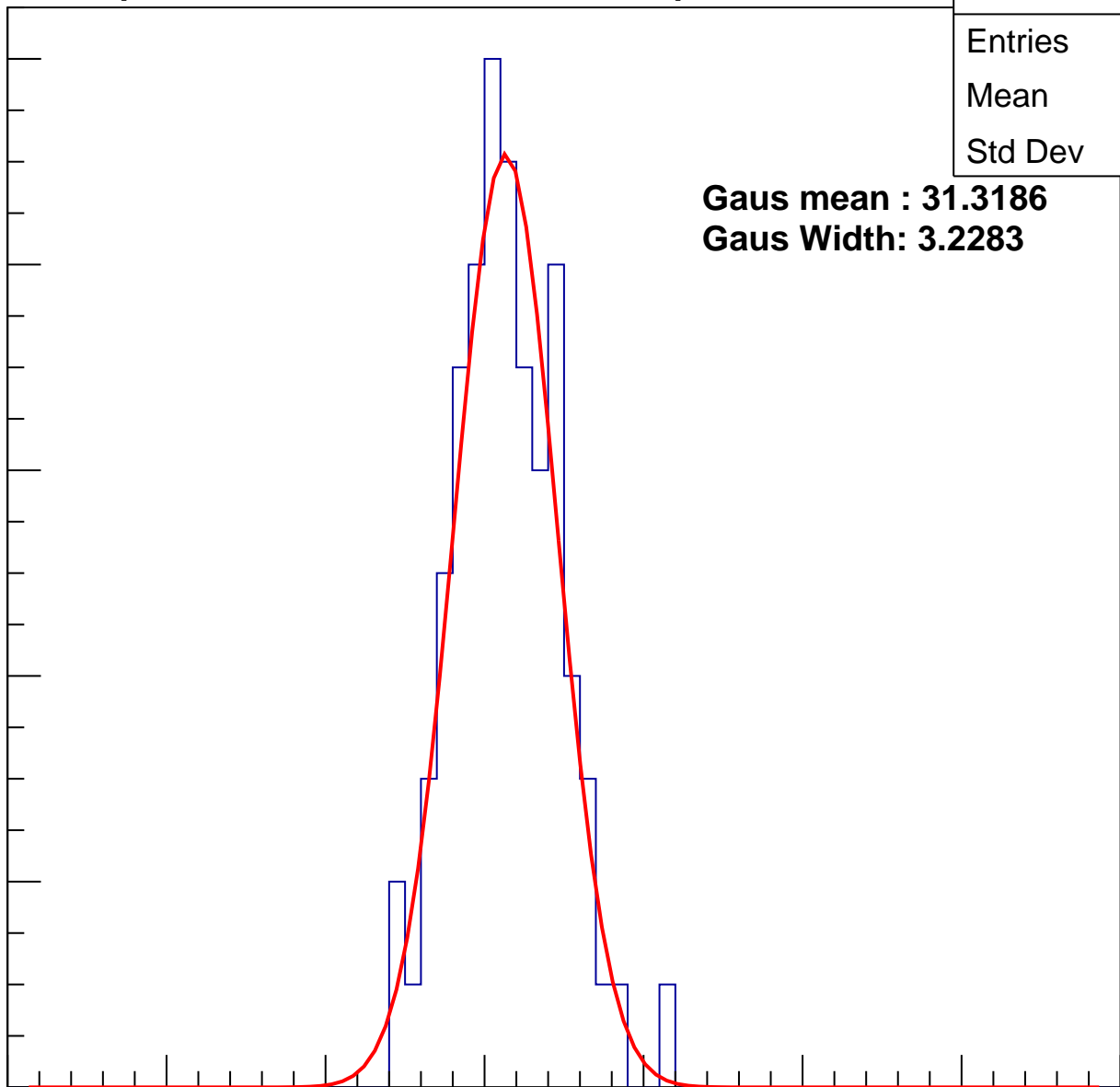
Gaus Width: 3.2283

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch50, adc1

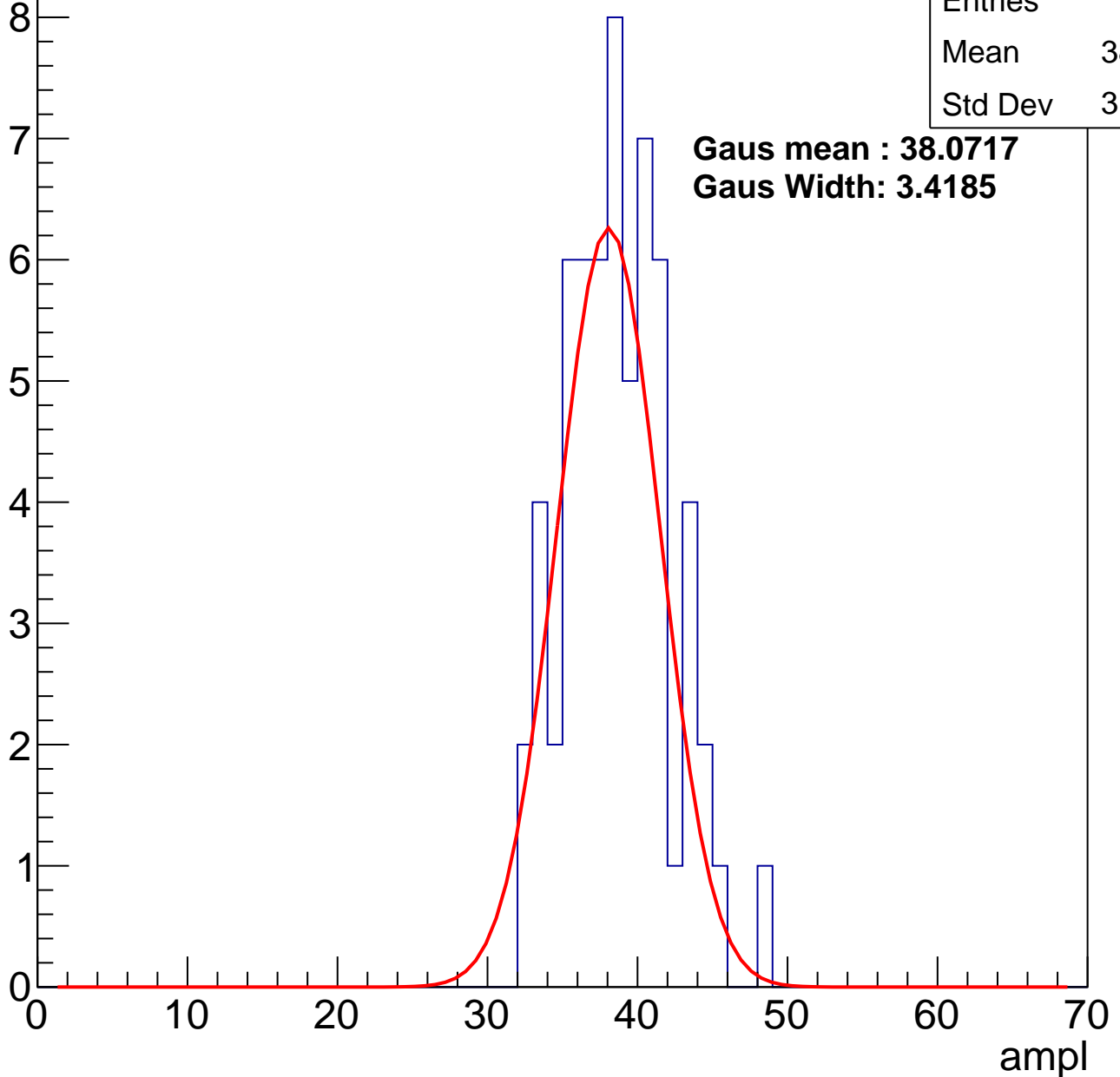
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.23
Std Dev	3.409

Gaus mean : 38.0717

Gaus Width: 3.4185



B0L001S, U23-ch50, adc2

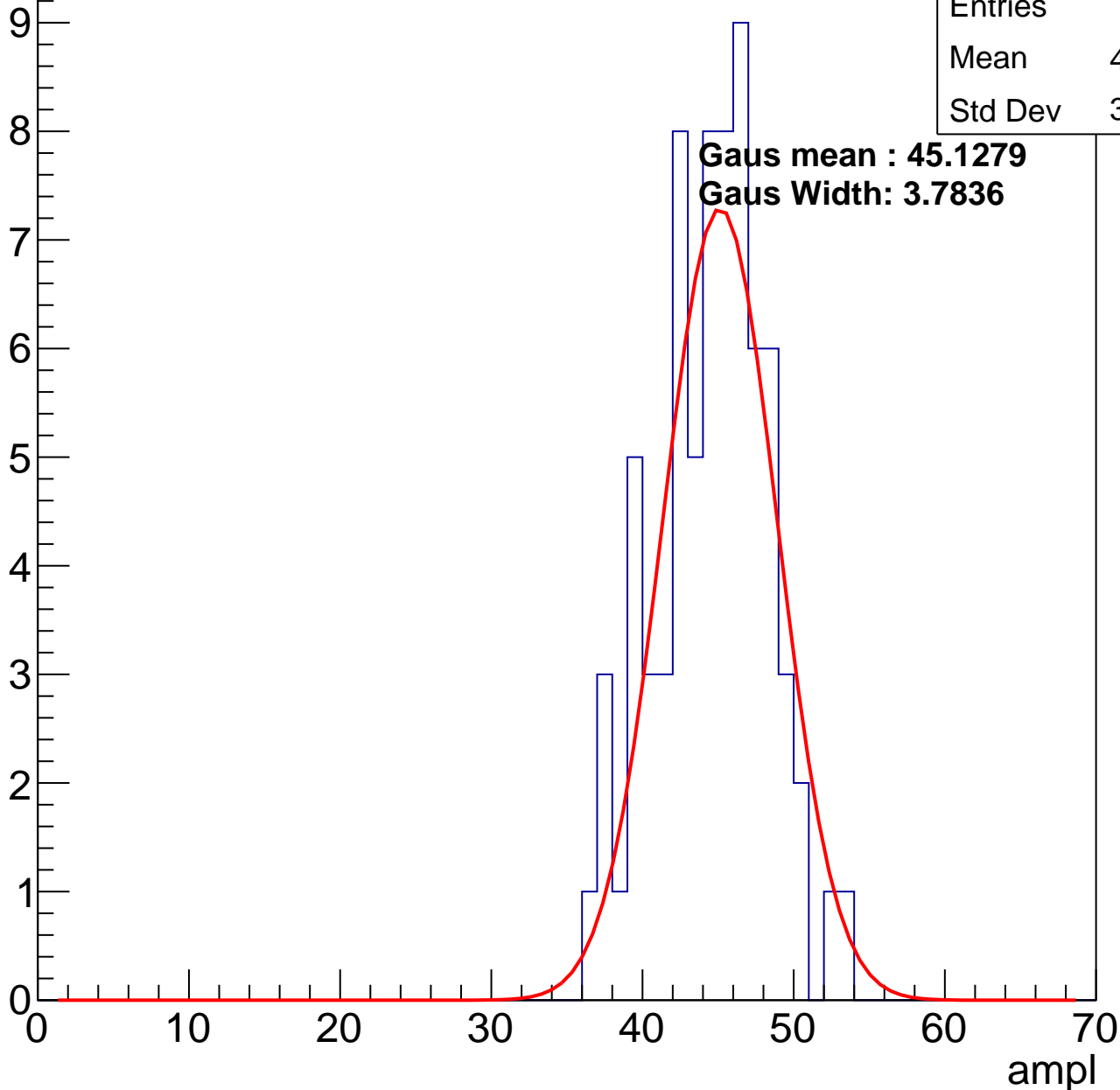
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.14
Std Dev	3.643

Gaus mean : 45.1279

Gaus Width: 3.7836

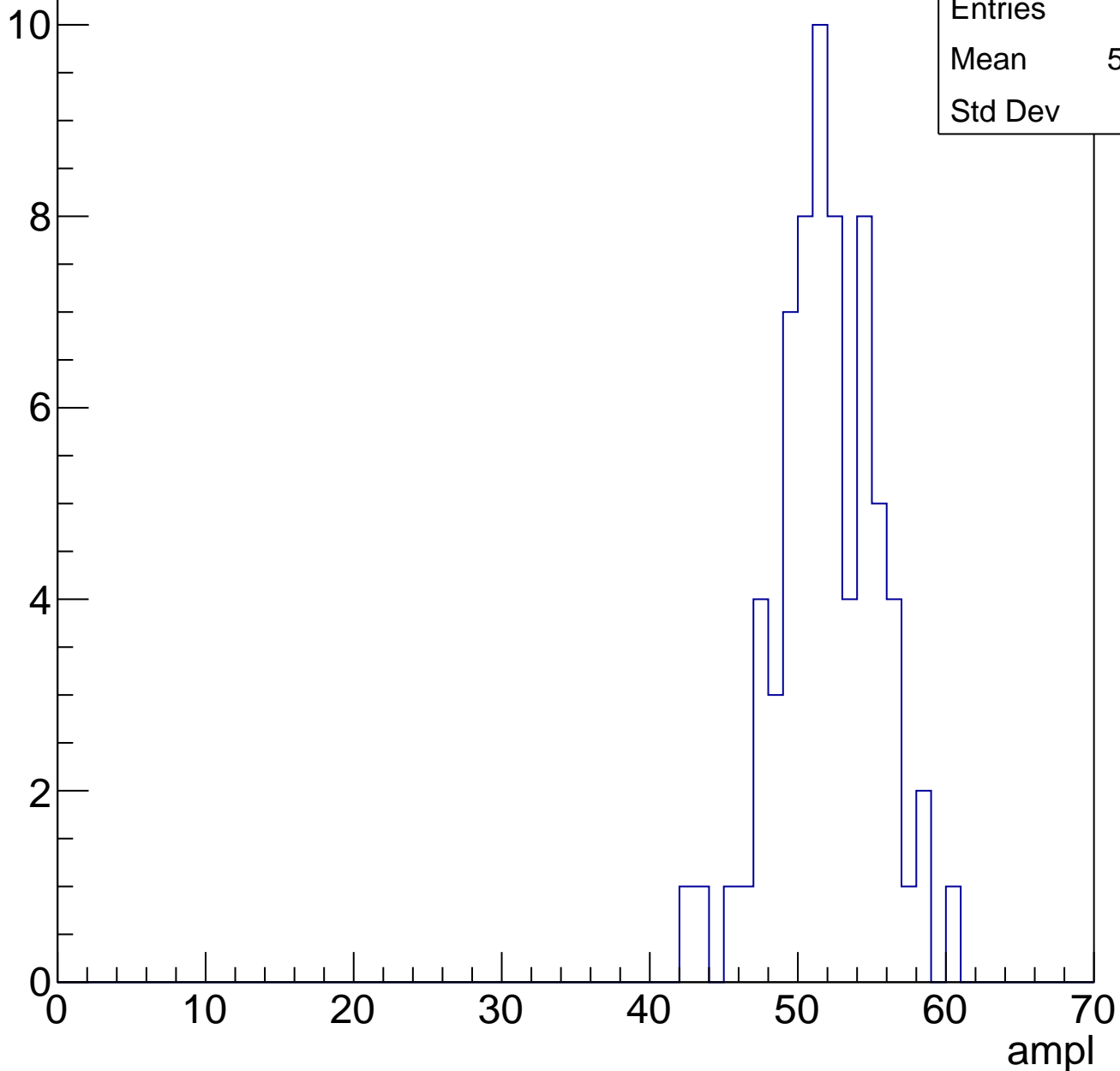


B0L001S, U23-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	51.49
Std Dev	3.42

Entry

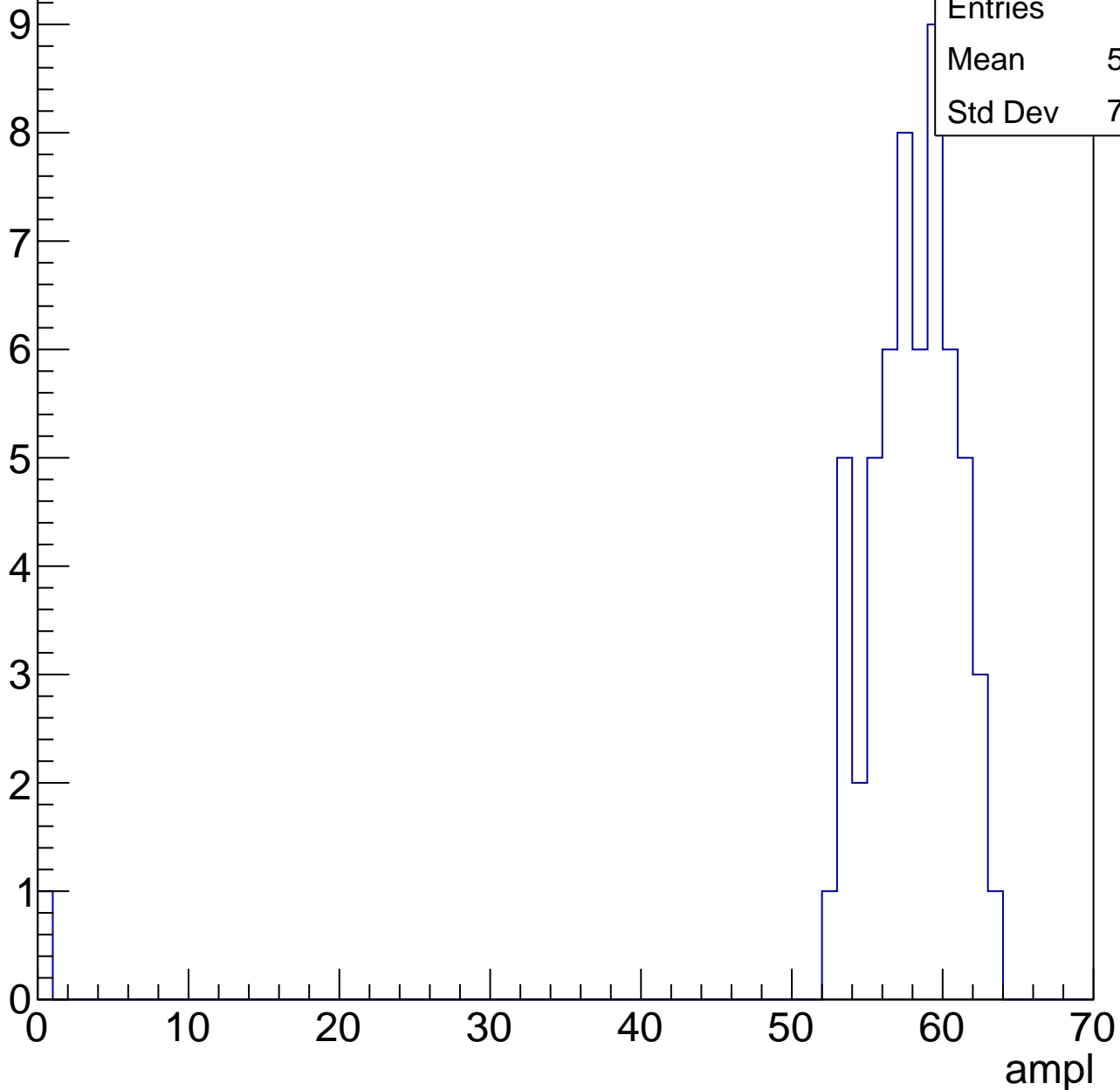


B0L001S, U23-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.64
Std Dev	7.963

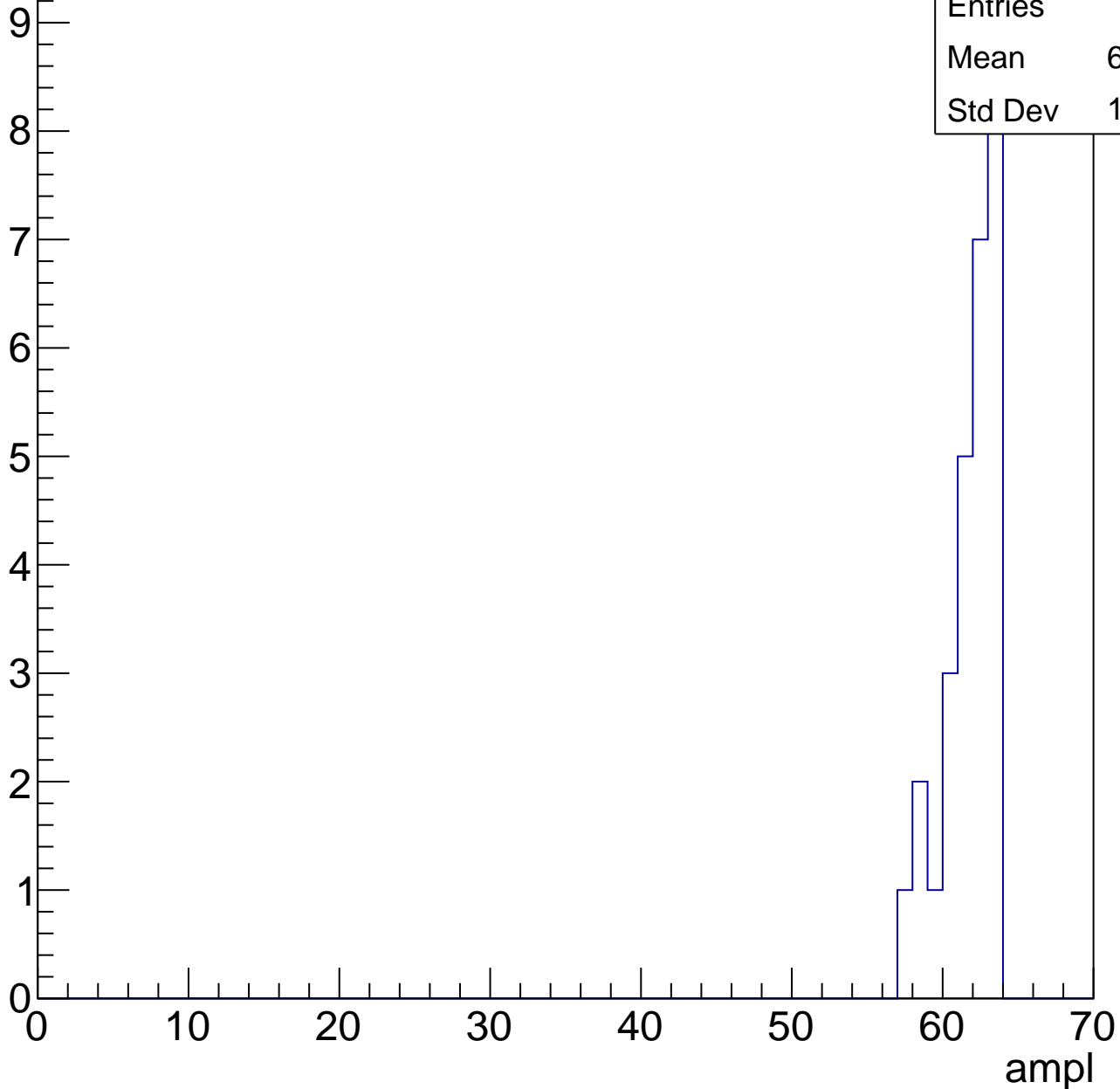


B0L001S, U23-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	61.36
Std Dev	1.695



B0L001S, U23-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U23-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U23-ch51, adc0

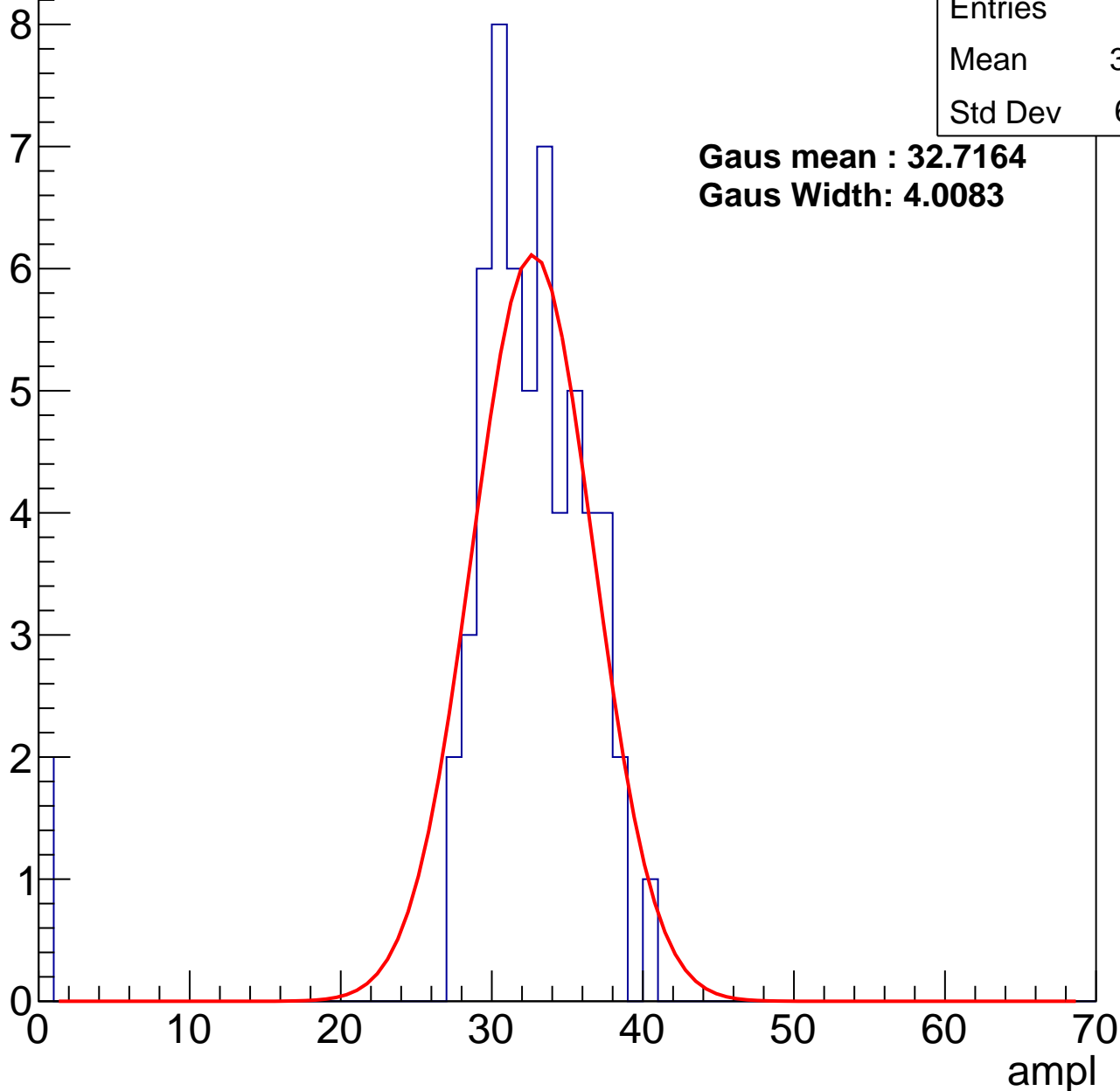
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.32
Std Dev	6.611

Gaus mean : 32.7164

Gaus Width: 4.0083



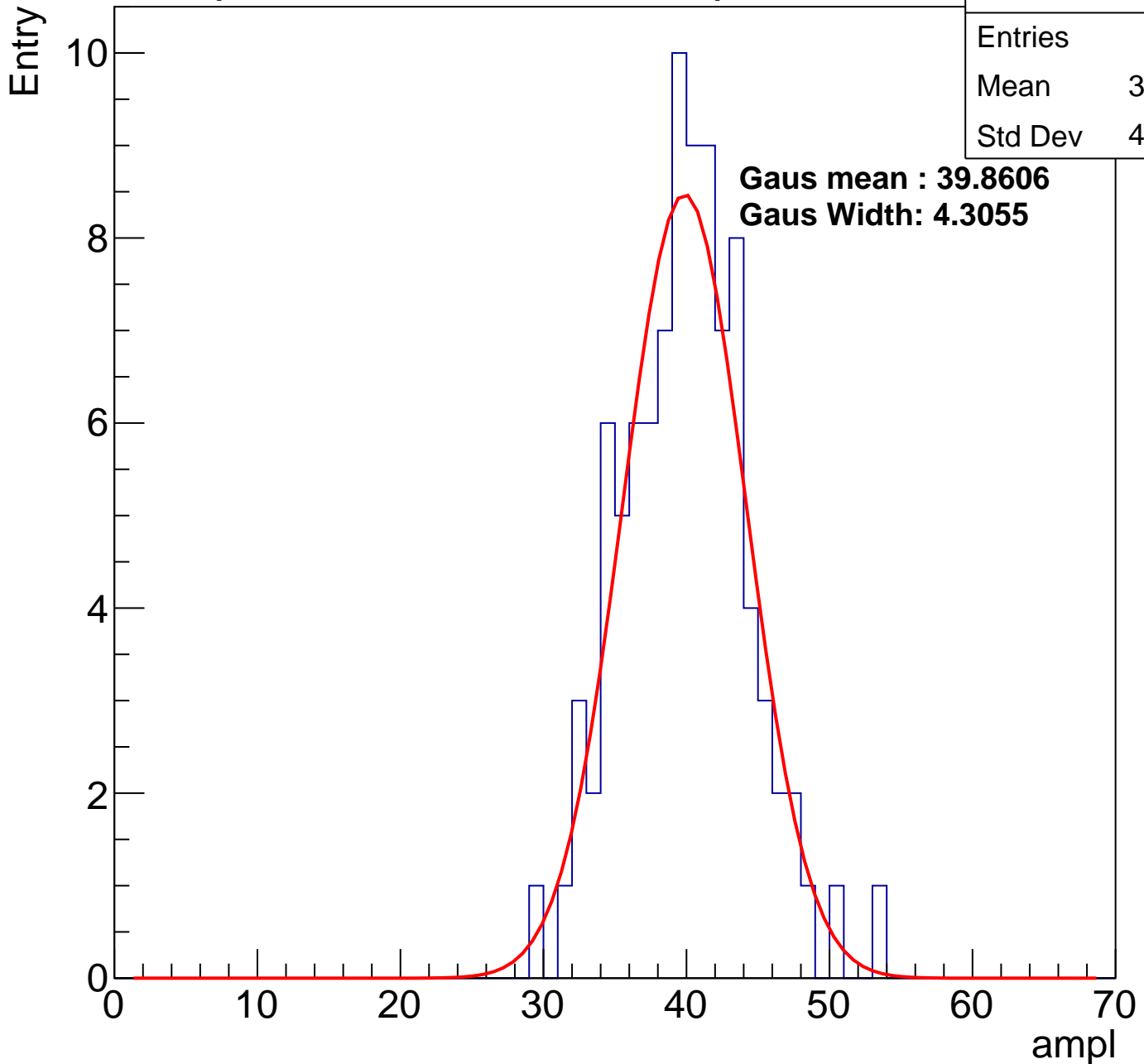
B0L001S, U23-ch51, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	94
Mean	39.47
Std Dev	4.309

Gaus mean : 39.8606

Gaus Width: 4.3055

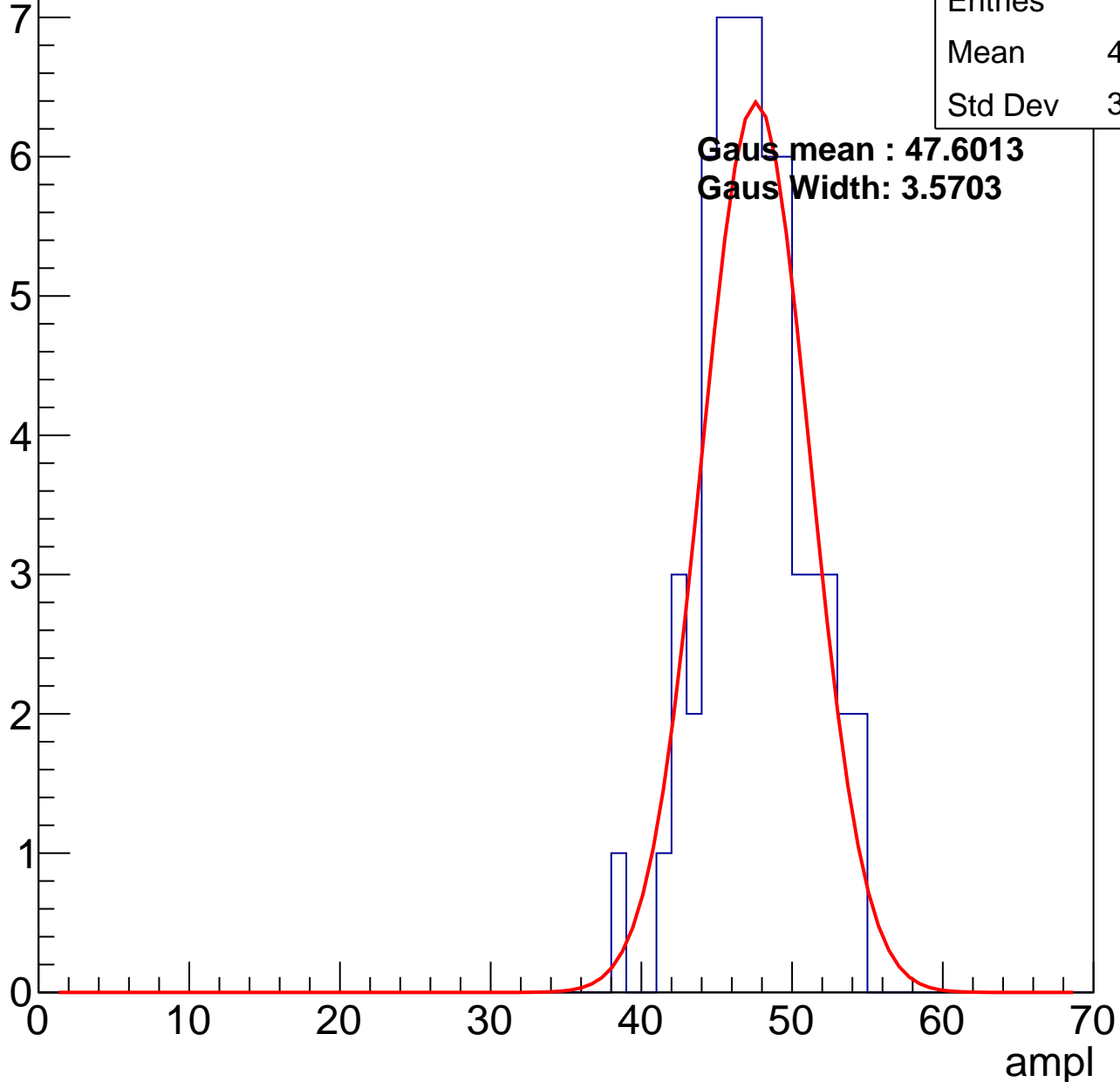


B0L001S, U23-ch51, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	47.05
Std Dev	3.352

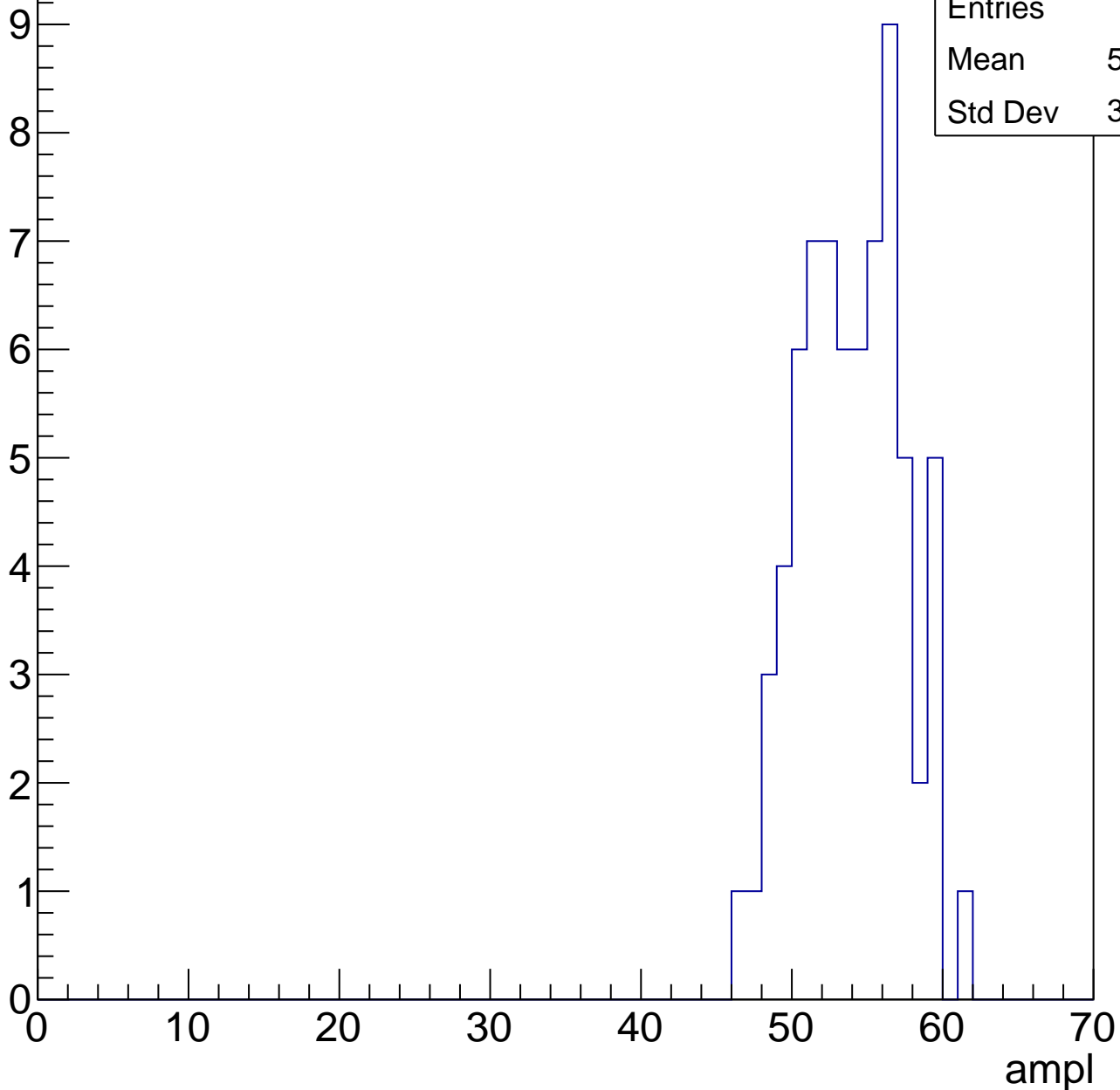


B0L001S, U23-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	53.46
Std Dev	3.354

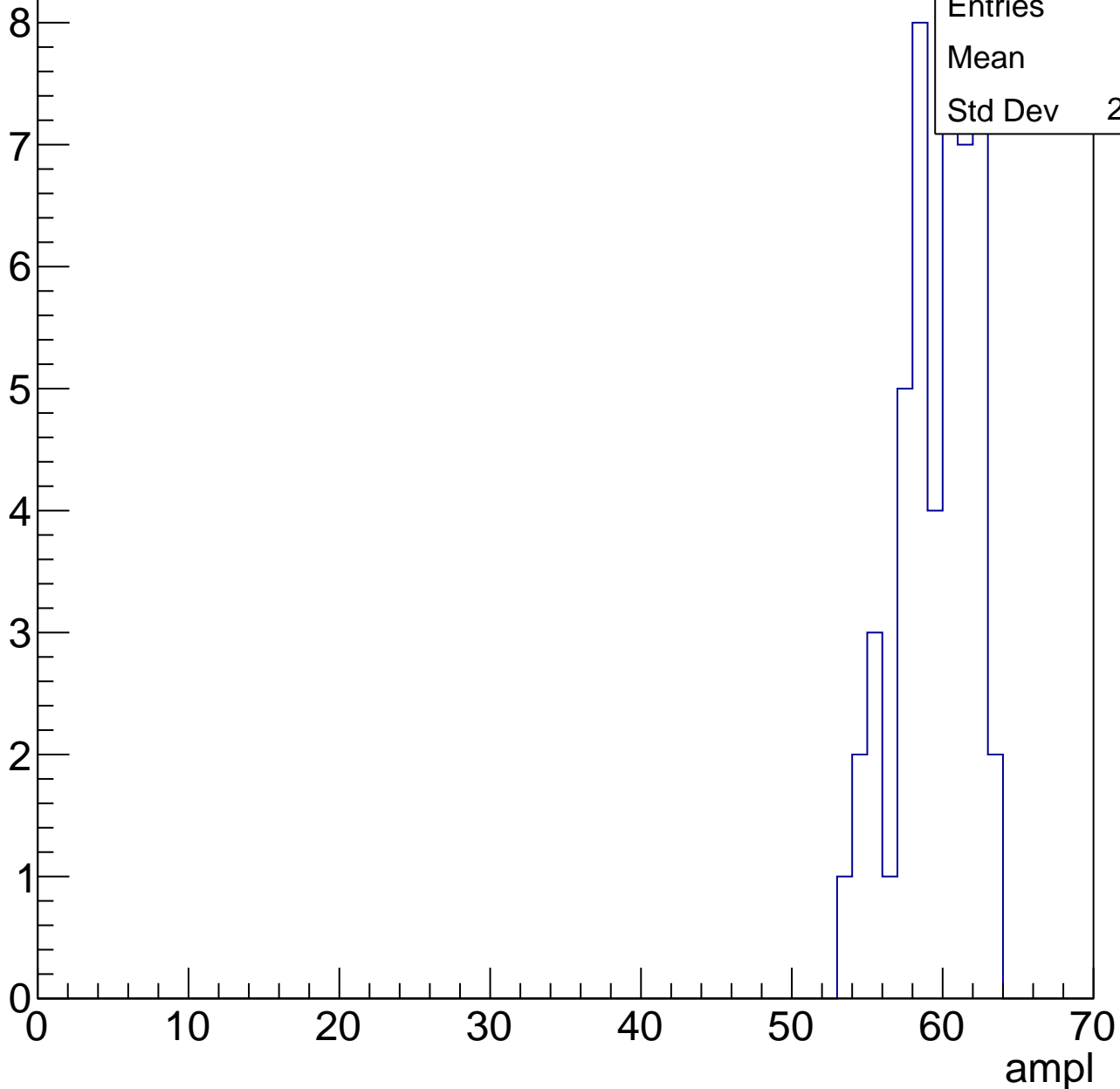


B0L001S, U23-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	59.1
Std Dev	2.517

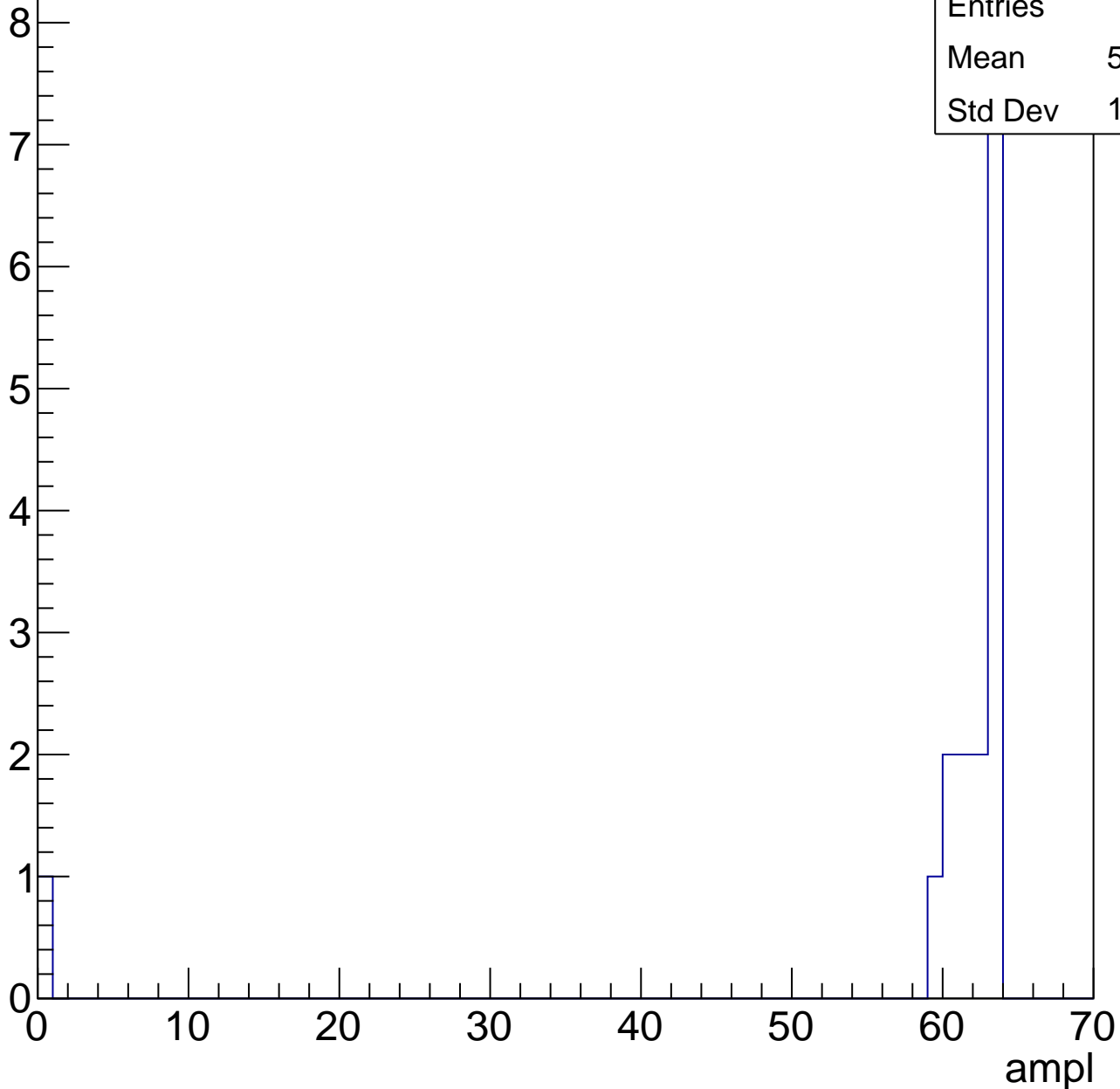


B0L001S, U23-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	58.06
Std Dev	15.05



B0L001S, U23-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch52, adc0

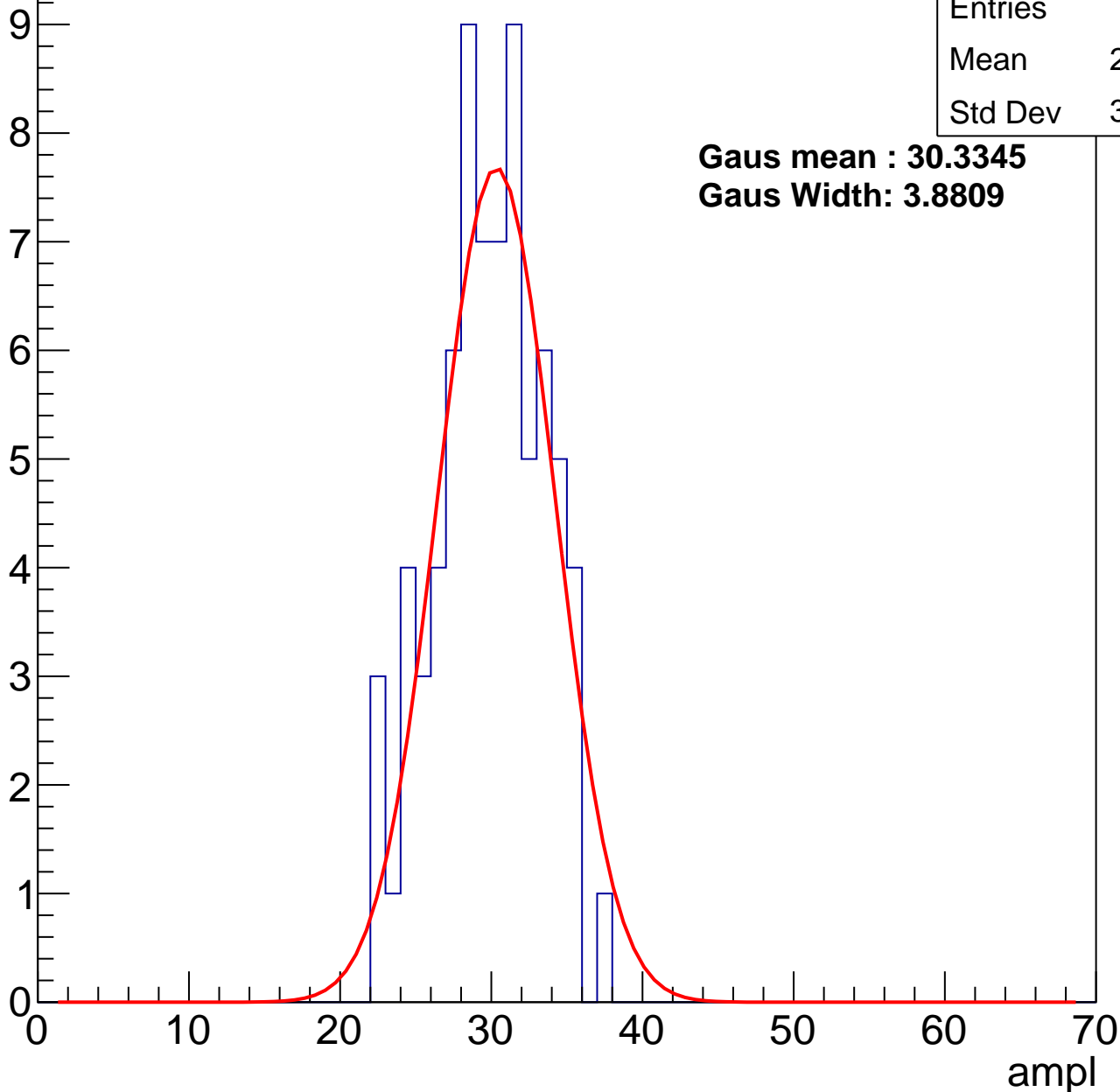
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.39
Std Dev	3.498

Gaus mean : 30.3345

Gaus Width: 3.8809



B0L001S, U23-ch52, adc1

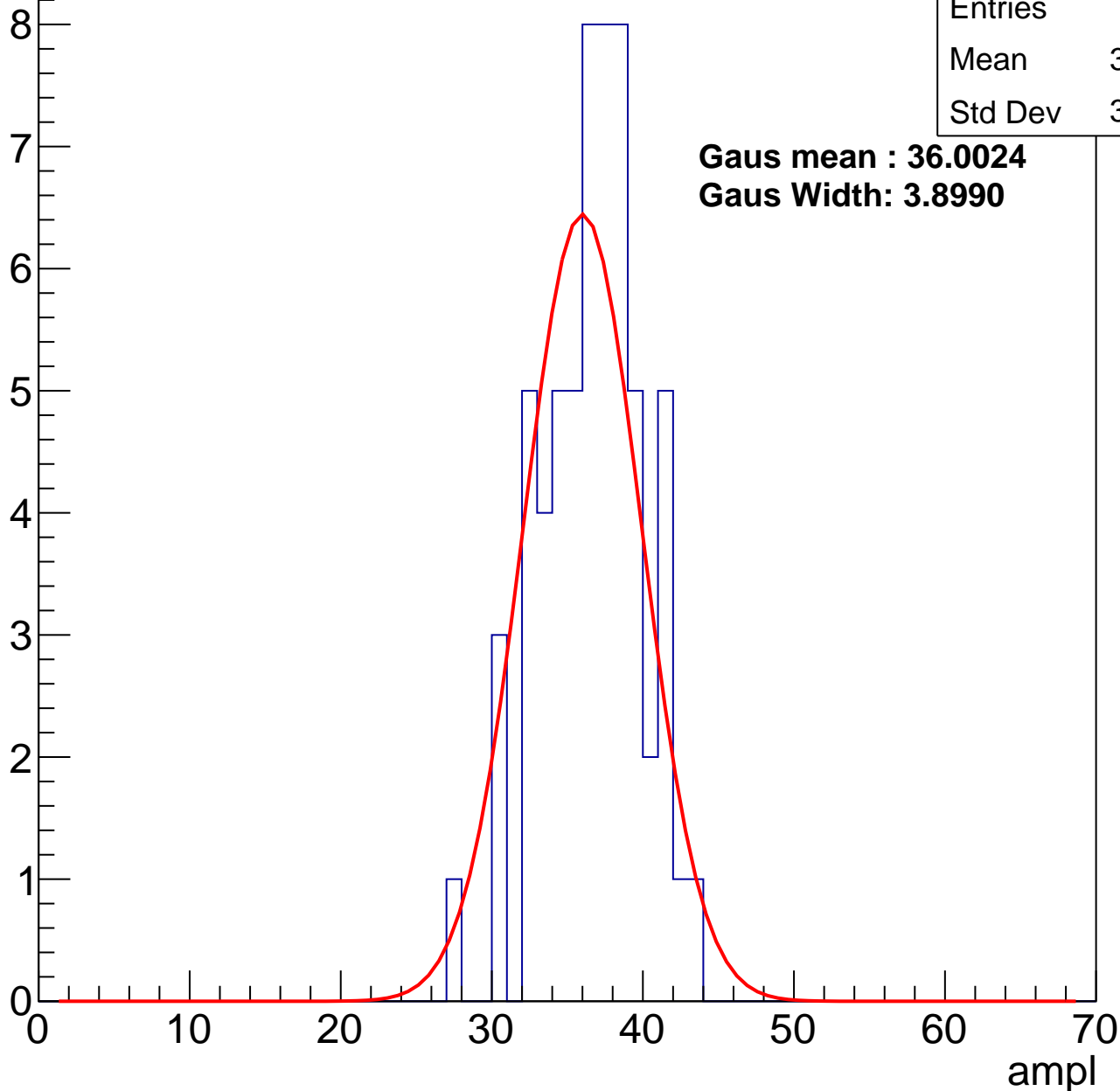
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.18
Std Dev	3.277

Gaus mean : 36.0024

Gaus Width: 3.8990



B0L001S, U23-ch52, adc2

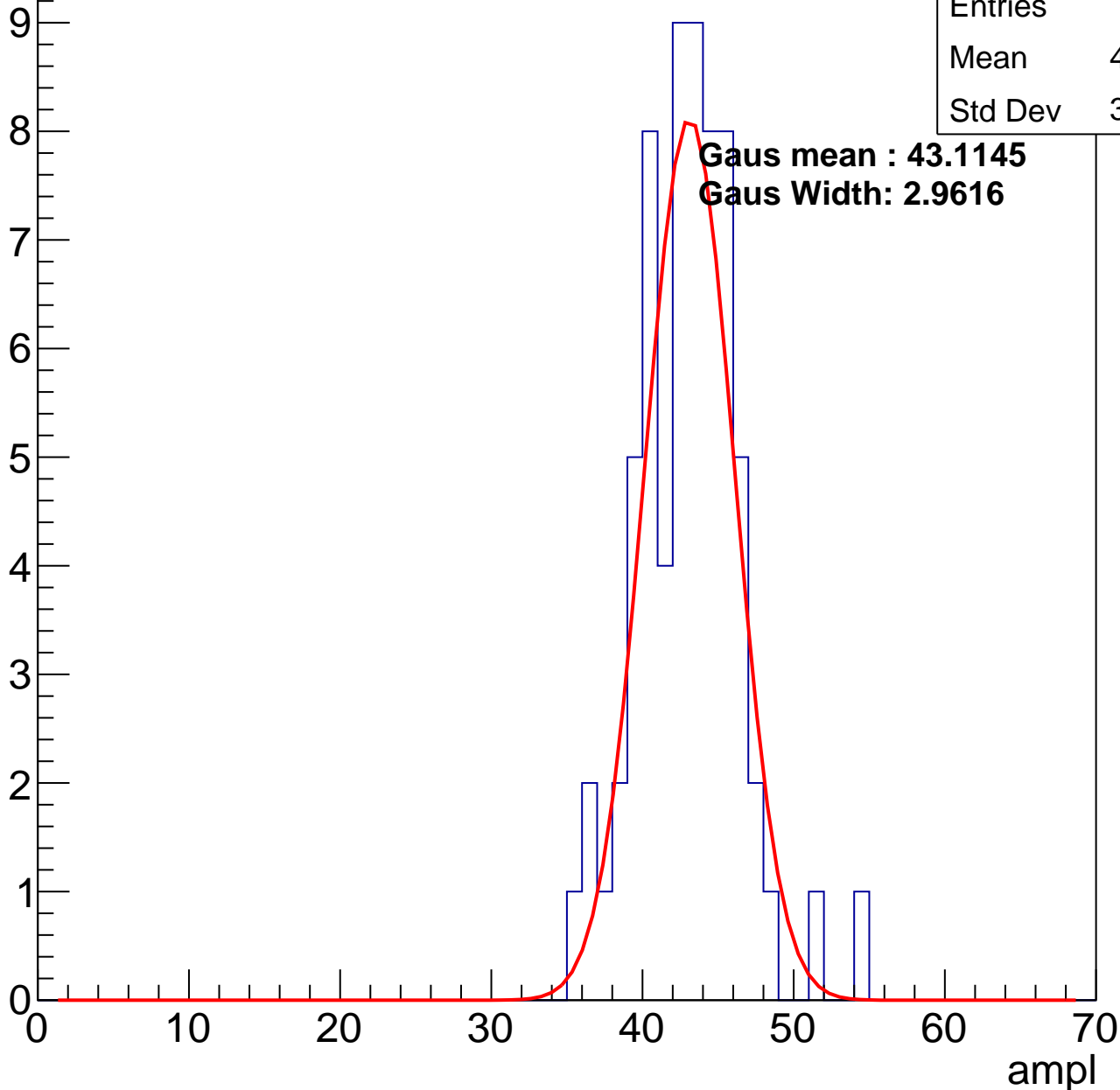
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.58
Std Dev	3.324

Gaus mean : 43.1145

Gaus Width: 2.9616



B0L001S, U23-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	50.27
Std Dev	3.578

Entry

10

8

6

4

2

0

0

10

20

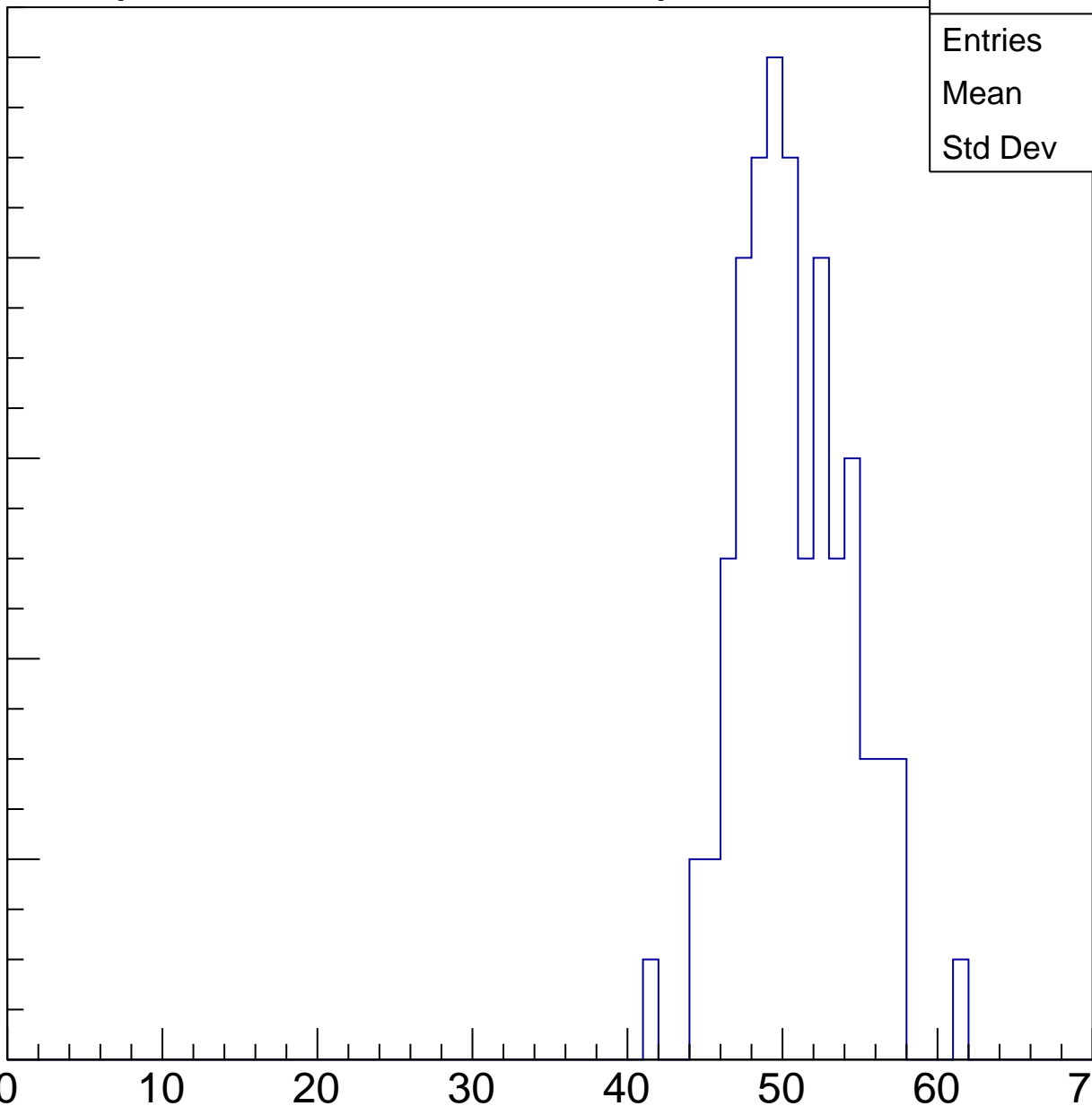
30

40

50

60

ampl

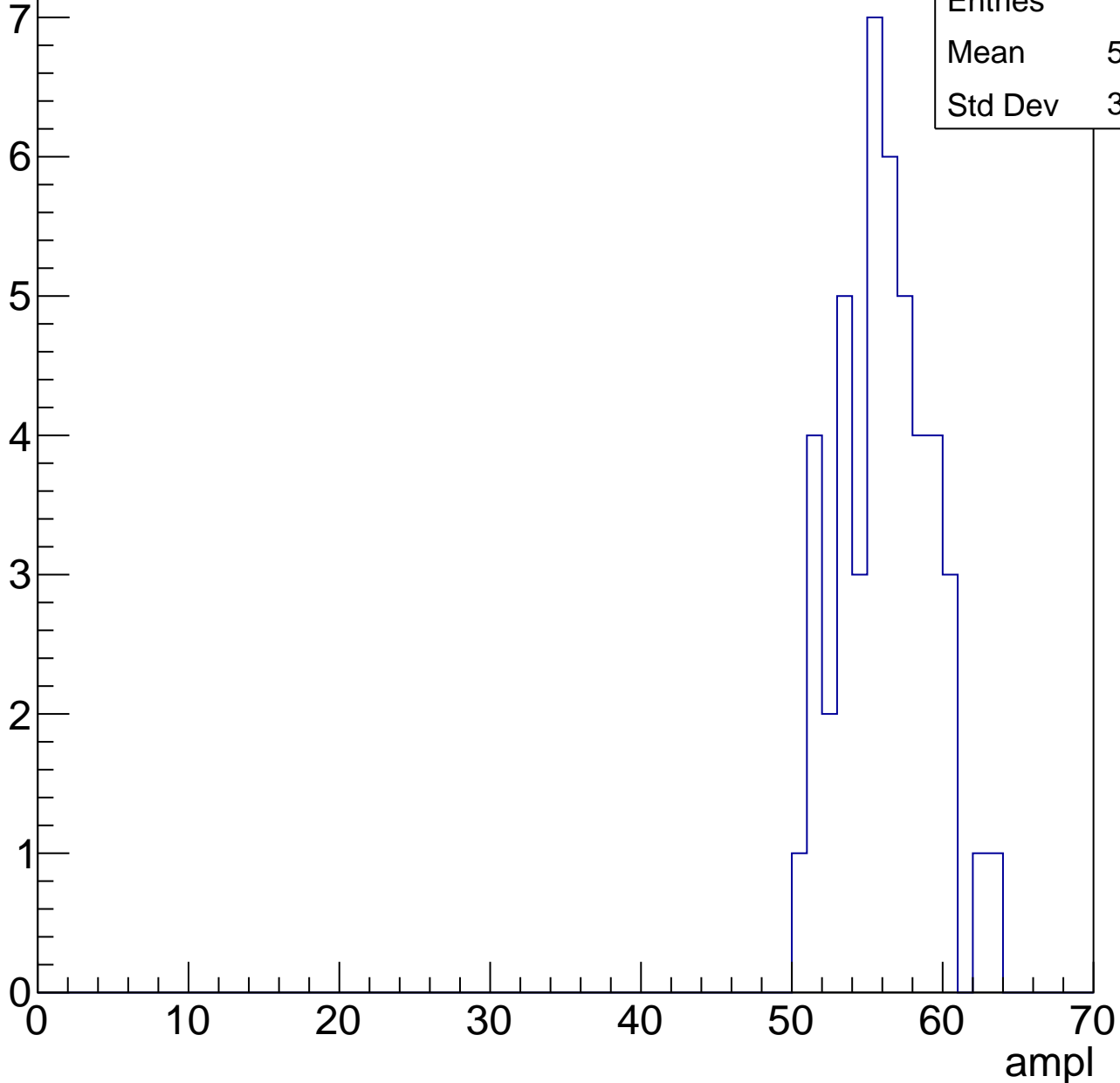


B0L001S, U23-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	55.74
Std Dev	3.018

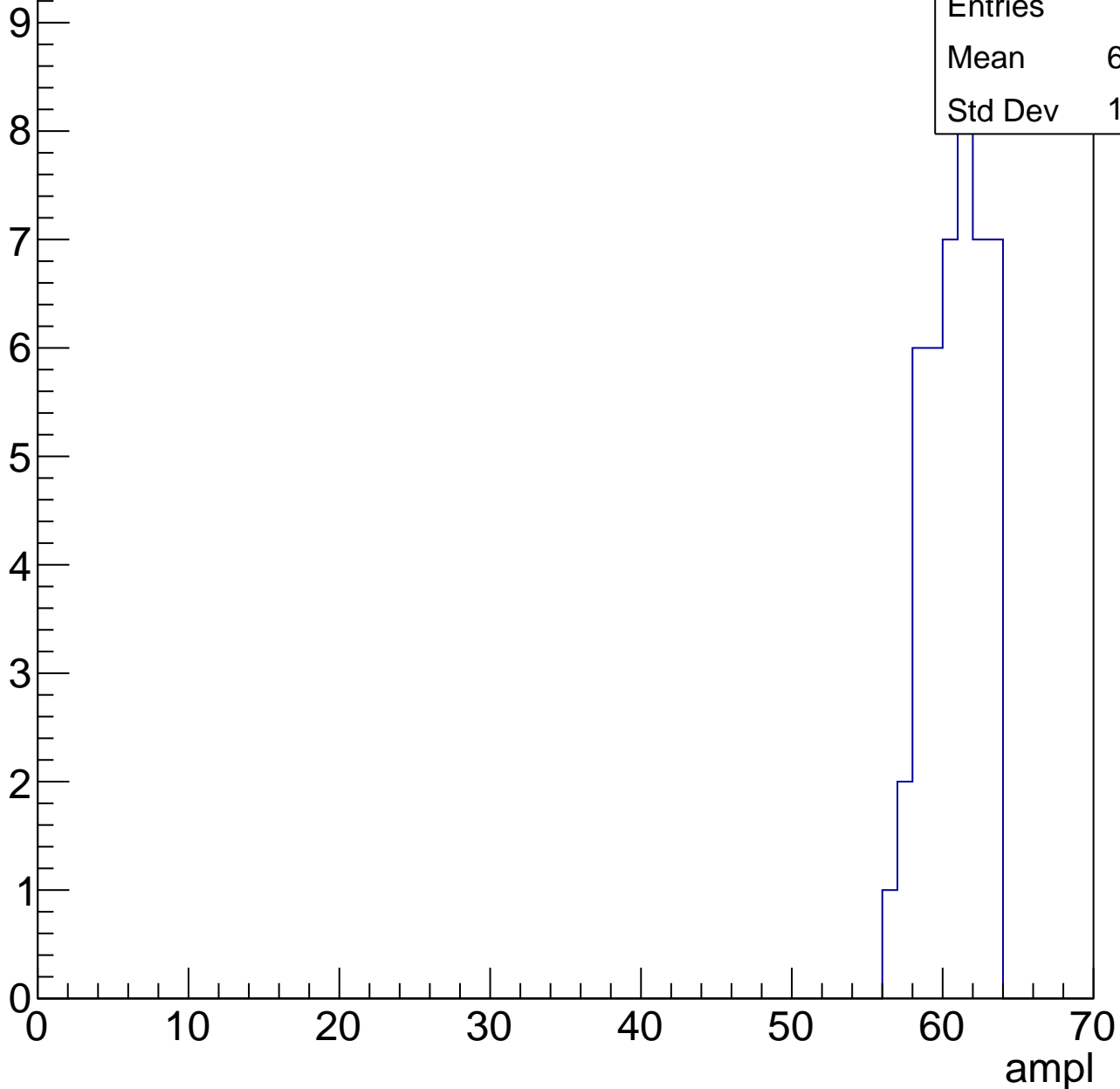


B0L001S, U23-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	60.36
Std Dev	1.876



B0L001S, U23-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch53, adc0

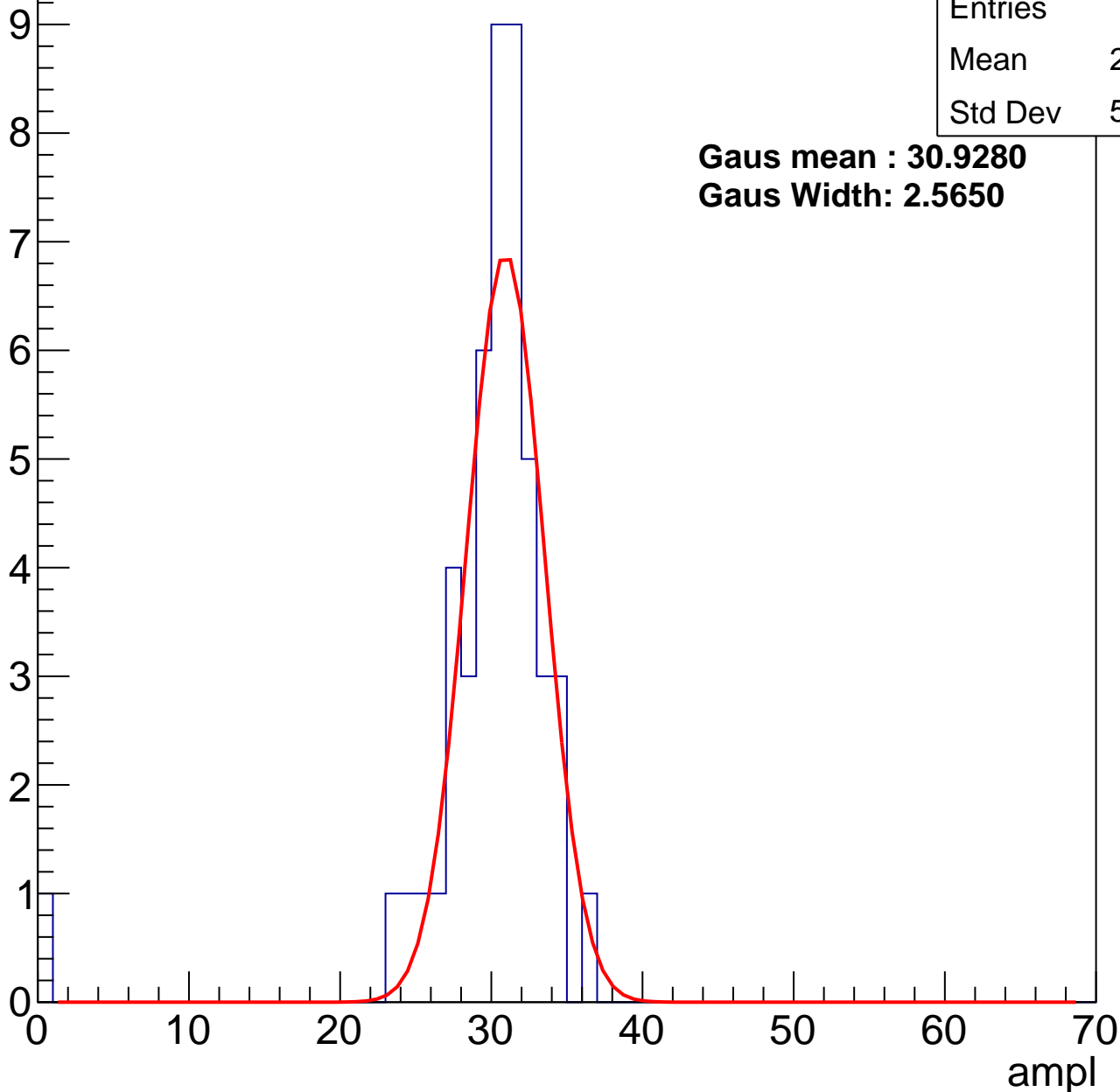
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	29.38
Std Dev	5.003

Gaus mean : 30.9280

Gaus Width: 2.5650



B0L001S, U23-ch53, adc1

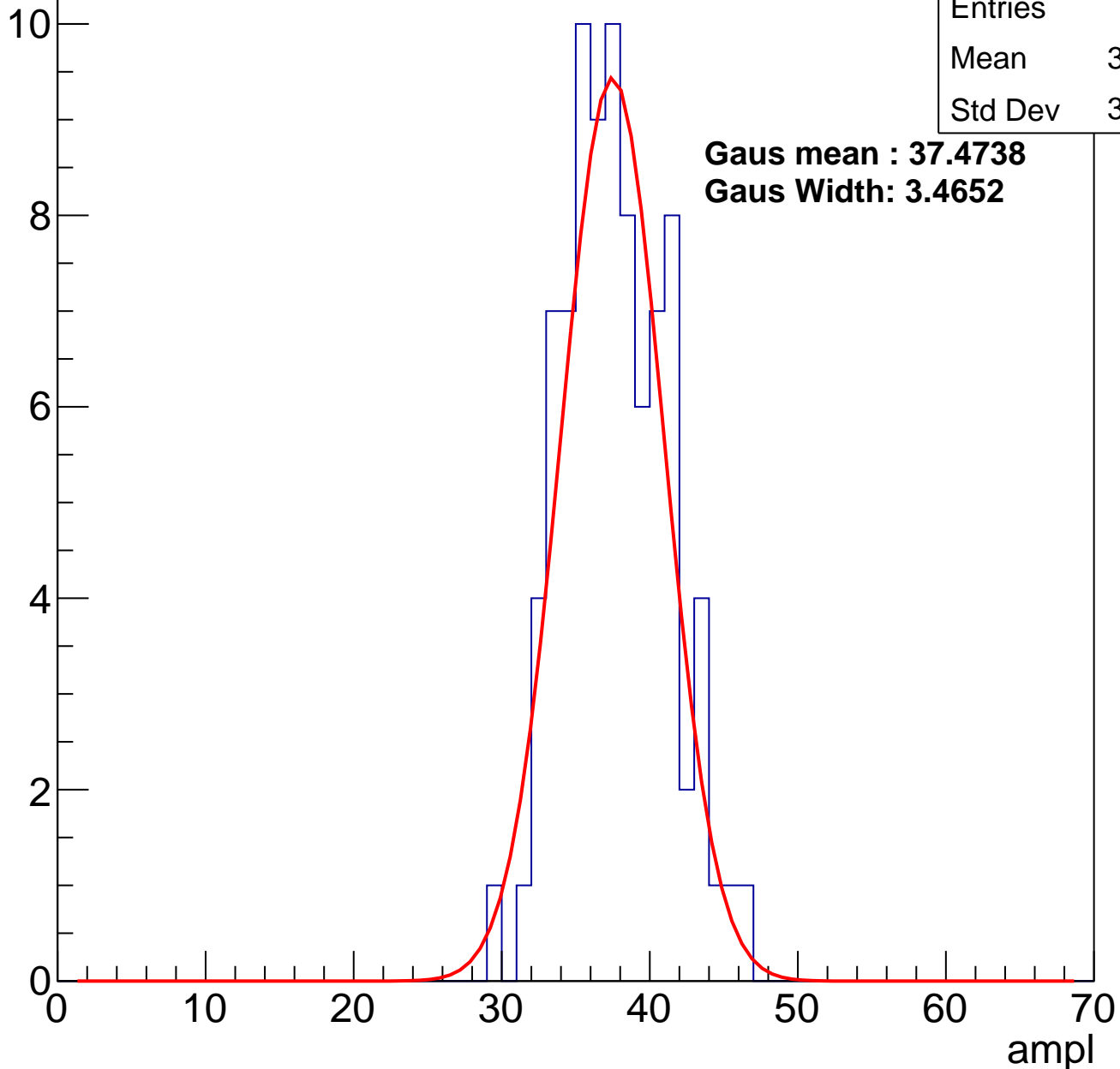
calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	37.22
Std Dev	3.442

Gaus mean : 37.4738

Gaus Width: 3.4652

Entry



B0L001S, U23-ch53, adc2

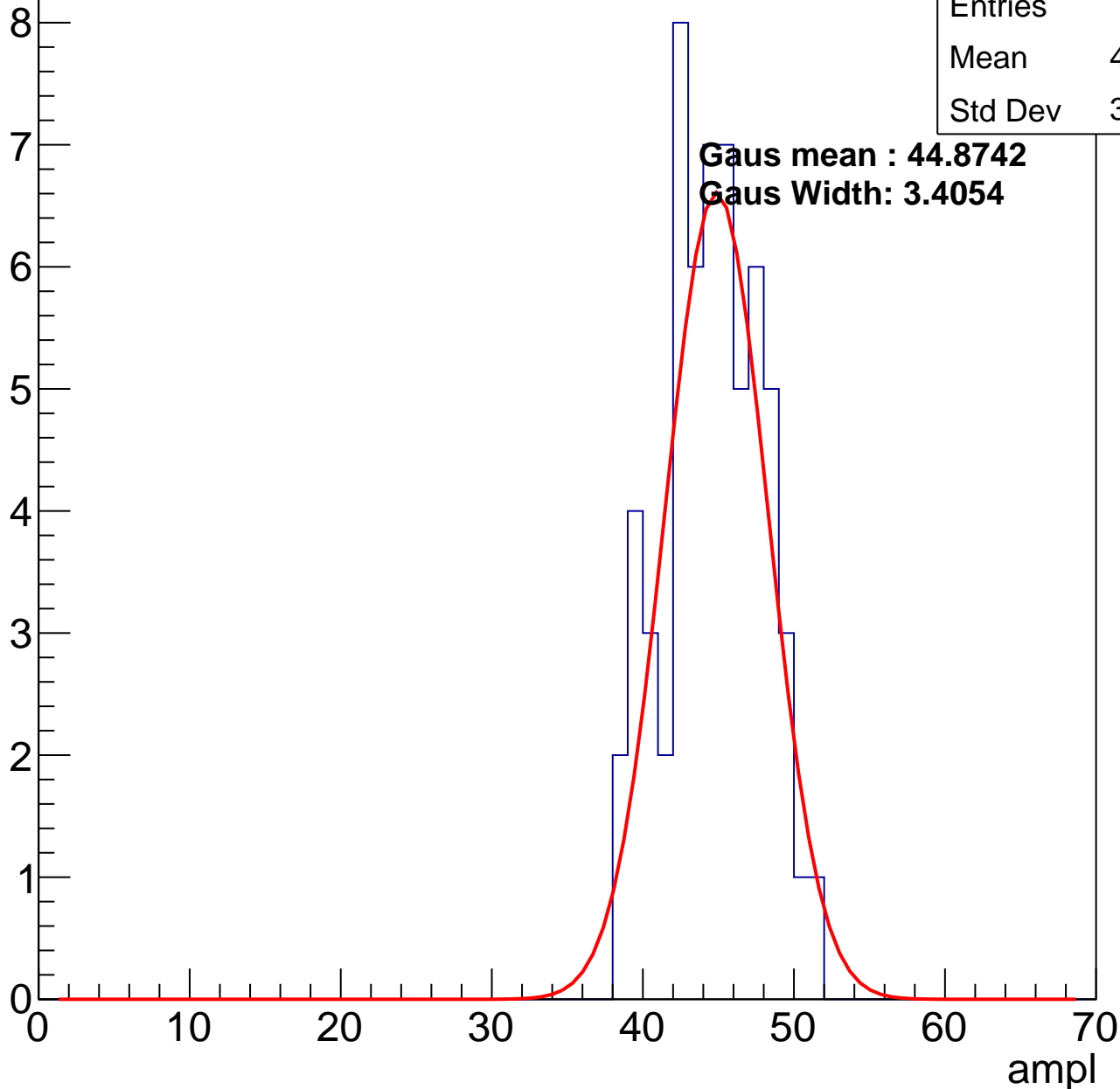
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.18
Std Dev	3.149

Gaus mean : 44.8742

Gaus Width: 3.4054

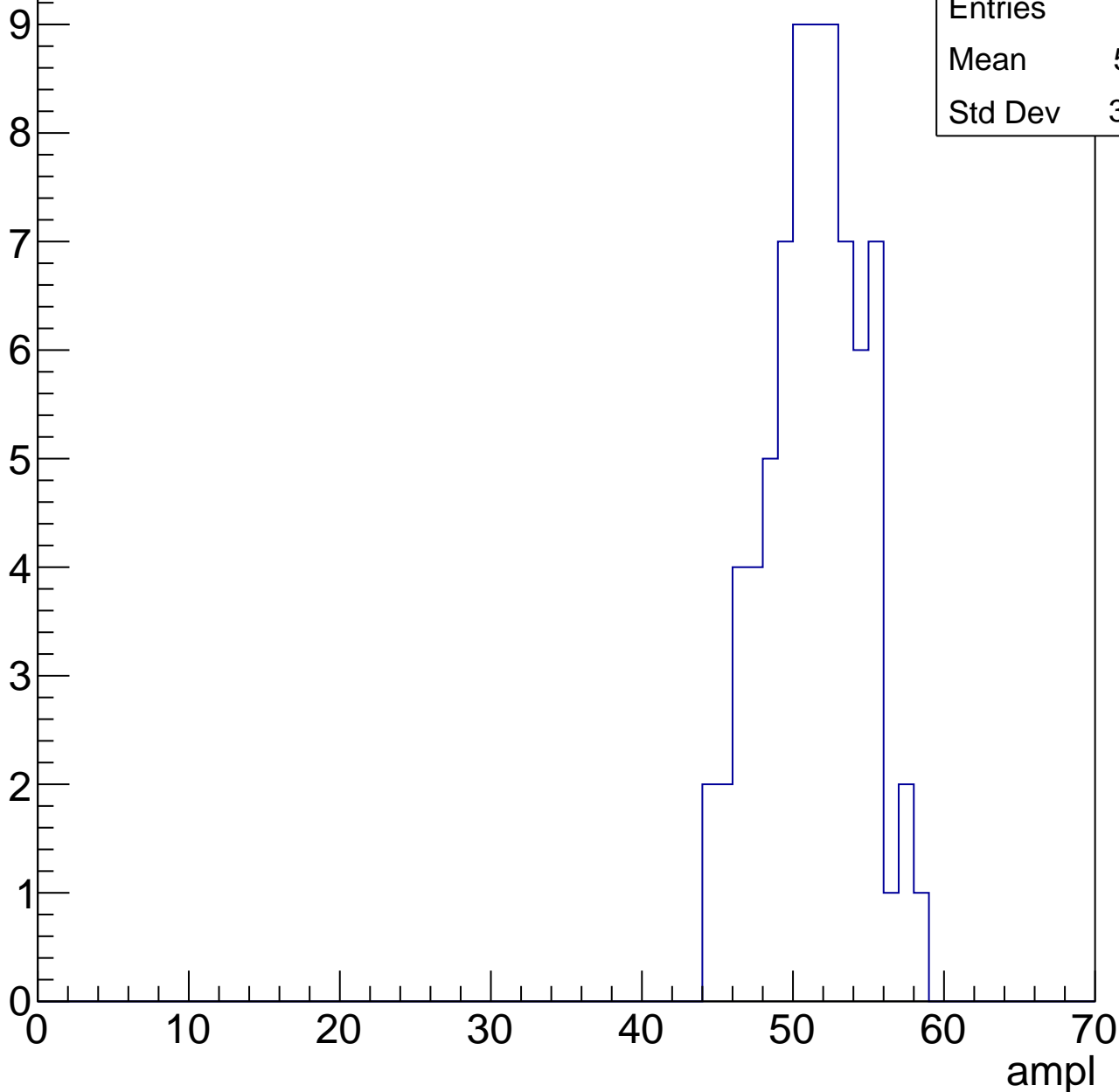


B0L001S, U23-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.91
Std Dev	3.192

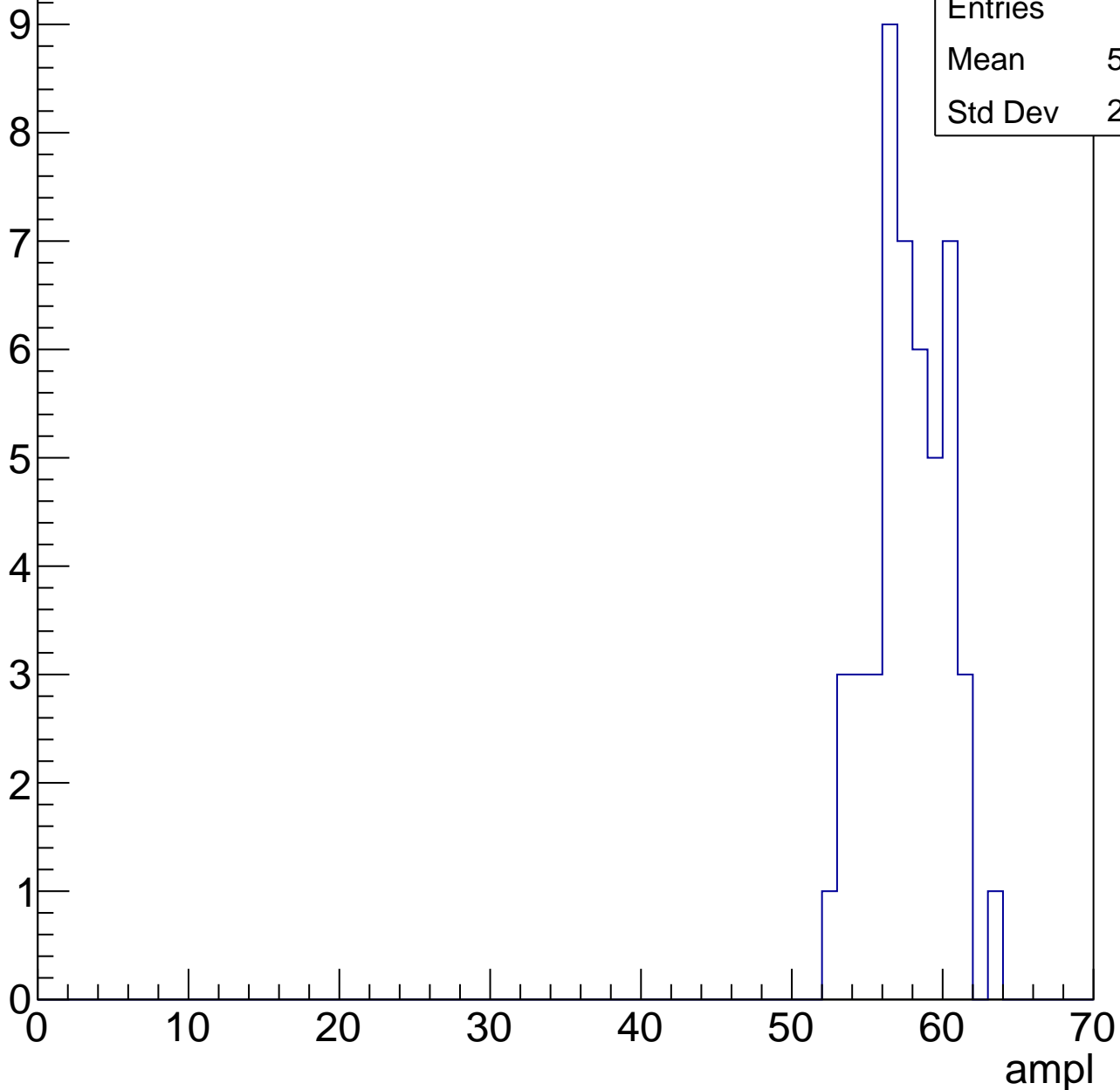


B0L001S, U23-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57.29
Std Dev	2.458

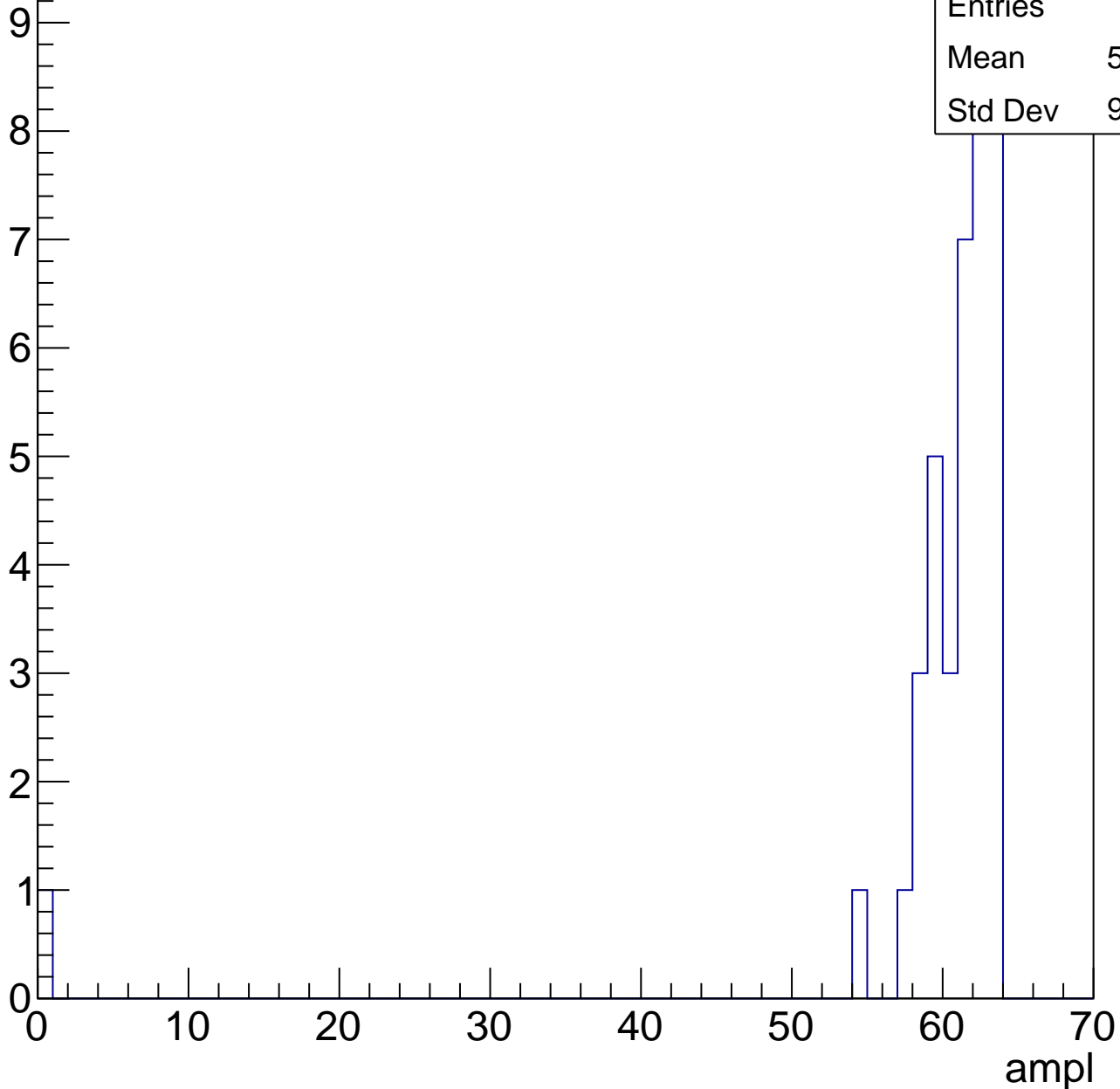


B0L001S, U23-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	59.28
Std Dev	9.827



B0L001S, U23-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch54, adc0

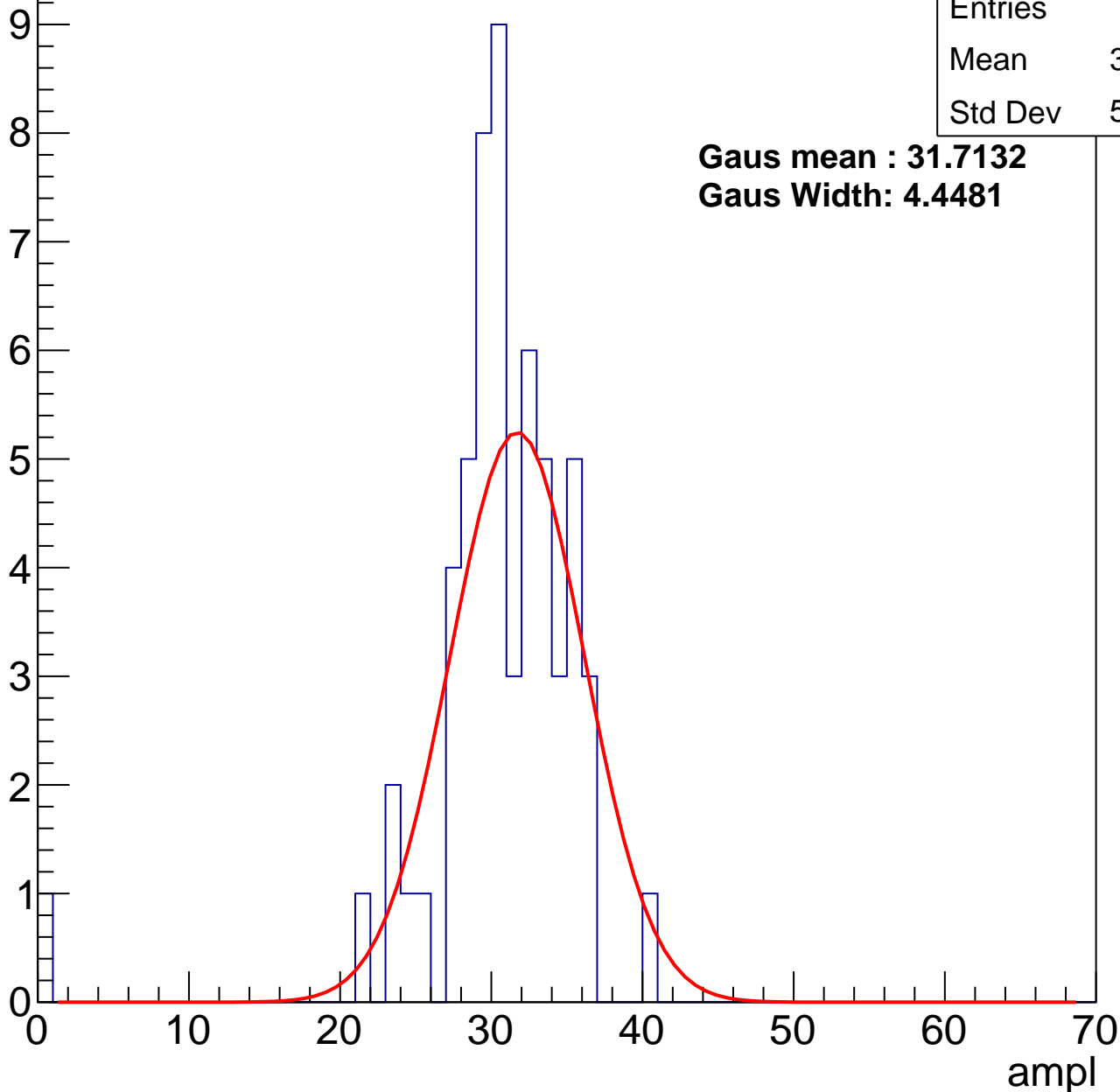
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	30.02
Std Dev	5.345

Gaus mean : 31.7132

Gaus Width: 4.4481



B0L001S, U23-ch54, adc1

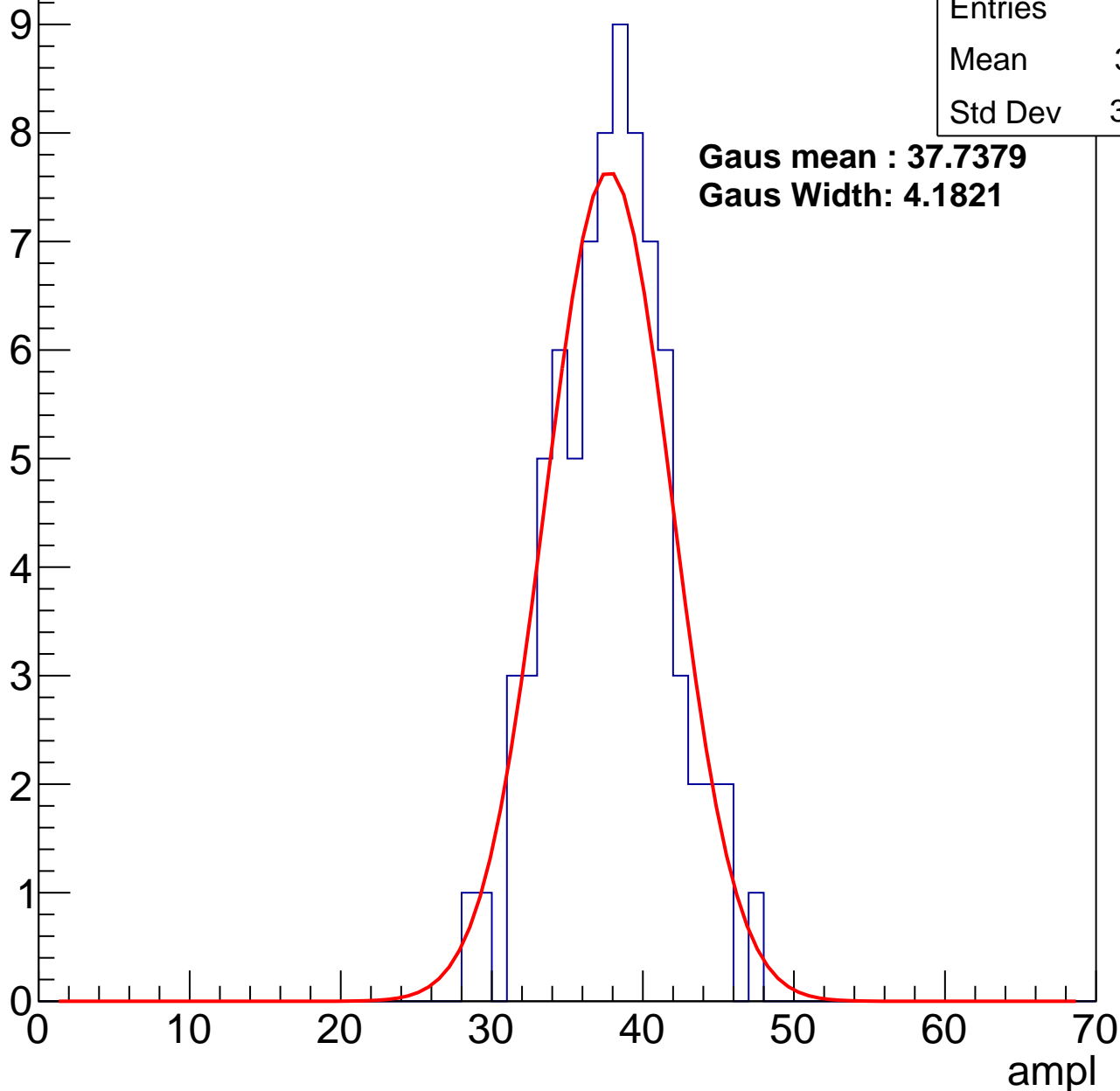
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	37.41
Std Dev	3.814

Gaus mean : 37.7379

Gaus Width: 4.1821



B0L001S, U23-ch54, adc2

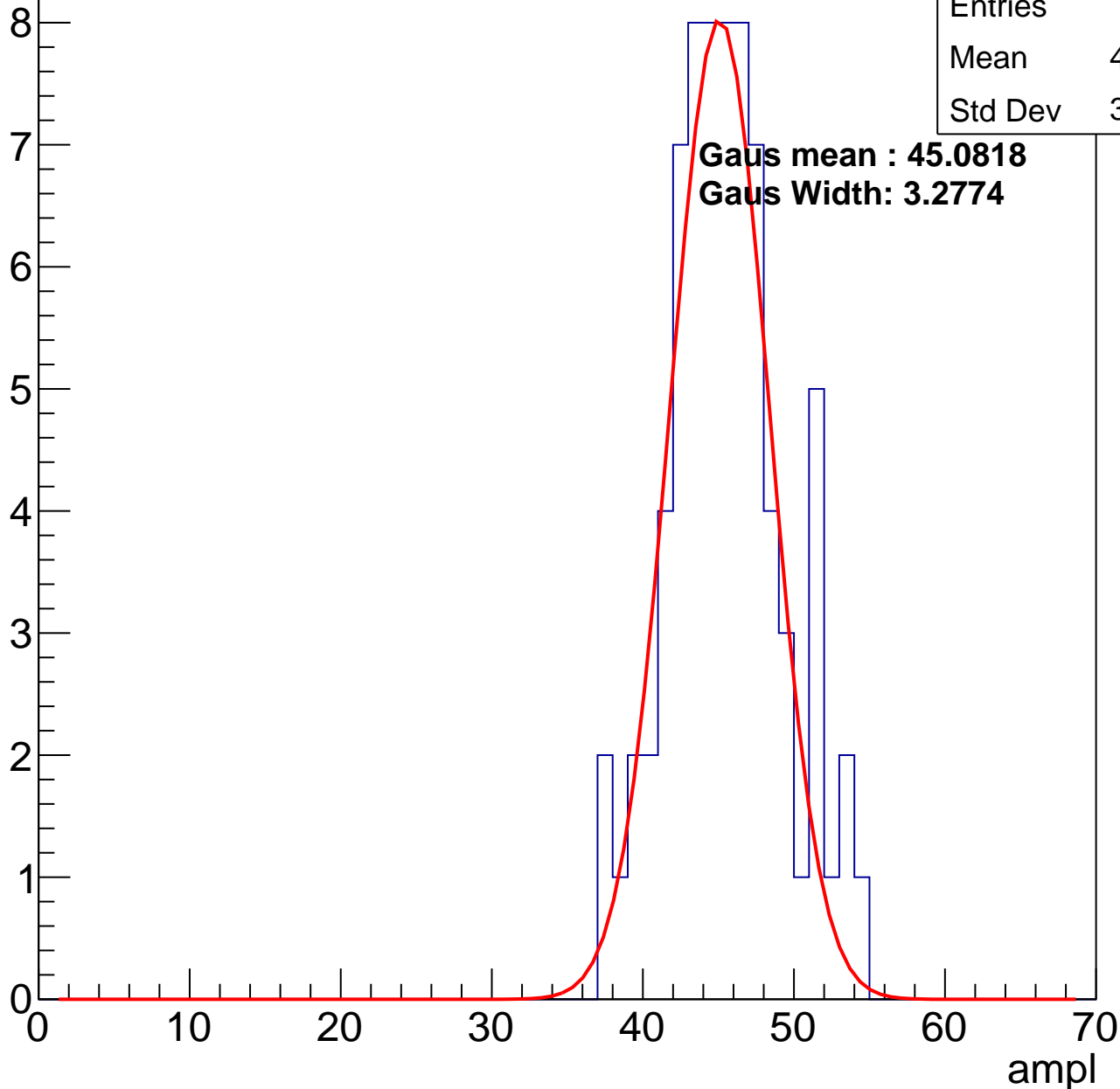
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	45.09
Std Dev	3.764

Gaus mean : 45.0818

Gaus Width: 3.2774

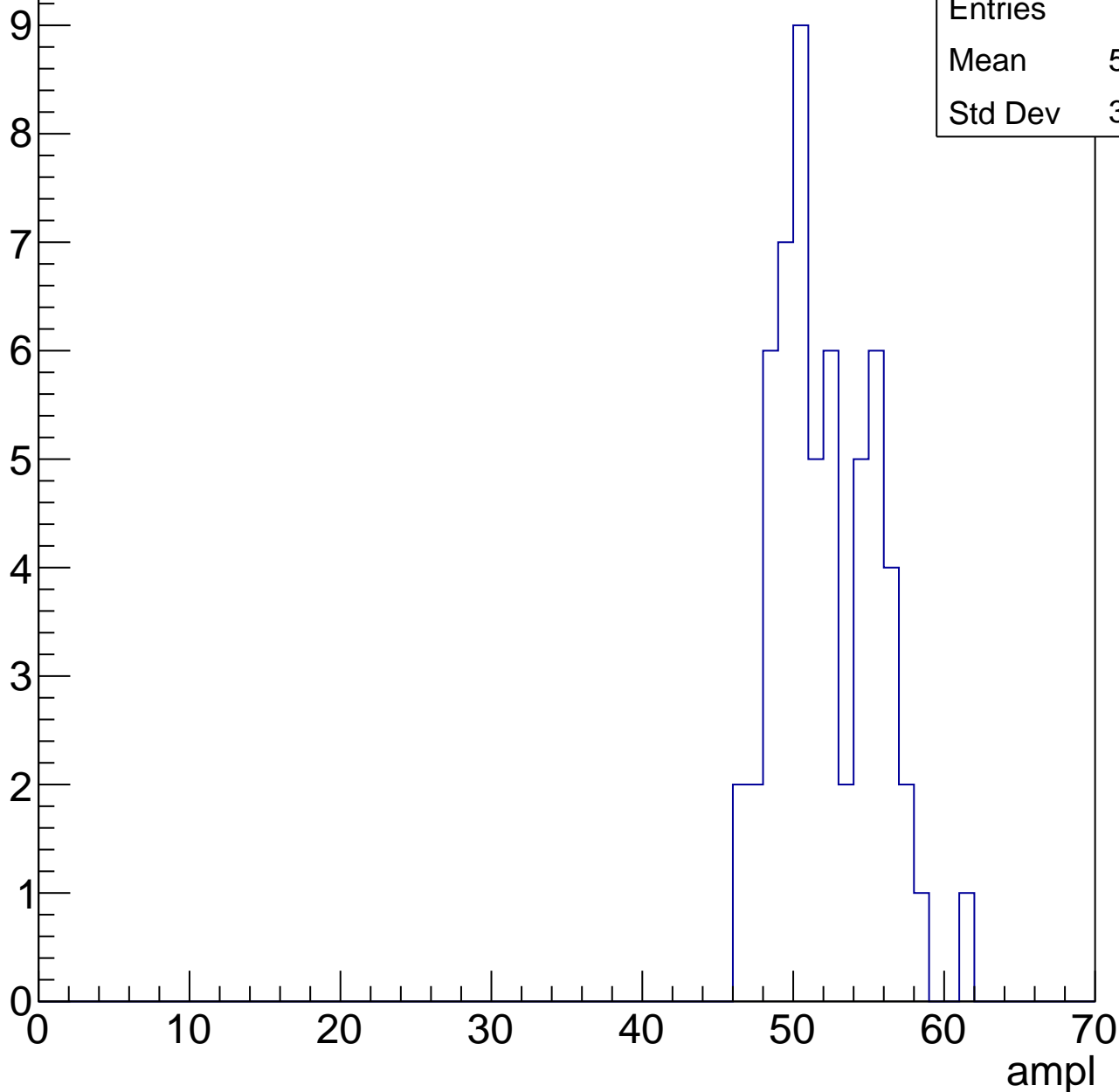


B0L001S, U23-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

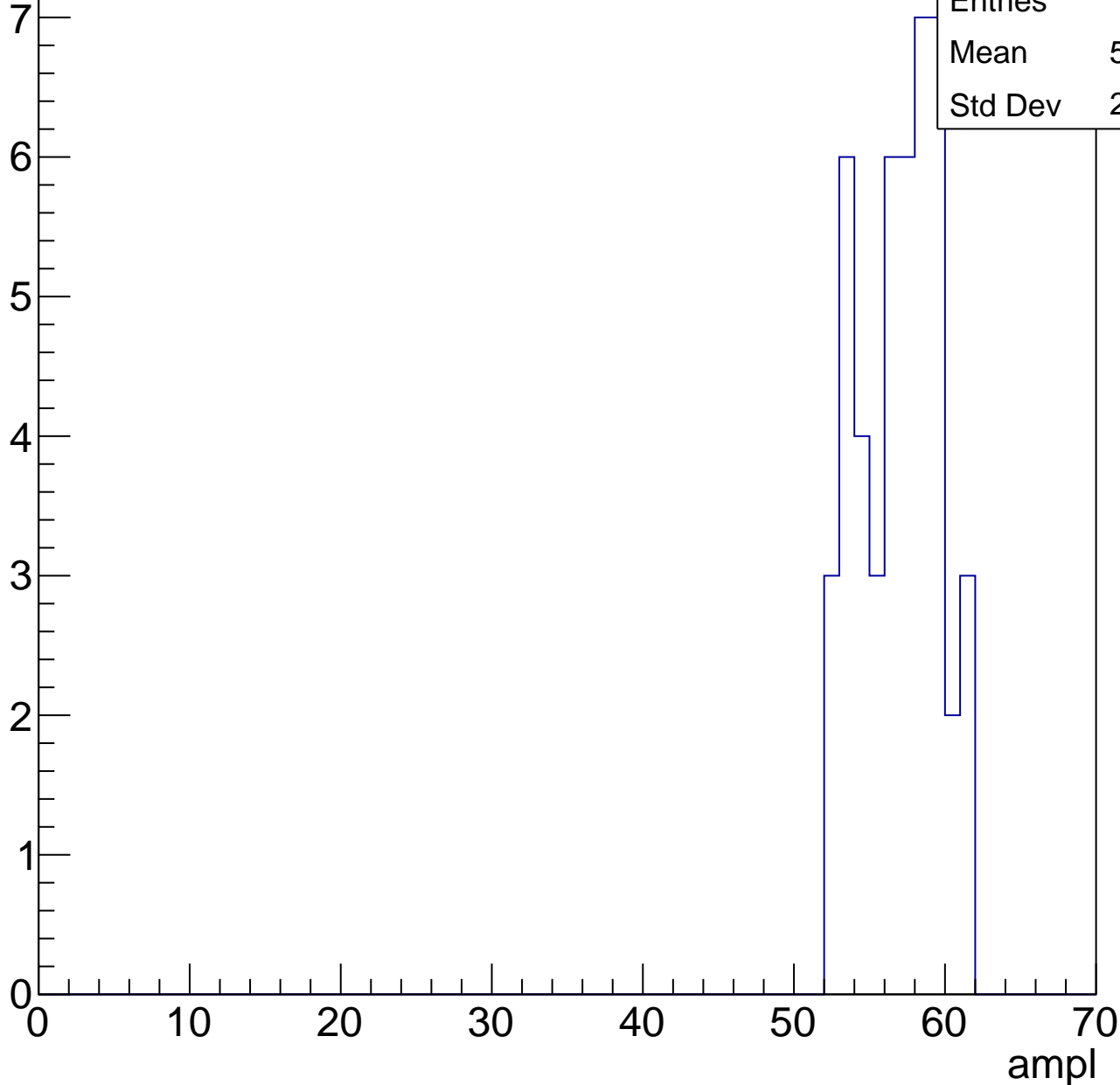
Entries	58
Mean	51.67
Std Dev	3.277



B0L001S, U23-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

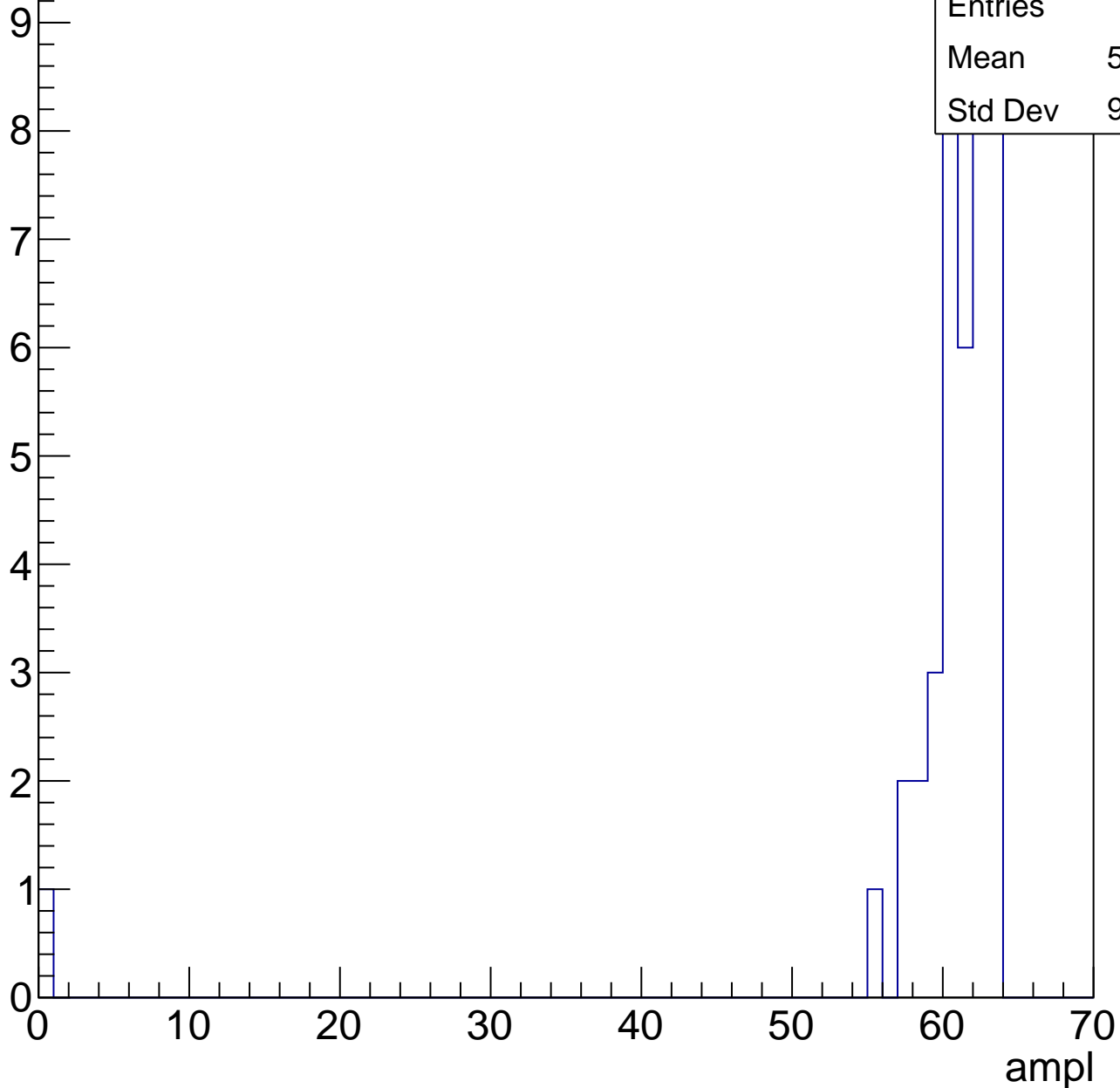


Entries	47
Mean	56.49
Std Dev	2.584

B0L001S, U23-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch55, adc0

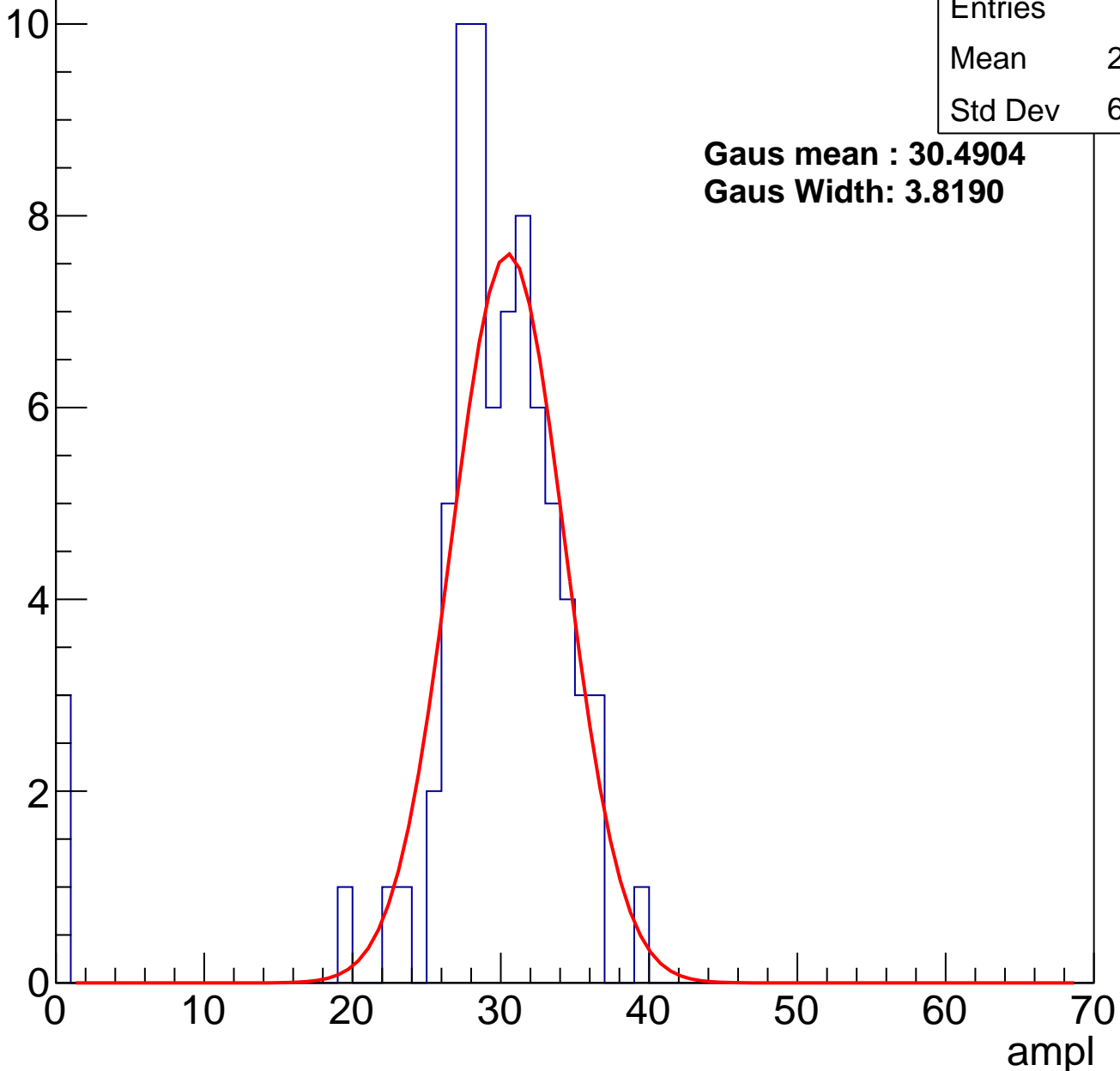
calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	28.57
Std Dev	6.736

Gaus mean : 30.4904

Gaus Width: 3.8190

Entry



B0L001S, U23-ch55, adc1

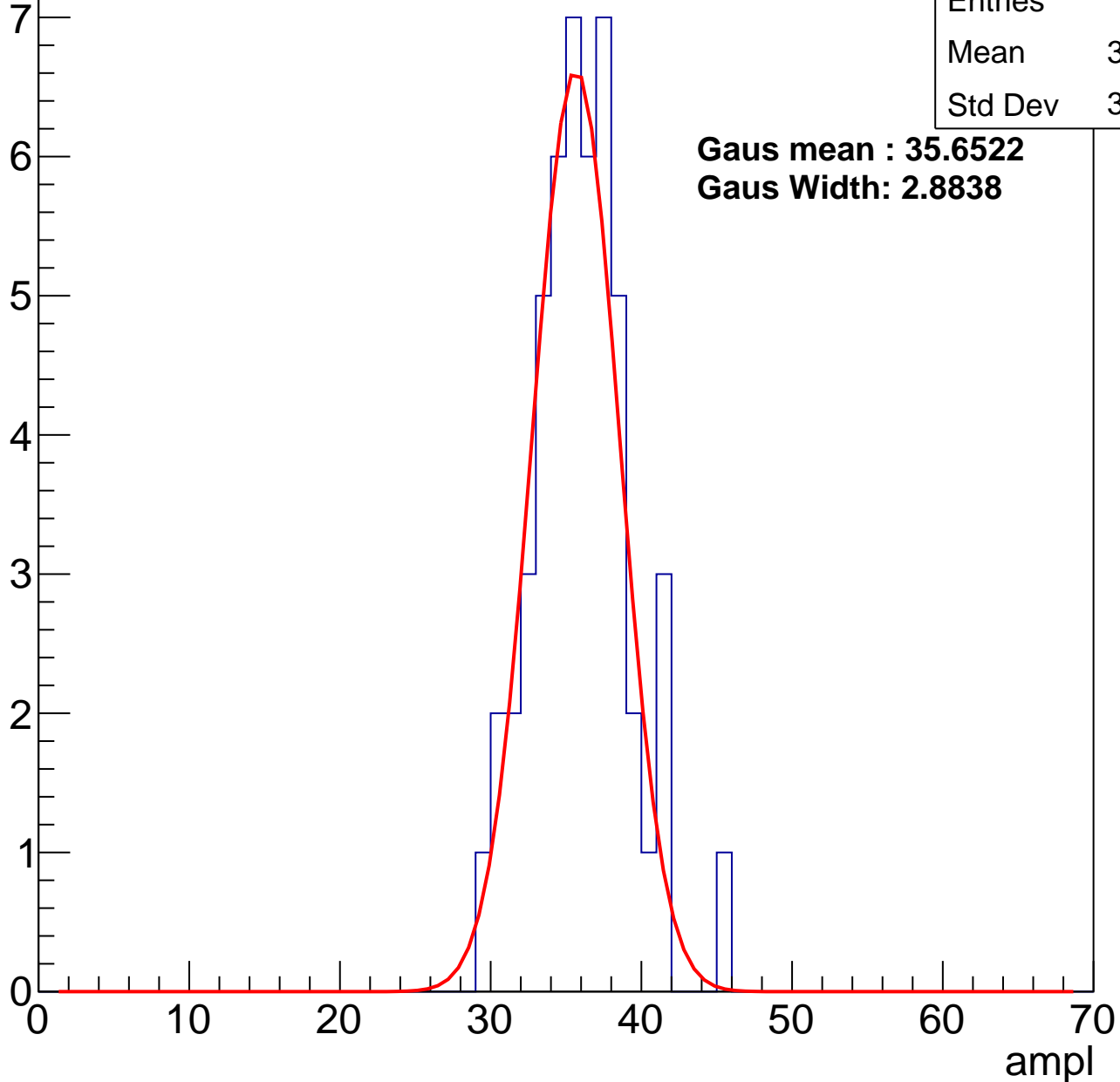
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	35.53
Std Dev	3.133

Gaus mean : 35.6522

Gaus Width: 2.8838



B0L001S, U23-ch55, adc2

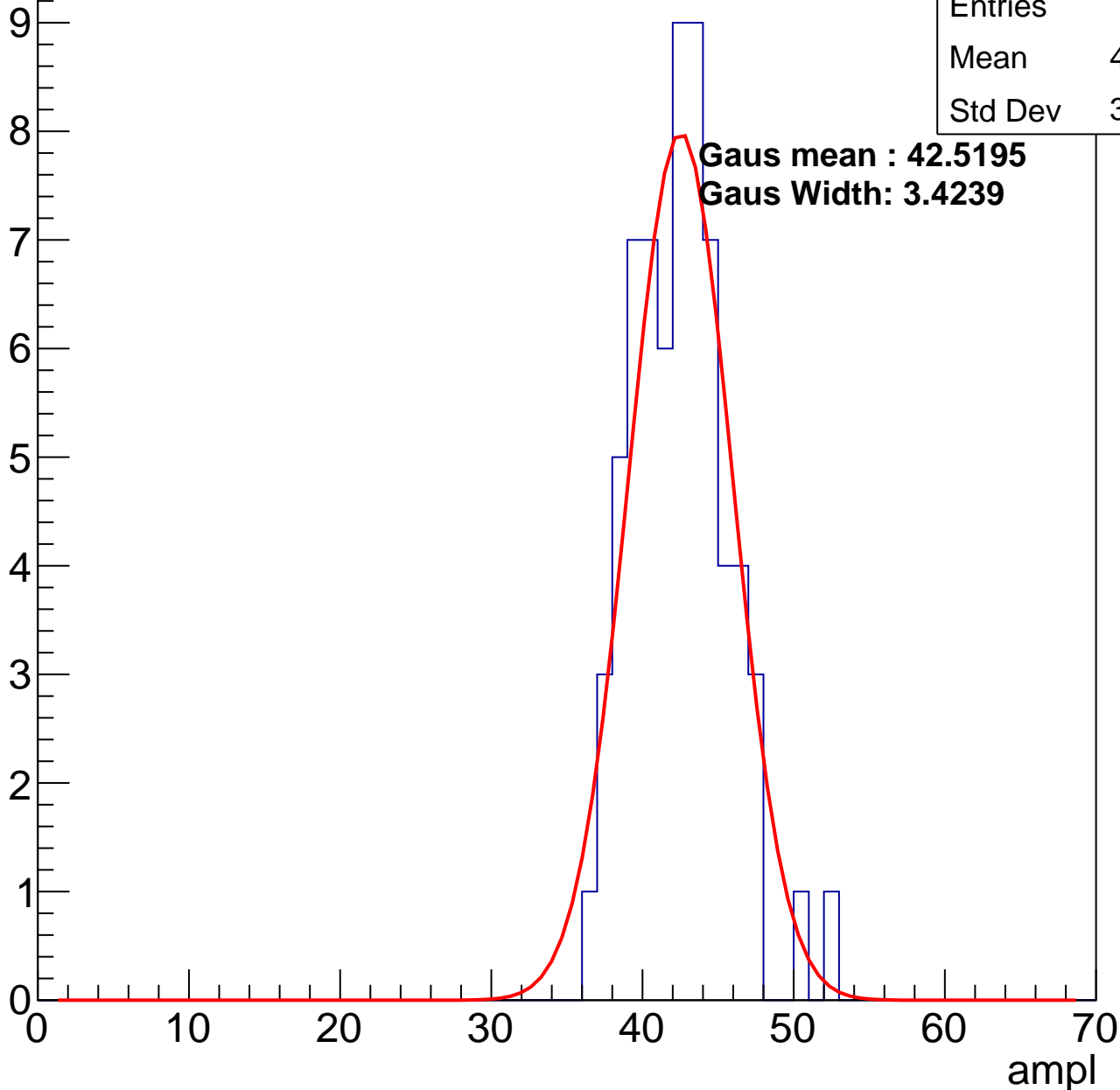
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.03
Std Dev	3.148

Gaus mean : 42.5195

Gaus Width: 3.4239



B0L001S, U23-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

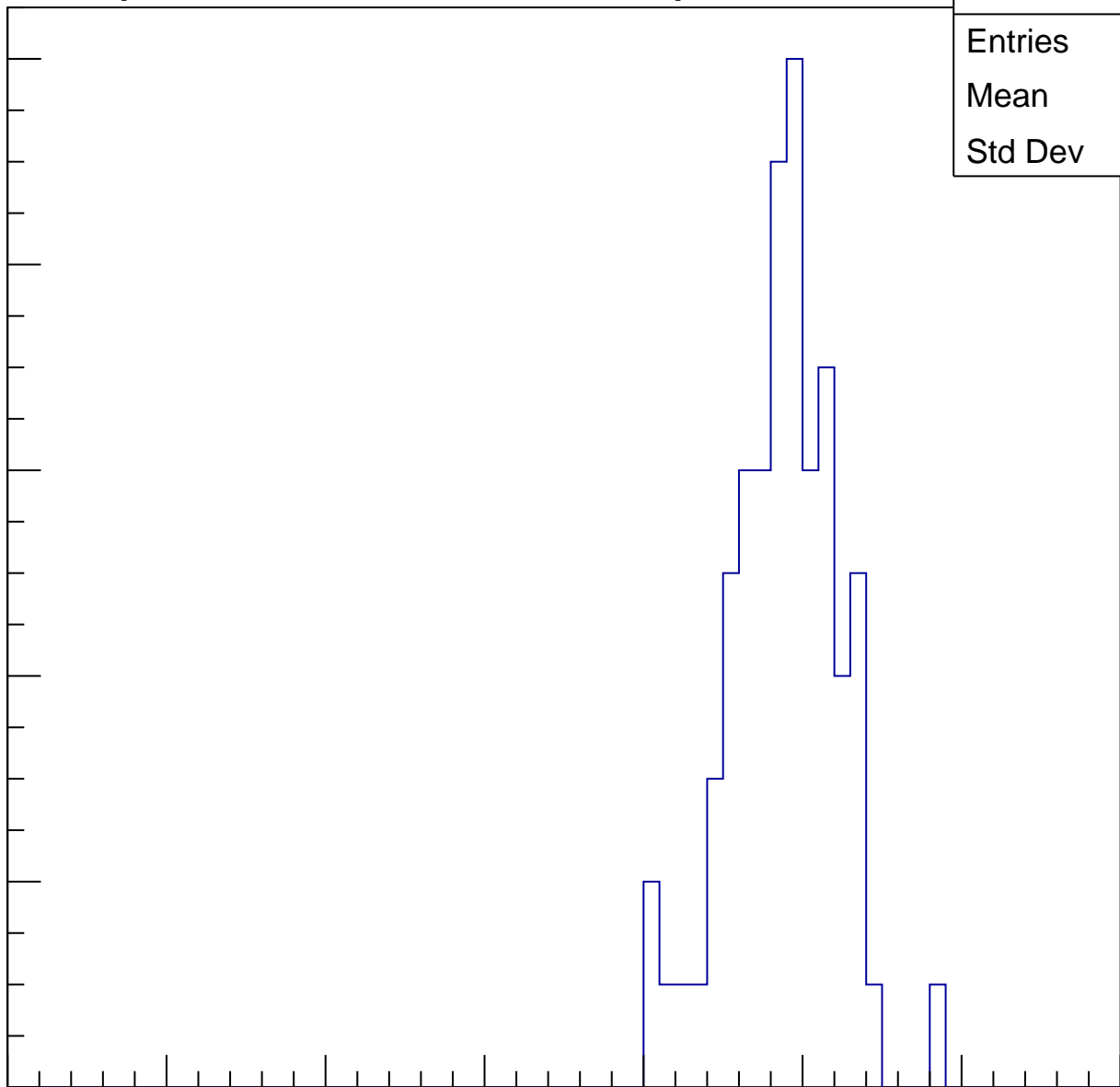
Entries	68
Mean	48.31
Std Dev	3.379

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

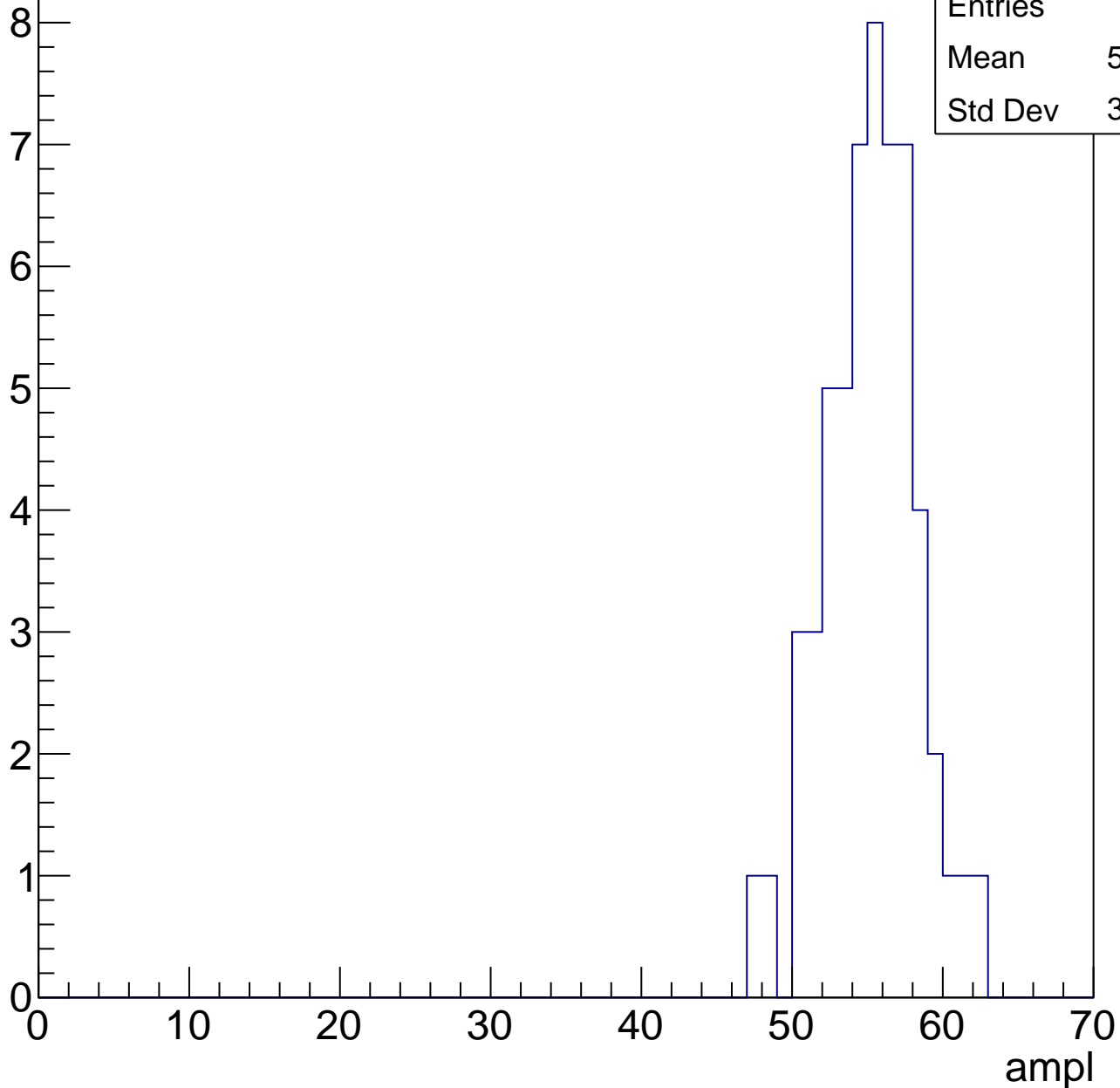


B0L001S, U23-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	54.73
Std Dev	3.038



B0L001S, U23-ch55, adc5

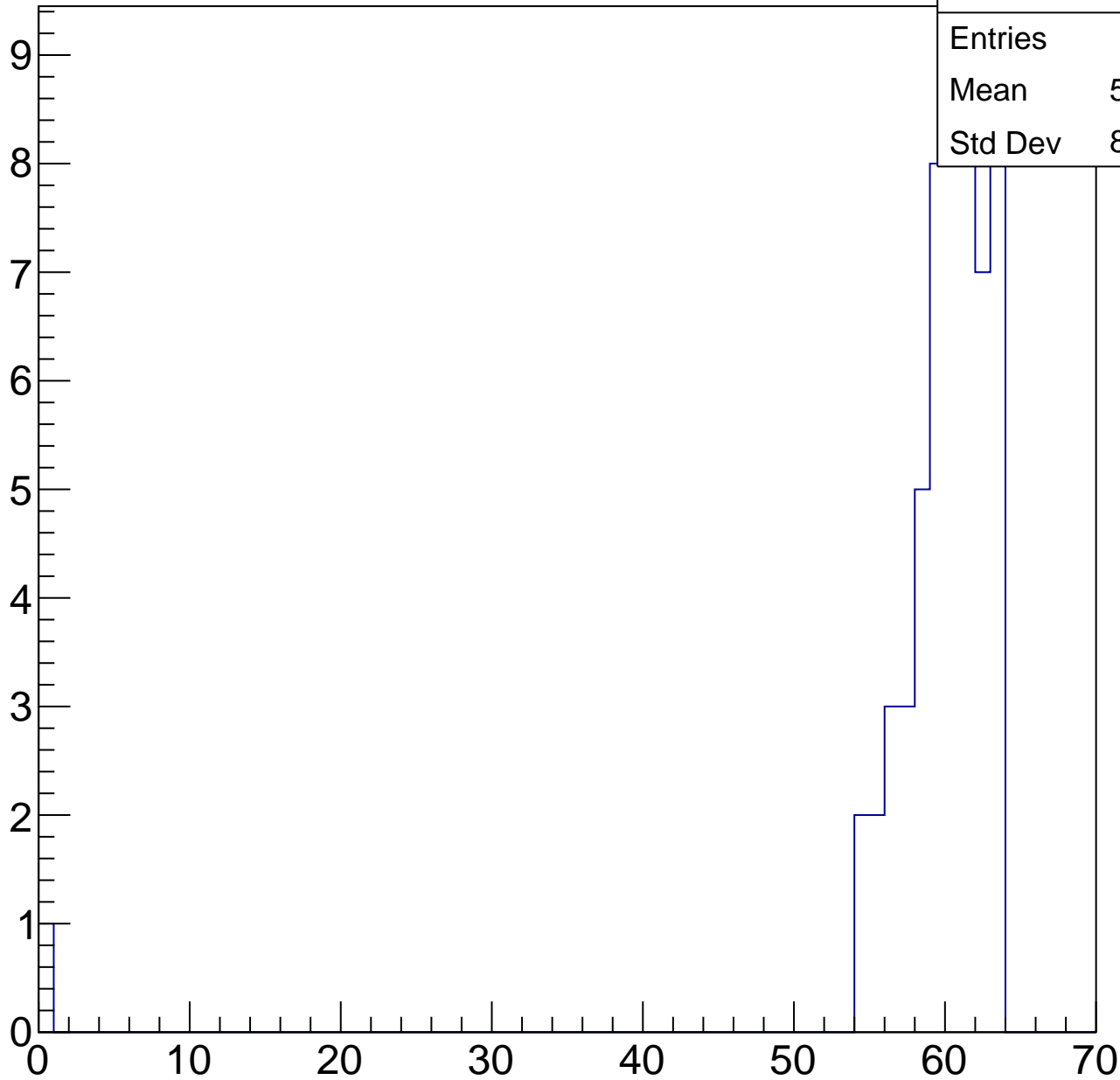
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.68
Std Dev	8.277

ampl



B0L001S, U23-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch56, adc0

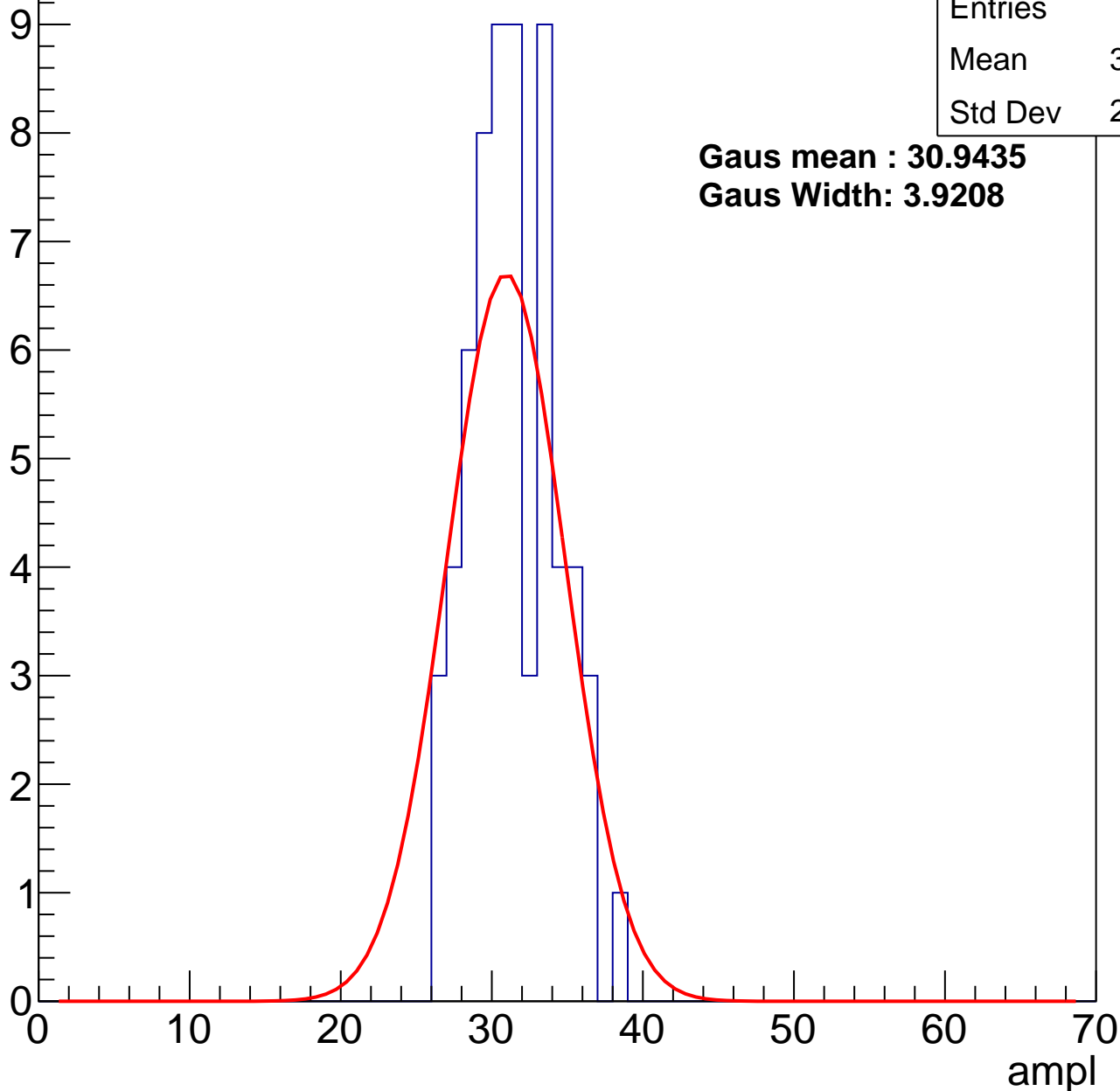
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.95
Std Dev	2.808

Gaus mean : 30.9435

Gaus Width: 3.9208



B0L001S, U23-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37.69
Std Dev	3.058

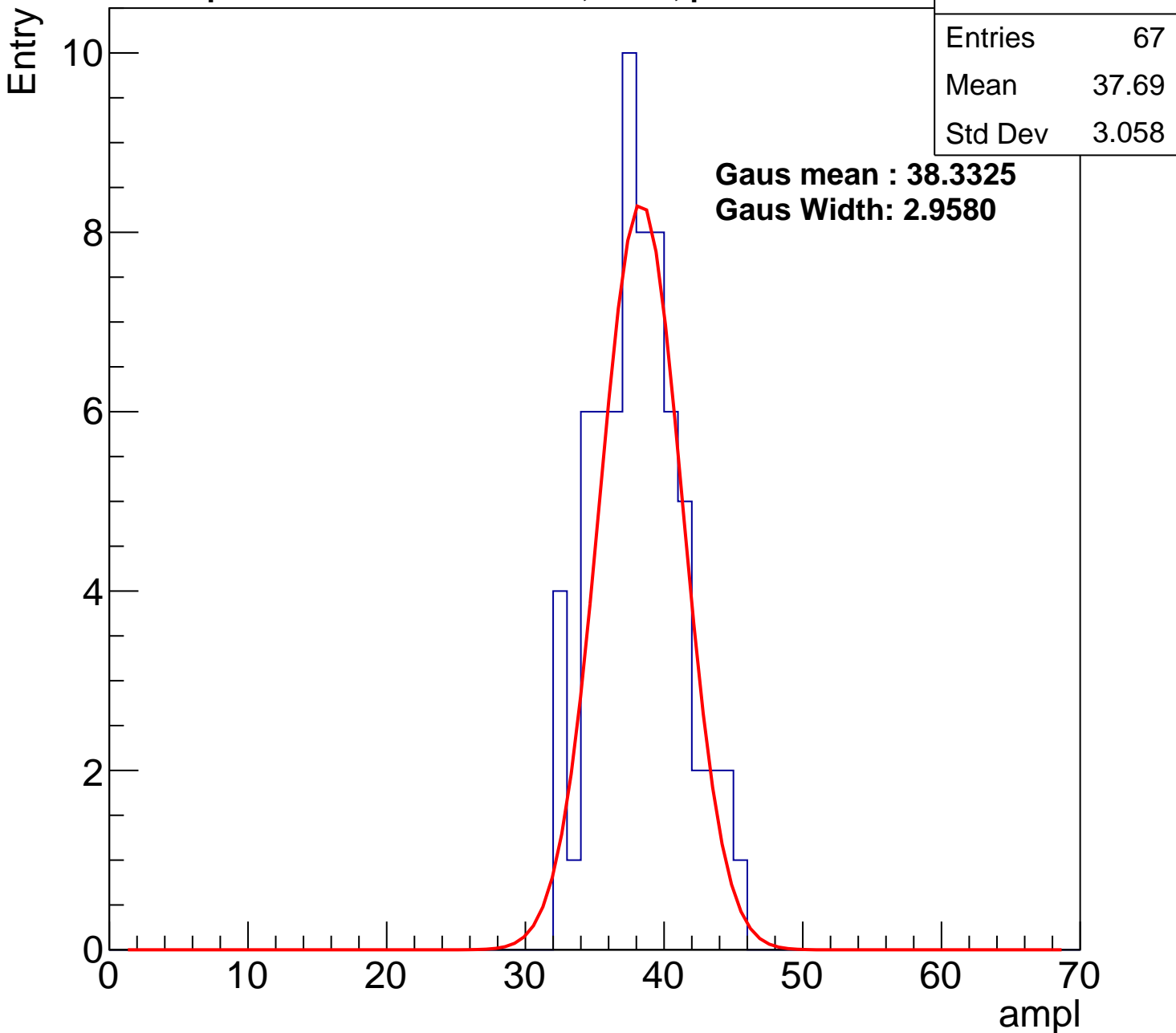
Gaus mean : 38.3325
Gaus Width: 2.9580

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch56, adc2

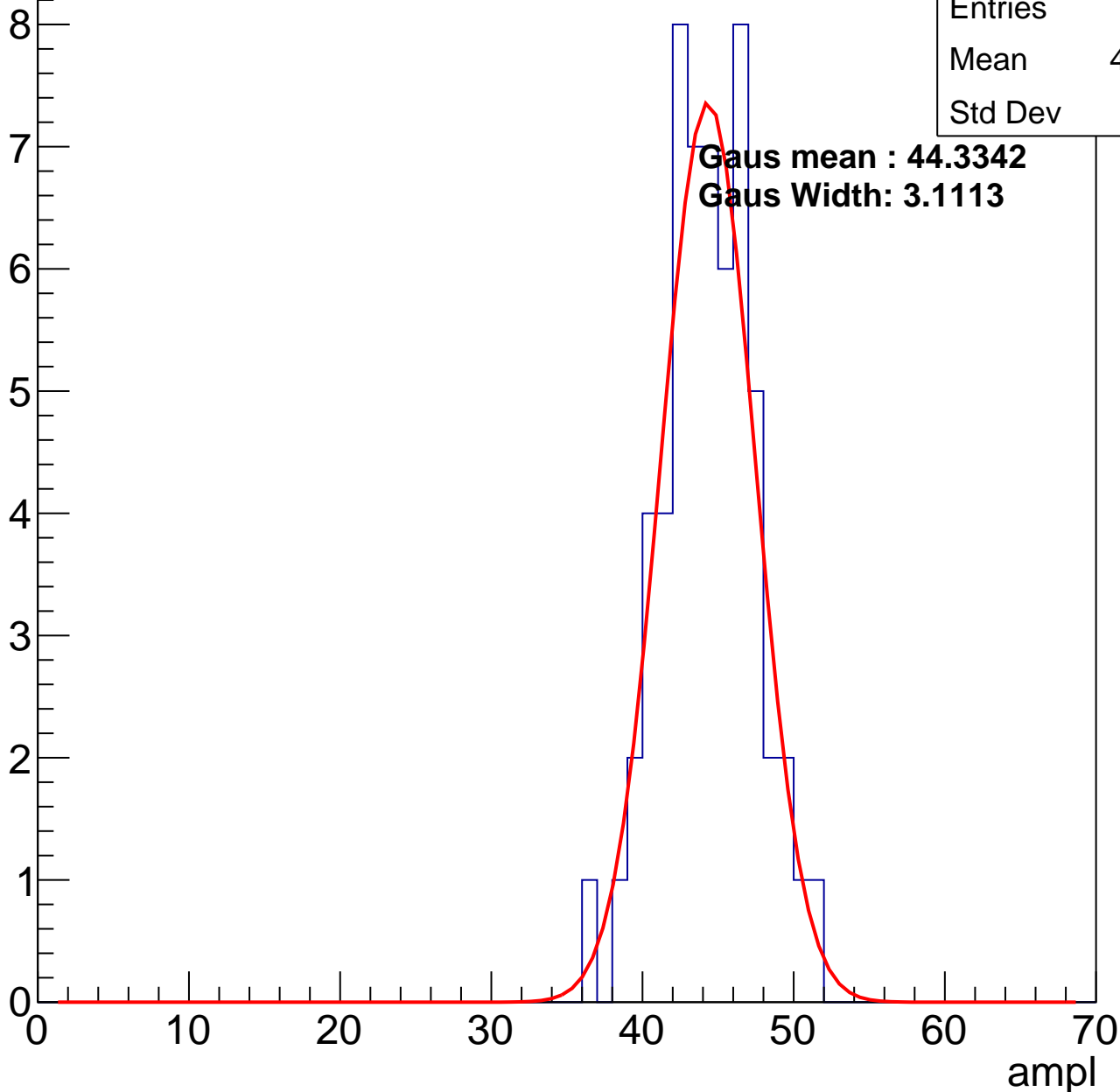
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.88
Std Dev	3.02

Gaus mean : 44.3342

Gaus Width: 3.1113

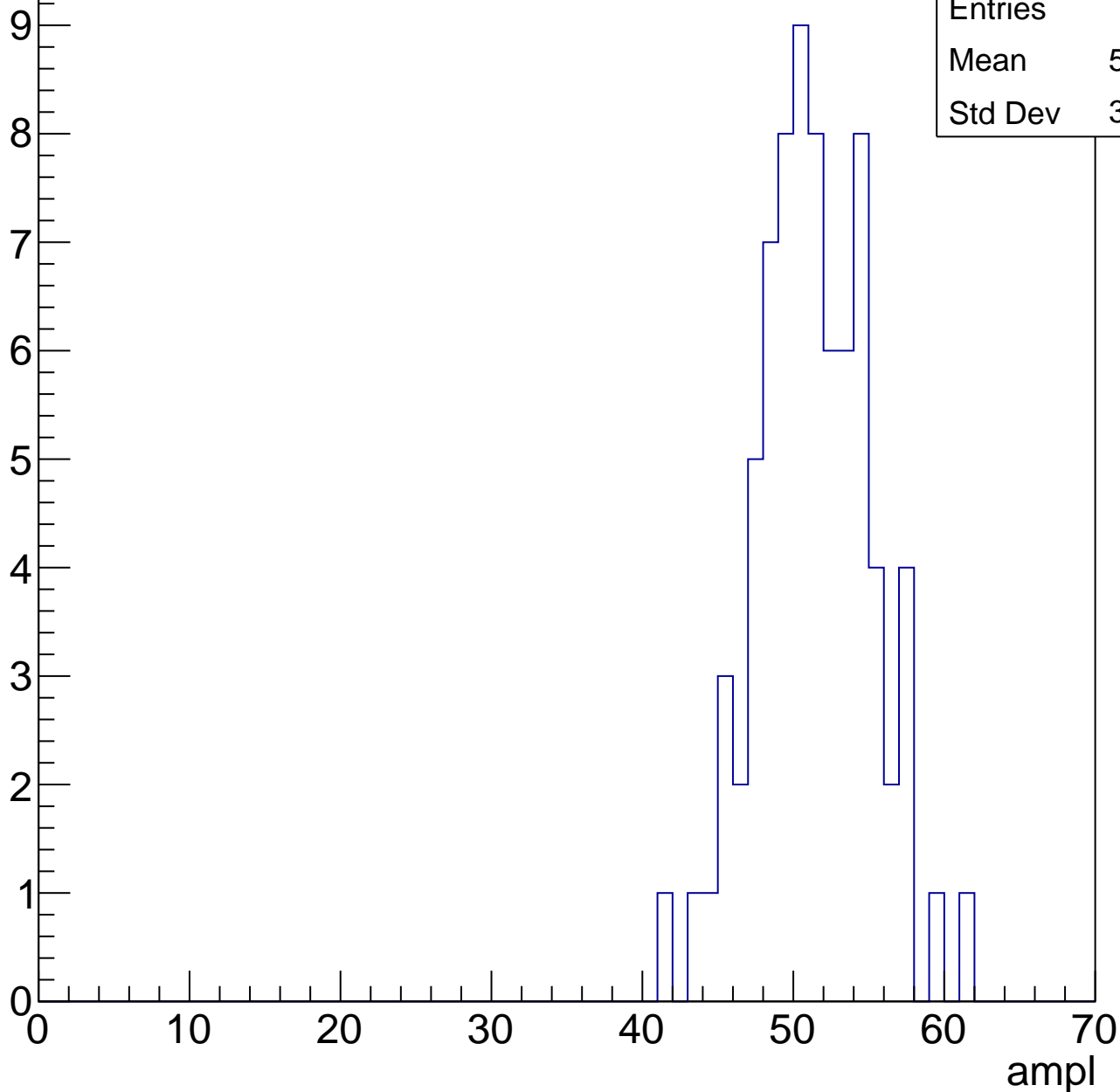


B0L001S, U23-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	50.88
Std Dev	3.742

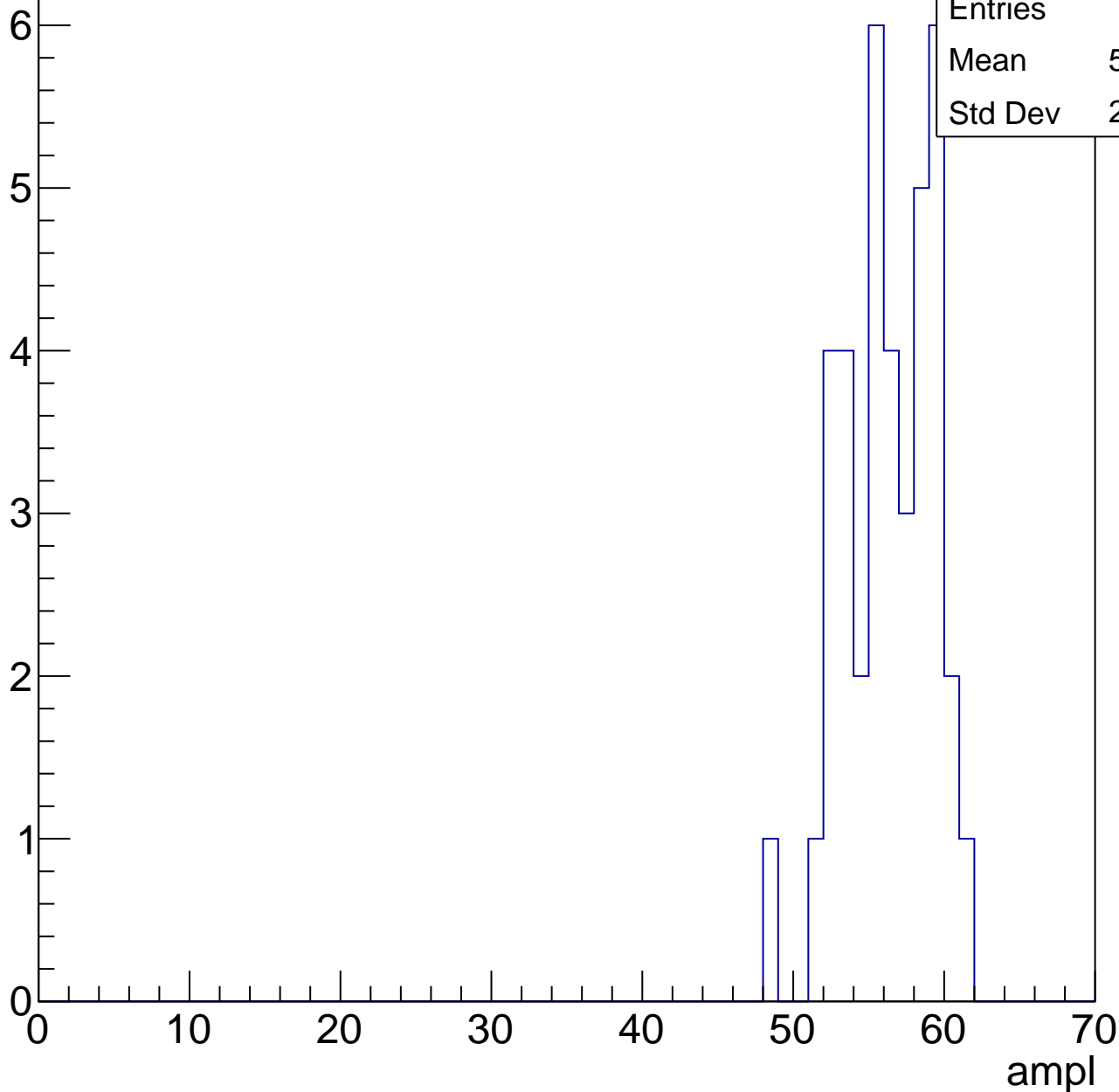


B0L001S, U23-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	55.82
Std Dev	2.934

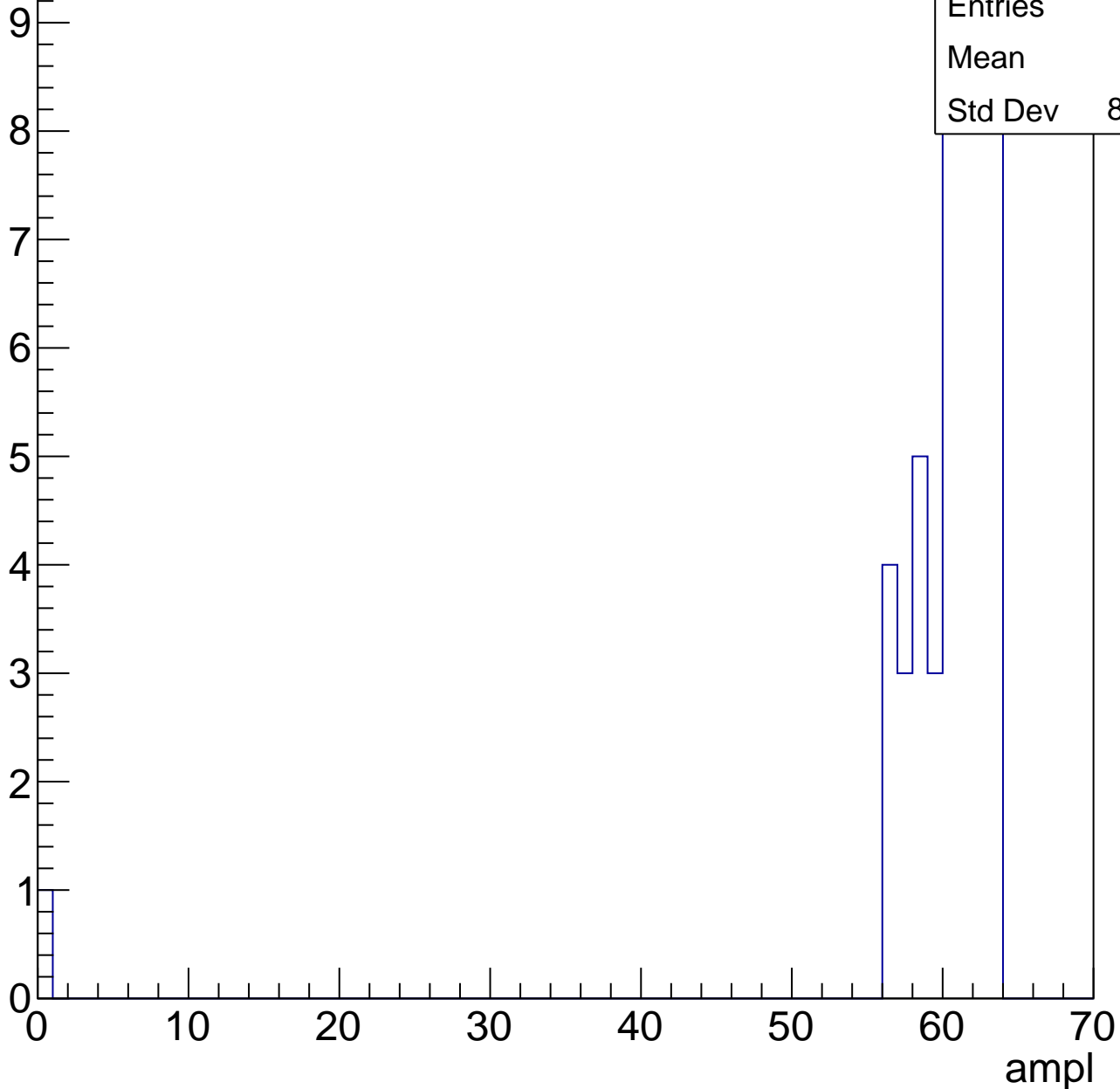


B0L001S, U23-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	59.1
Std Dev	8.716



B0L001S, U23-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch57, adc0

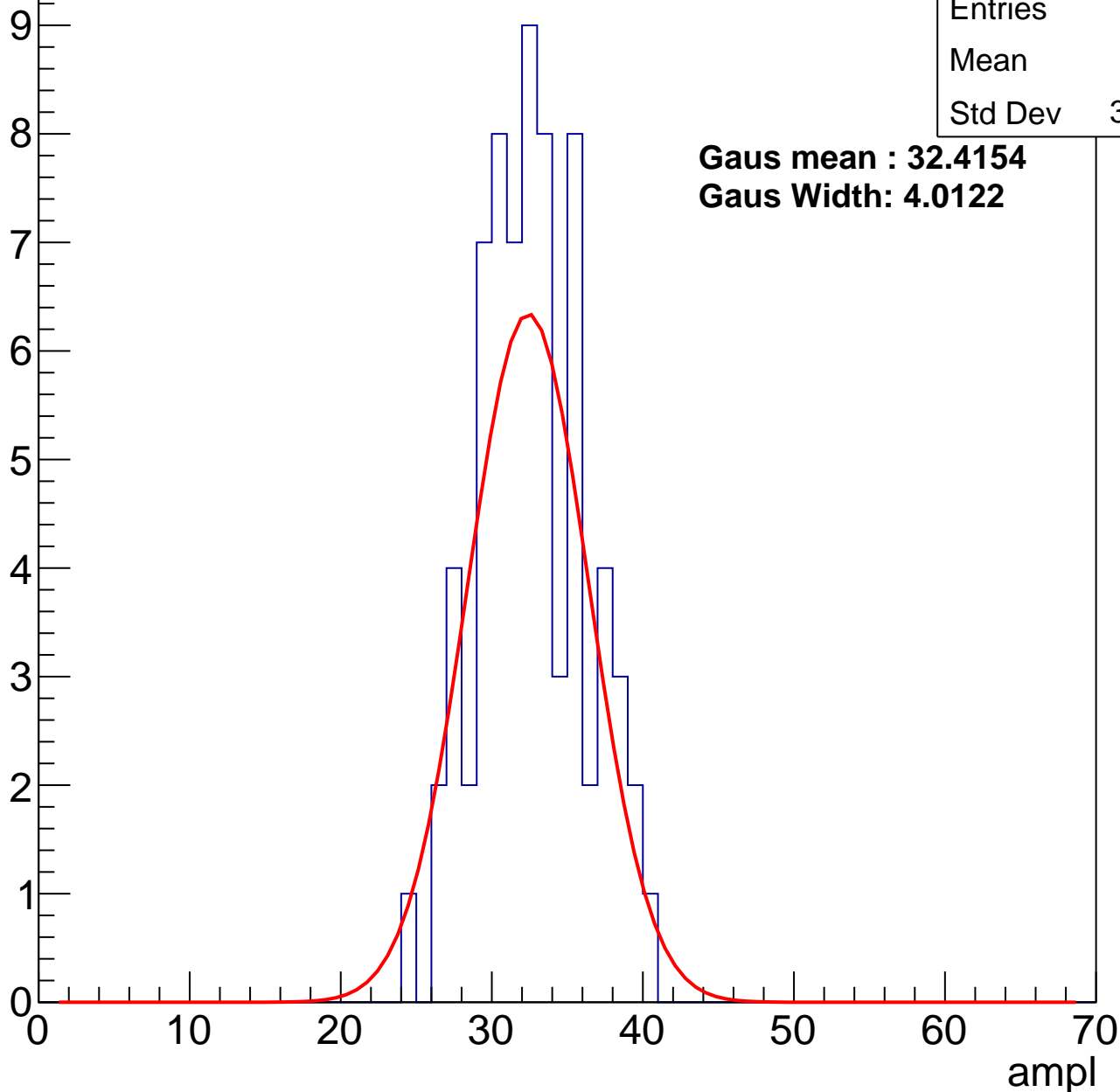
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	32.2
Std Dev	3.483

Gaus mean : 32.4154

Gaus Width: 4.0122



B0L001S, U23-ch57, adc1

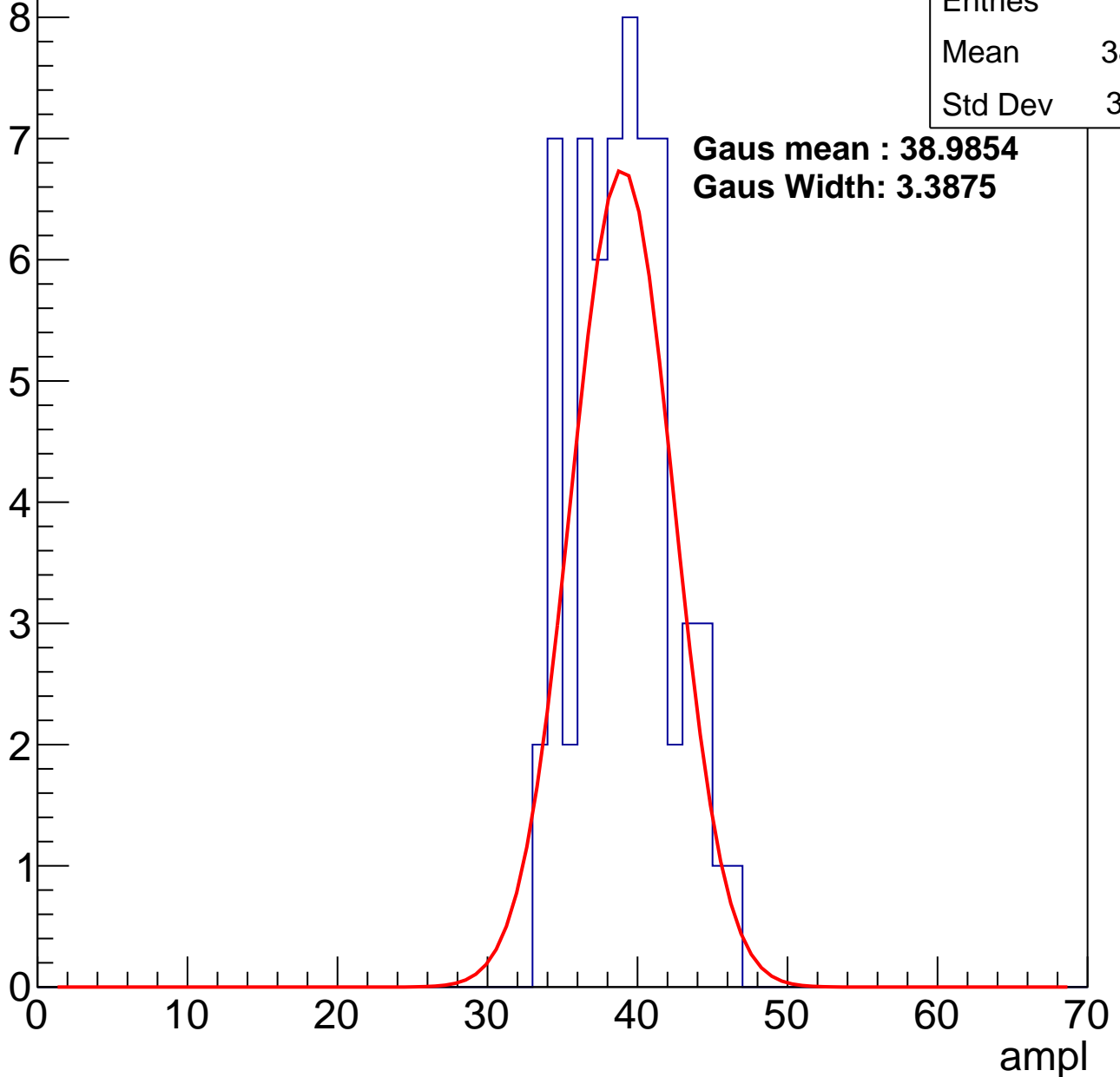
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.56
Std Dev	3.141

Gaus mean : 38.9854

Gaus Width: 3.3875



B0L001S, U23-ch57, adc2

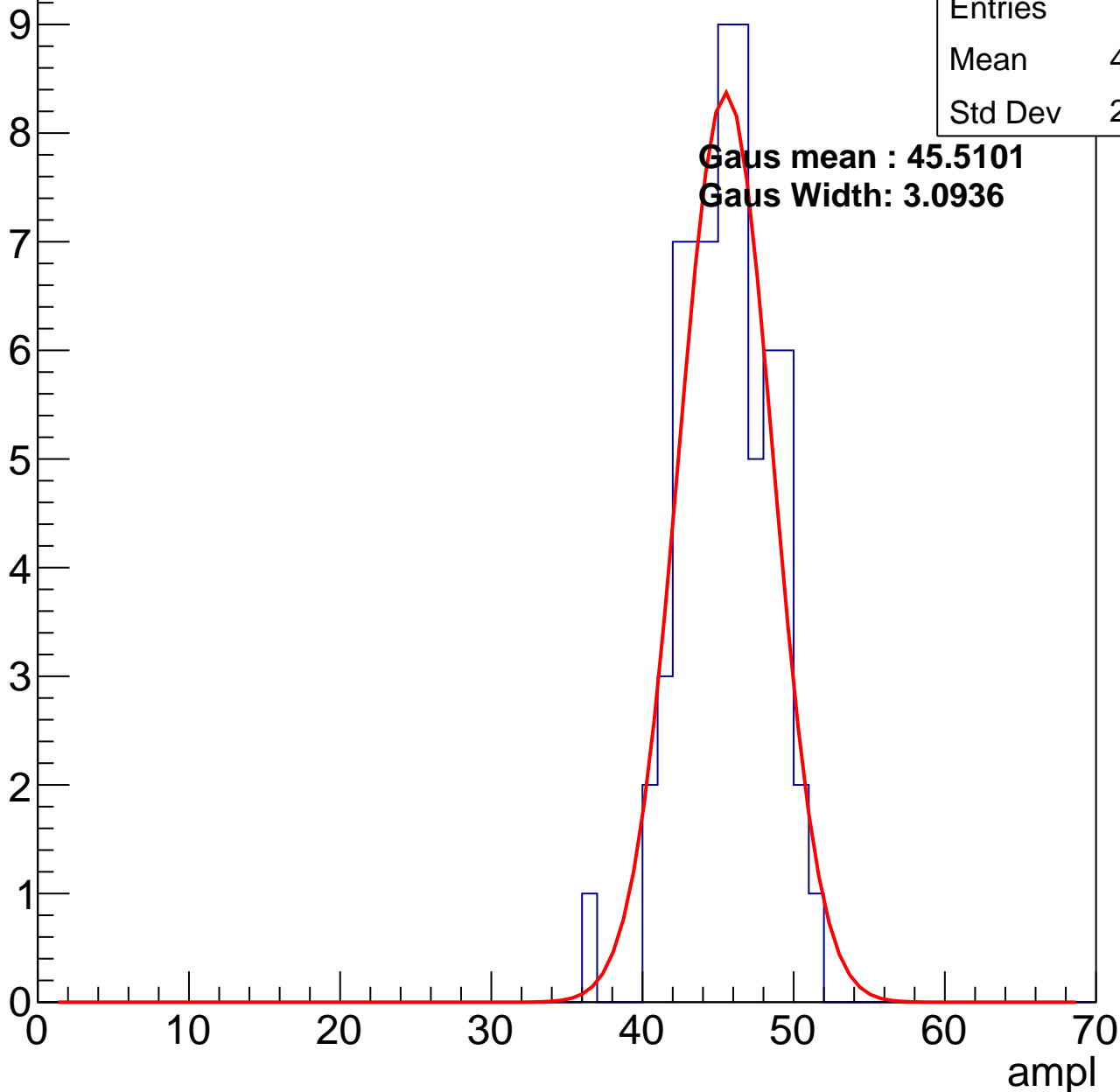
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.06
Std Dev	2.887

Gaus mean : 45.5101

Gaus Width: 3.0936

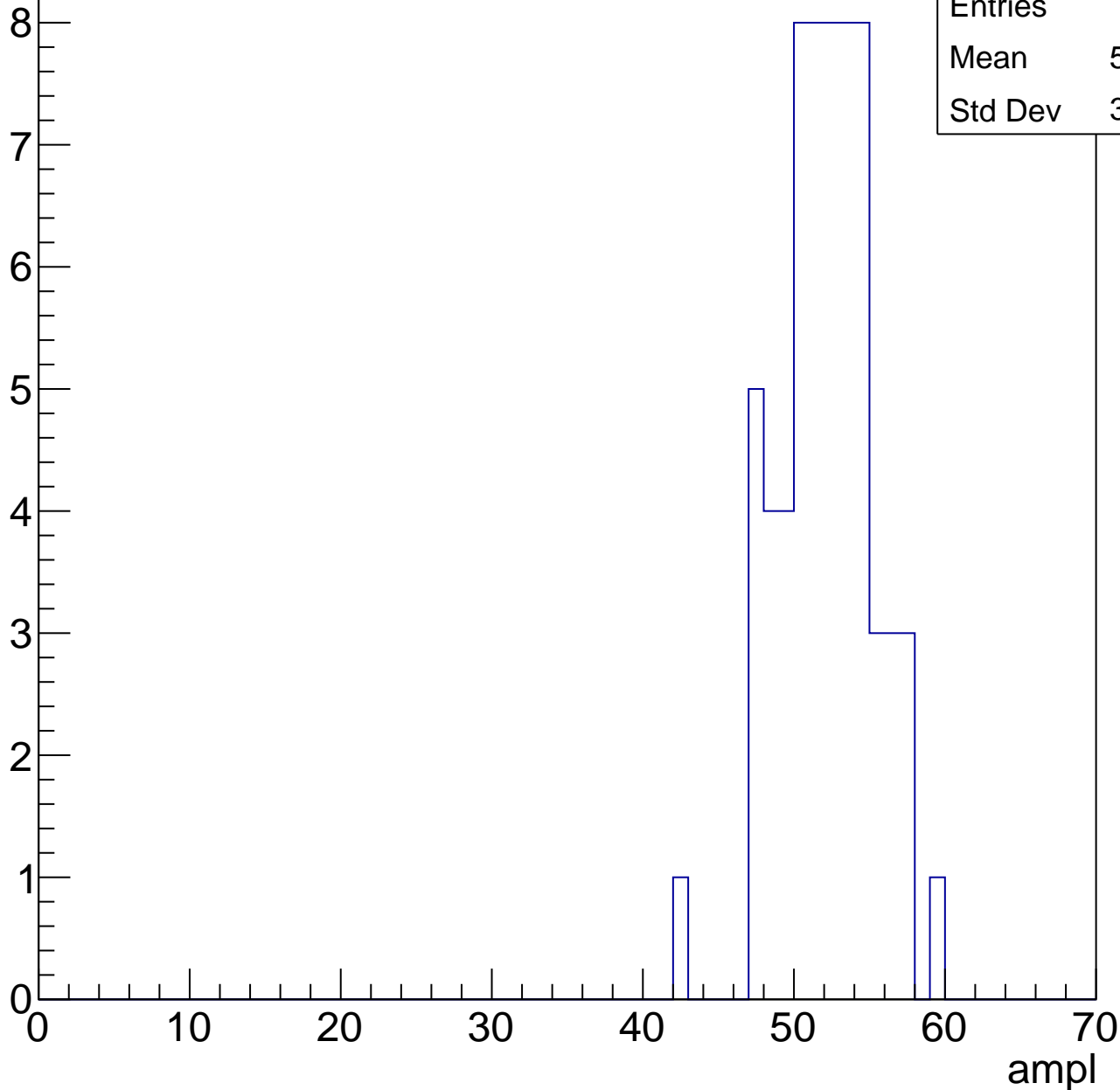


B0L001S, U23-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.69
Std Dev	3.056

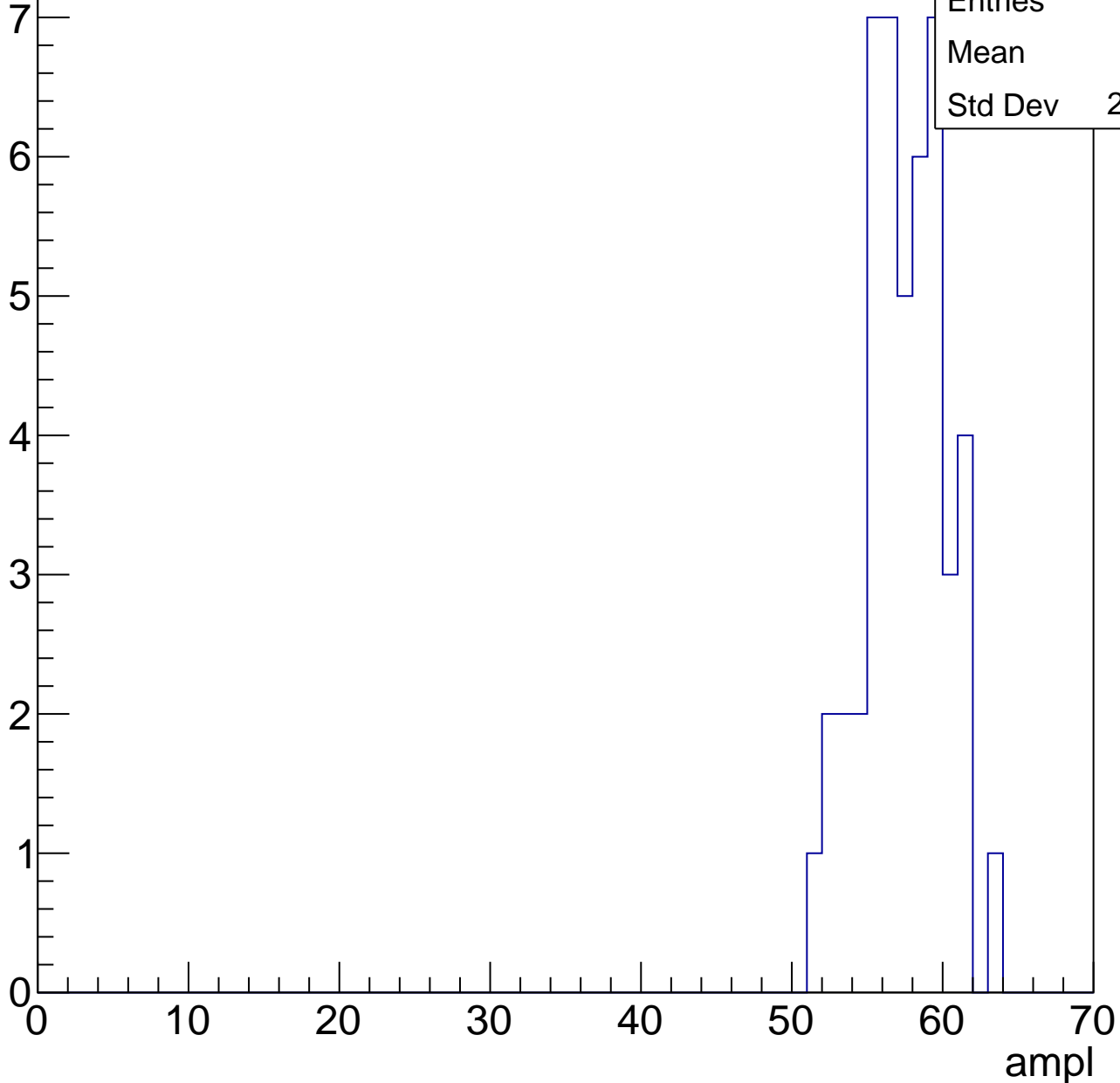


B0L001S, U23-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	57
Std Dev	2.658

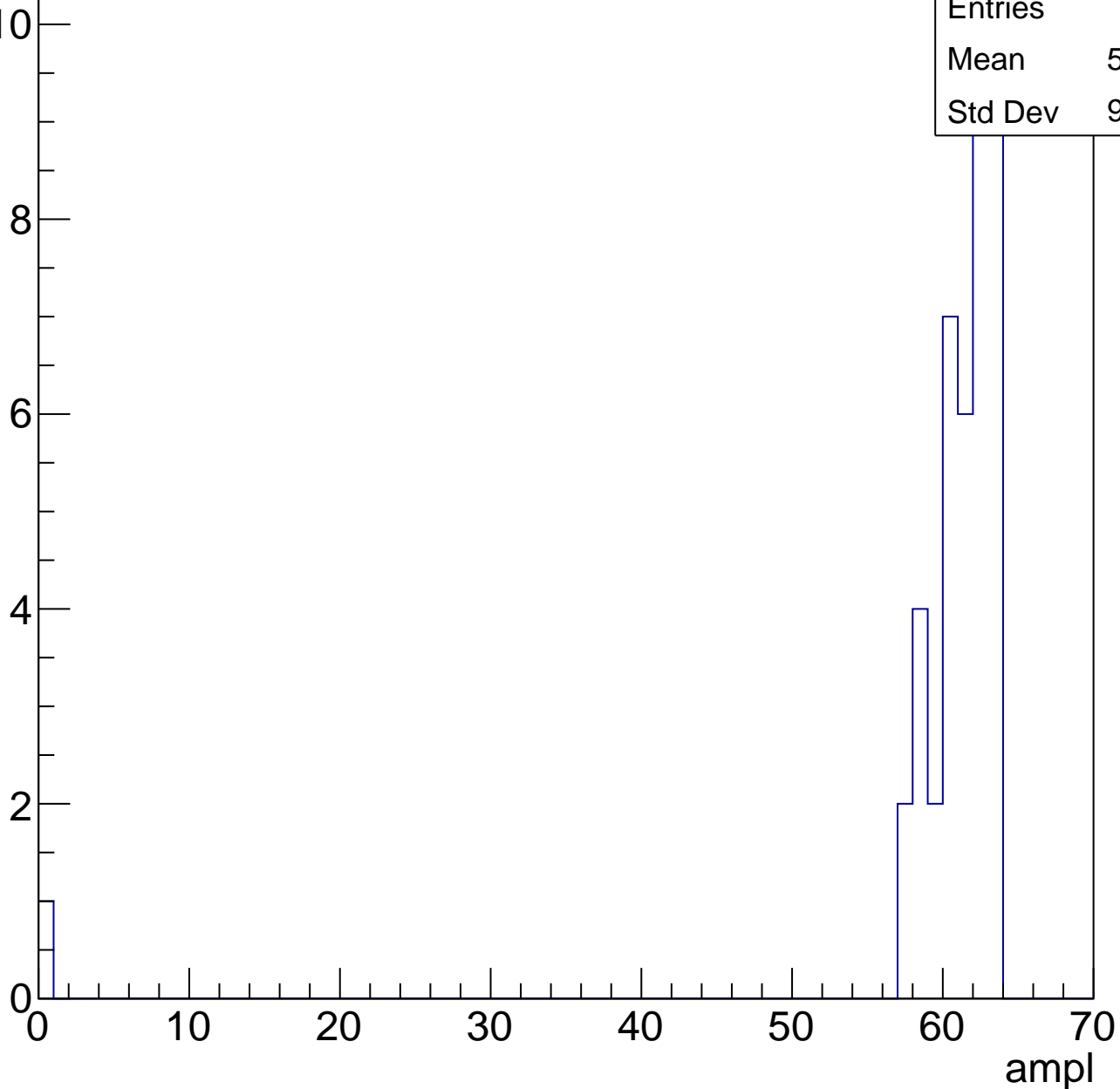


B0L001S, U23-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	59.44
Std Dev	9.564



B0L001S, U23-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch58, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	31.52
Std Dev	3.617

Gaus mean : 31.6226

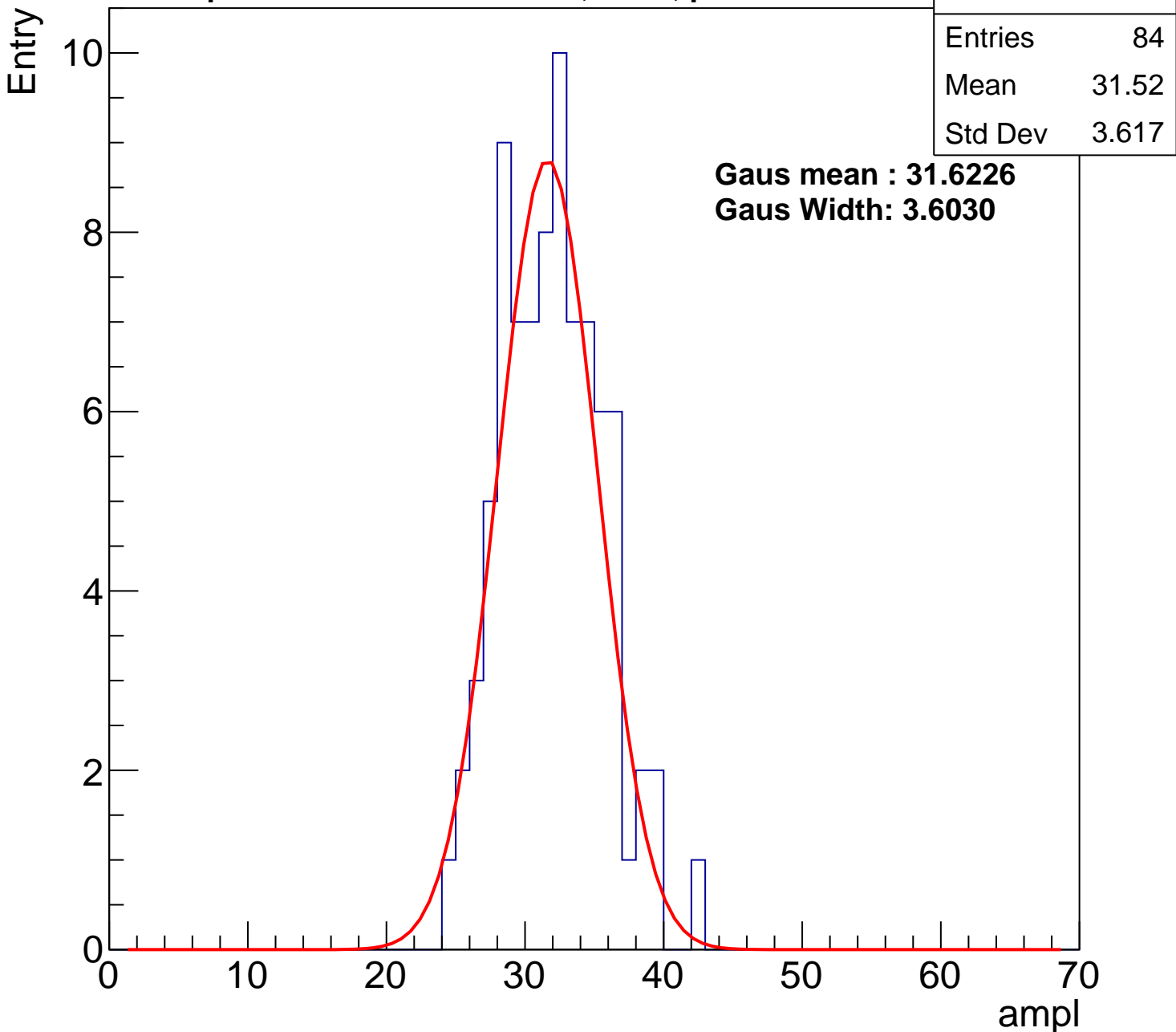
Gaus Width: 3.6030

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch58, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	39.07
Std Dev	3.483

Gaus mean : 39.3666

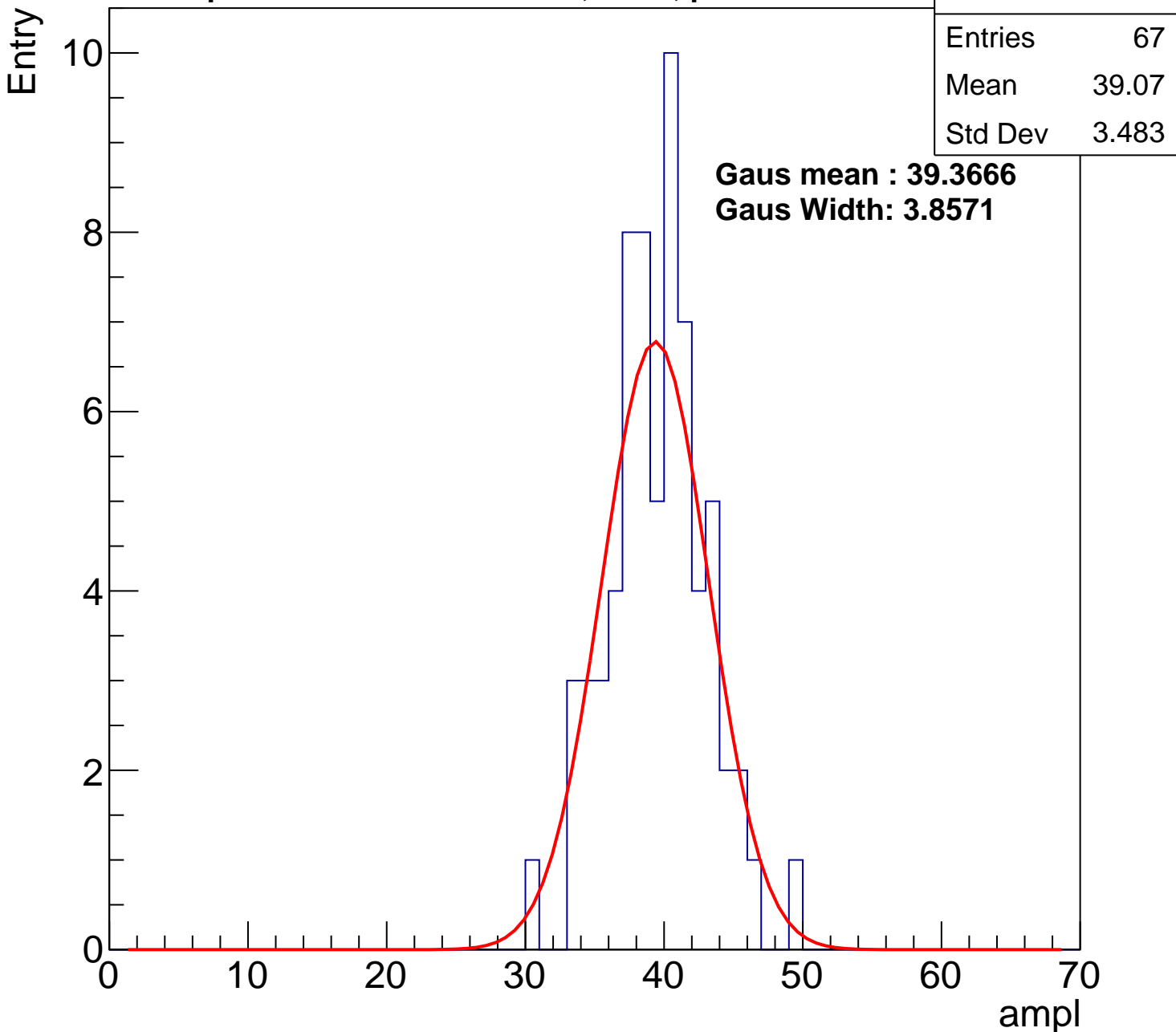
Gaus Width: 3.8571

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch58, adc2

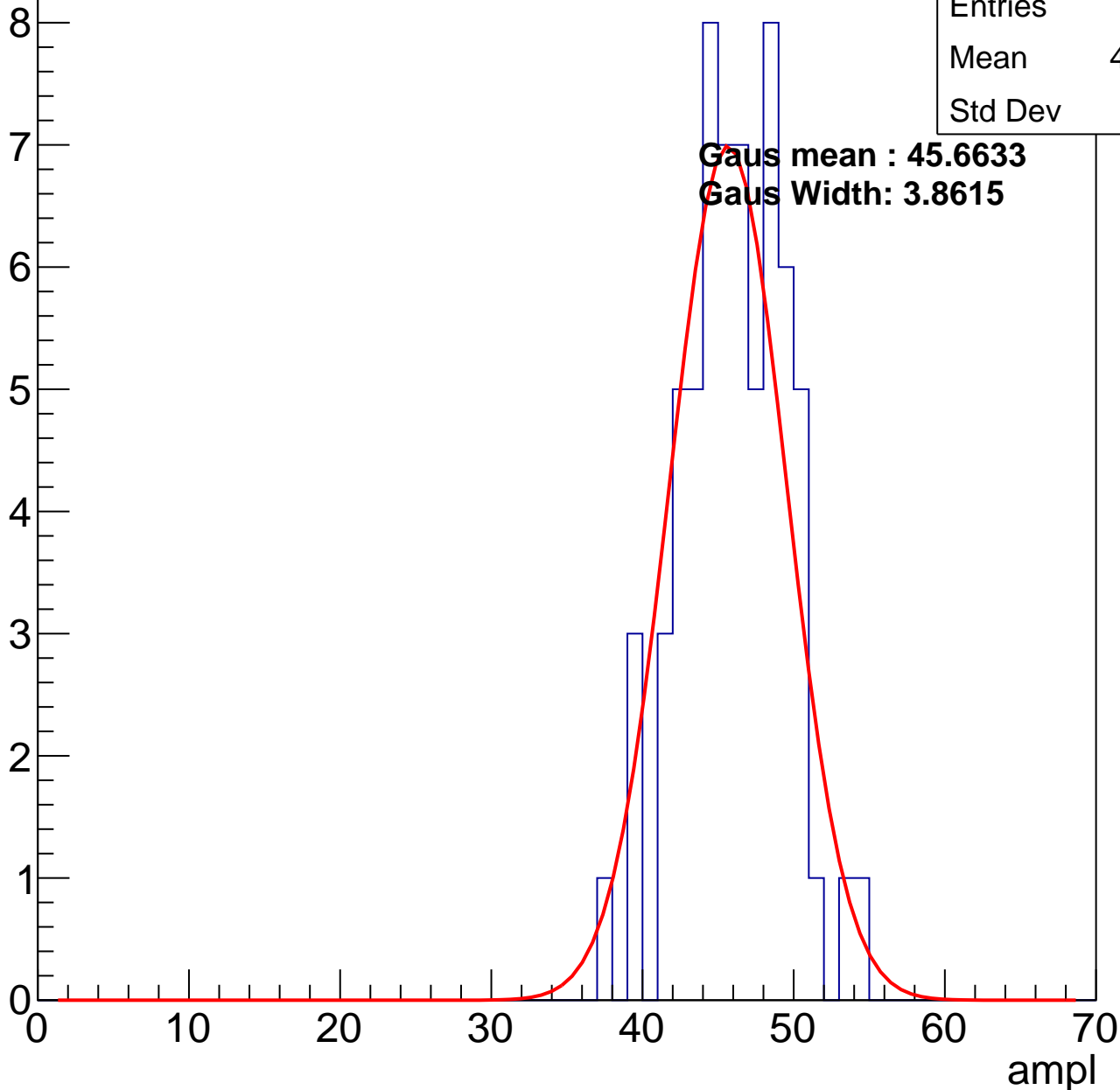
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.64
Std Dev	3.41

Gaus mean : 45.6633

Gaus Width: 3.8615



B0L001S, U23-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

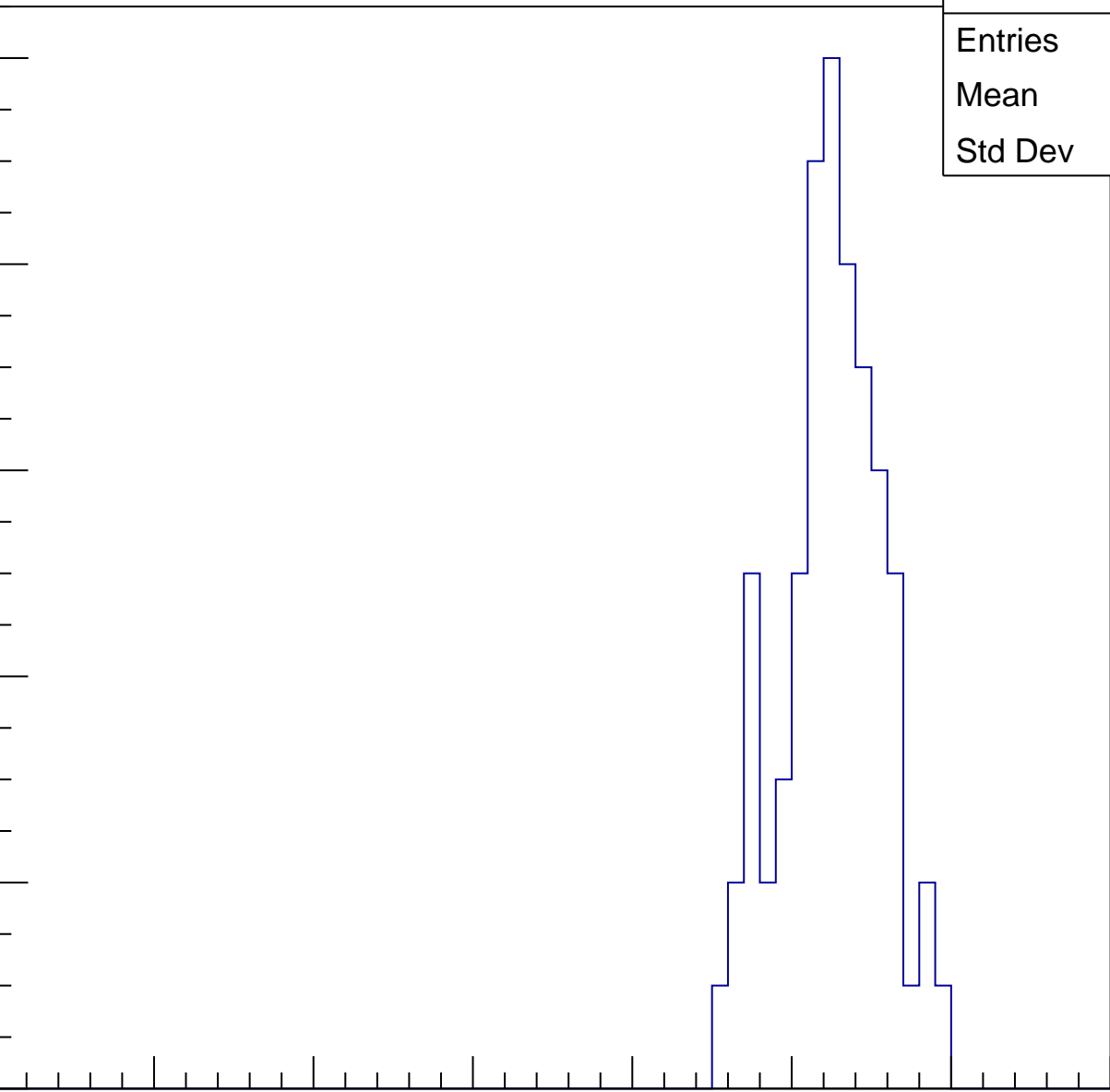
Entries	67
Mean	52.06
Std Dev	3.114

Entry

10
8
6
4
2
0

ampl

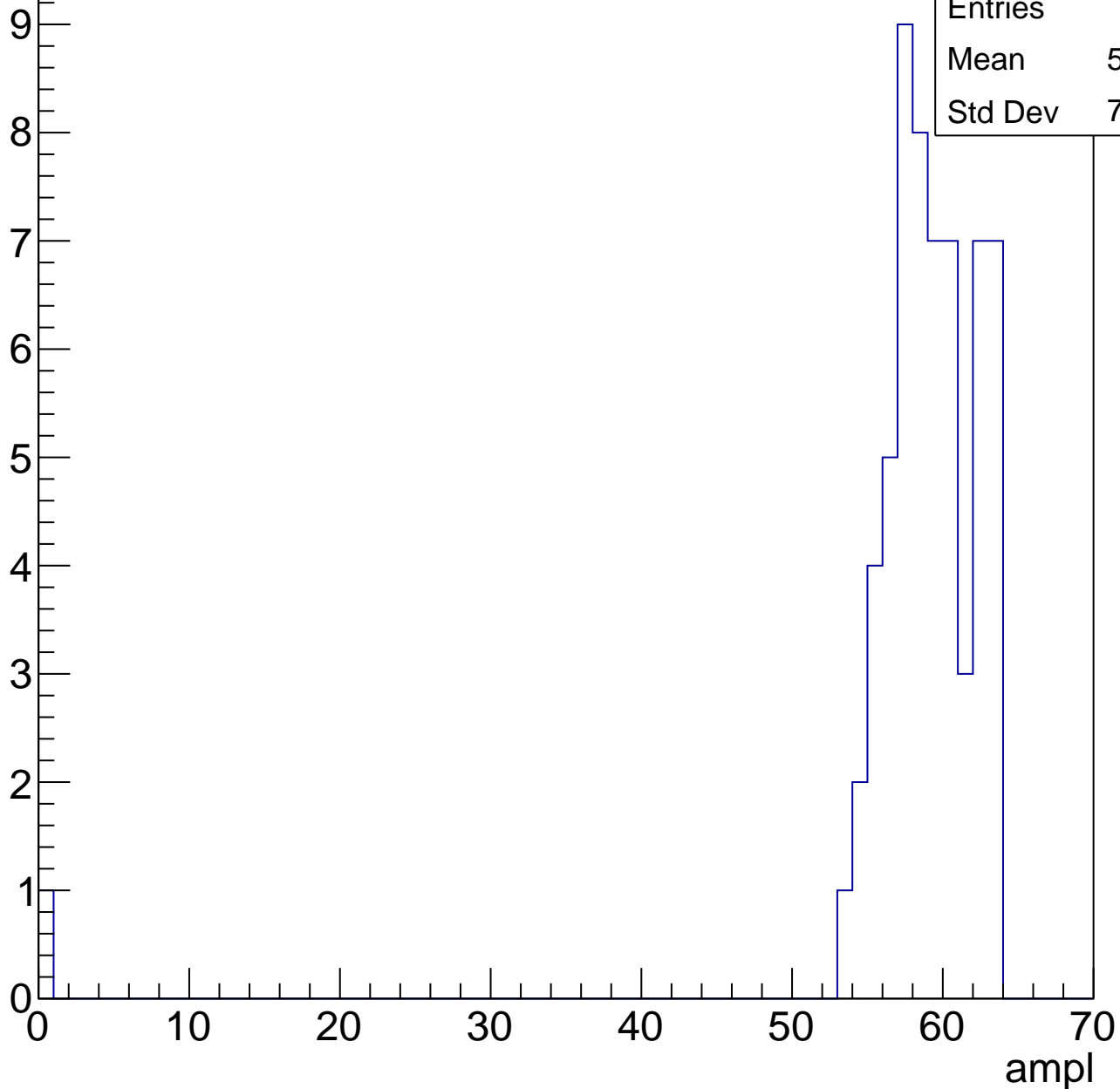
0 10 20 30 40 50 60 70



B0L001S, U23-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

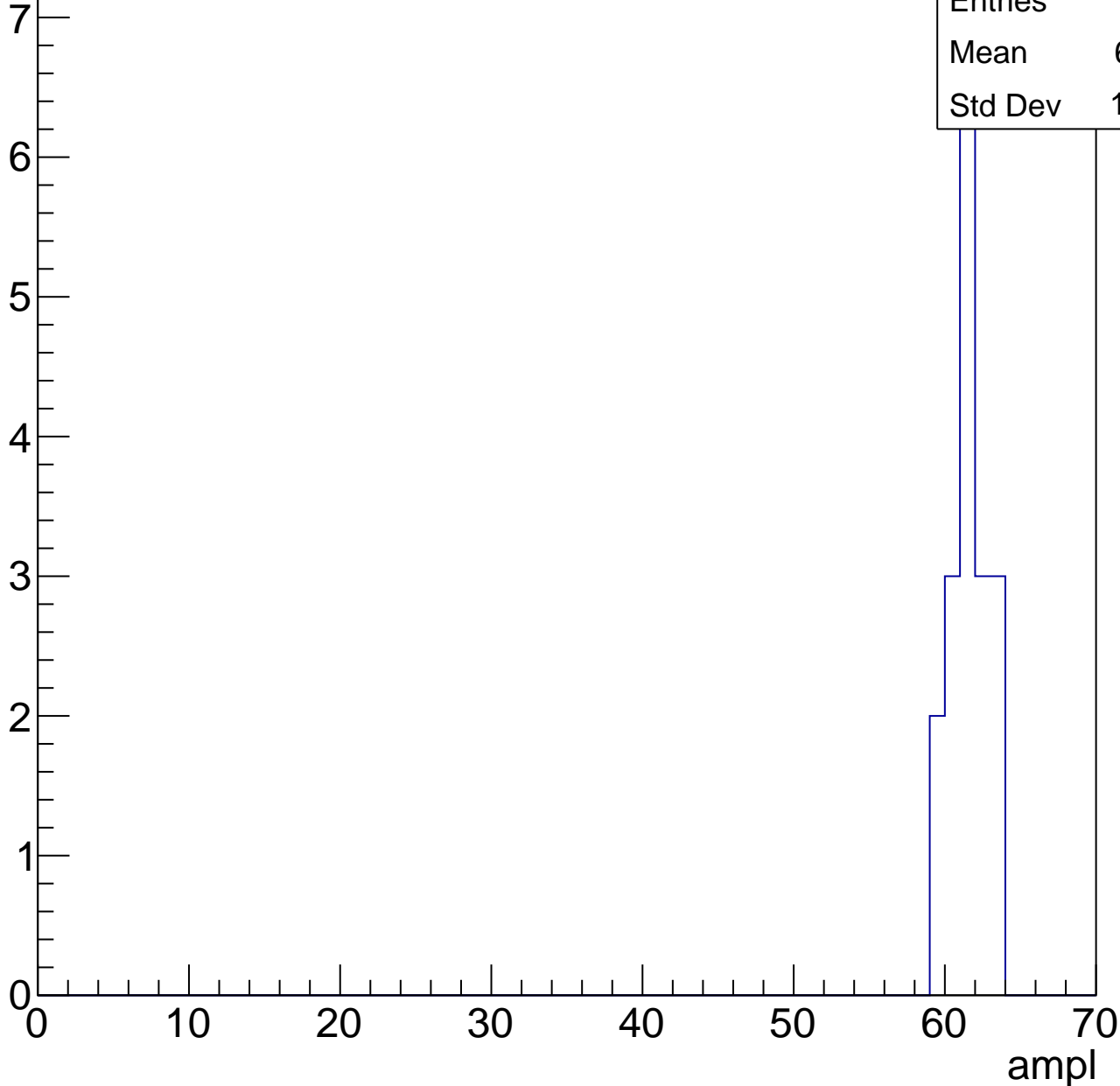


B0L001S, U23-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	61.11
Std Dev	1.197



B0L001S, U23-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch59, adc0

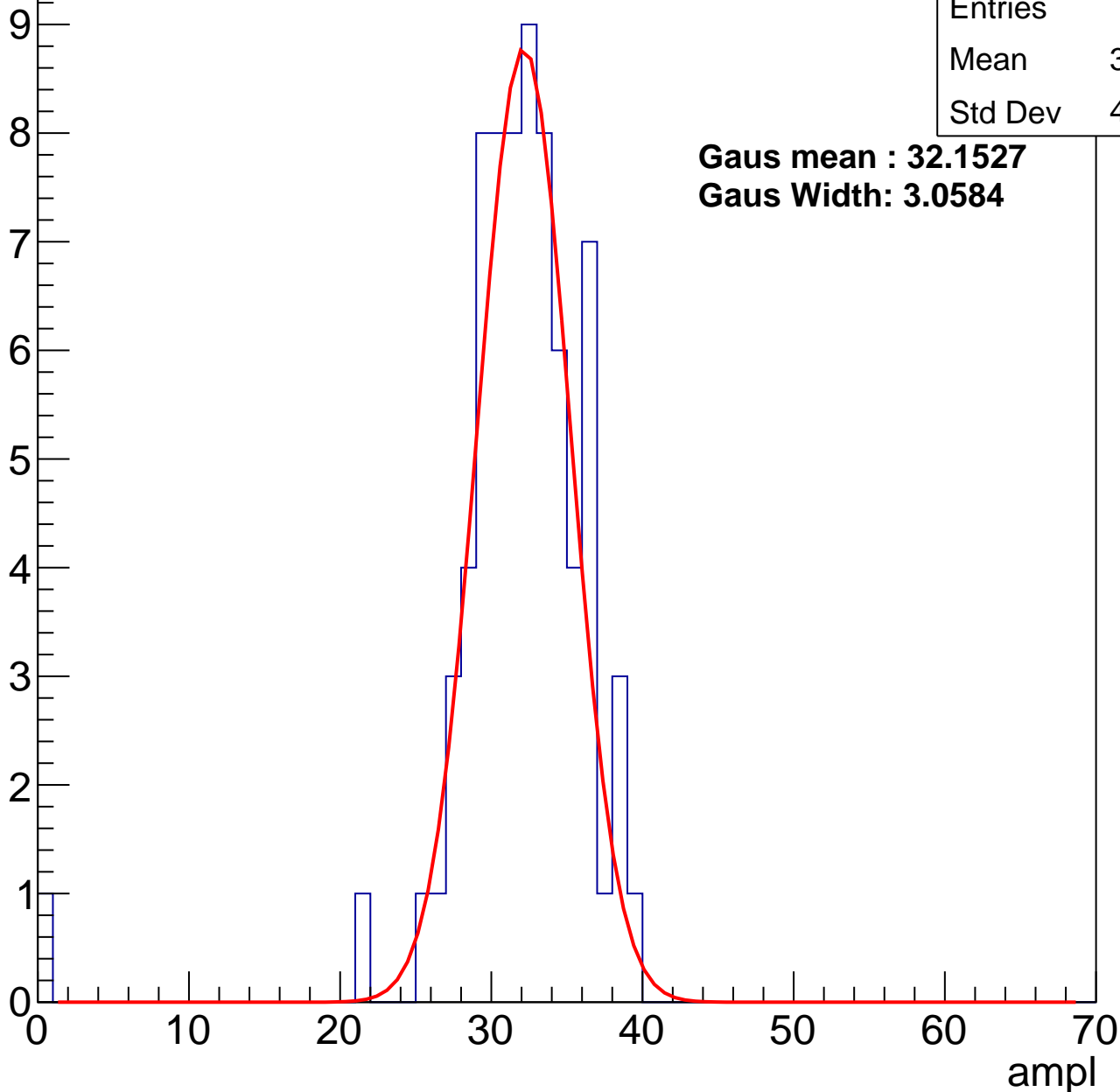
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	31.39
Std Dev	4.953

Gaus mean : 32.1527

Gaus Width: 3.0584



B0L001S, U23-ch59, adc1

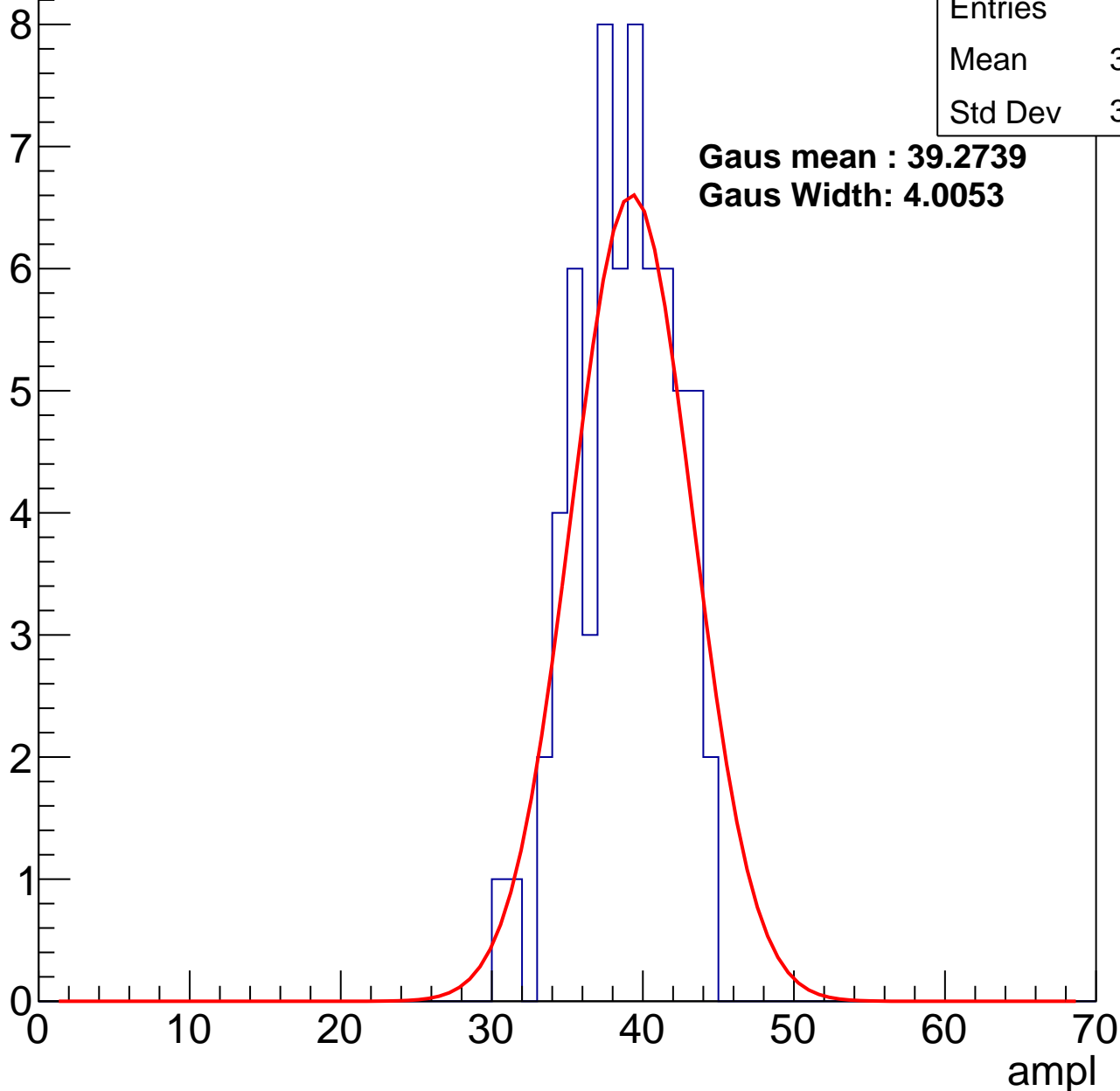
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.35
Std Dev	3.228

Gaus mean : 39.2739

Gaus Width: 4.0053



B0L001S, U23-ch59, adc2

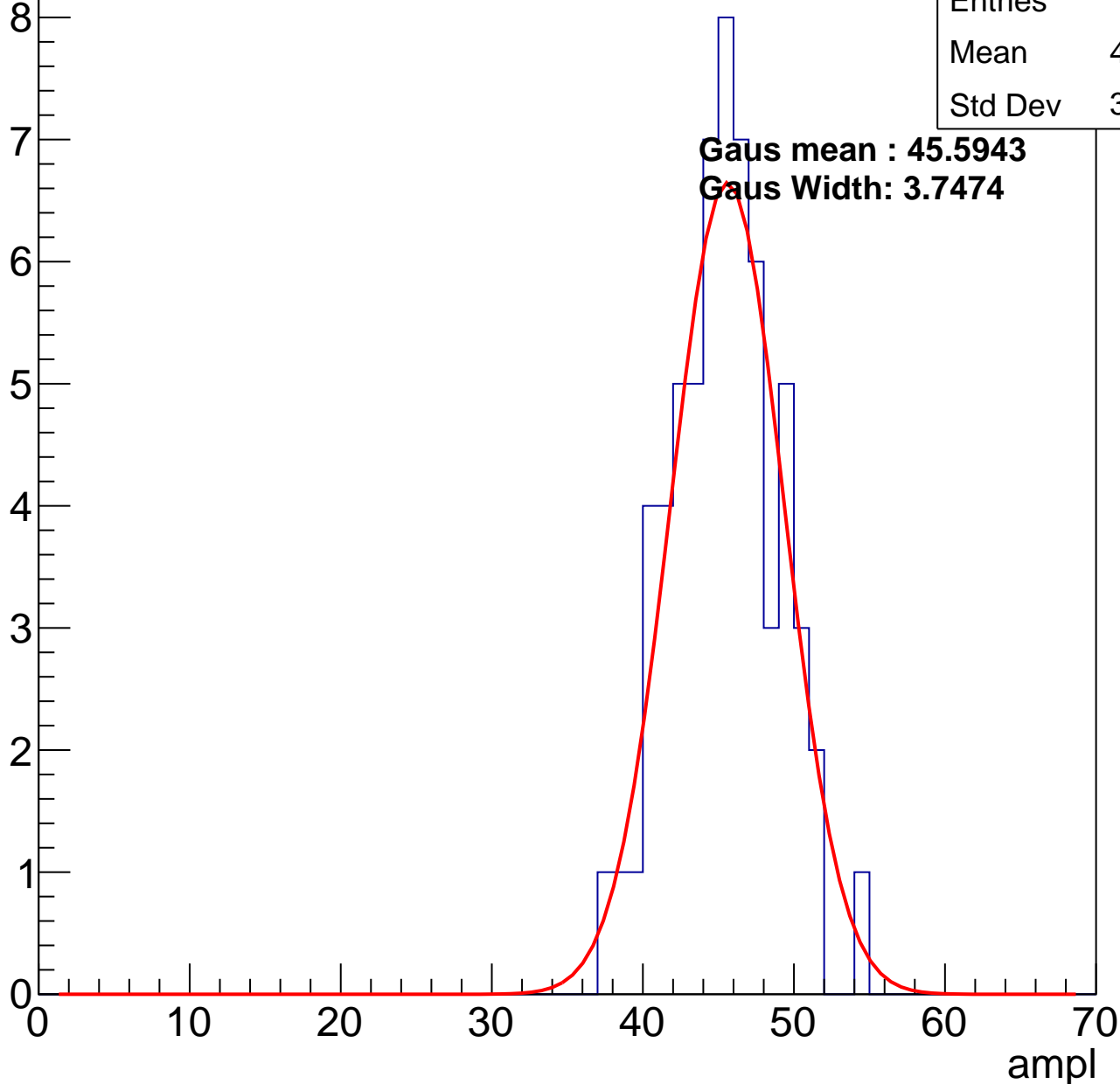
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.92
Std Dev	3.452

Gaus mean : 45.5943

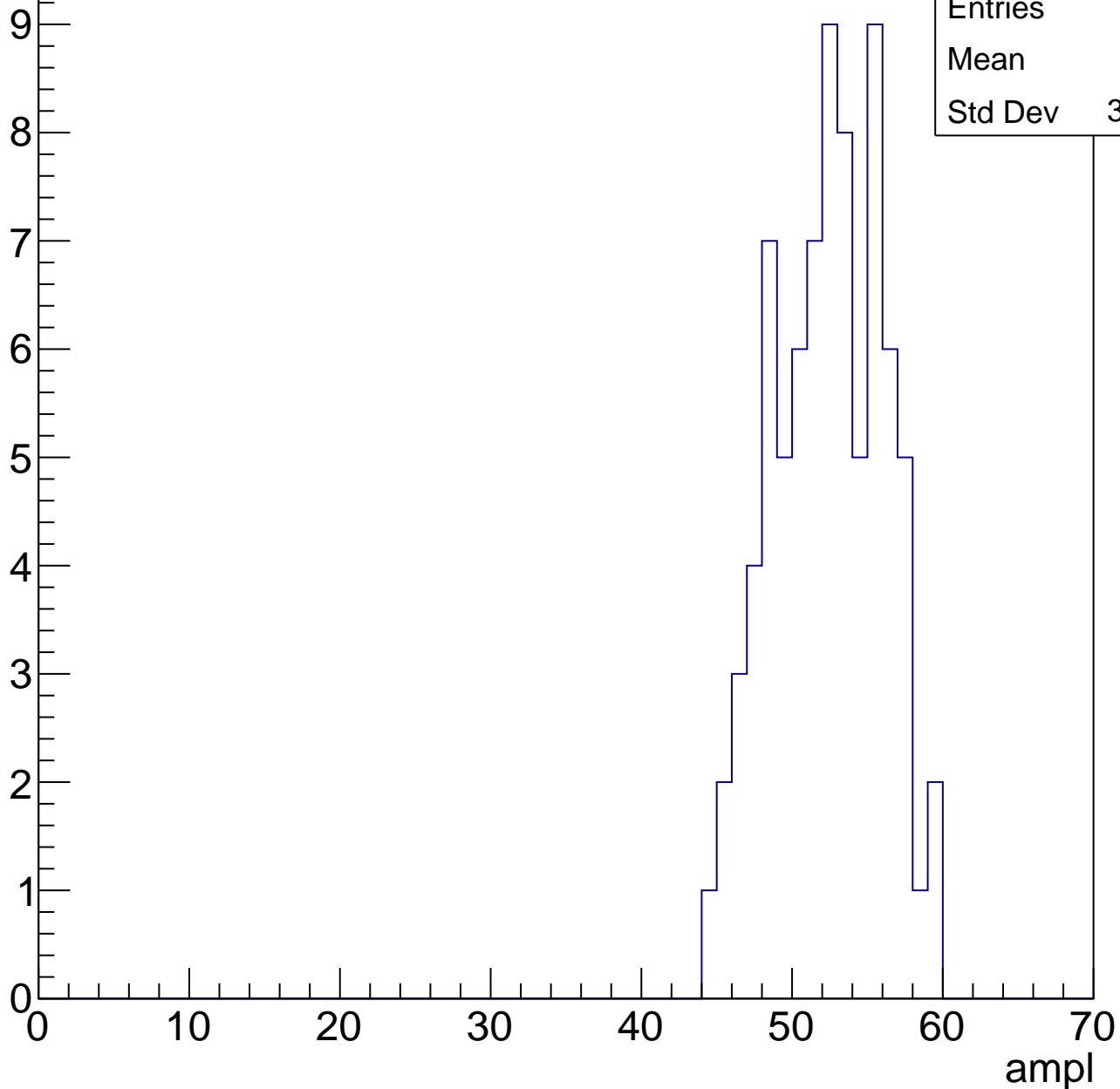
Gaus Width: 3.7474



B0L001S, U23-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

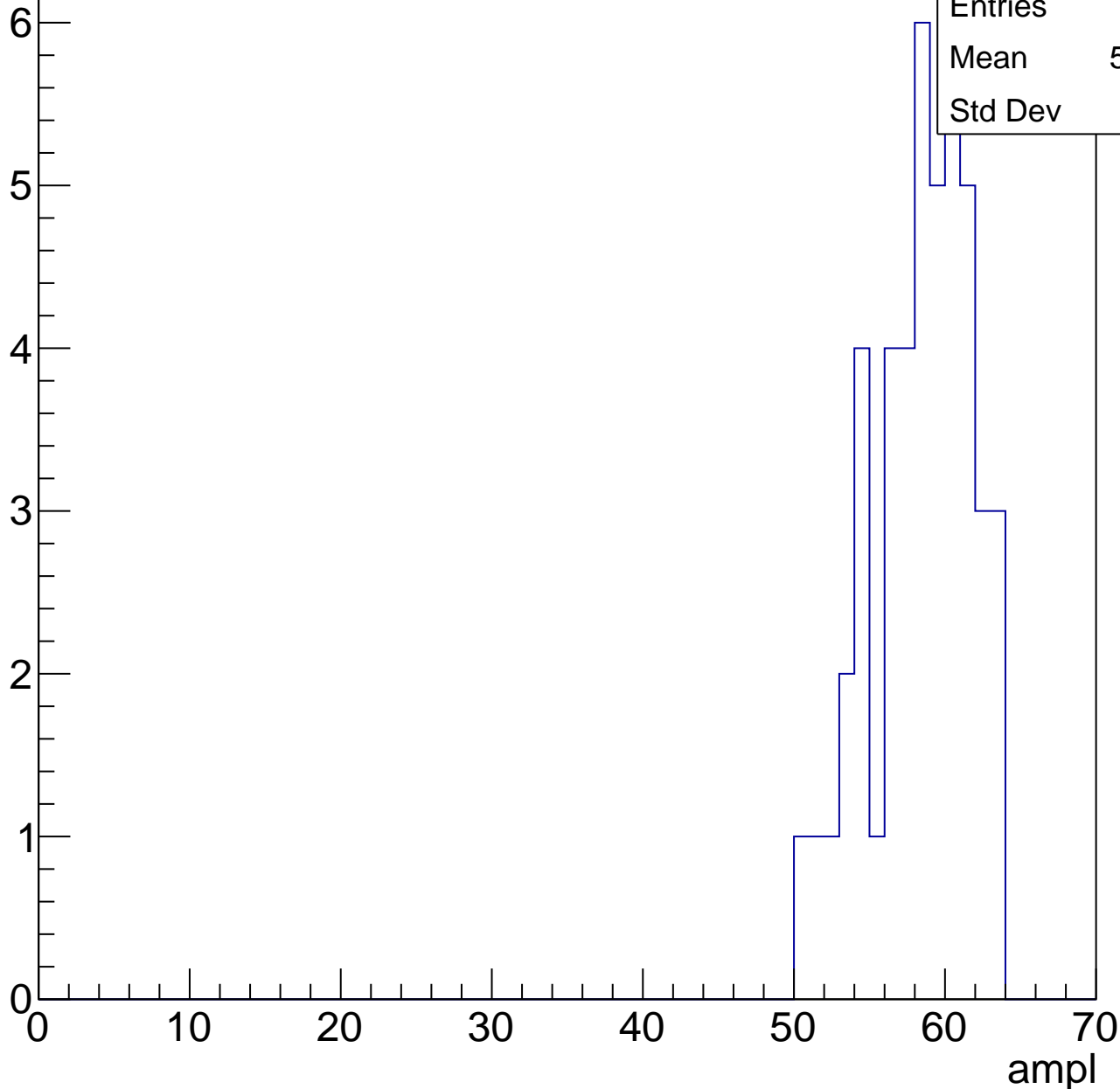
Entry



B0L001S, U23-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

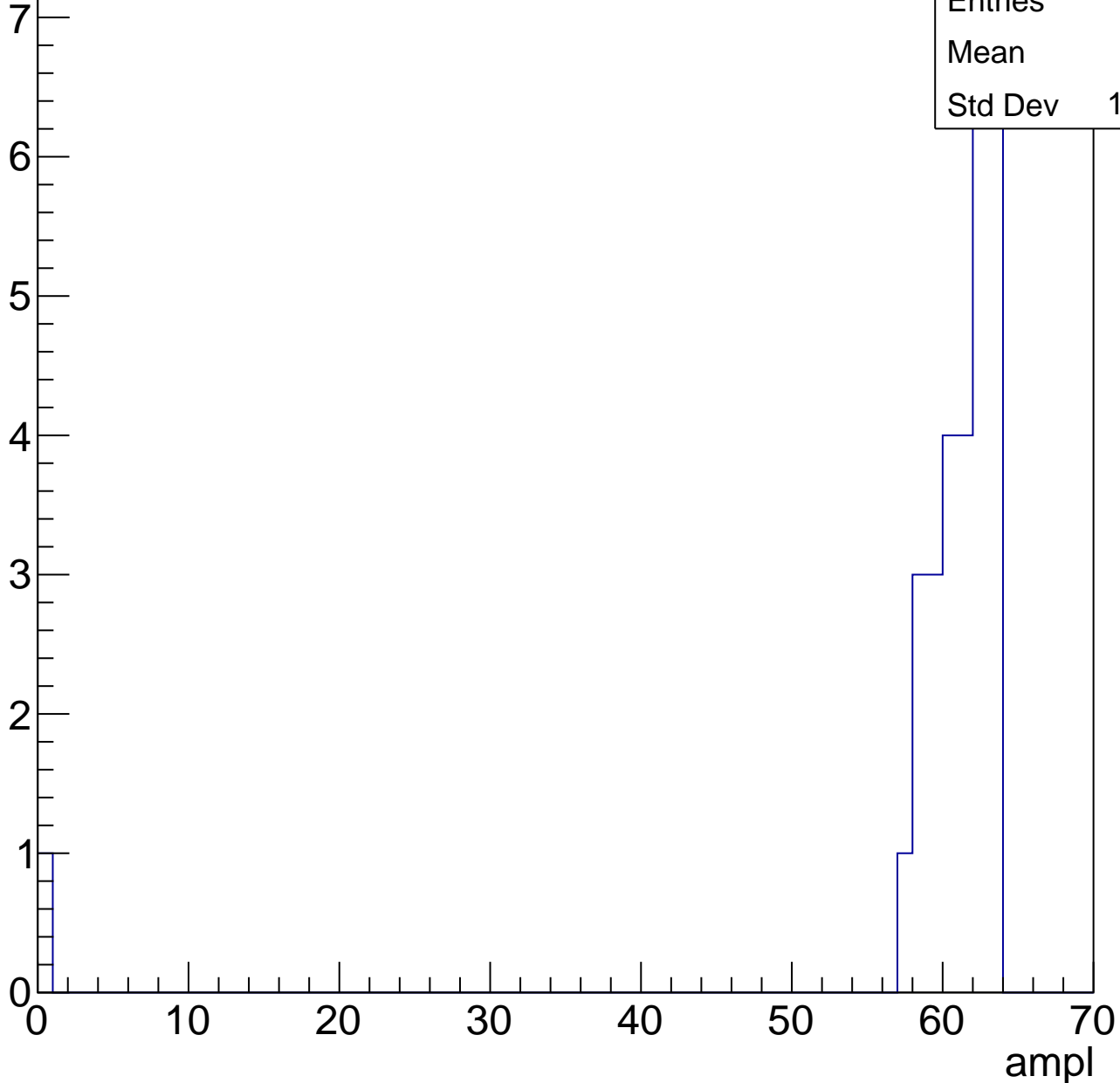


B0L001S, U23-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.9
Std Dev	11.08



B0L001S, U23-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch60, adc0

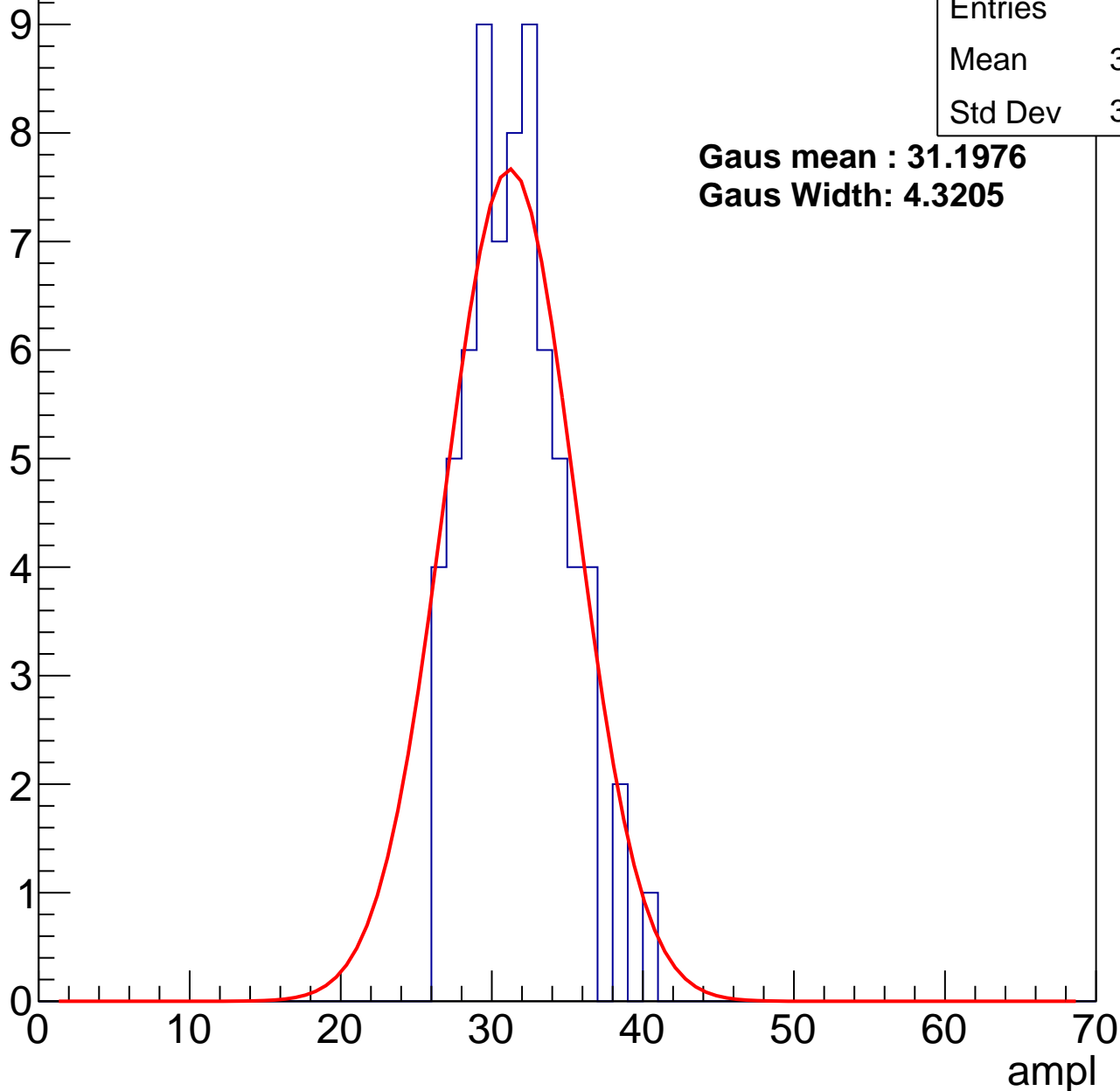
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	31.17
Std Dev	3.153

Gaus mean : 31.1976

Gaus Width: 4.3205



B0L001S, U23-ch60, adc1

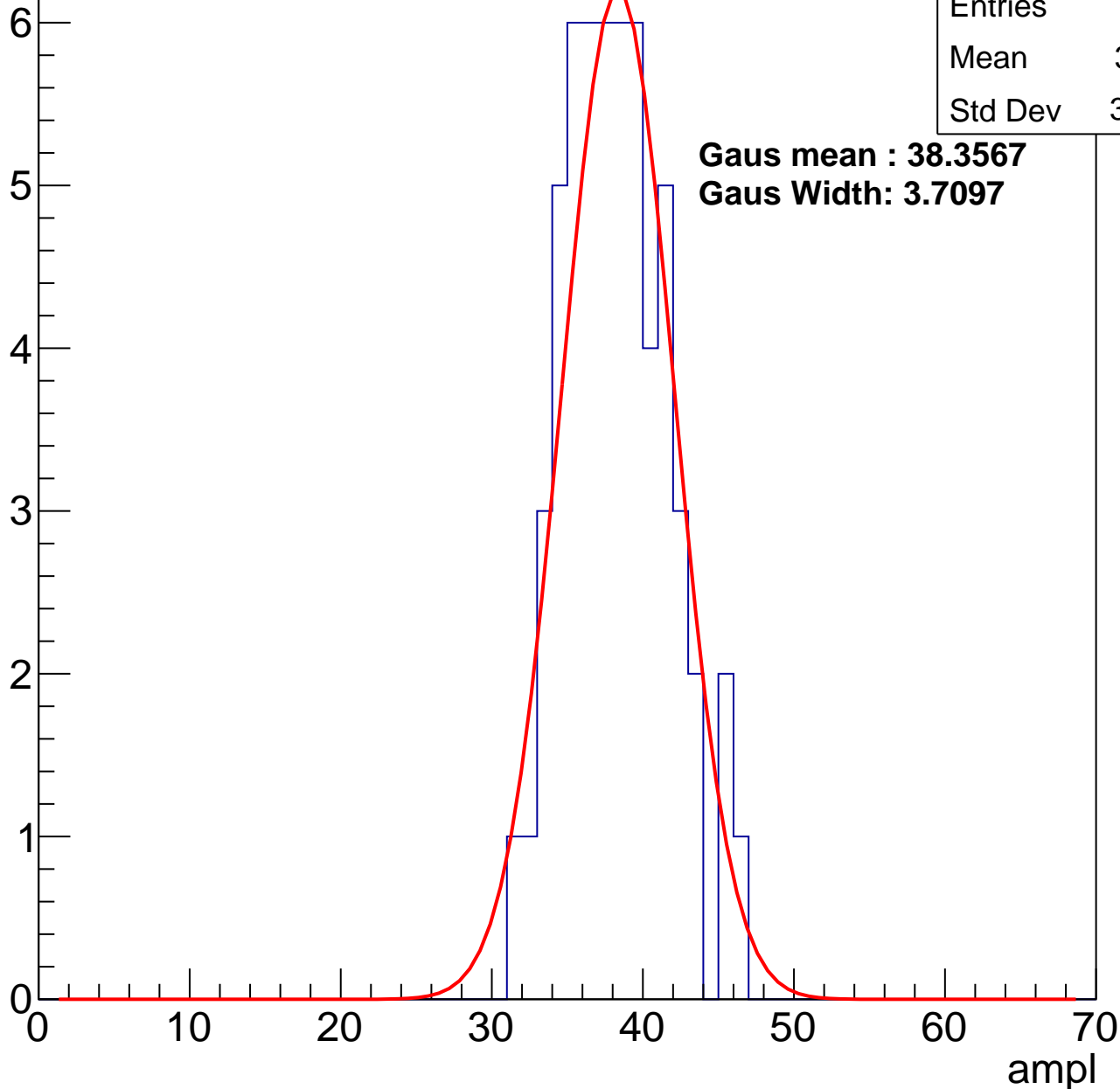
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.81
Std Dev	3.374

Gaus mean : 38.3567

Gaus Width: 3.7097



B0L001S, U23-ch60, adc2

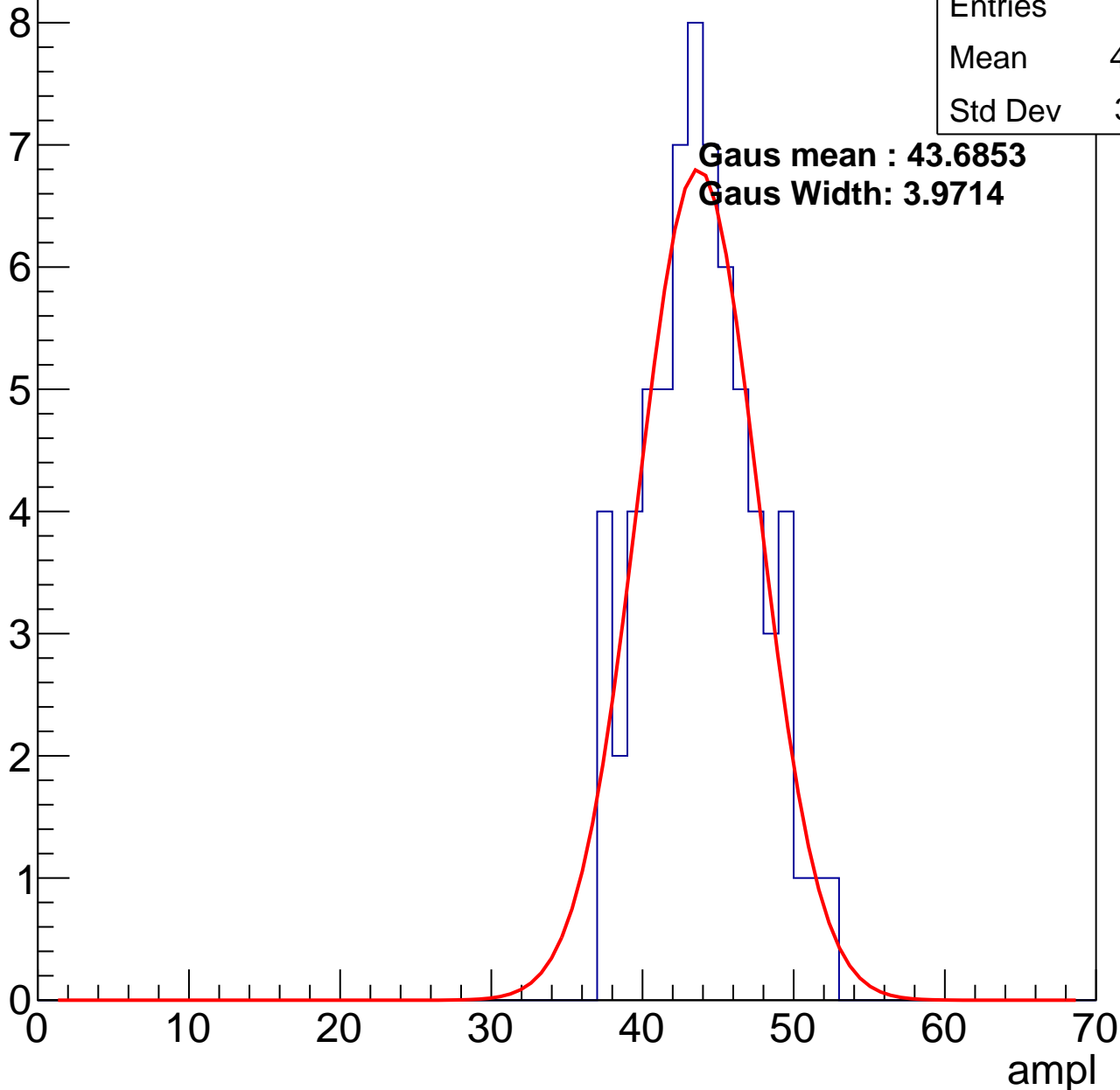
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.46
Std Dev	3.601

Gaus mean : 43.6853

Gaus Width: 3.9714

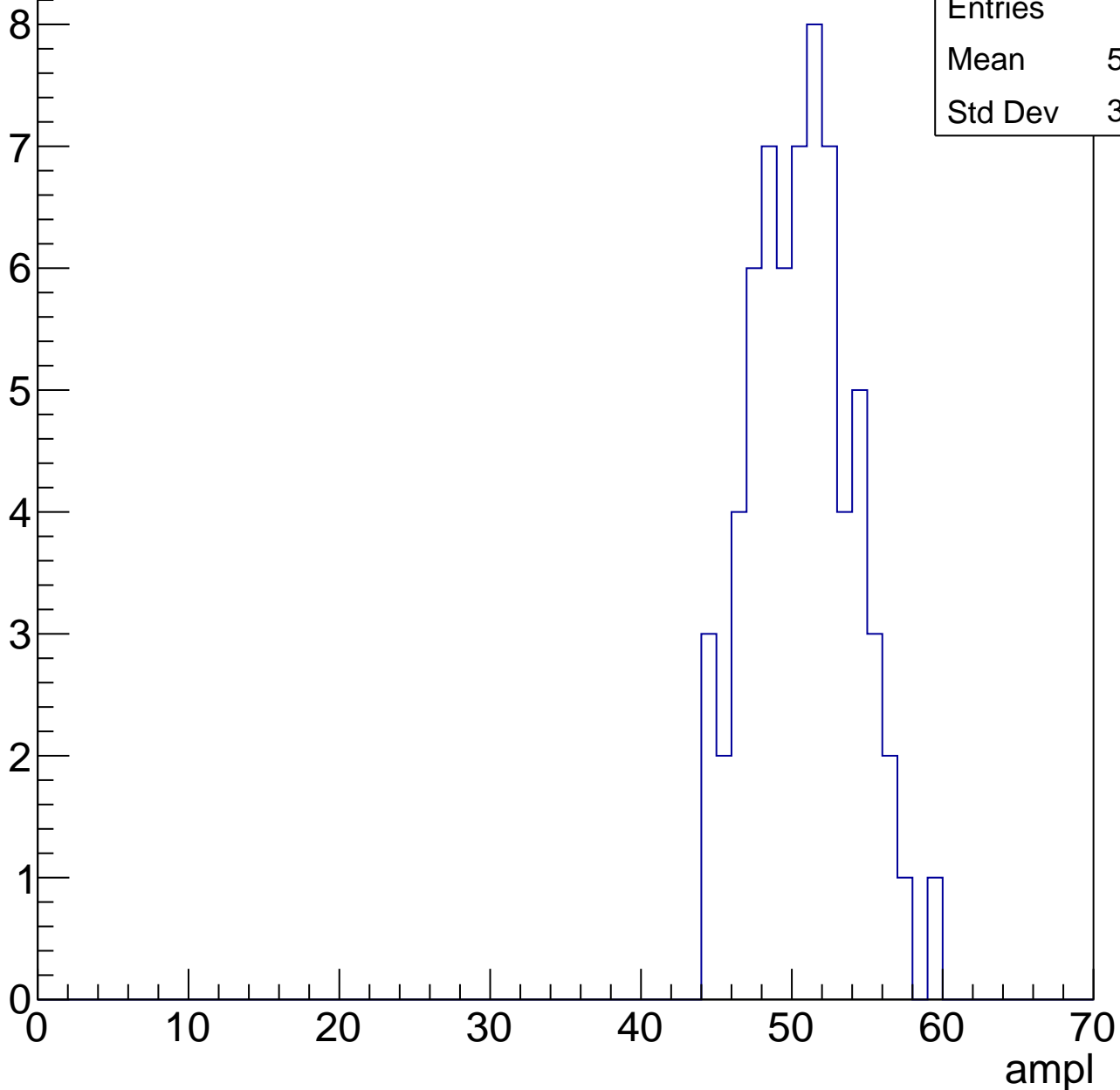


B0L001S, U23-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.23
Std Dev	3.338

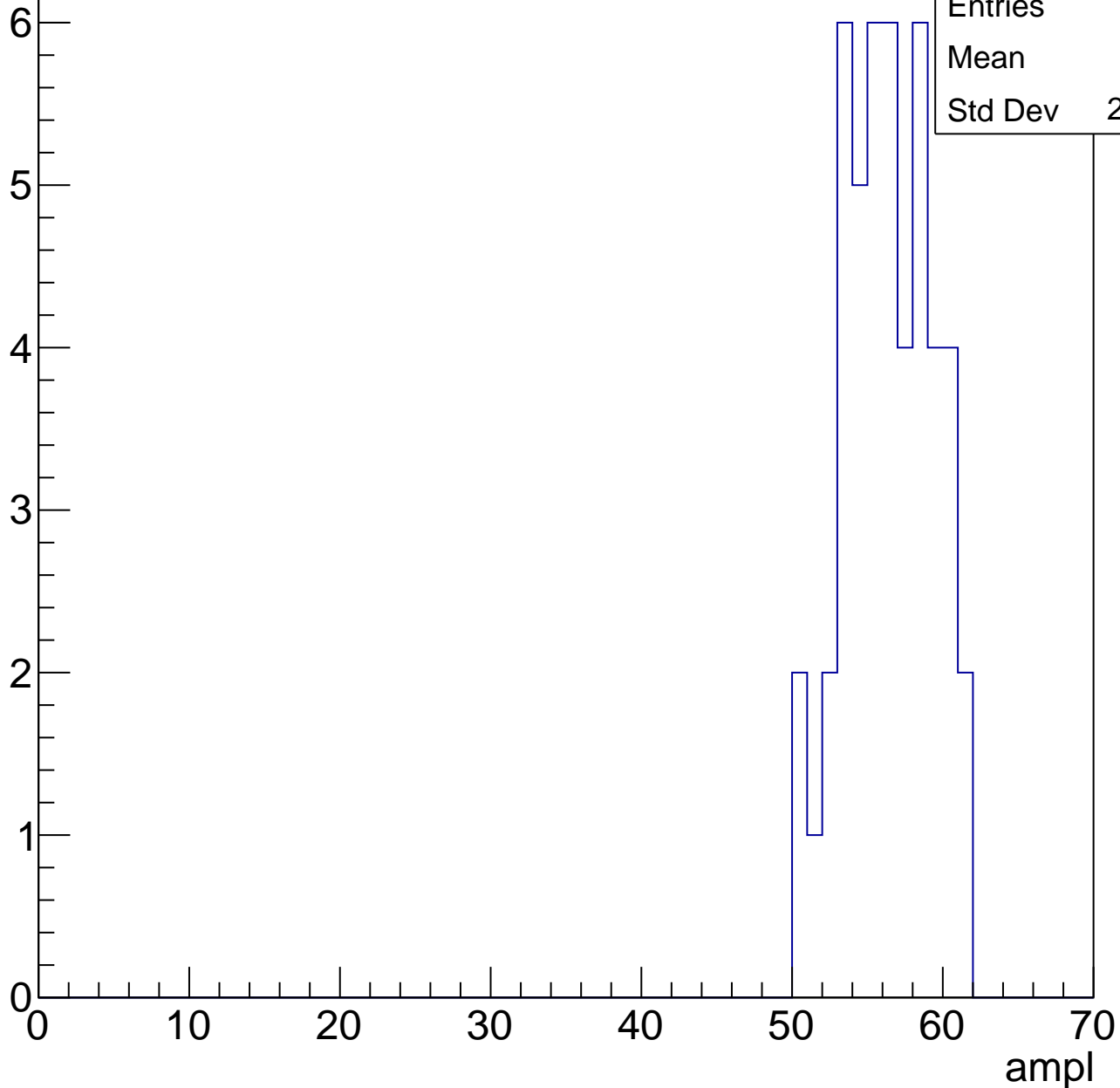


B0L001S, U23-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	55.9
Std Dev	2.838

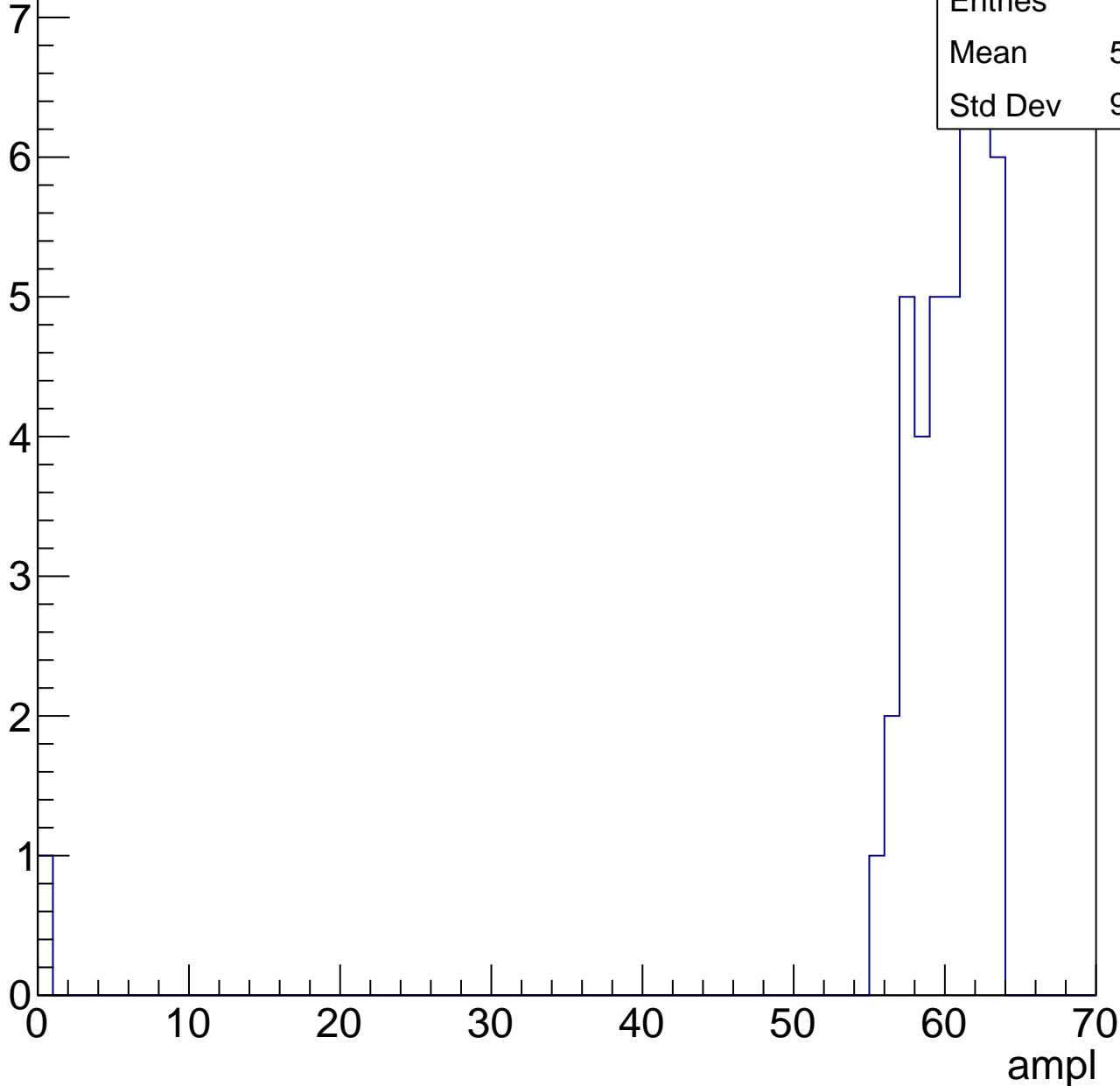


B0L001S, U23-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.56
Std Dev	9.304



B0L001S, U23-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch61, adc0

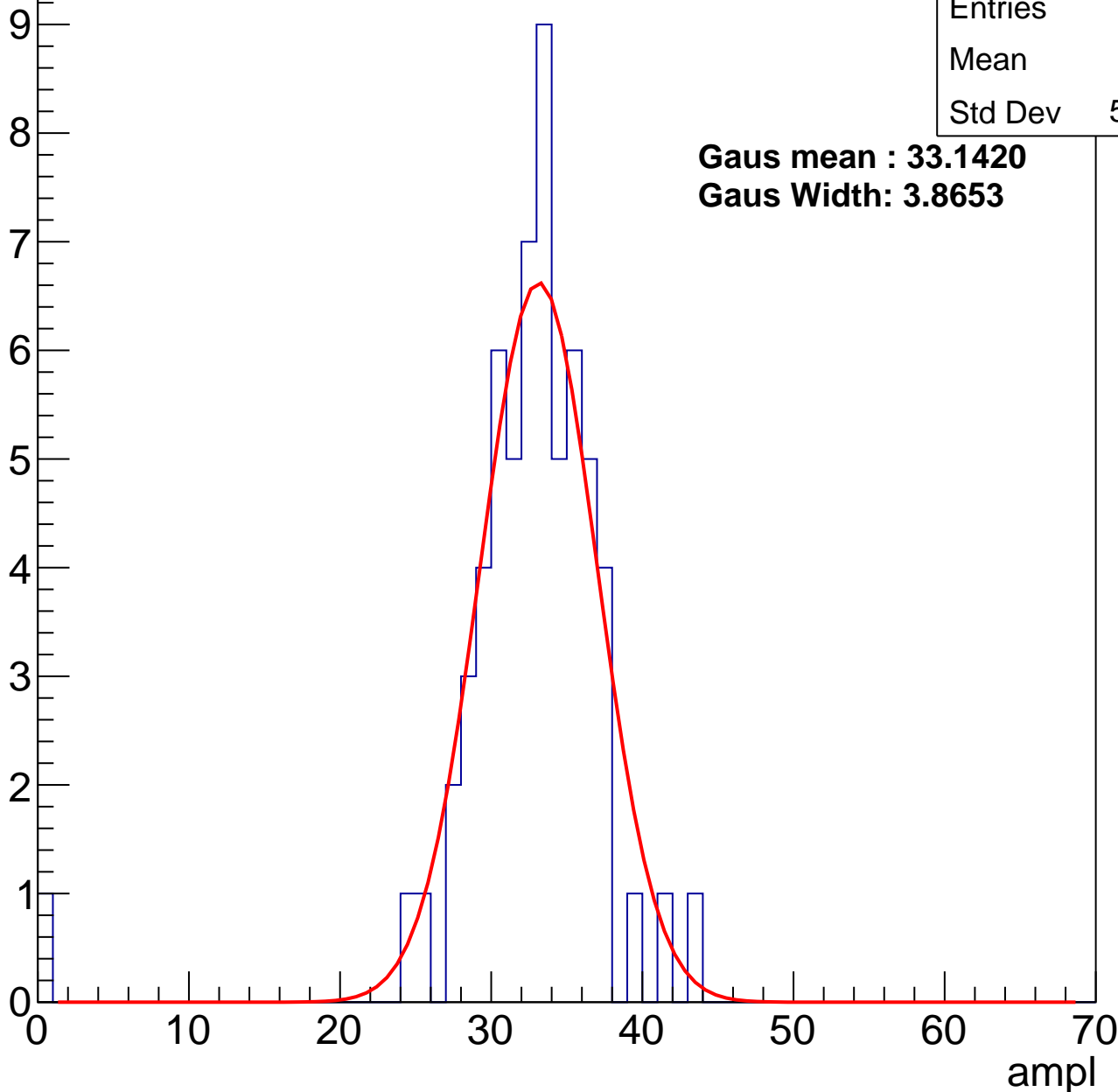
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	32.1
Std Dev	5.408

Gaus mean : 33.1420

Gaus Width: 3.8653



B0L001S, U23-ch61, adc1

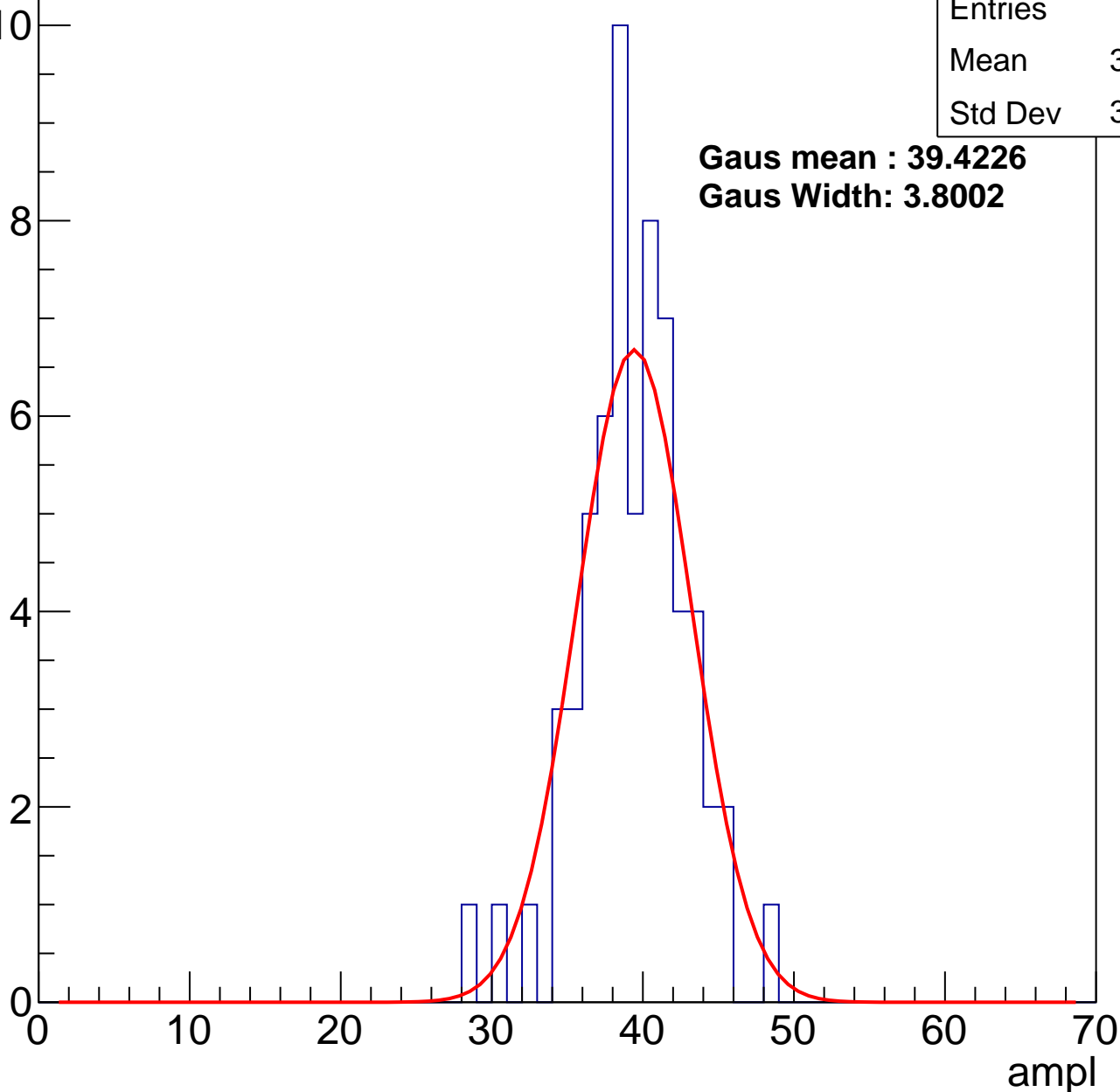
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.84
Std Dev	3.542

Gaus mean : 39.4226

Gaus Width: 3.8002



B0L001S, U23-ch61, adc2

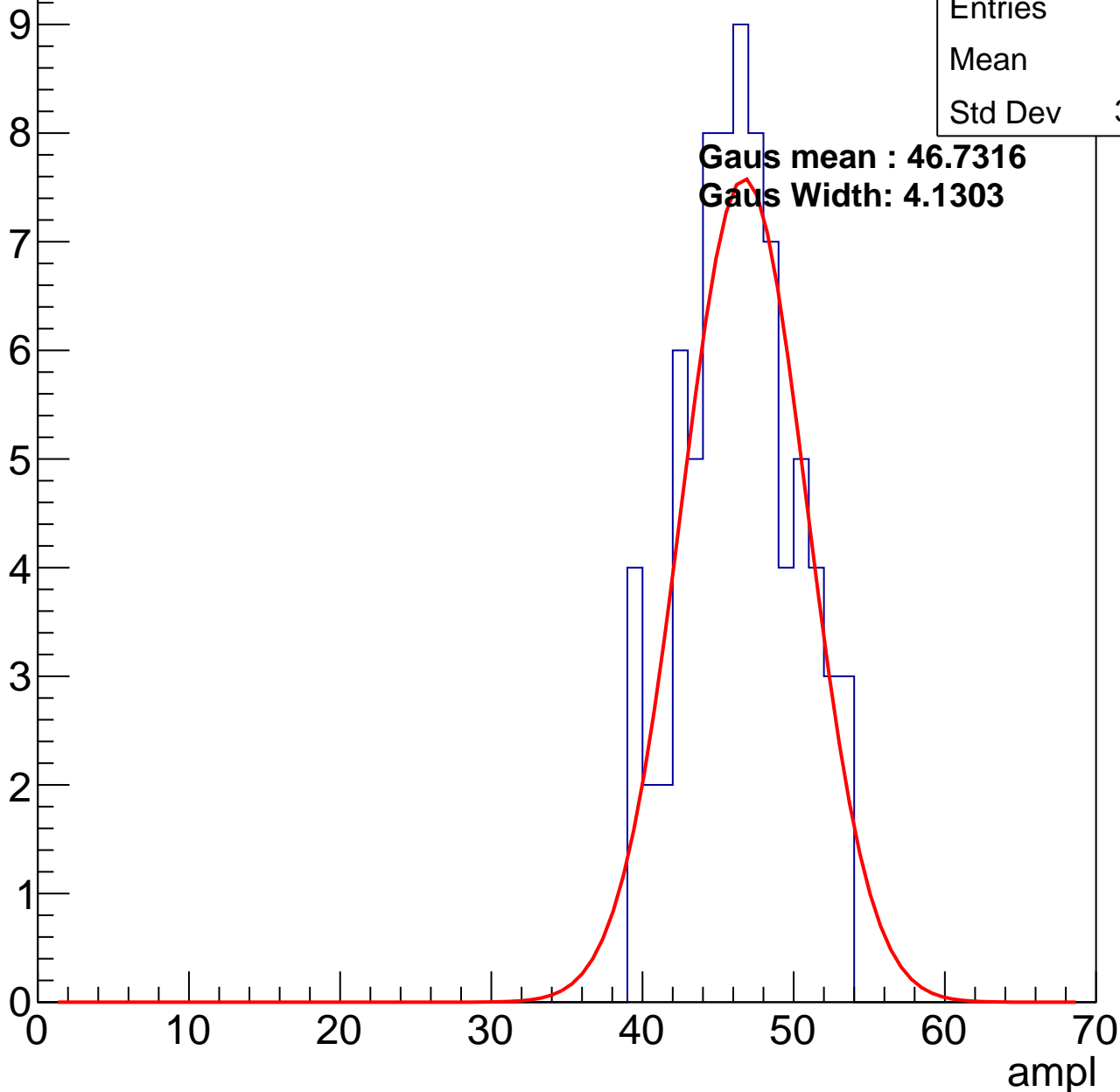
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	46
Std Dev	3.591

Gaus mean : 46.7316

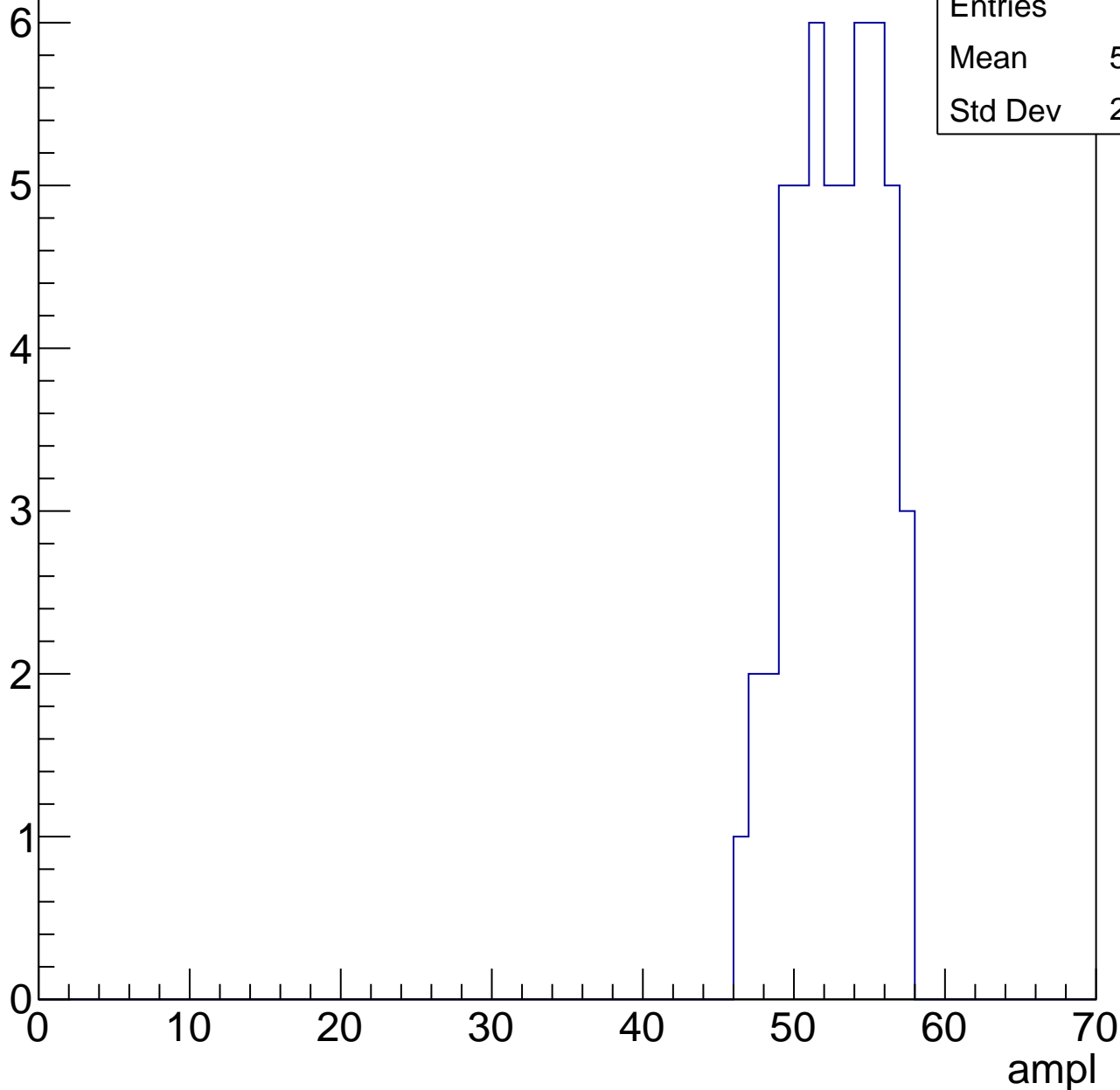
Gaus Width: 4.1303



B0L001S, U23-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



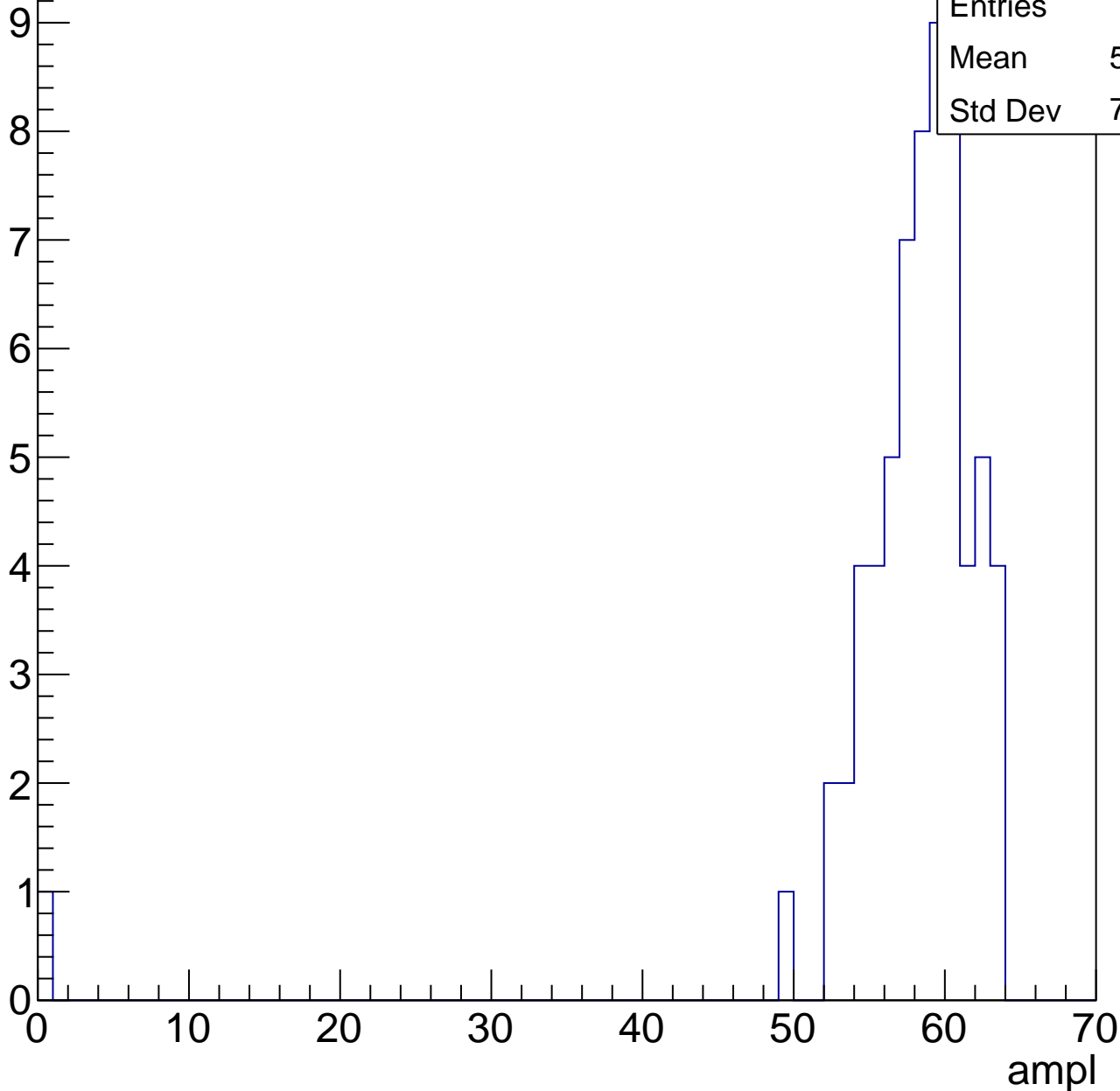
Entries	51
Mean	52.29
Std Dev	2.879

B0L001S, U23-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	57.15
Std Dev	7.754



B0L001S, U23-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

Entries

20

Mean

61.3

Std Dev

1.52

ampl

0

10

20

30

40

50

60

70

B0L001S, U23-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

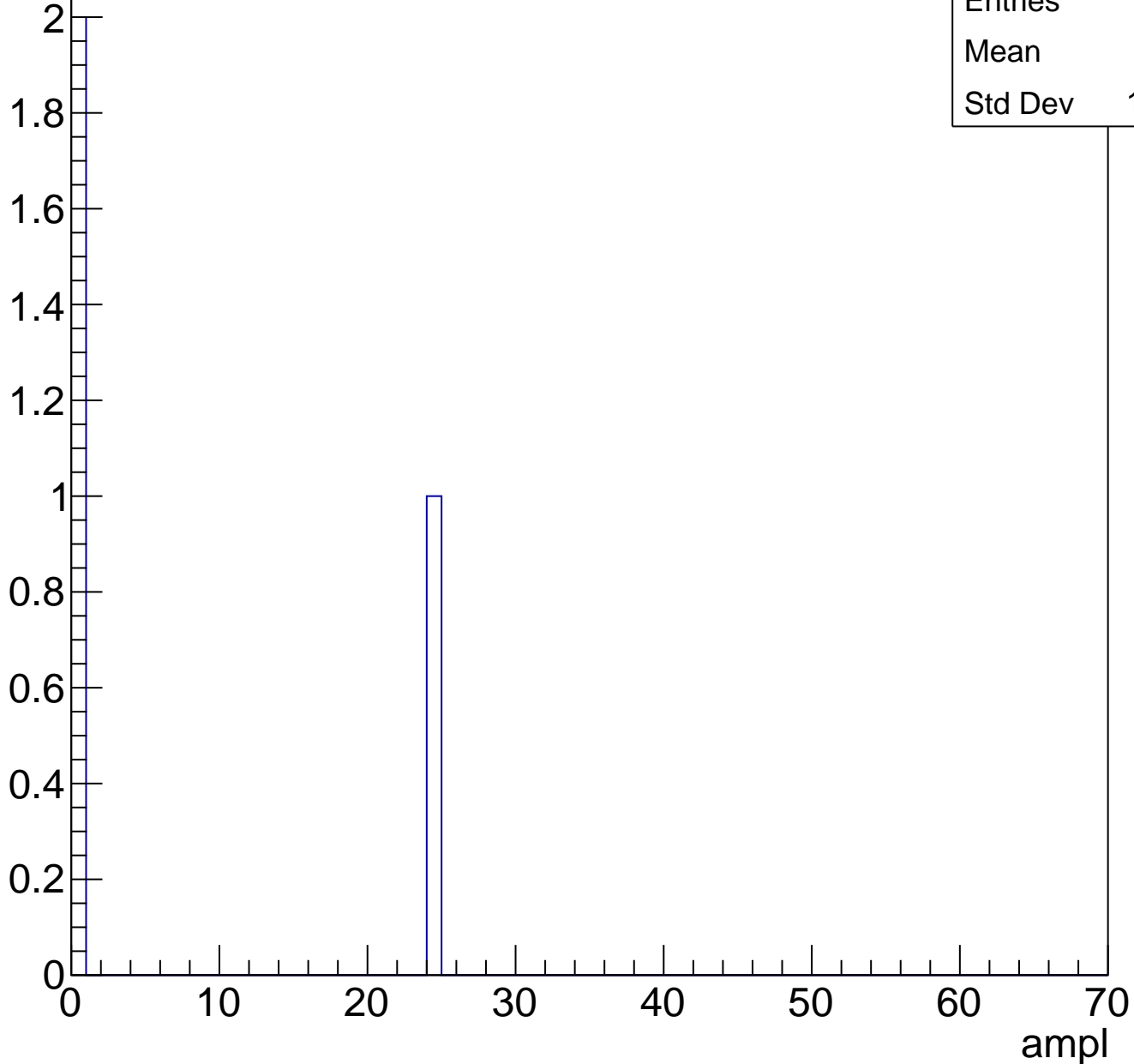
Entry



B0L001S, U23-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8
Std Dev	11.31

B0L001S, U23-ch62, adc0

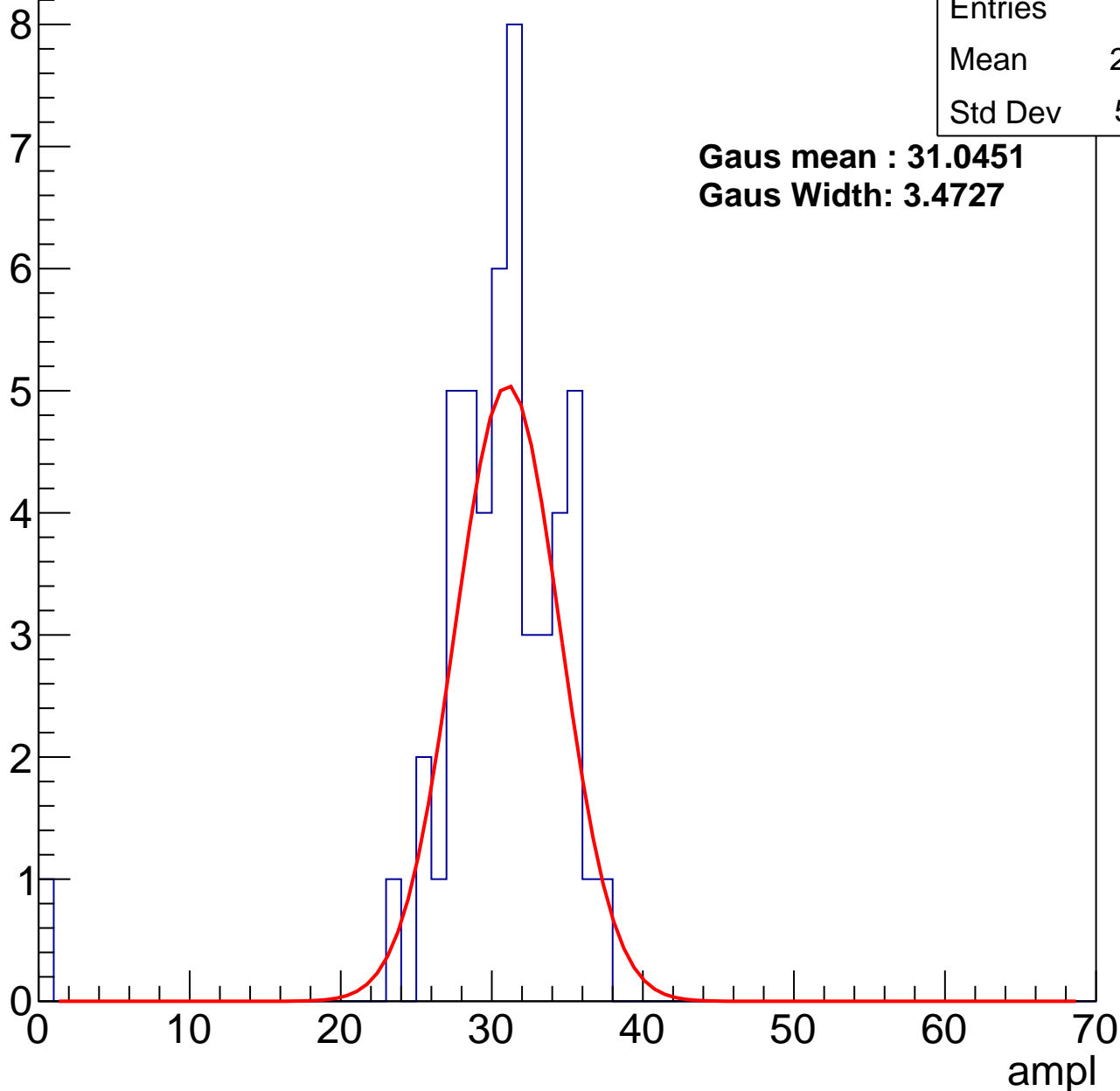
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	29.94
Std Dev	5.301

Gaus mean : 31.0451

Gaus Width: 3.4727



B0L001S, U23-ch62, adc1

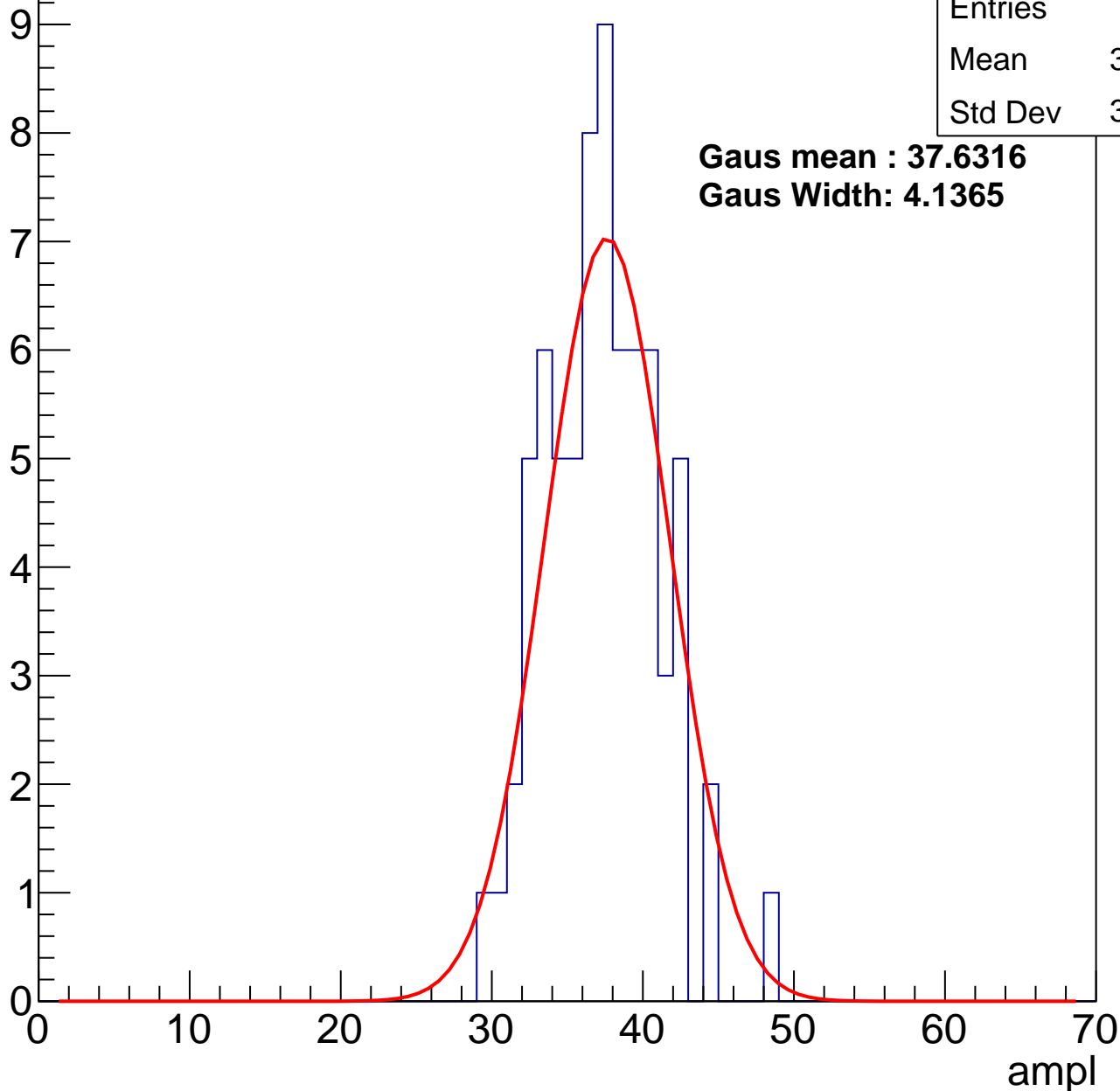
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.85
Std Dev	3.664

Gaus mean : 37.6316

Gaus Width: 4.1365



B0L001S, U23-ch62, adc2

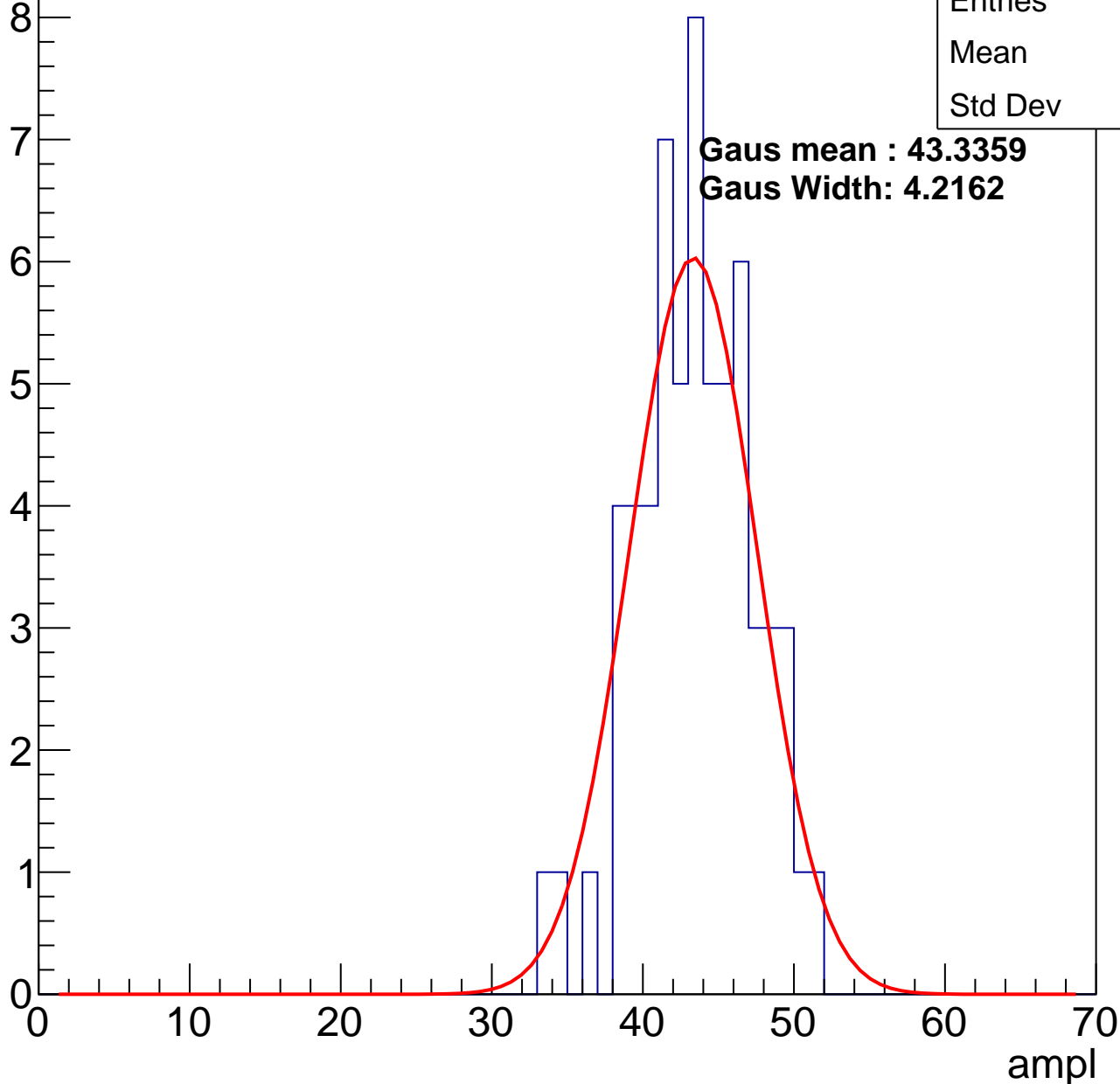
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43
Std Dev	3.78

Gaus mean : 43.3359

Gaus Width: 4.2162

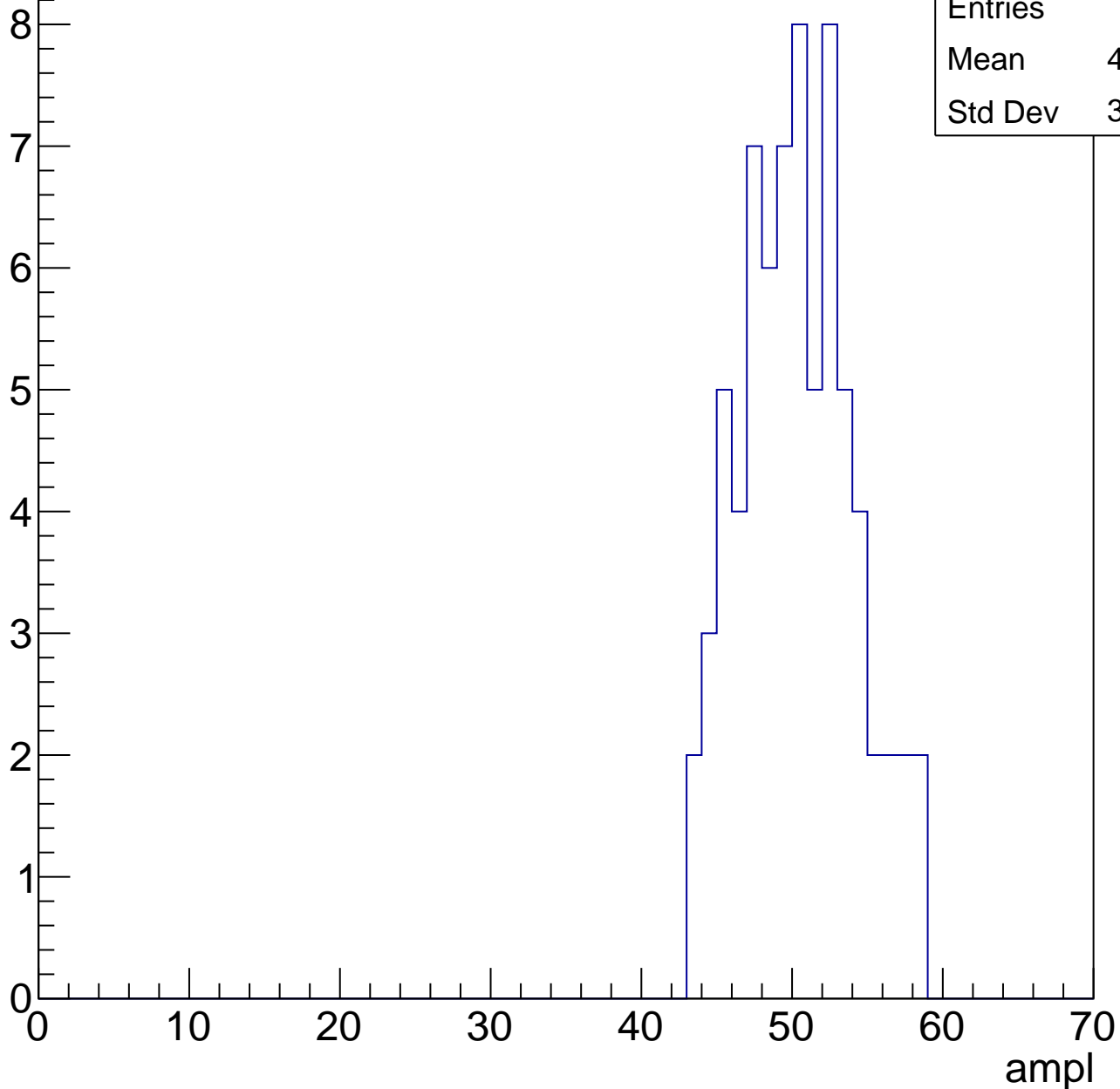


B0L001S, U23-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	49.88
Std Dev	3.693

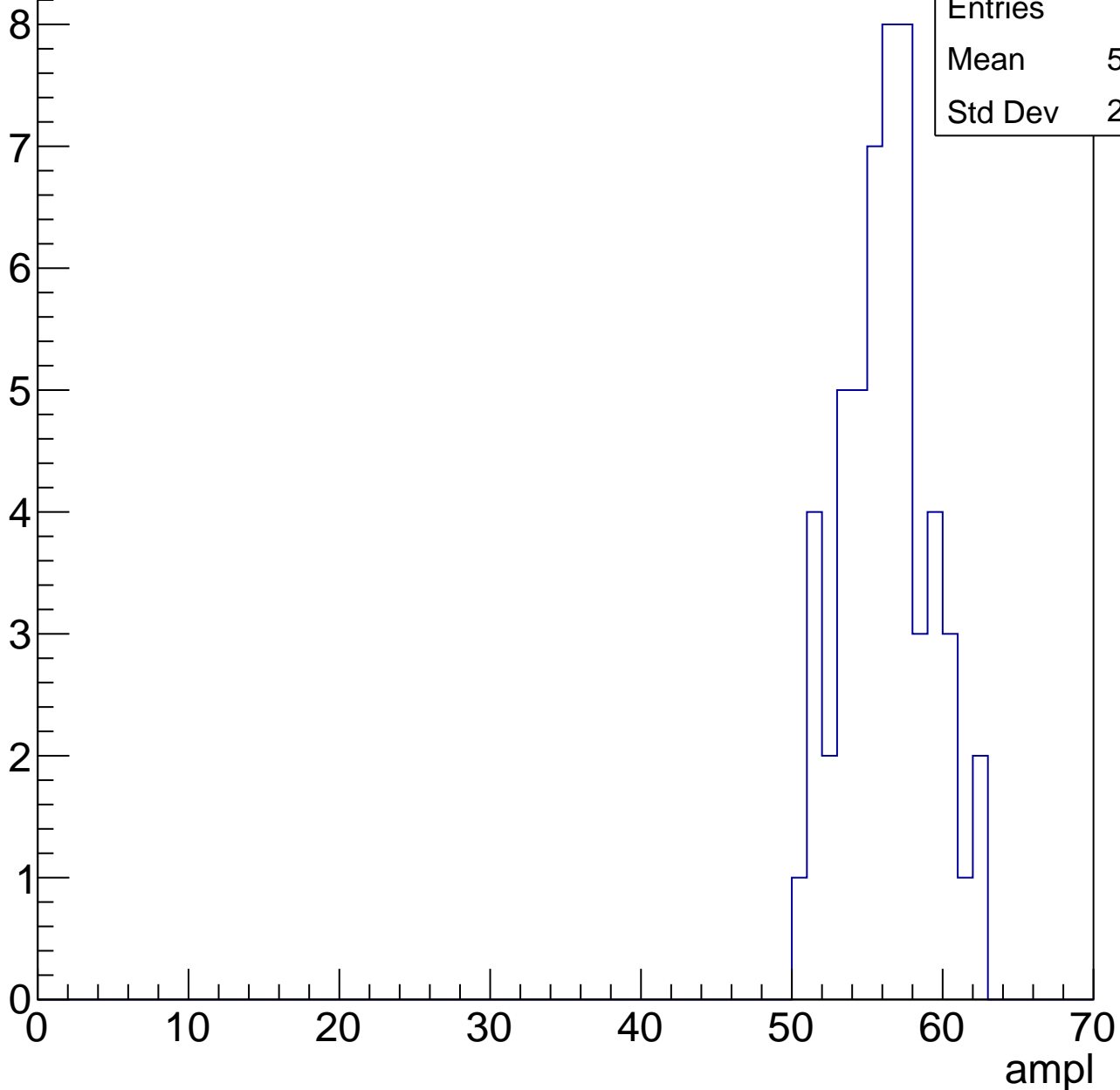


B0L001S, U23-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.79
Std Dev	2.877

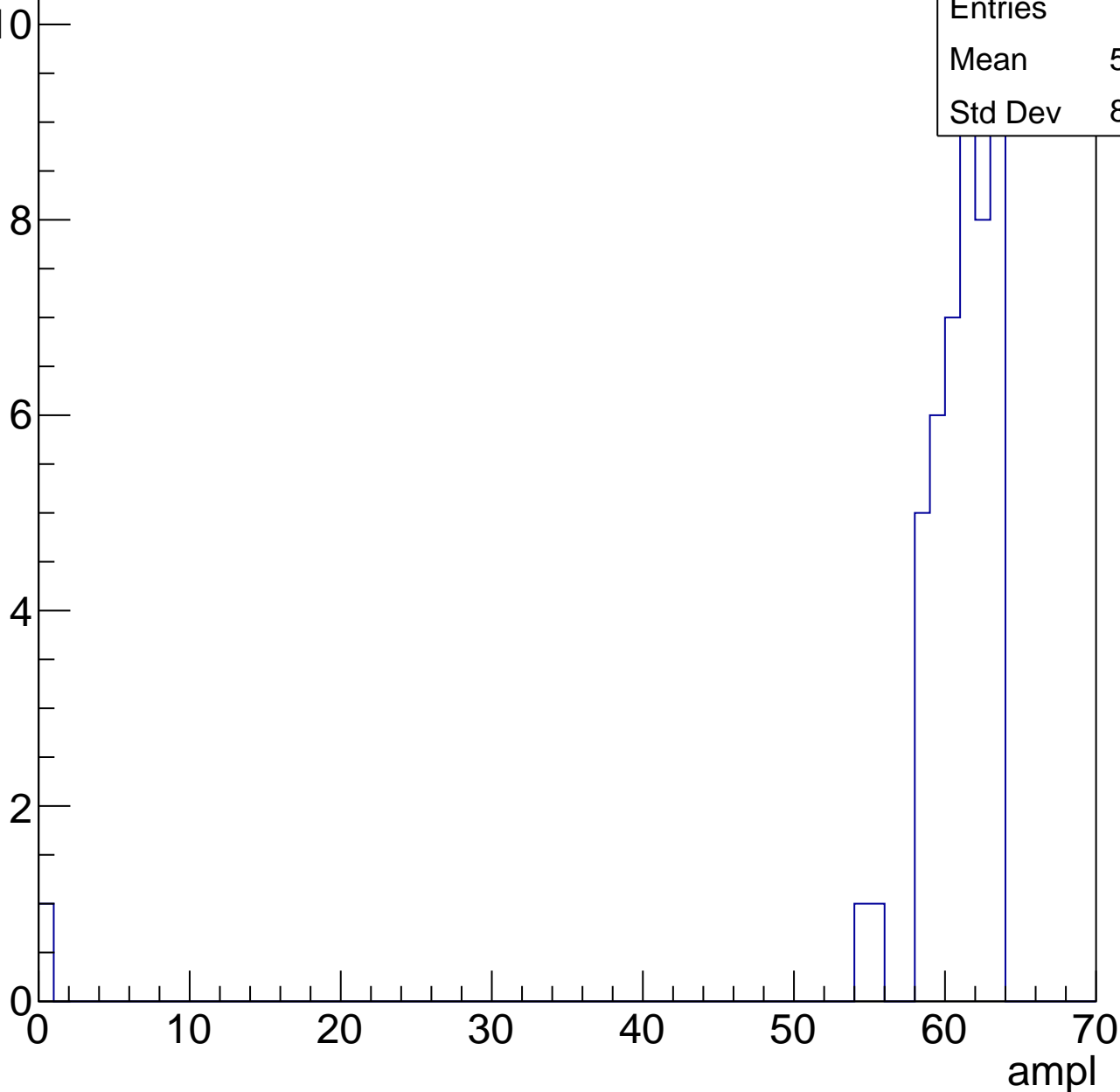


B0L001S, U23-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	59.33
Std Dev	8.894



B0L001S, U23-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch63, adc0

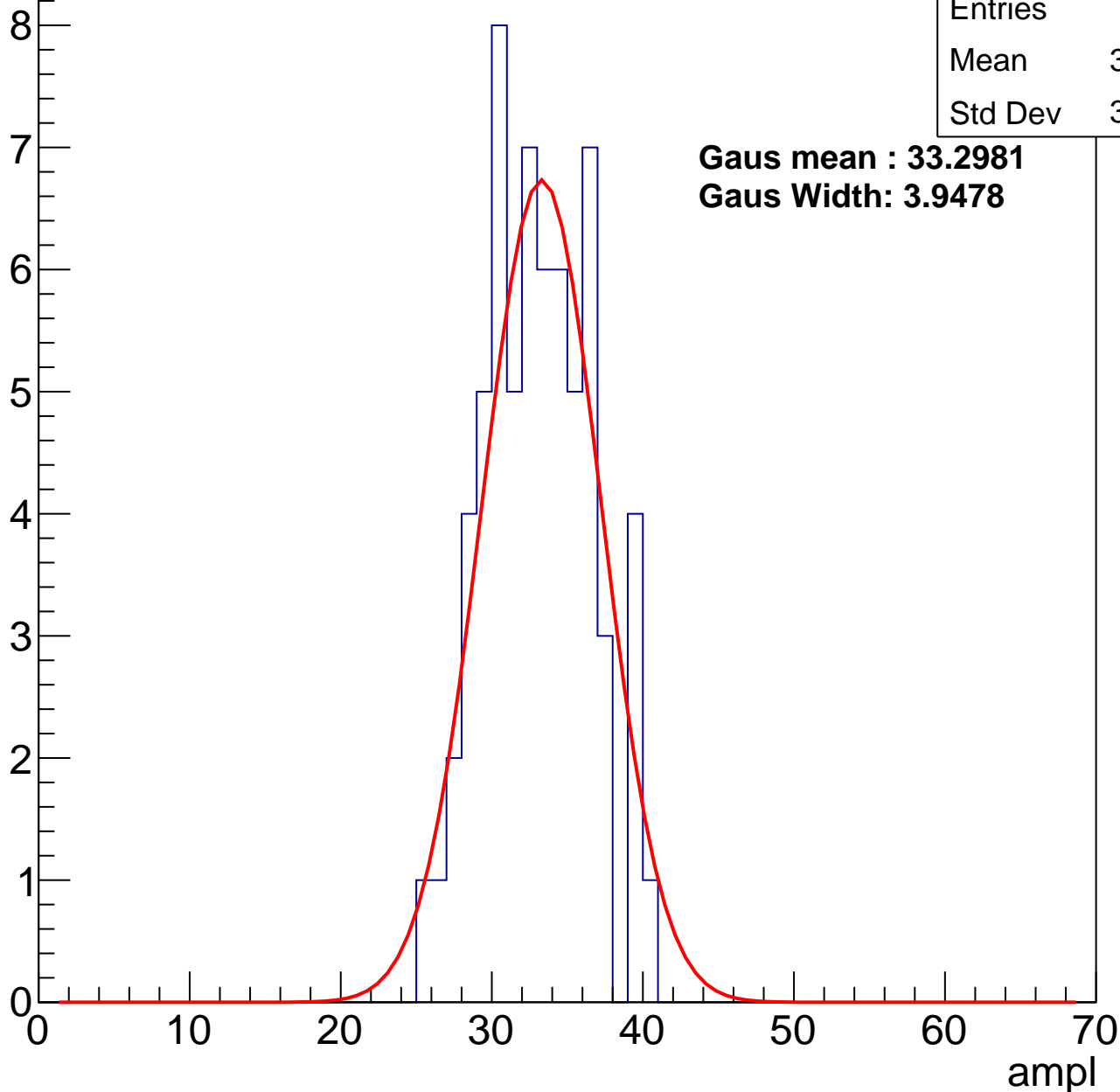
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.57
Std Dev	3.468

Gaus mean : 33.2981

Gaus Width: 3.9478



B0L001S, U23-ch63, adc1

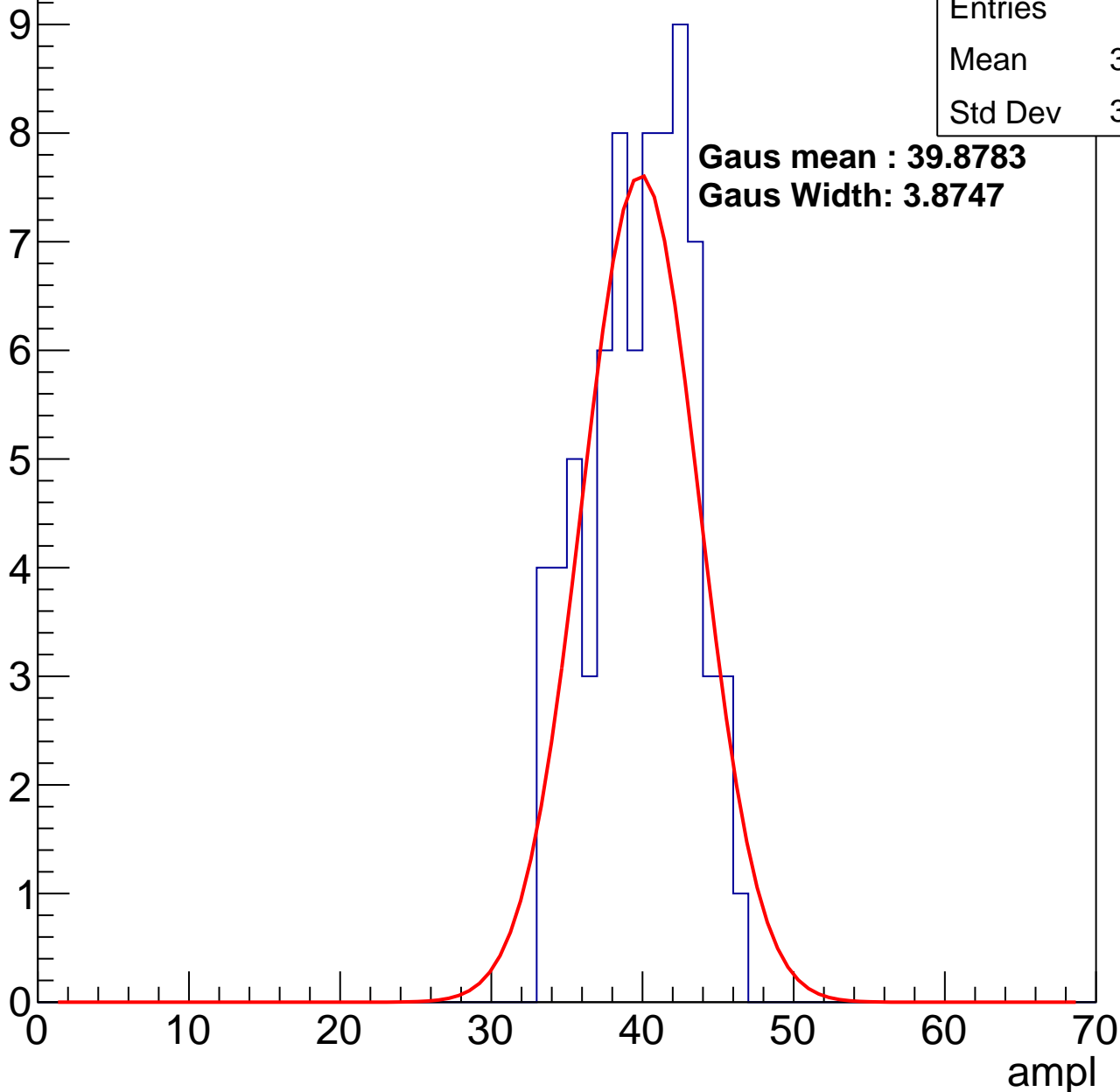
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	39.35
Std Dev	3.345

Gaus mean : 39.8783

Gaus Width: 3.8747



B0L001S, U23-ch63, adc2

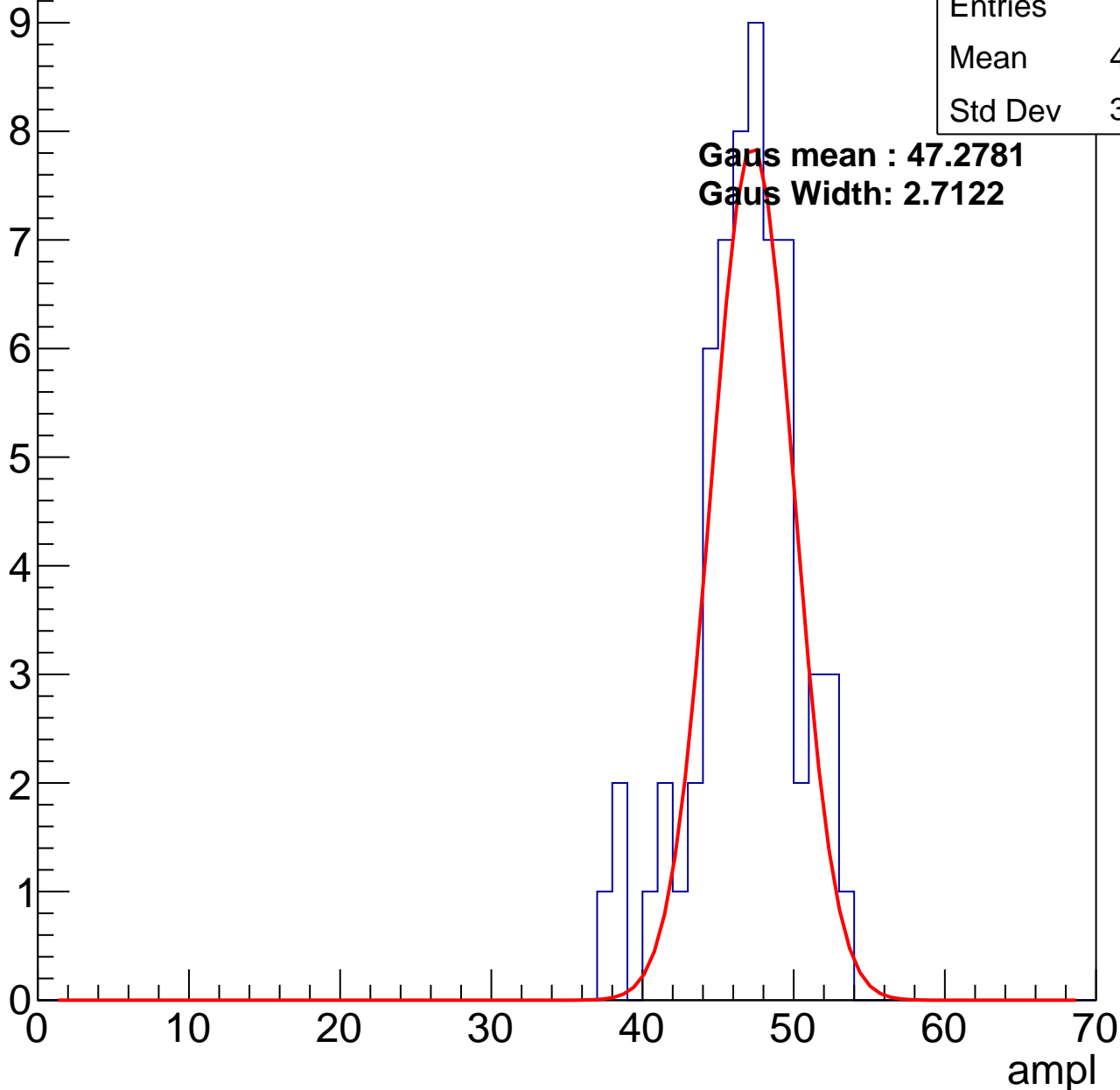
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	46.35
Std Dev	3.399

Gaus mean : 47.2781

Gaus Width: 2.7122

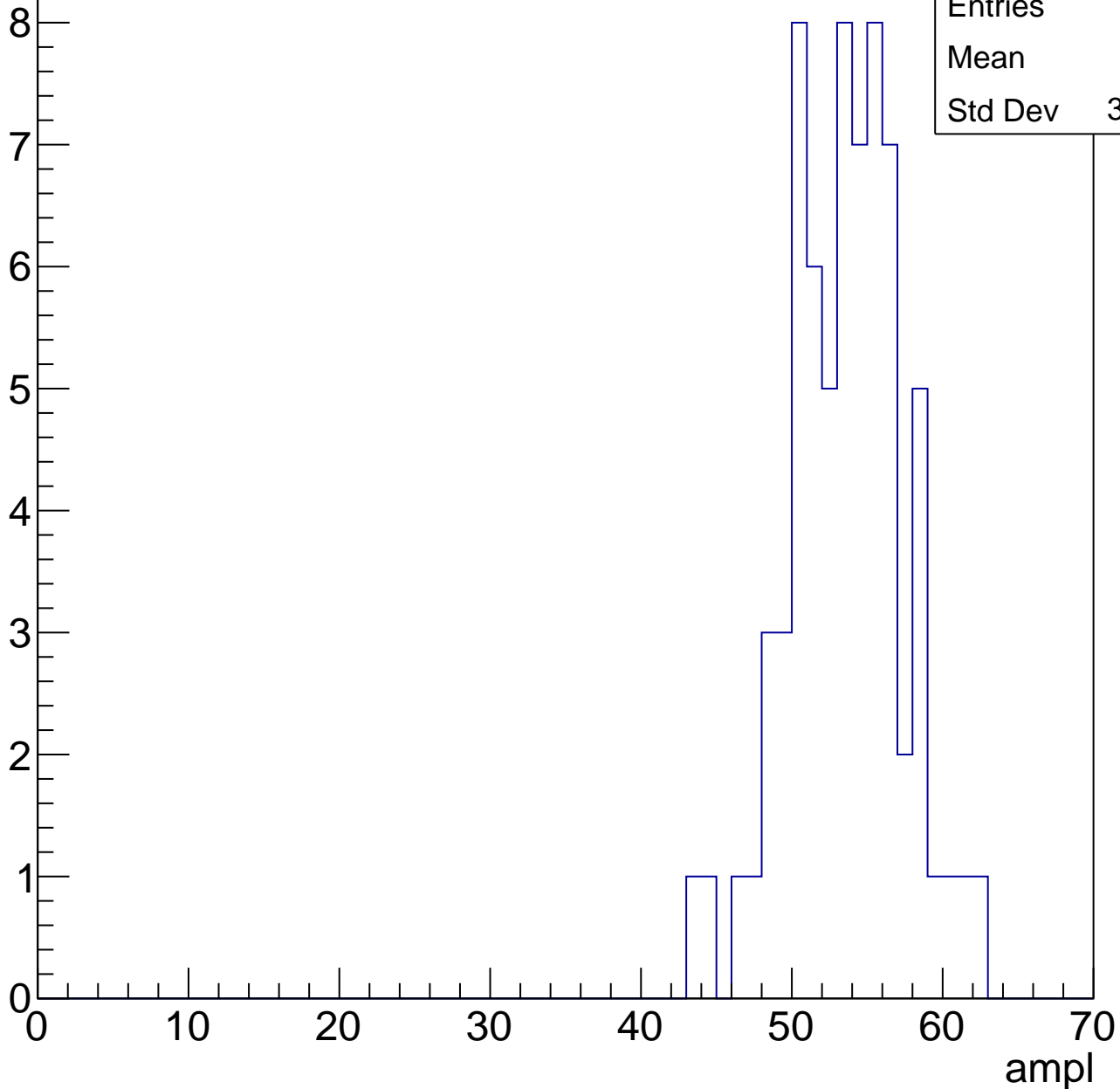


B0L001S, U23-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	53.1
Std Dev	3.738

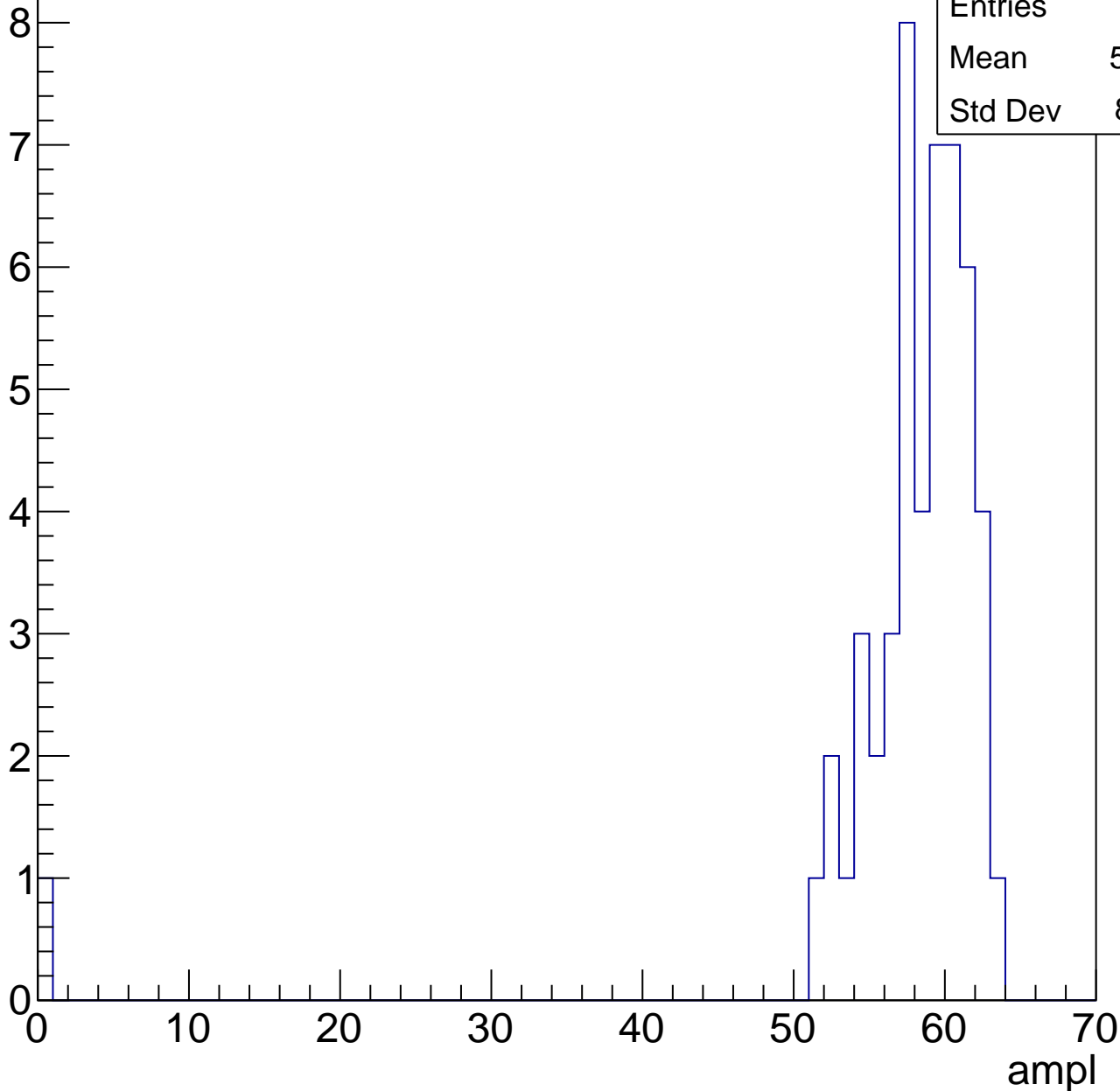


B0L001S, U23-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

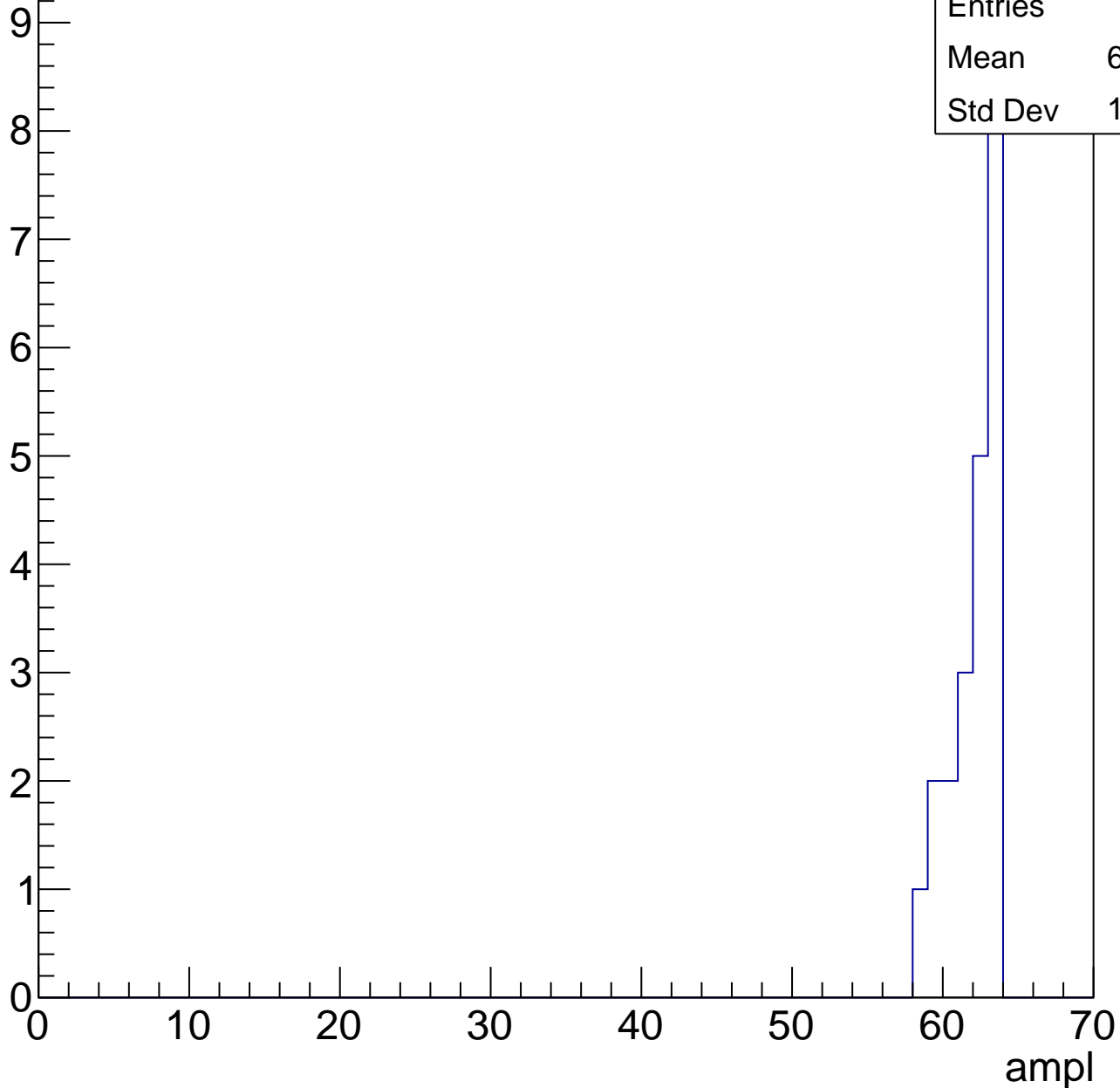
Entries	50
Mean	56.92
Std Dev	8.621



B0L001S, U23-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch63, adc7

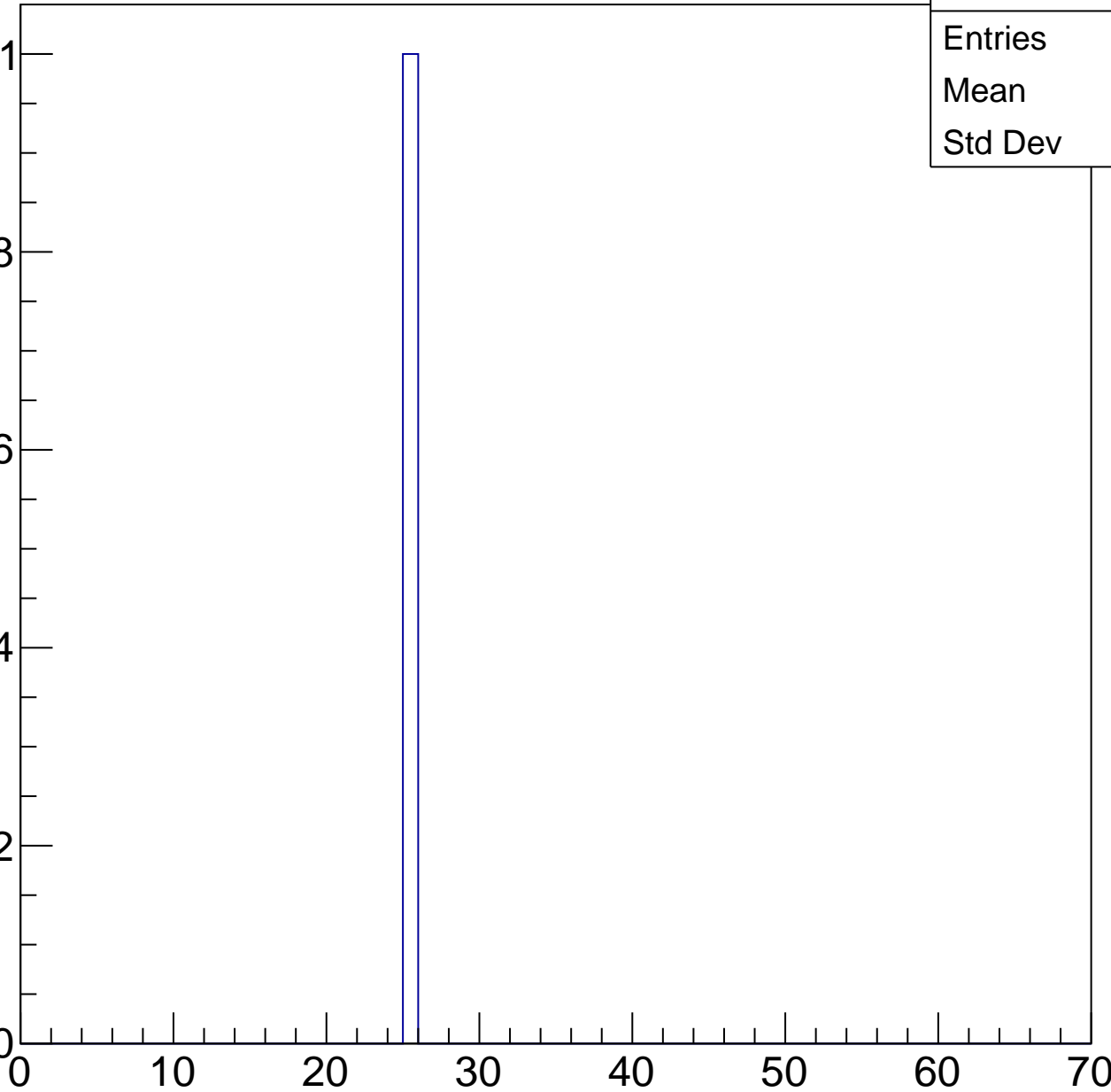
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L001S, U23-ch64, adc0

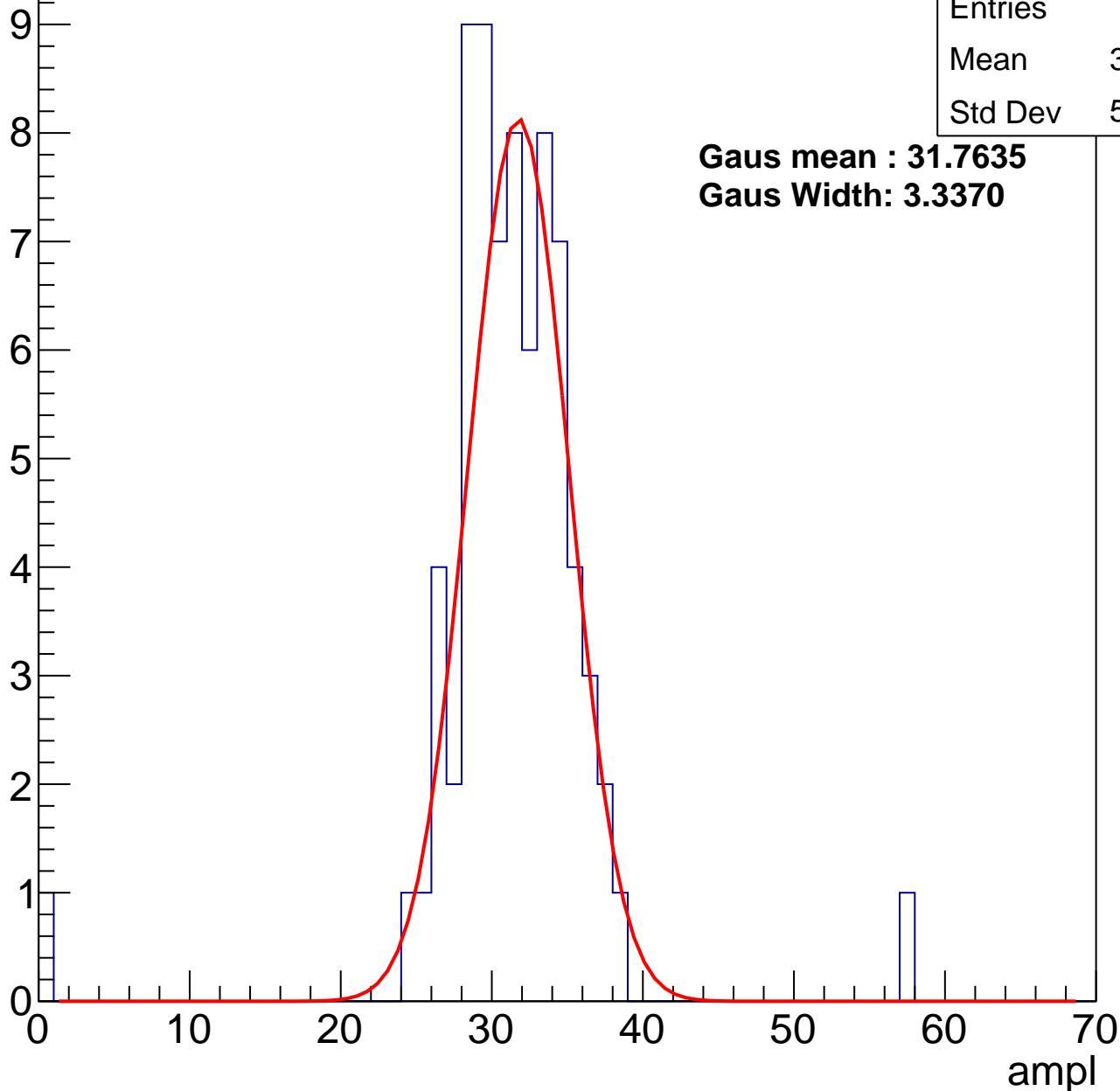
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.93
Std Dev	5.622

Gaus mean : 31.7635

Gaus Width: 3.3370



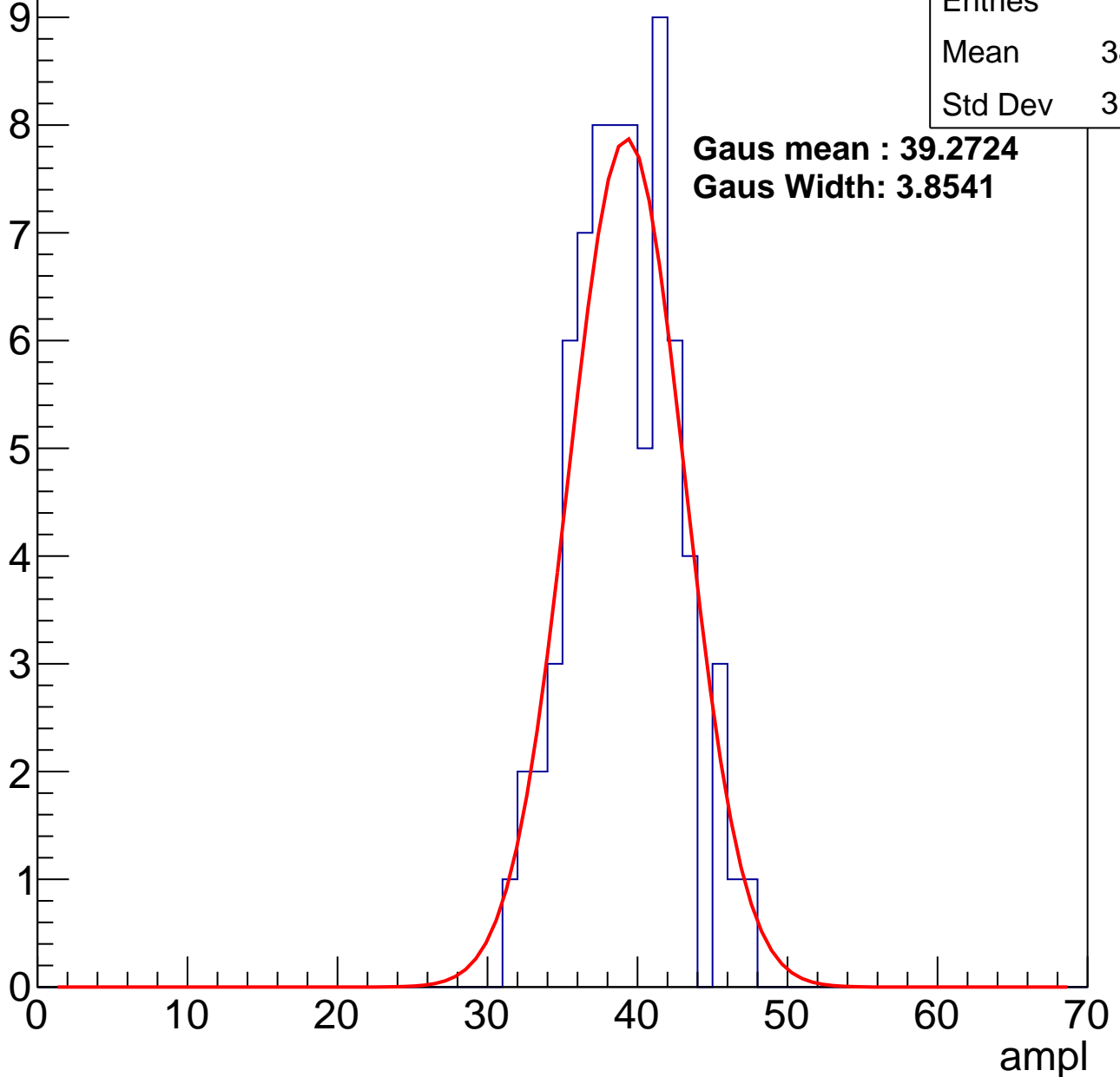
B0L001S, U23-ch64, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.62
Std Dev	3.443

Gaus mean : 39.2724
Gaus Width: 3.8541



B0L001S, U23-ch64, adc2

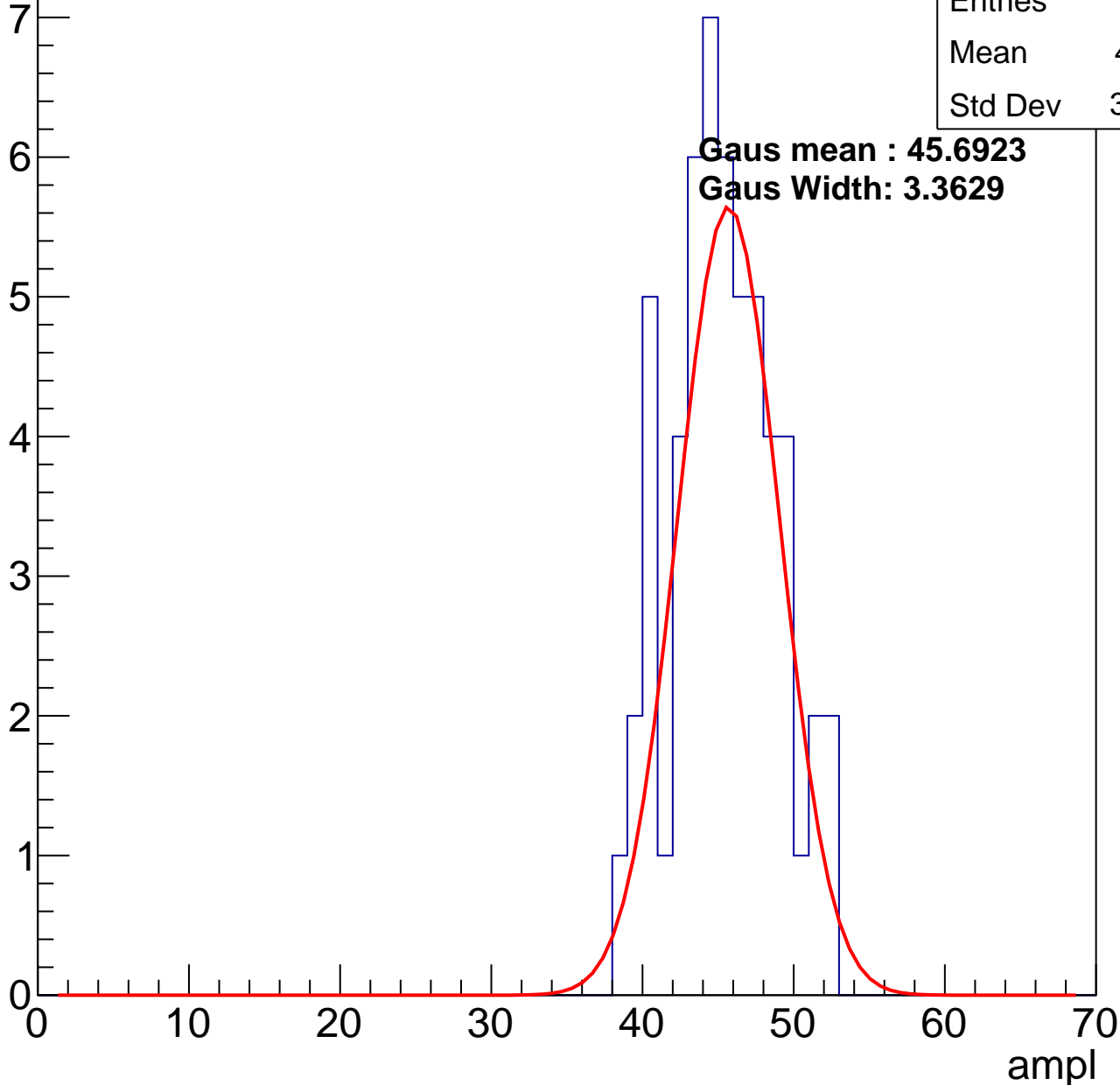
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.91
Std Dev	3.434

Gaus mean : 45.6923

Gaus Width: 3.3629

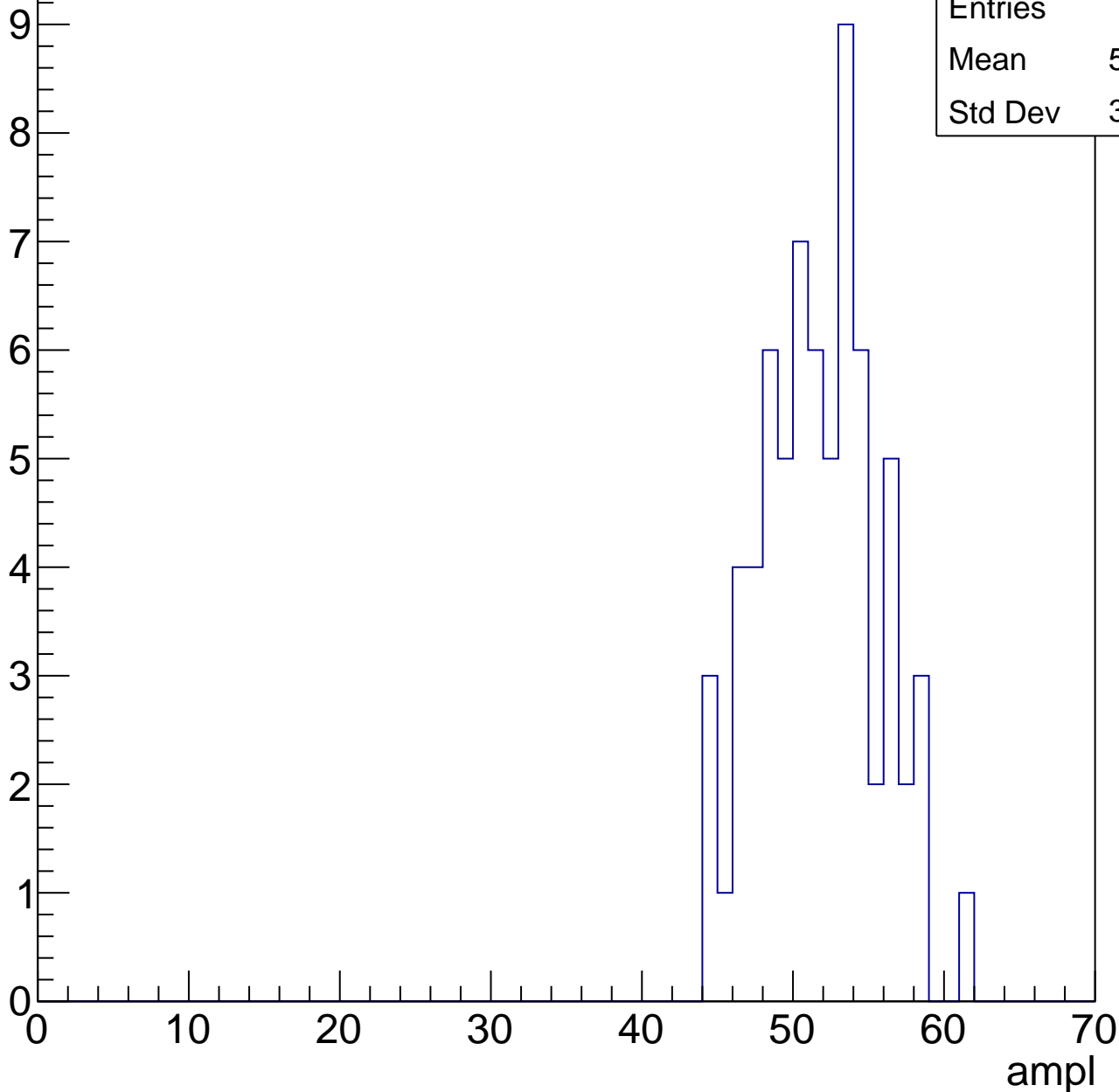


B0L001S, U23-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.28
Std Dev	3.795

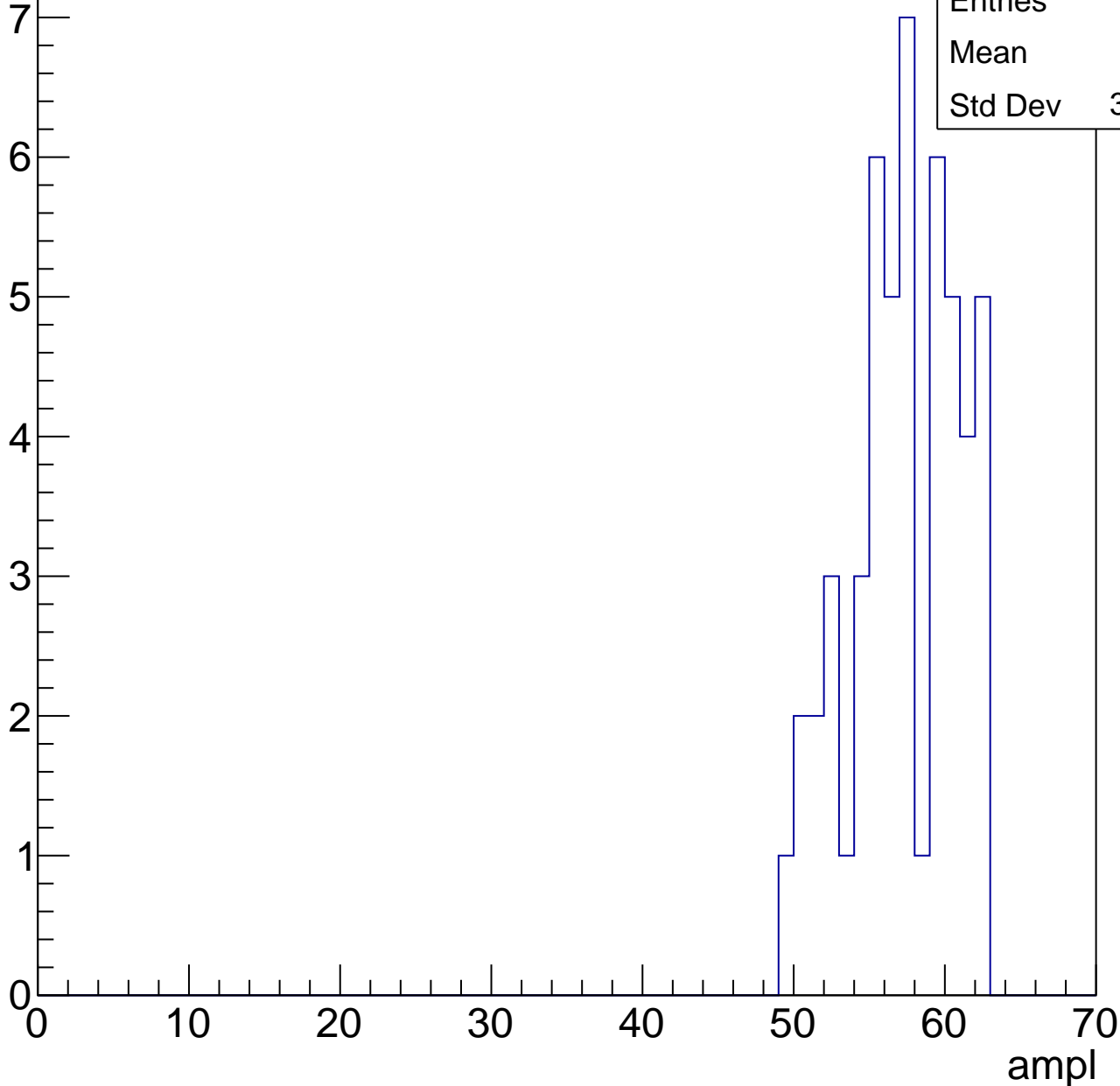


B0L001S, U23-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.8
Std Dev	3.537

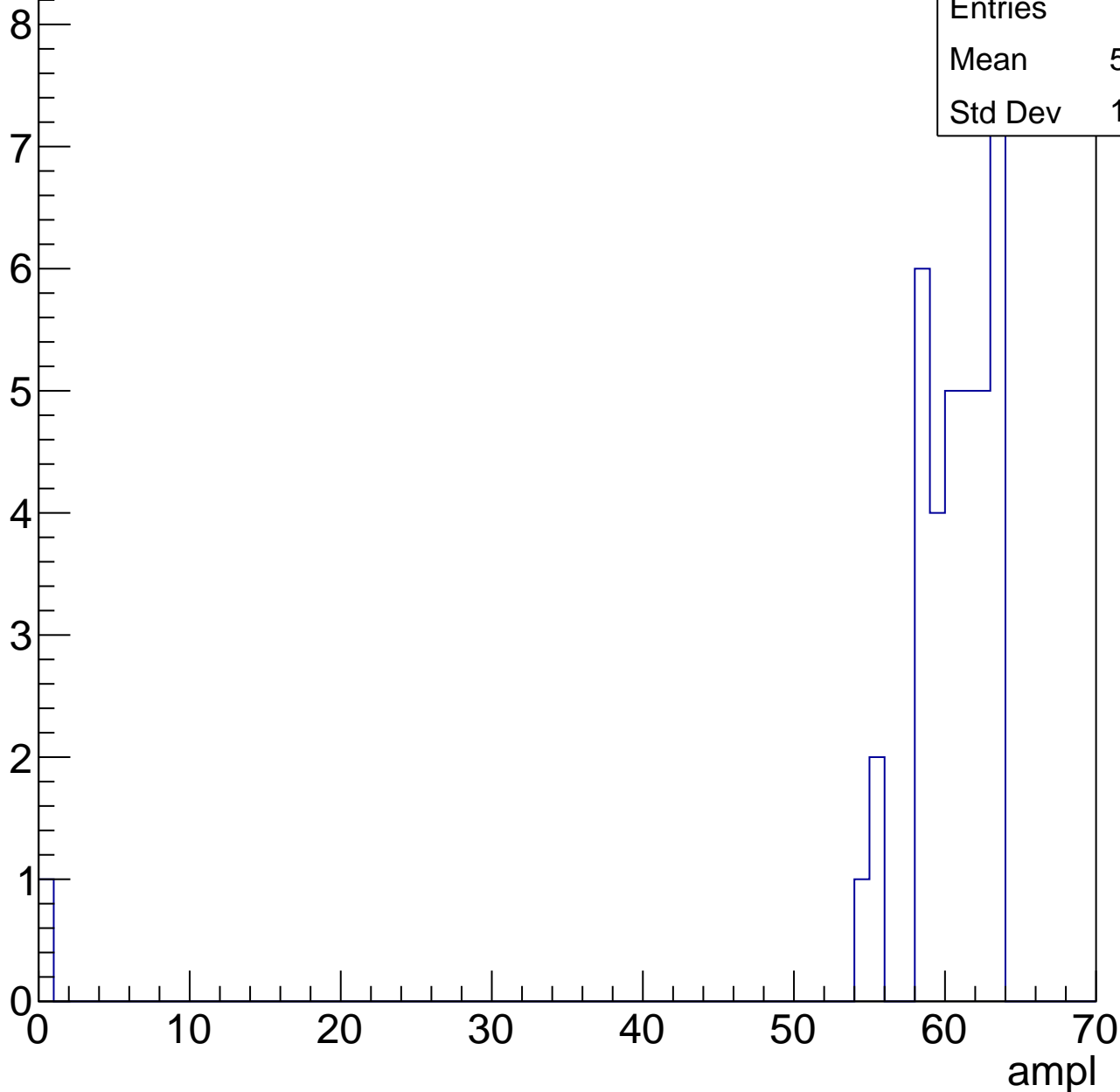


B0L001S, U23-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.57
Std Dev	10.05



B0L001S, U23-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch65, adc0

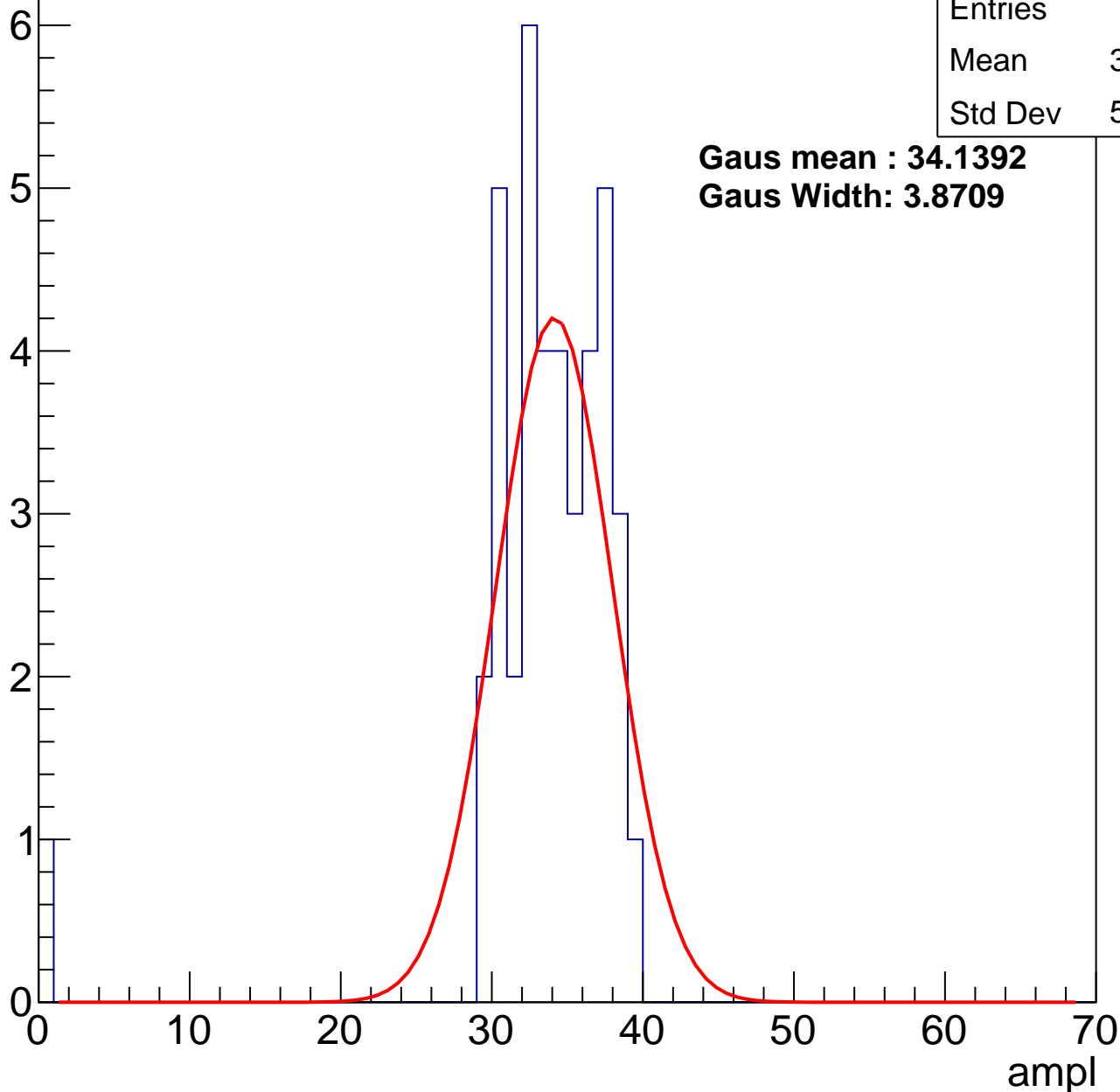
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	32.92
Std Dev	5.964

Gaus mean : 34.1392

Gaus Width: 3.8709



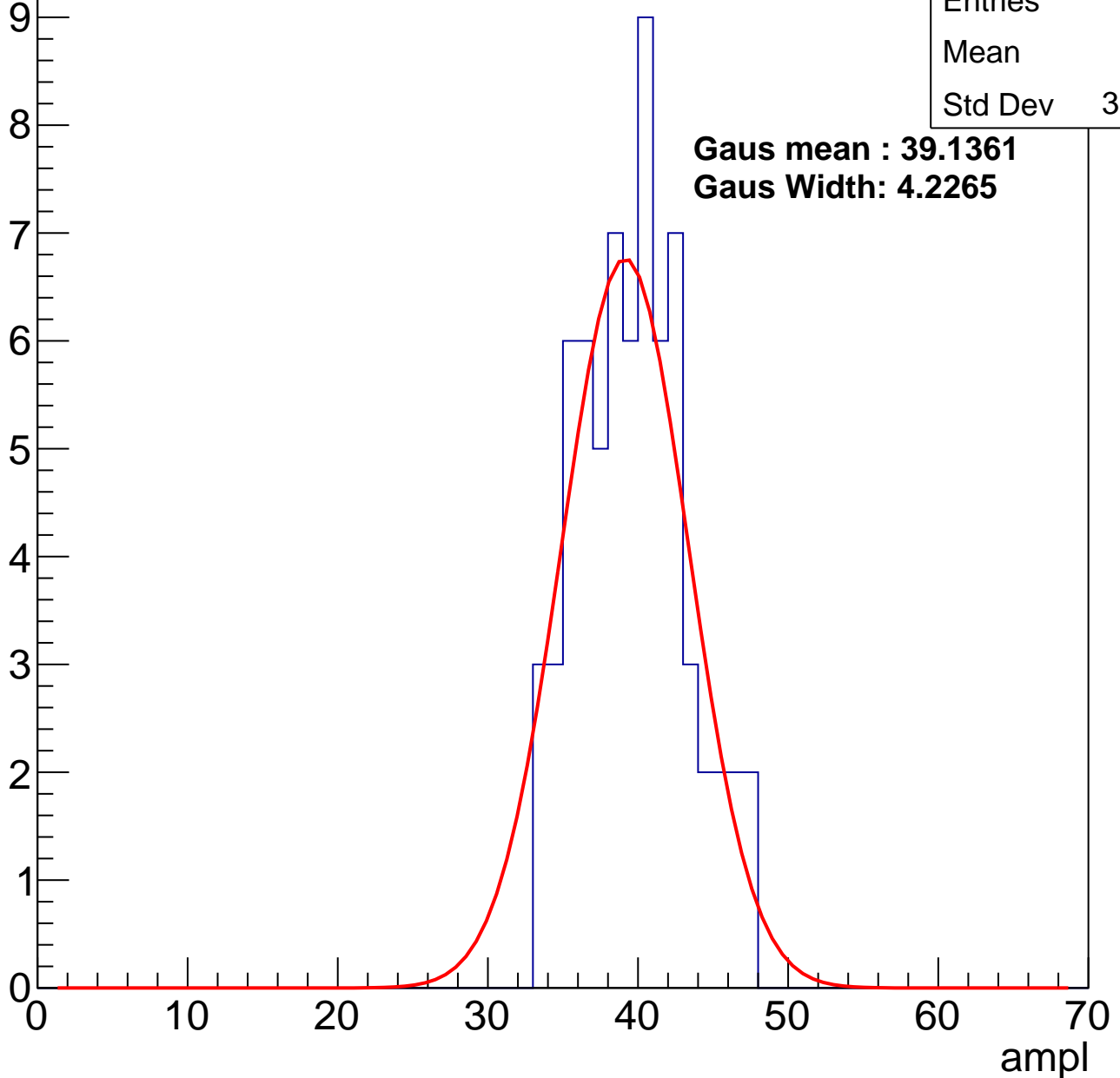
B0L001S, U23-ch65, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	39.2
Std Dev	3.508

Gaus mean : 39.1361
Gaus Width: 4.2265



B0L001S, U23-ch65, adc2

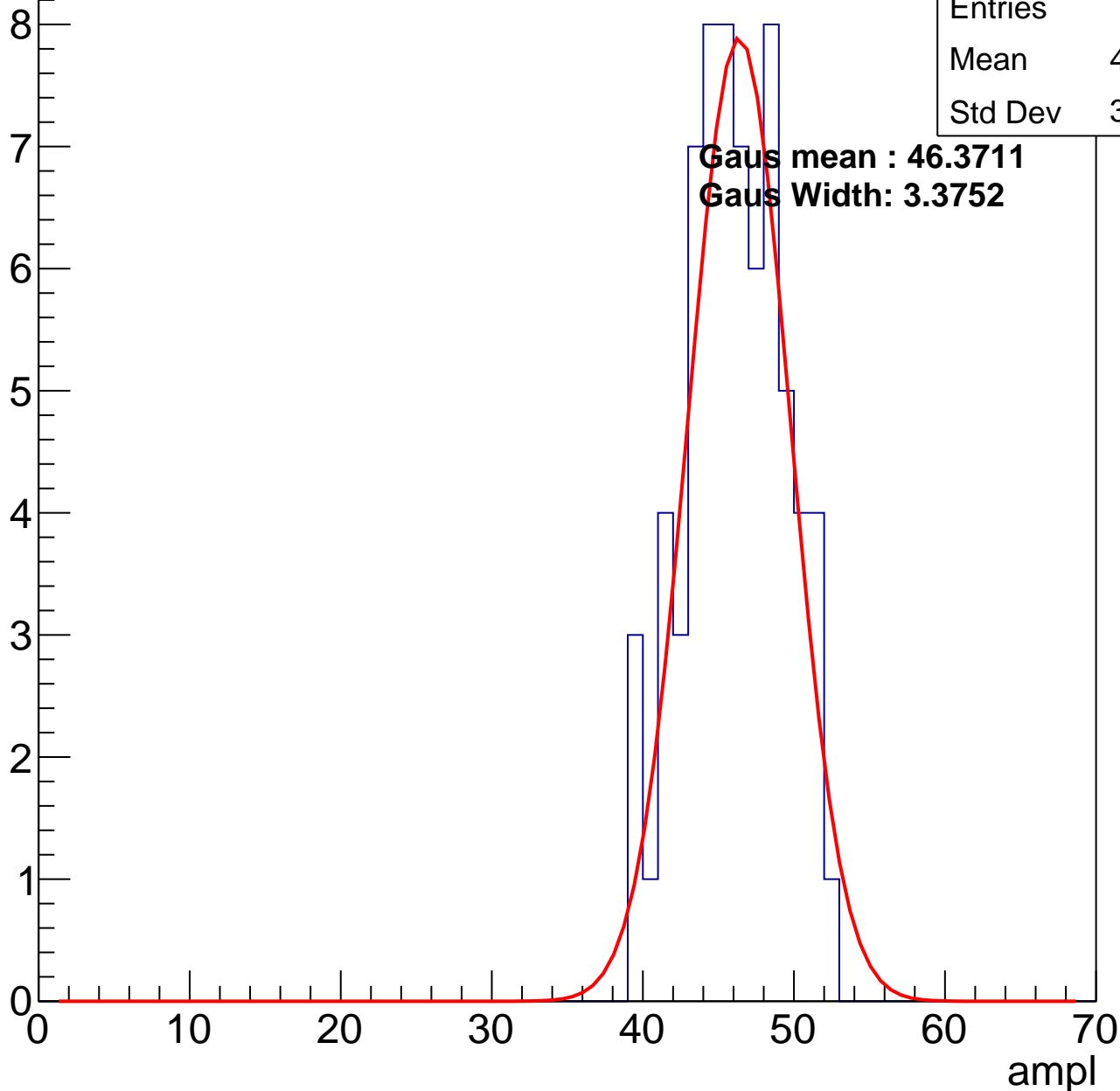
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	45.64
Std Dev	3.203

Gaus mean : 46.3711

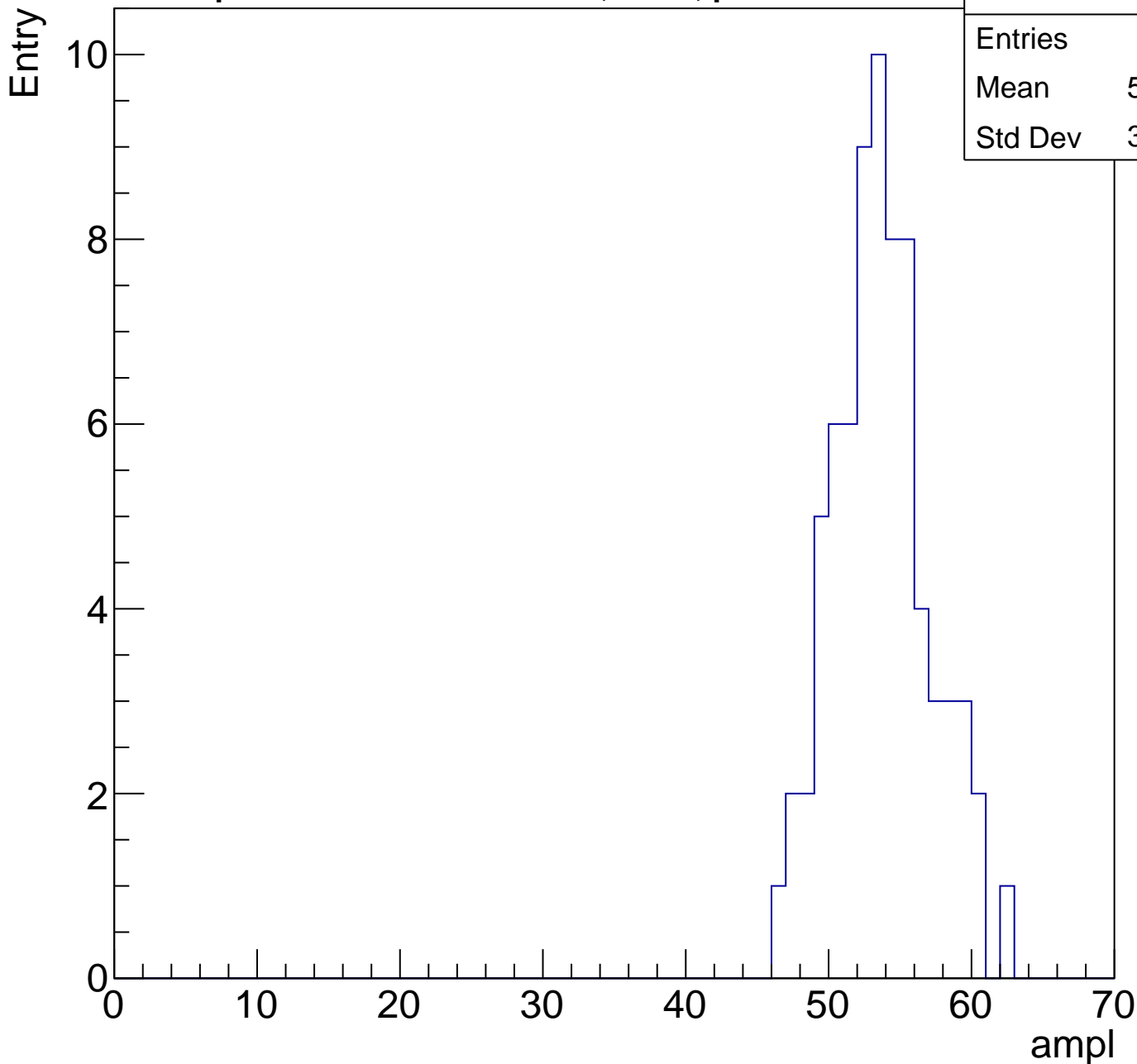
Gaus Width: 3.3752



B0L001S, U23-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	53.22
Std Dev	3.353

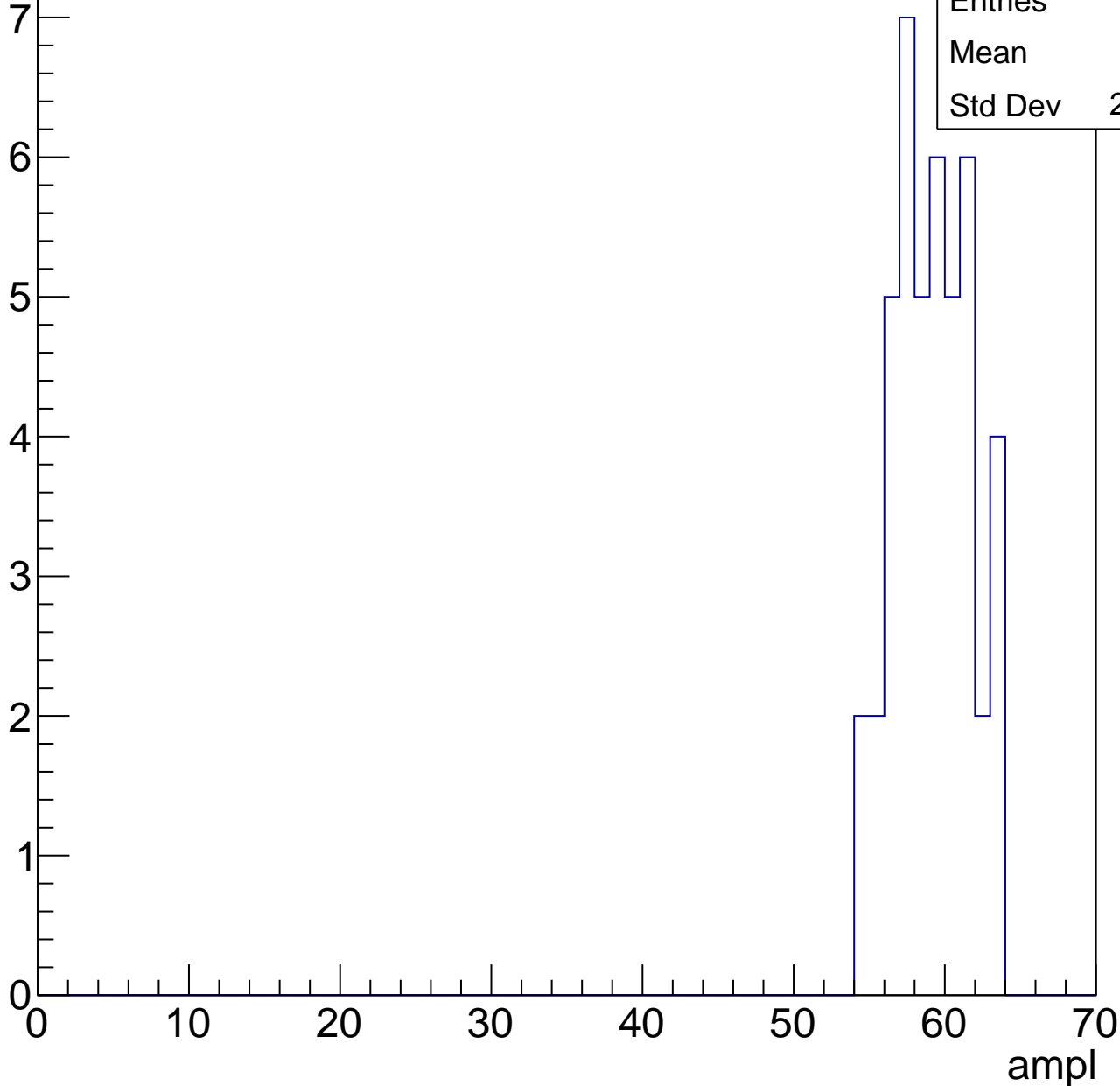


B0L001S, U23-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.7
Std Dev	2.464

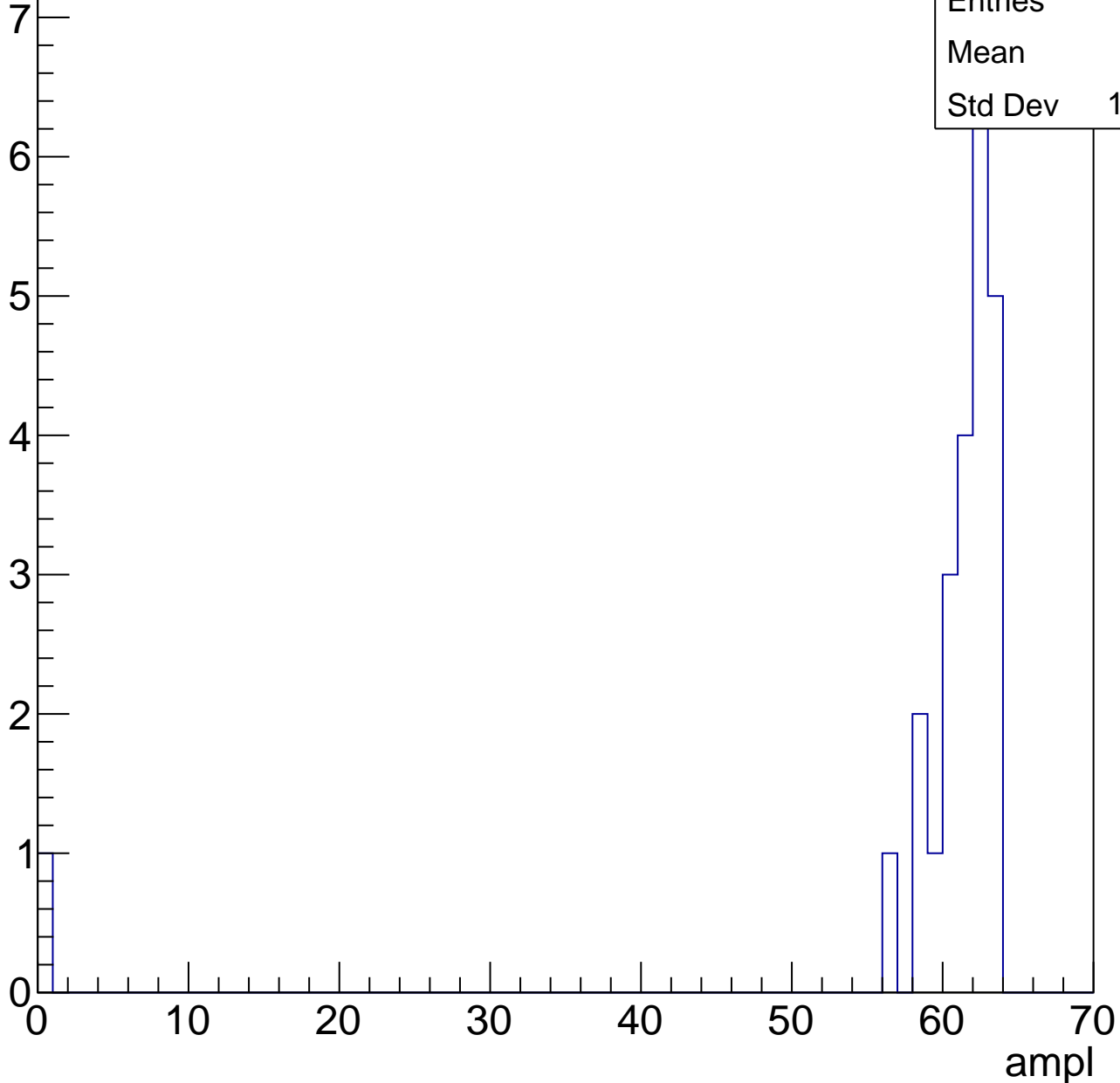


B0L001S, U23-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.5
Std Dev	12.33



B0L001S, U23-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

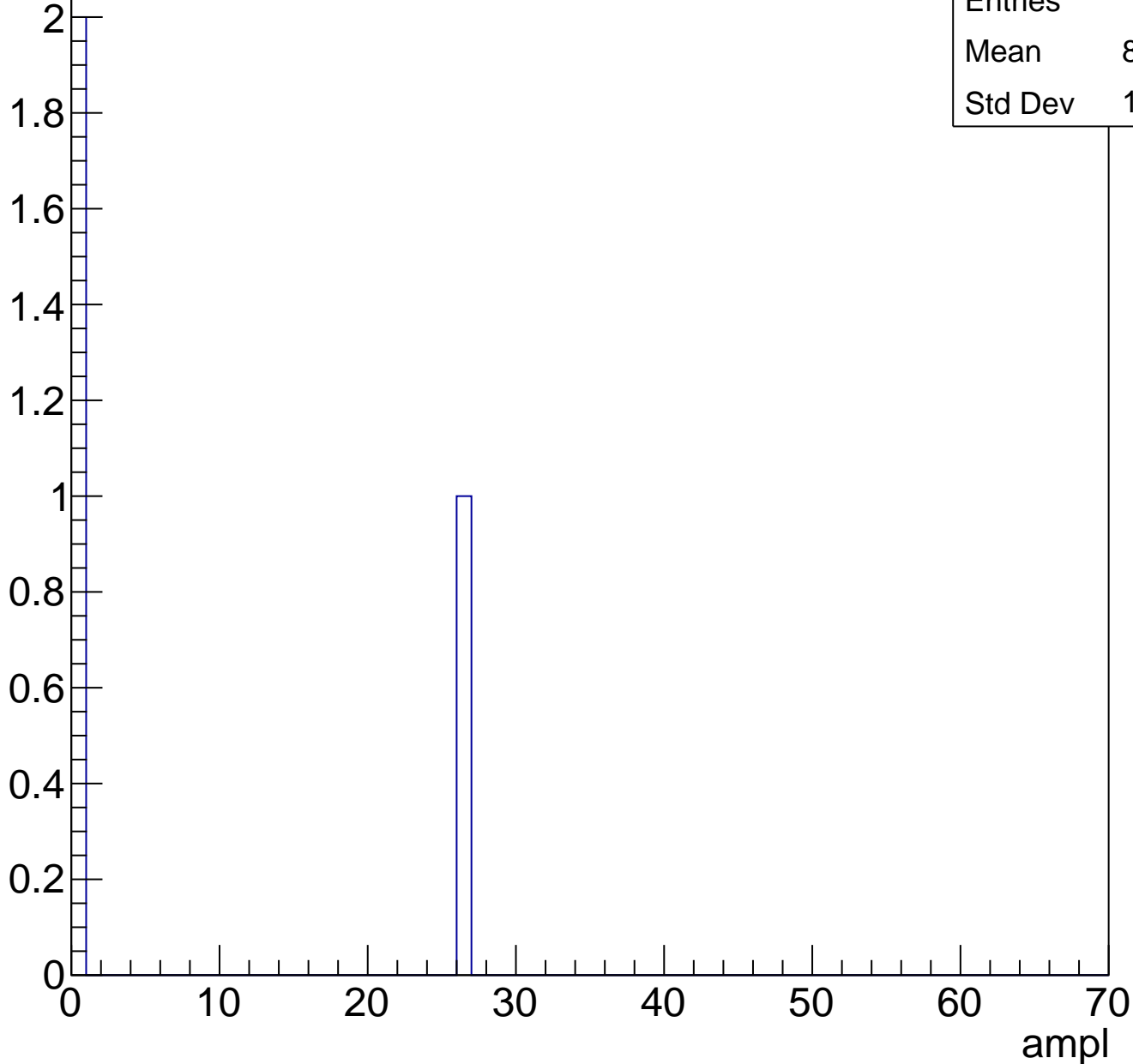
Entry



B0L001S, U23-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch66, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	30.6
Std Dev	3.196

Gaus mean : 31.1190

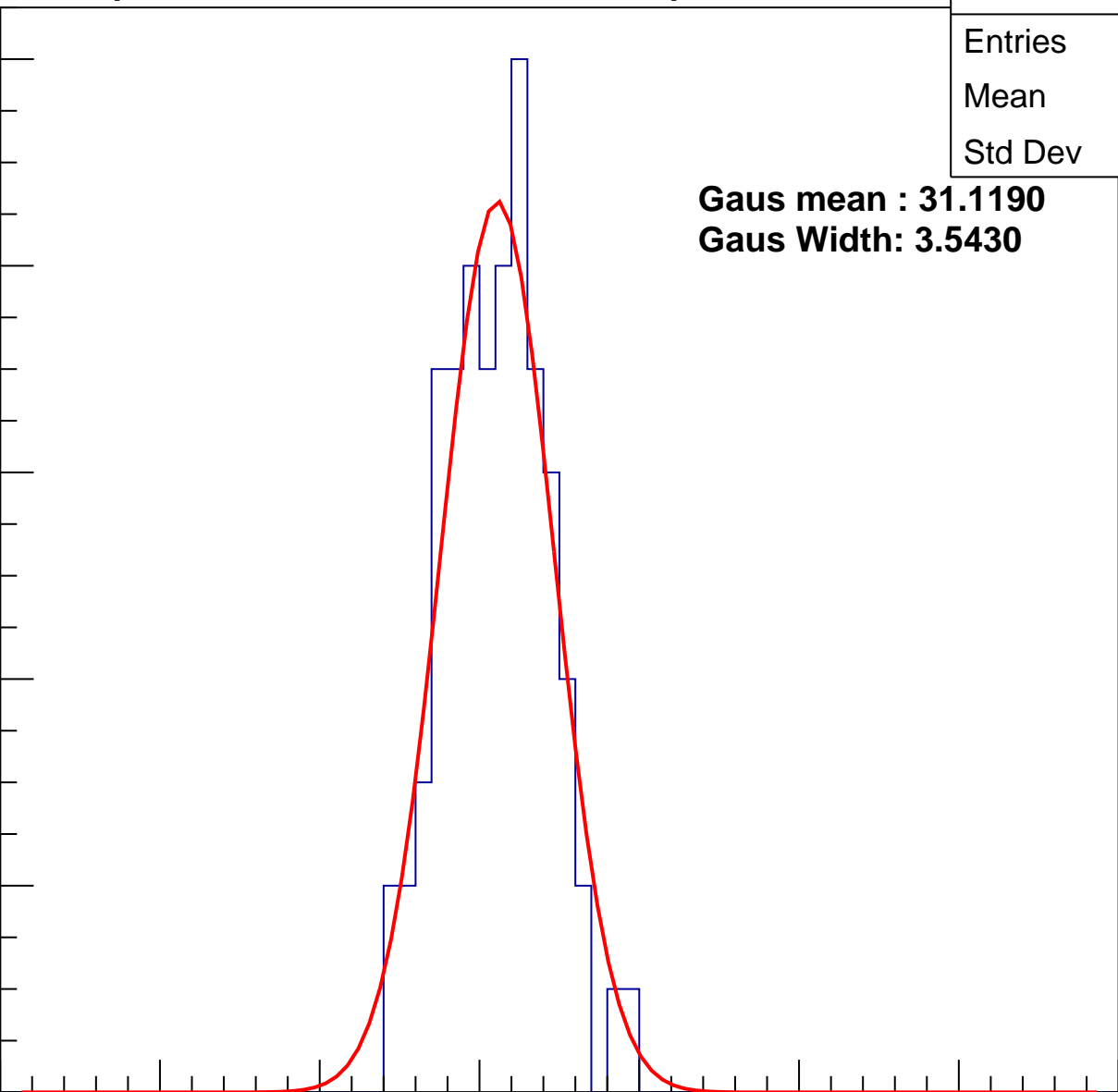
Gaus Width: 3.5430

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch66, adc1

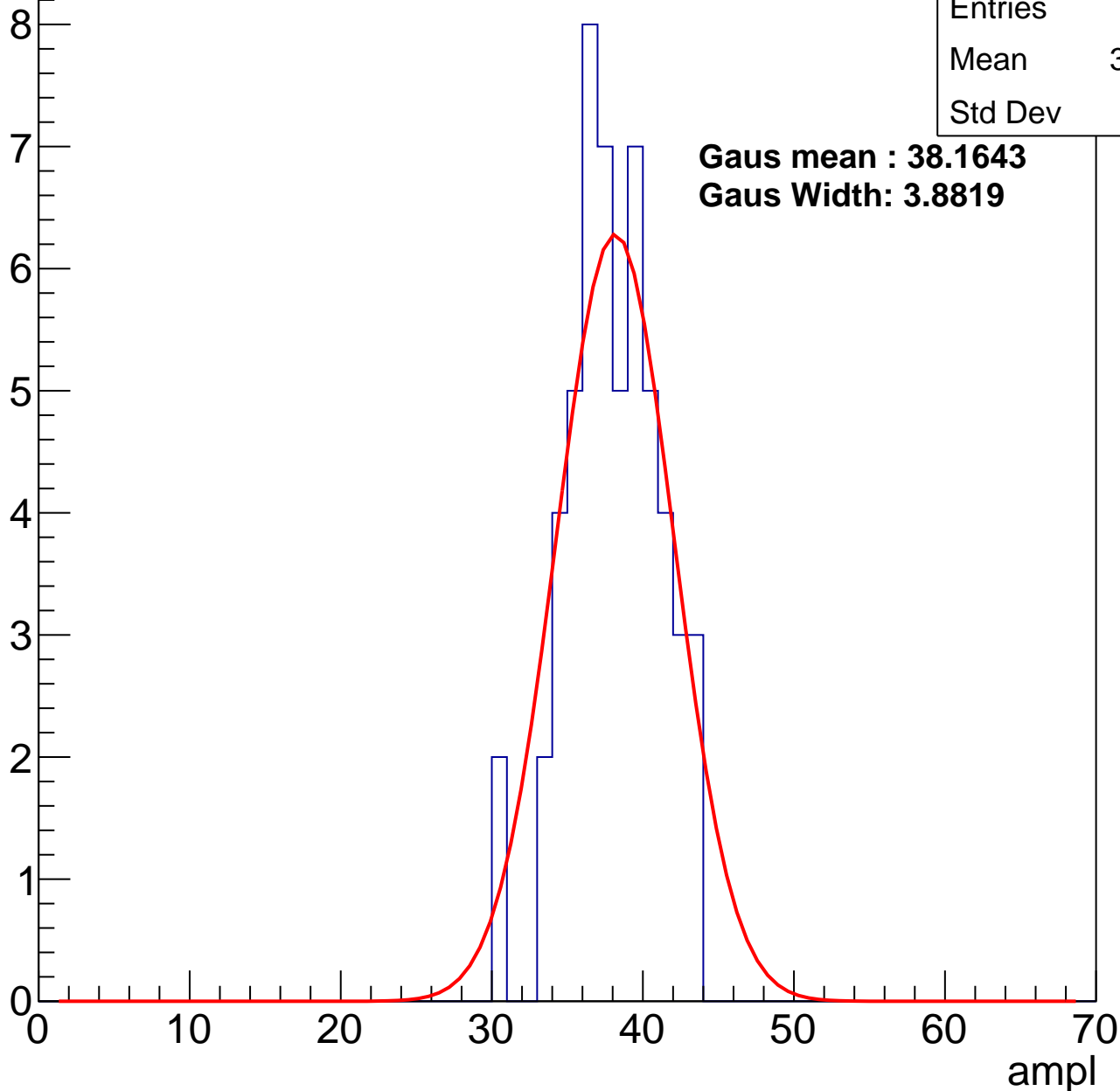
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.56
Std Dev	3.02

Gaus mean : 38.1643

Gaus Width: 3.8819



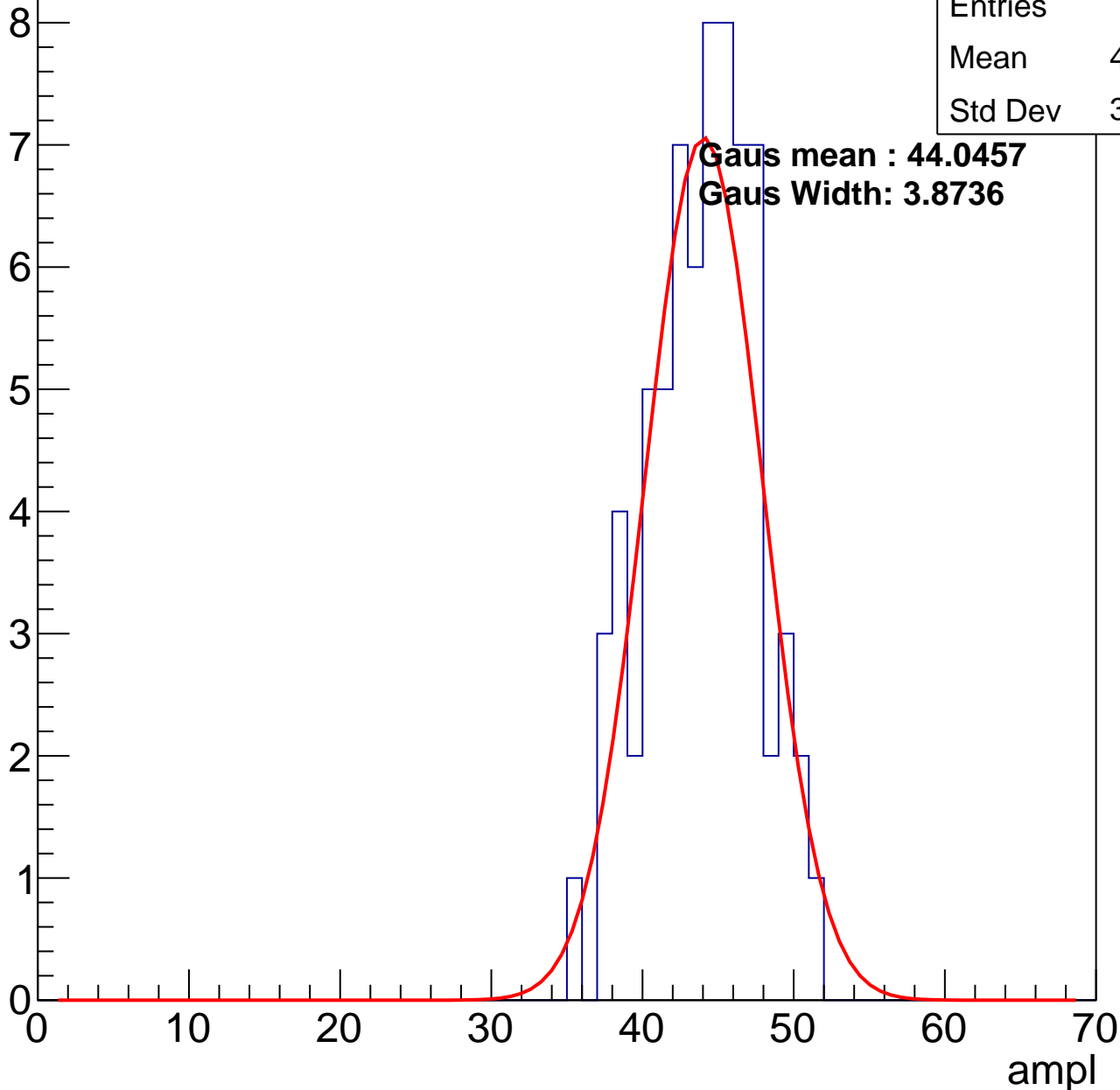
B0L001S, U23-ch66, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.52
Std Dev	3.544

Gaus mean : 44.0457
Gaus Width: 3.8736

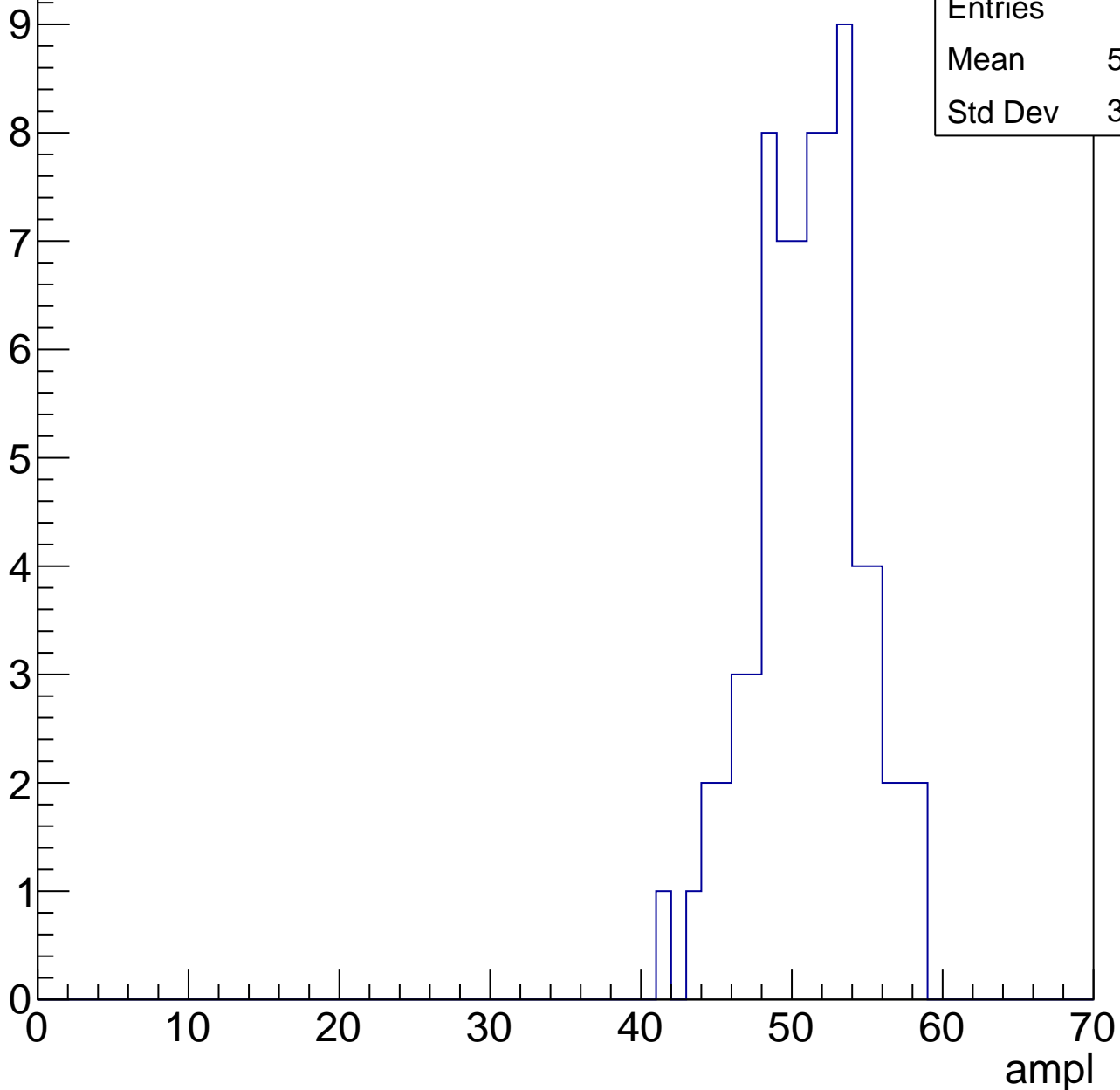


B0L001S, U23-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.64
Std Dev	3.548



B0L001S, U23-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	57.51
Std Dev	3.116

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

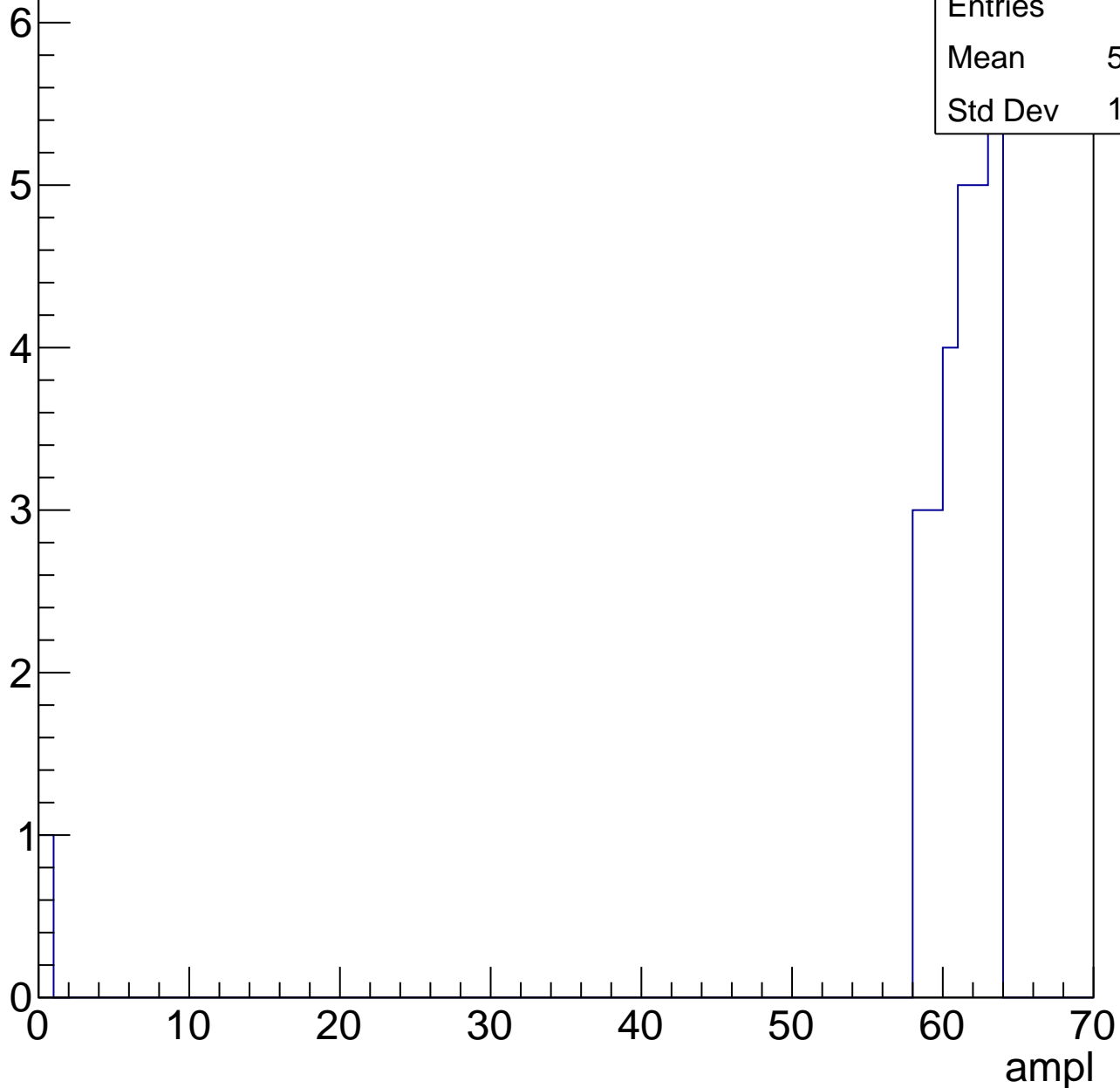
7

8

B0L001S, U23-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch67, adc0

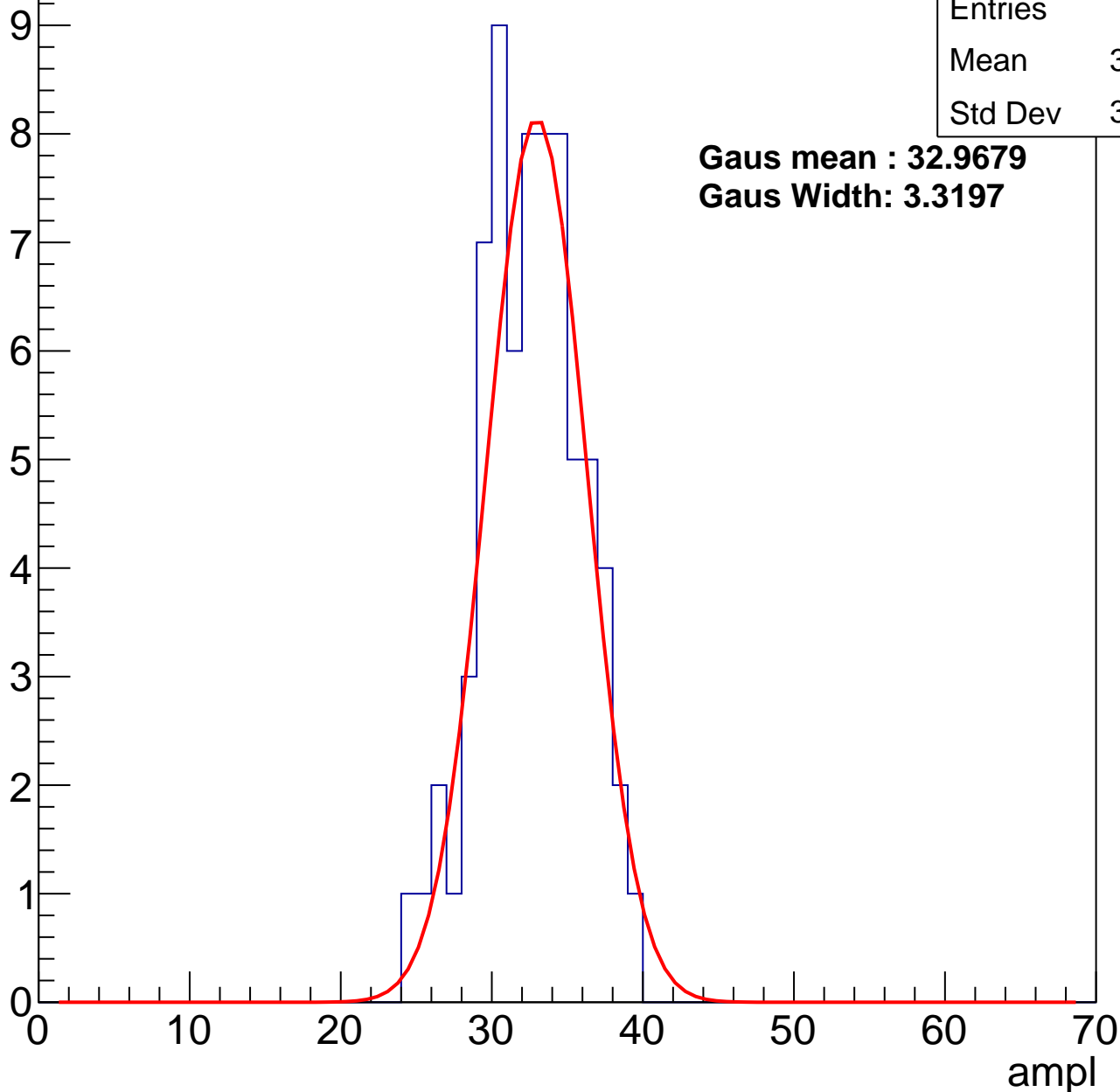
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	32.13
Std Dev	3.245

Gaus mean : 32.9679

Gaus Width: 3.3197



B0L001S, U23-ch67, adc1

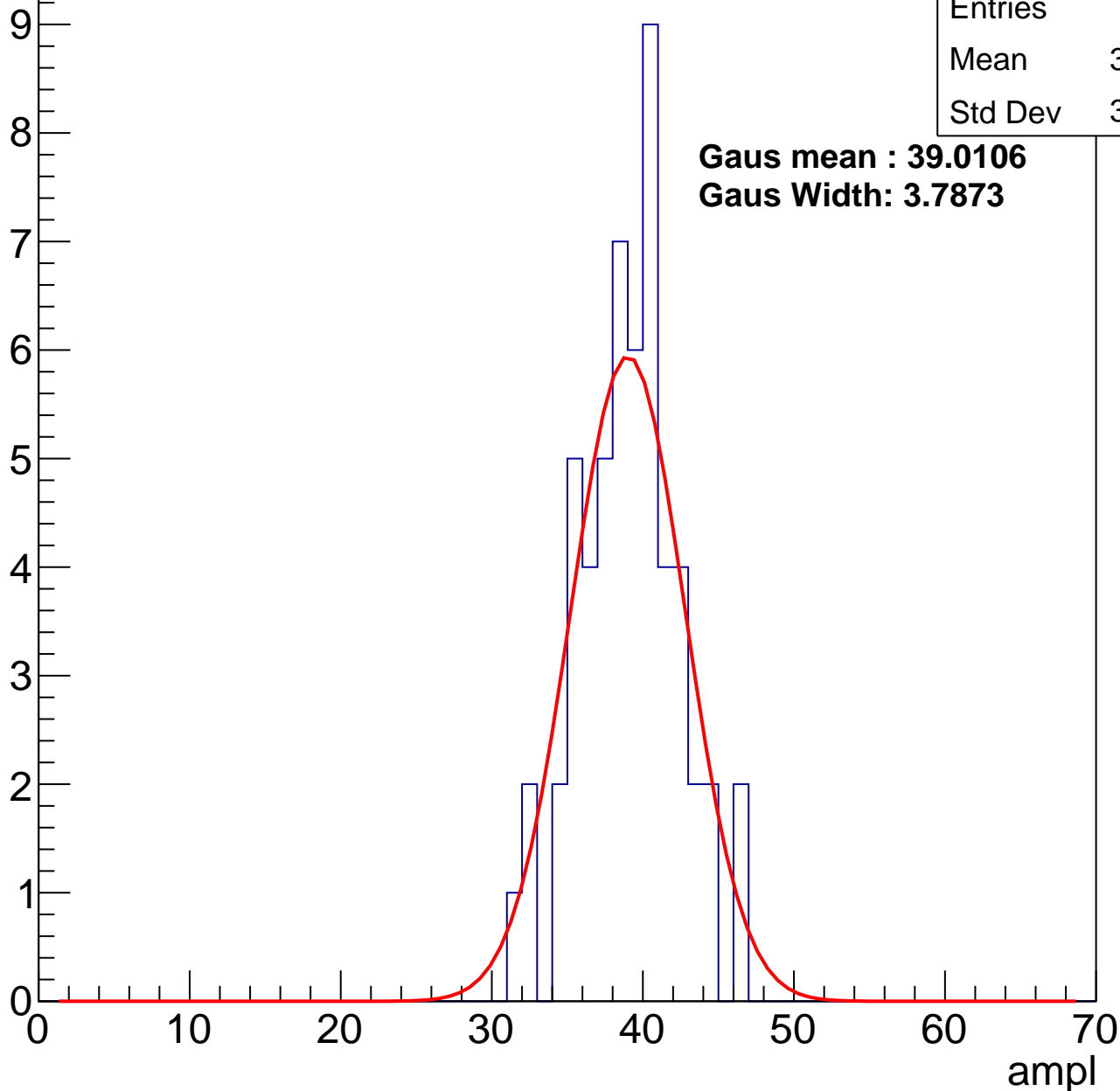
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	38.64
Std Dev	3.272

Gaus mean : 39.0106

Gaus Width: 3.7873



B0L001S, U23-ch67, adc2

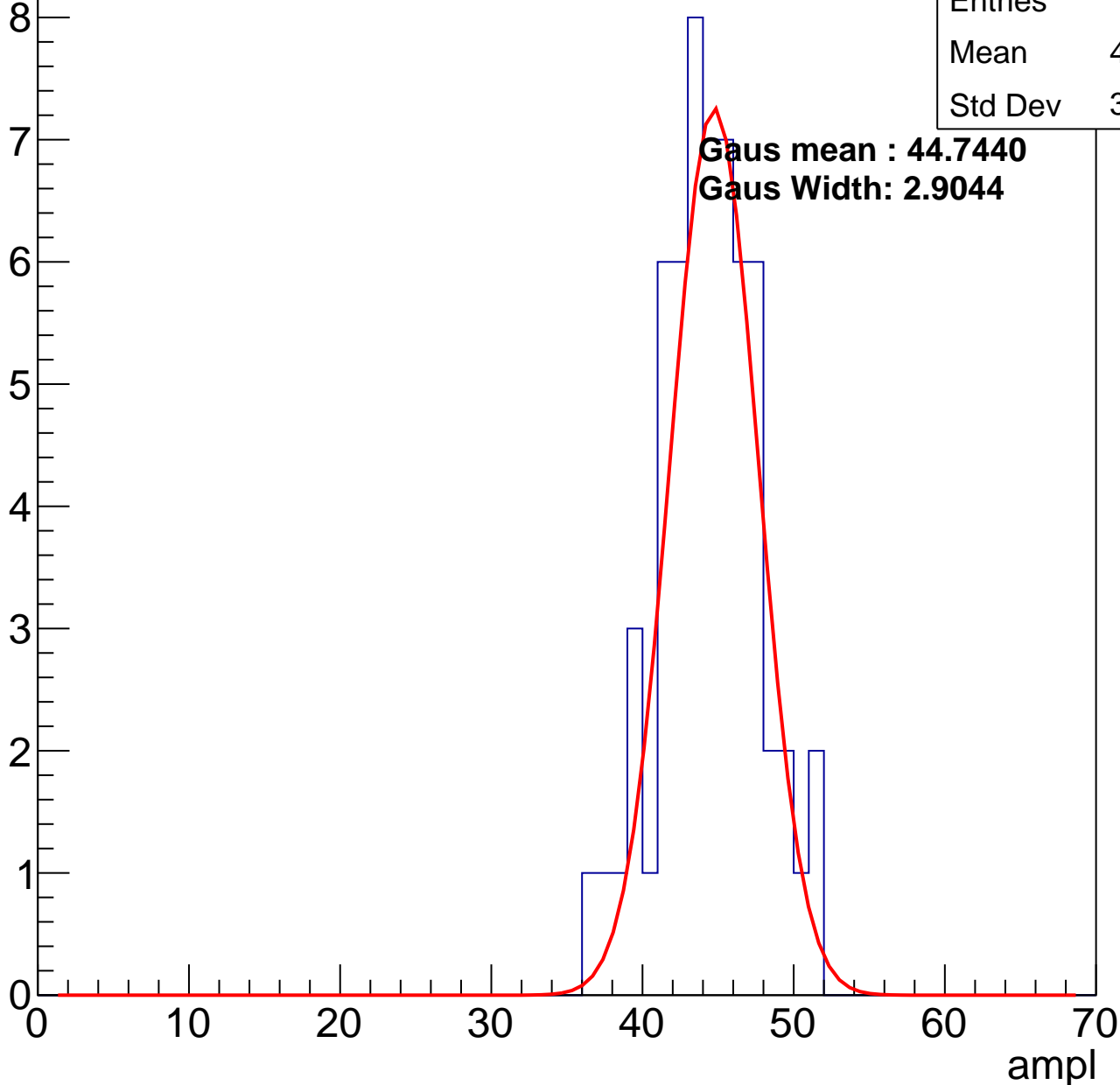
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.95
Std Dev	3.232

Gaus mean : 44.7440

Gaus Width: 2.9044



B0L001S, U23-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

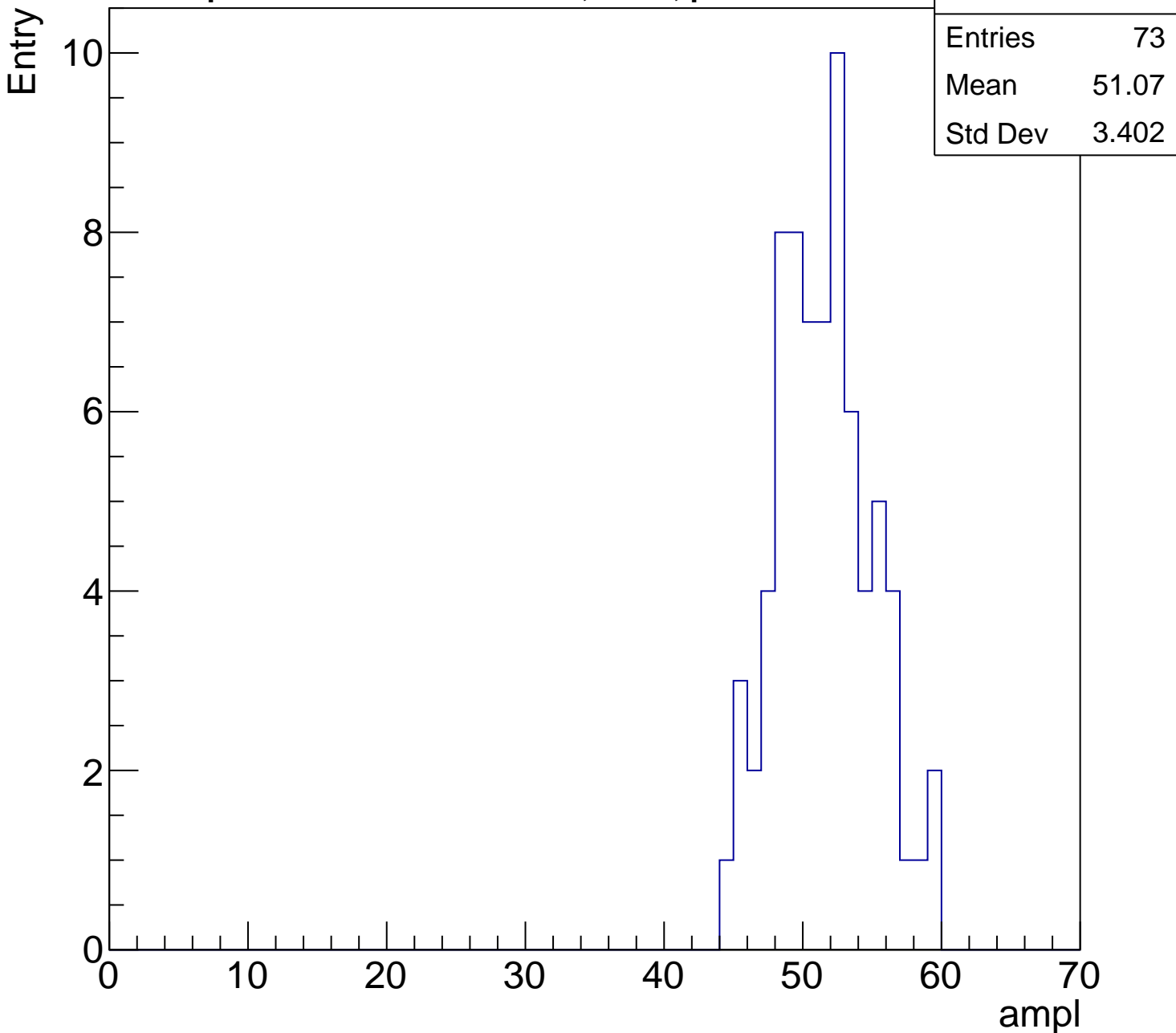
Entries	73
Mean	51.07
Std Dev	3.402

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

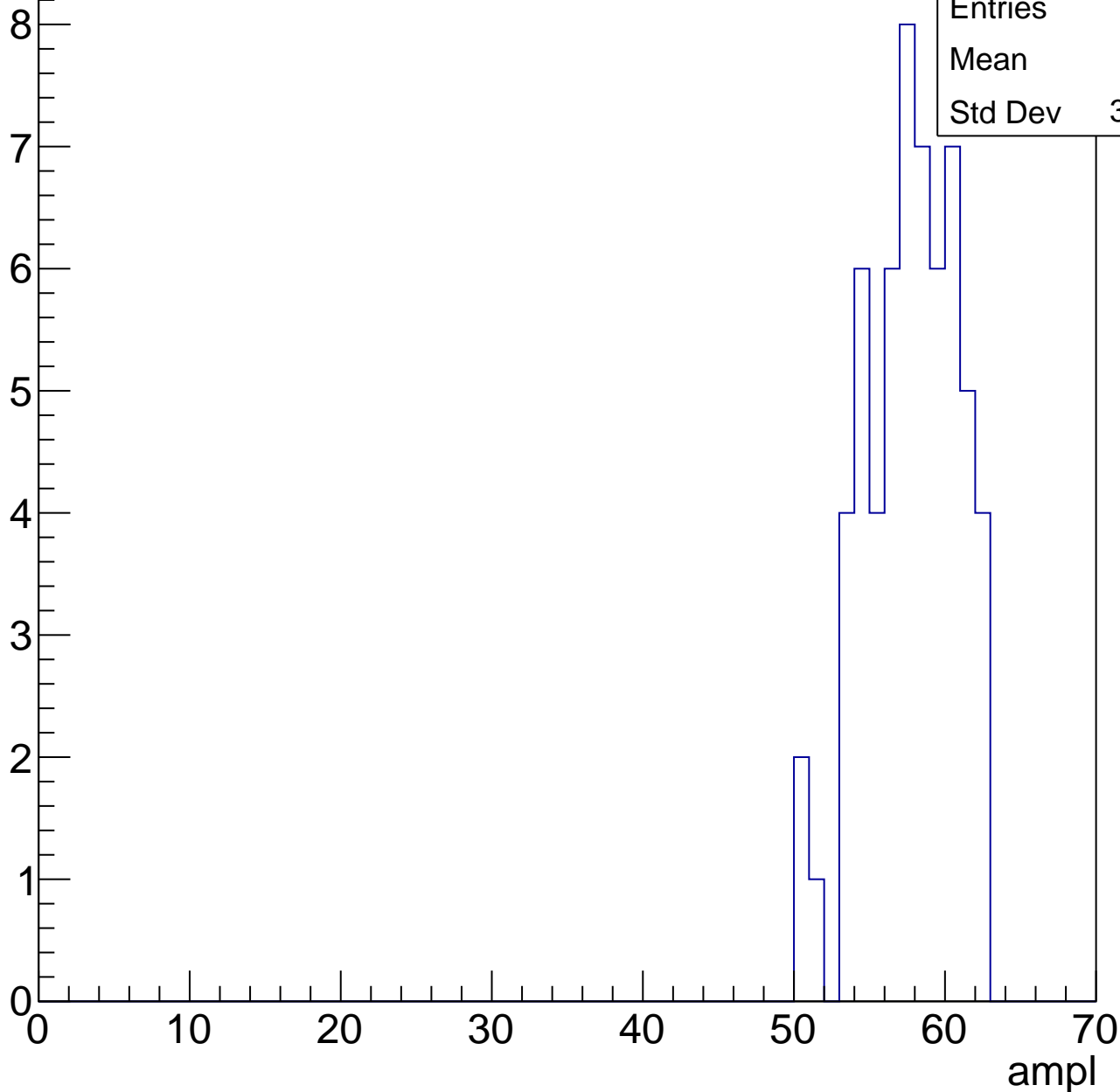


B0L001S, U23-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.2
Std Dev	3.016



B0L001S, U23-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	30
Mean	59.03
Std Dev	11.15

Entry

10

8

6

4

2

0

0

10

20

30

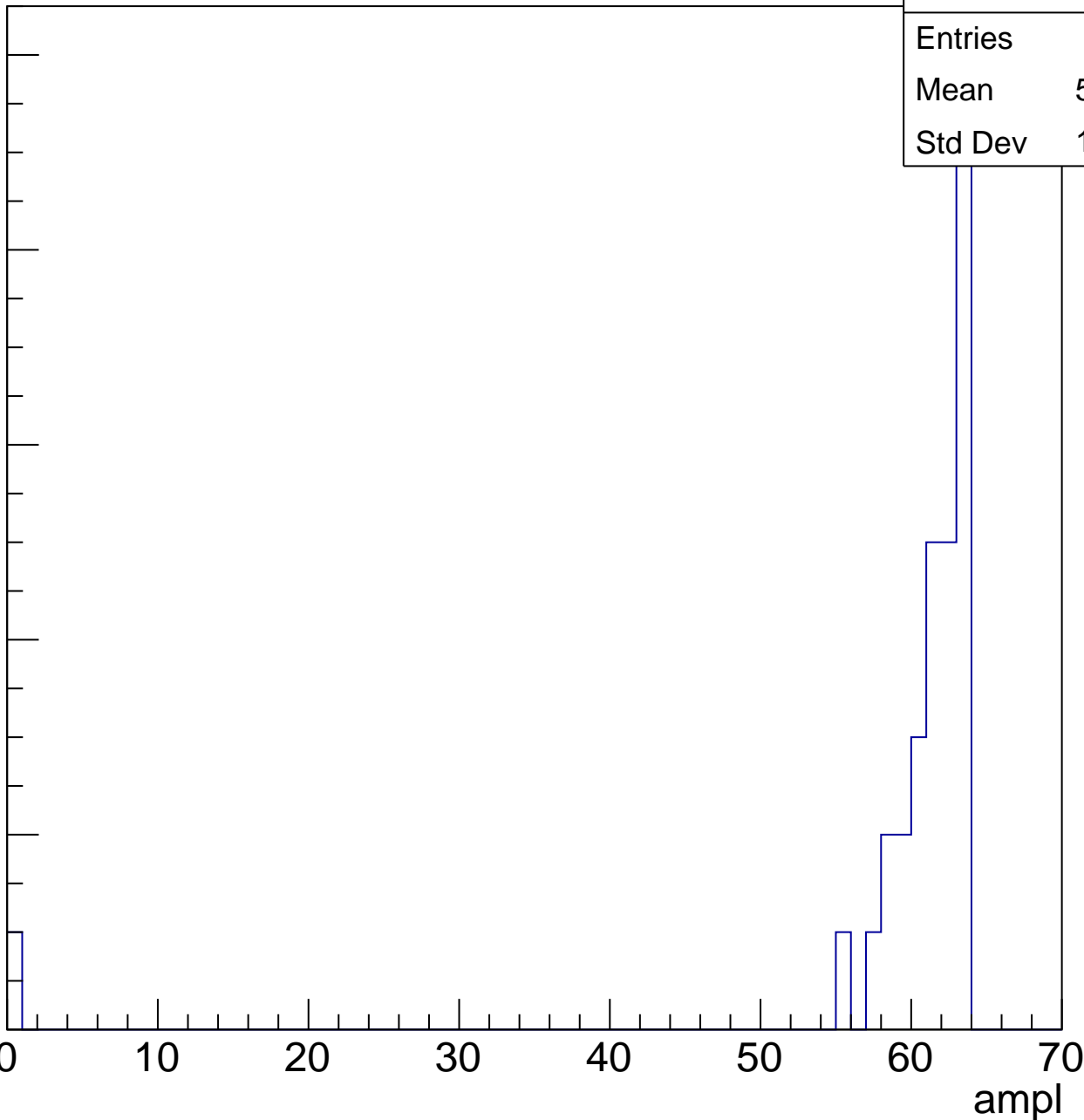
40

50

60

70

ampl



B0L001S, U23-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L001S, U23-ch68, adc0

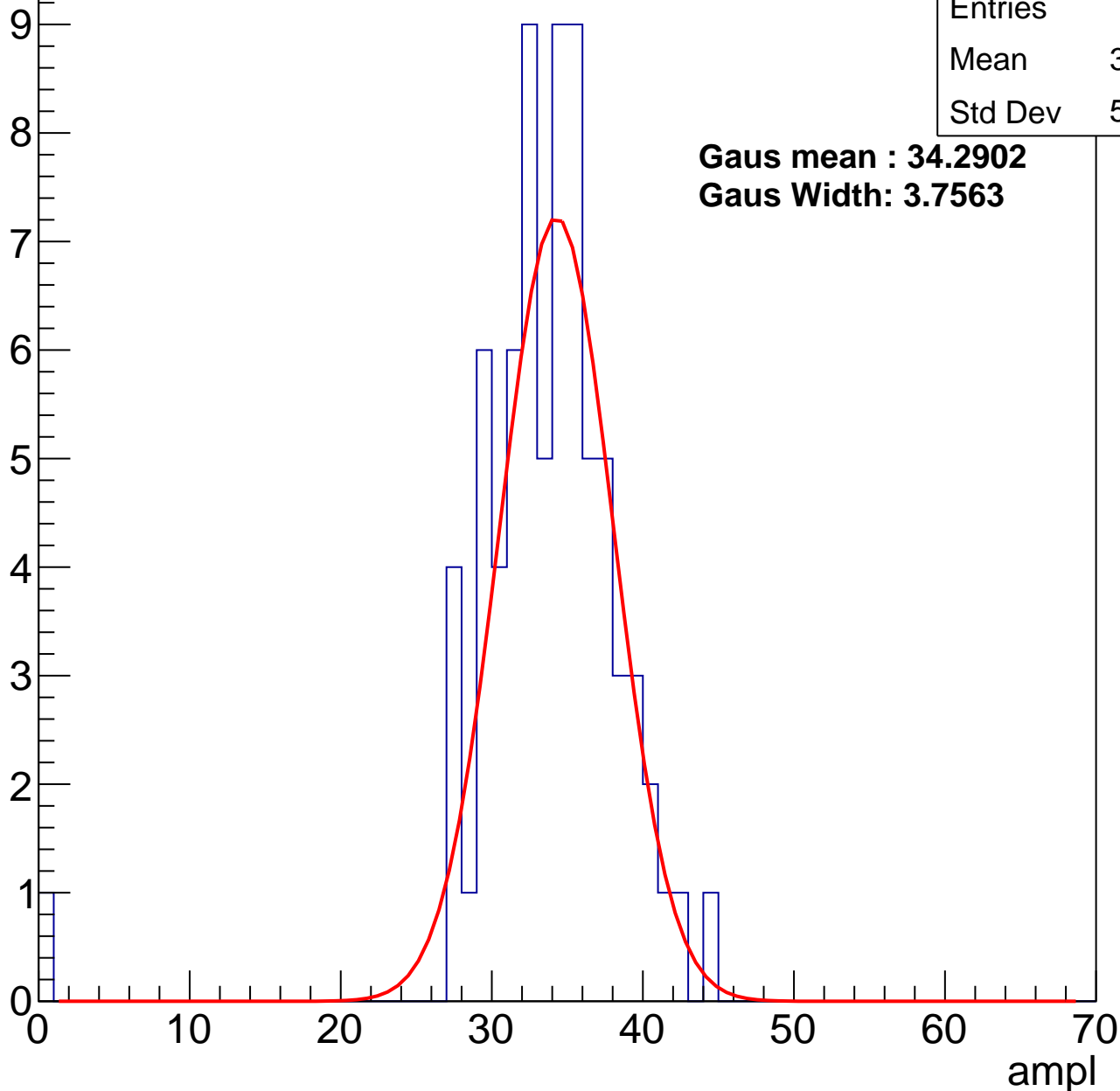
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	33.24
Std Dev	5.336

Gaus mean : 34.2902

Gaus Width: 3.7563



B0L001S, U23-ch68, adc1

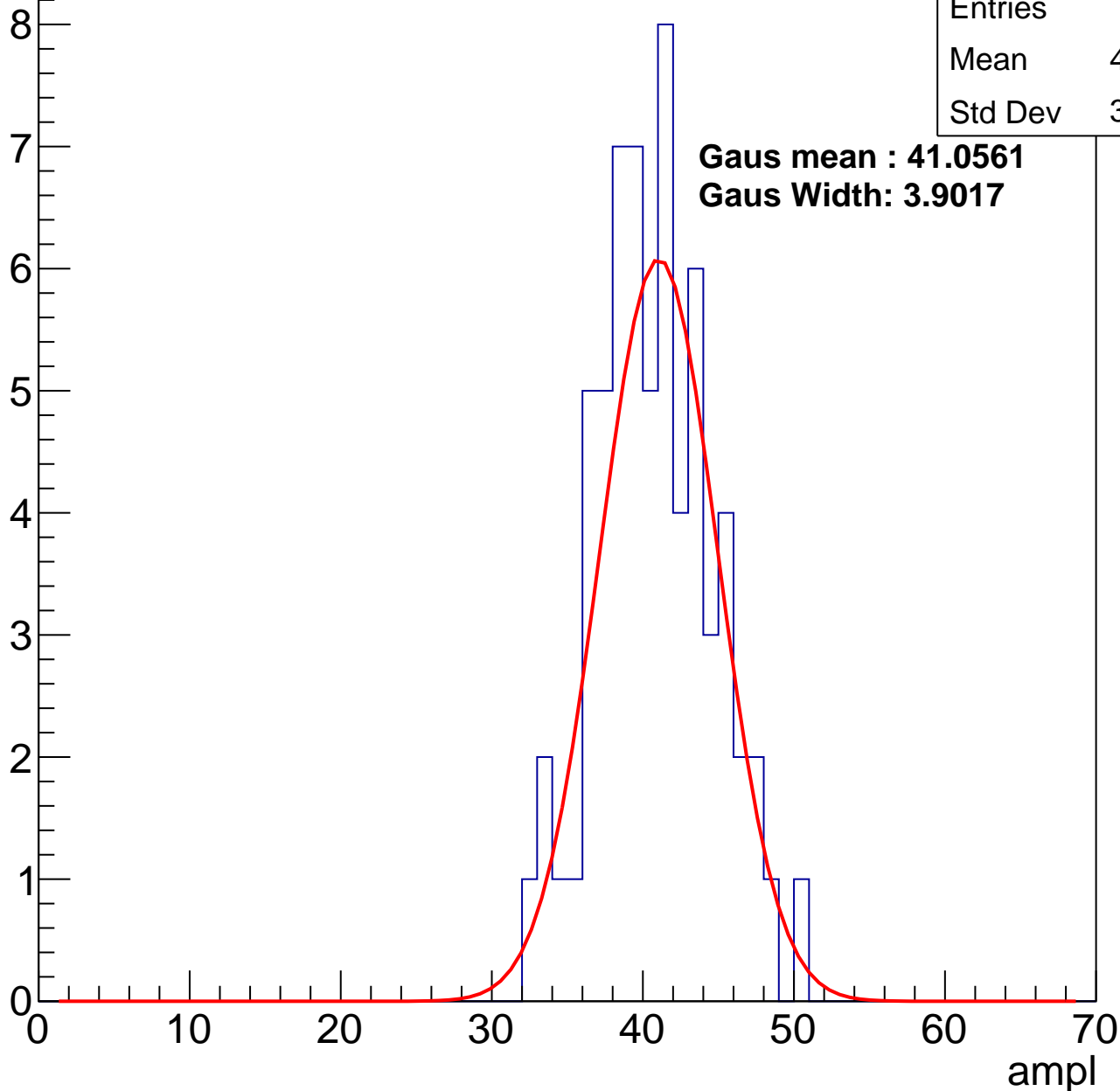
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	40.32
Std Dev	3.795

Gaus mean : 41.0561

Gaus Width: 3.9017



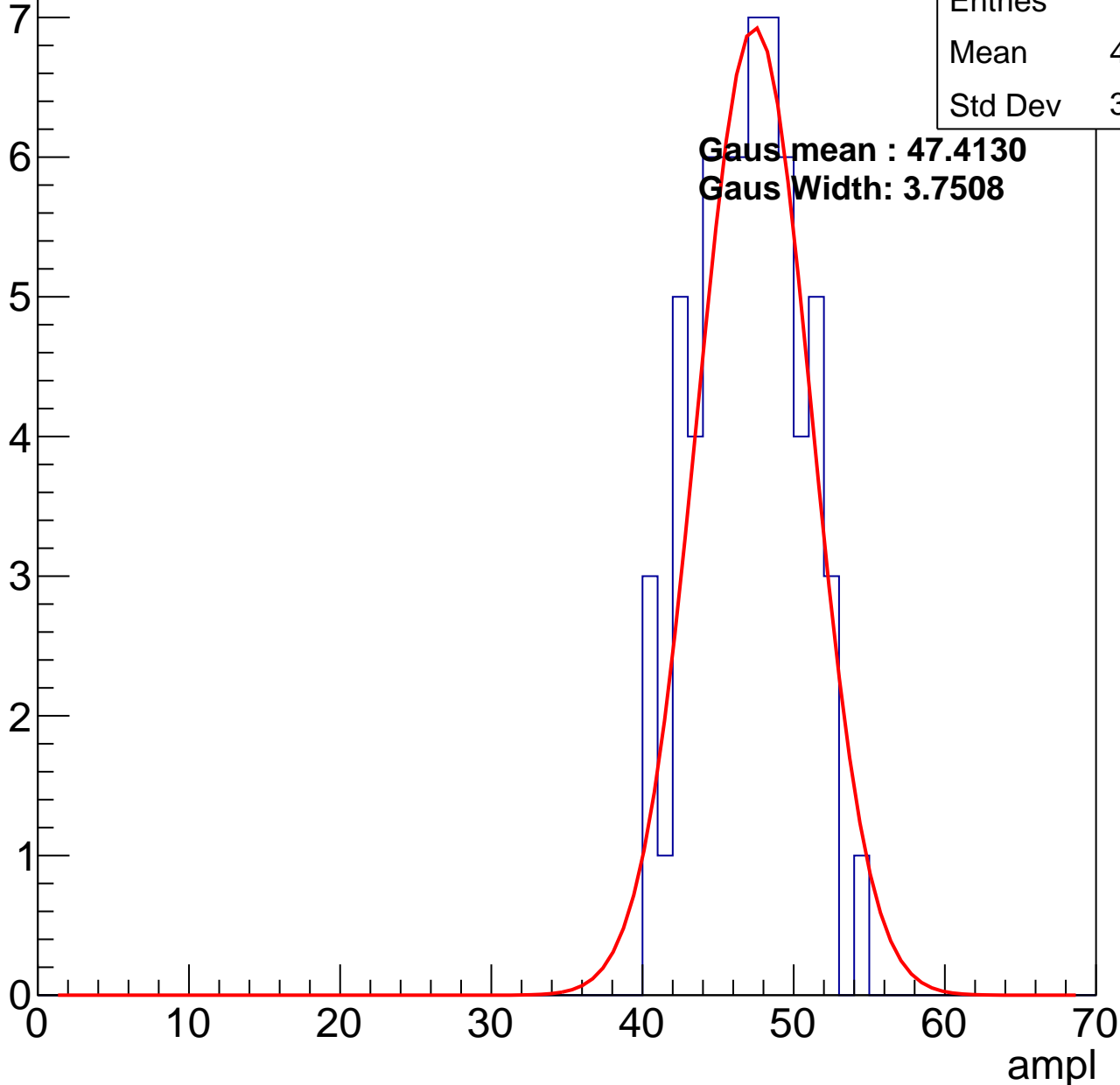
B0L001S, U23-ch68, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	46.52
Std Dev	3.335

Gaus mean : 47.4130
Gaus Width: 3.7508

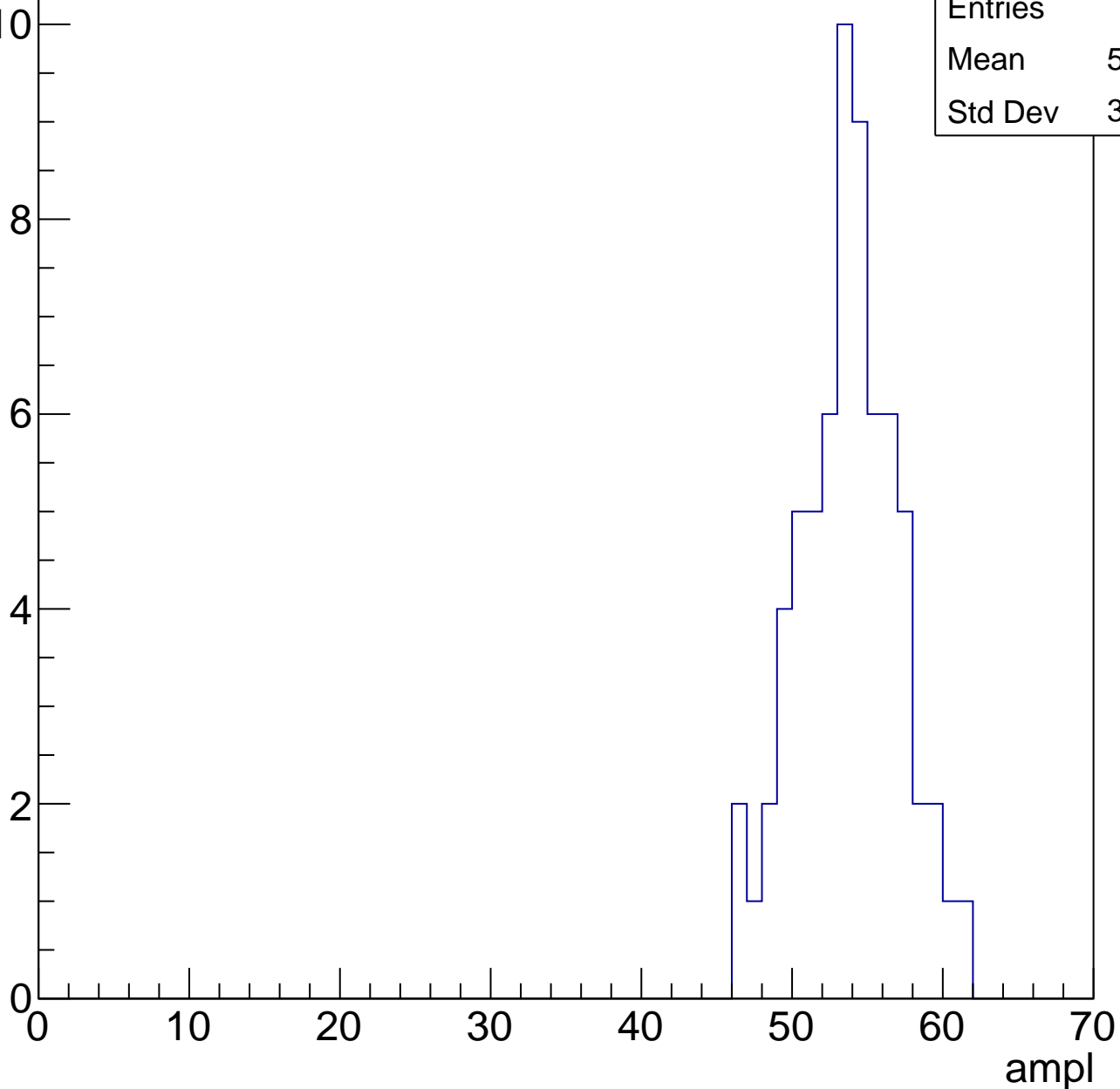


B0L001S, U23-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	53.28
Std Dev	3.268

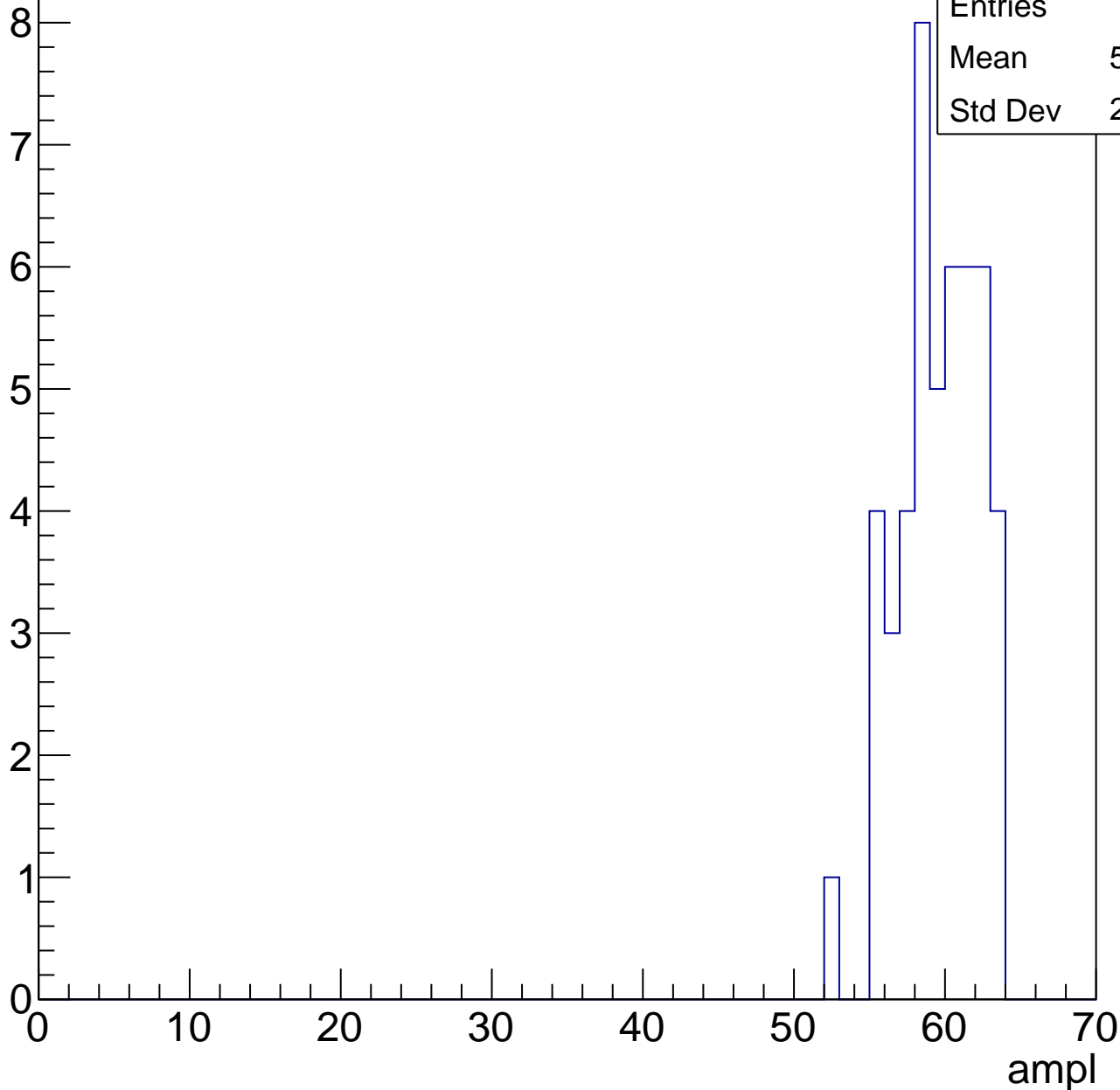


B0L001S, U23-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	59.09
Std Dev	2.575

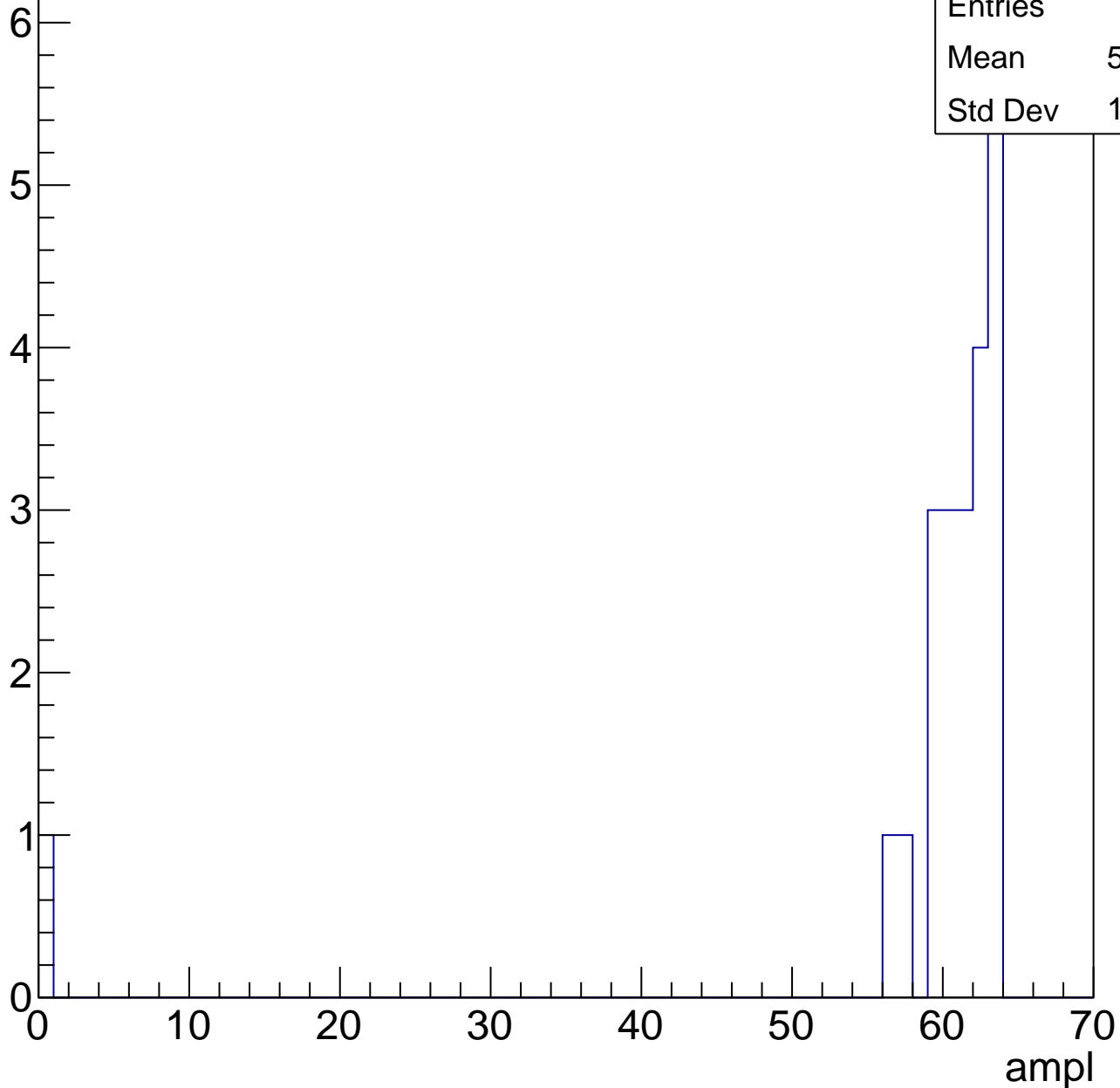


B0L001S, U23-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.14
Std Dev	12.84



B0L001S, U23-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

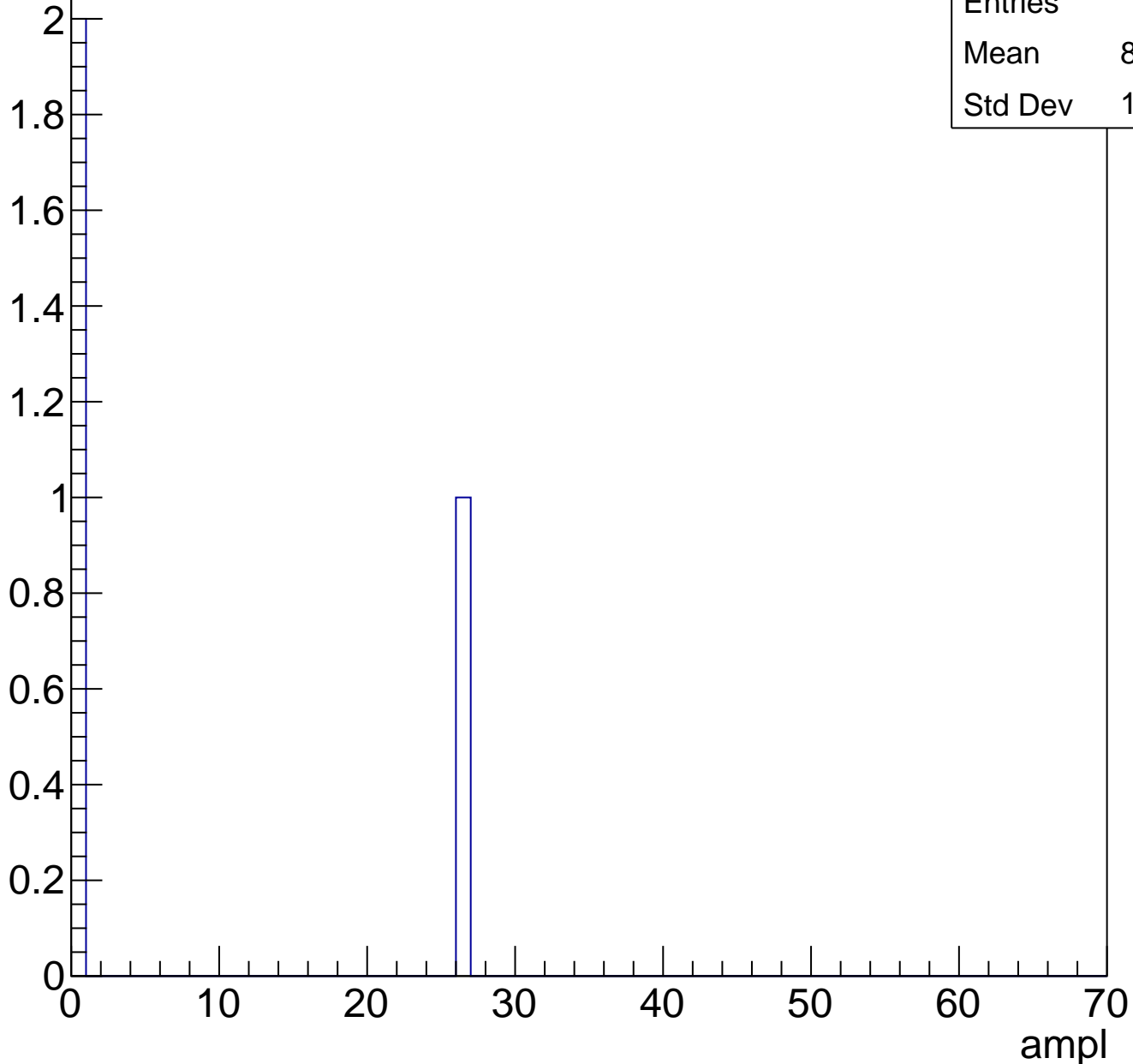


Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8.667
Std Dev	12.26

B0L001S, U23-ch69, adc0

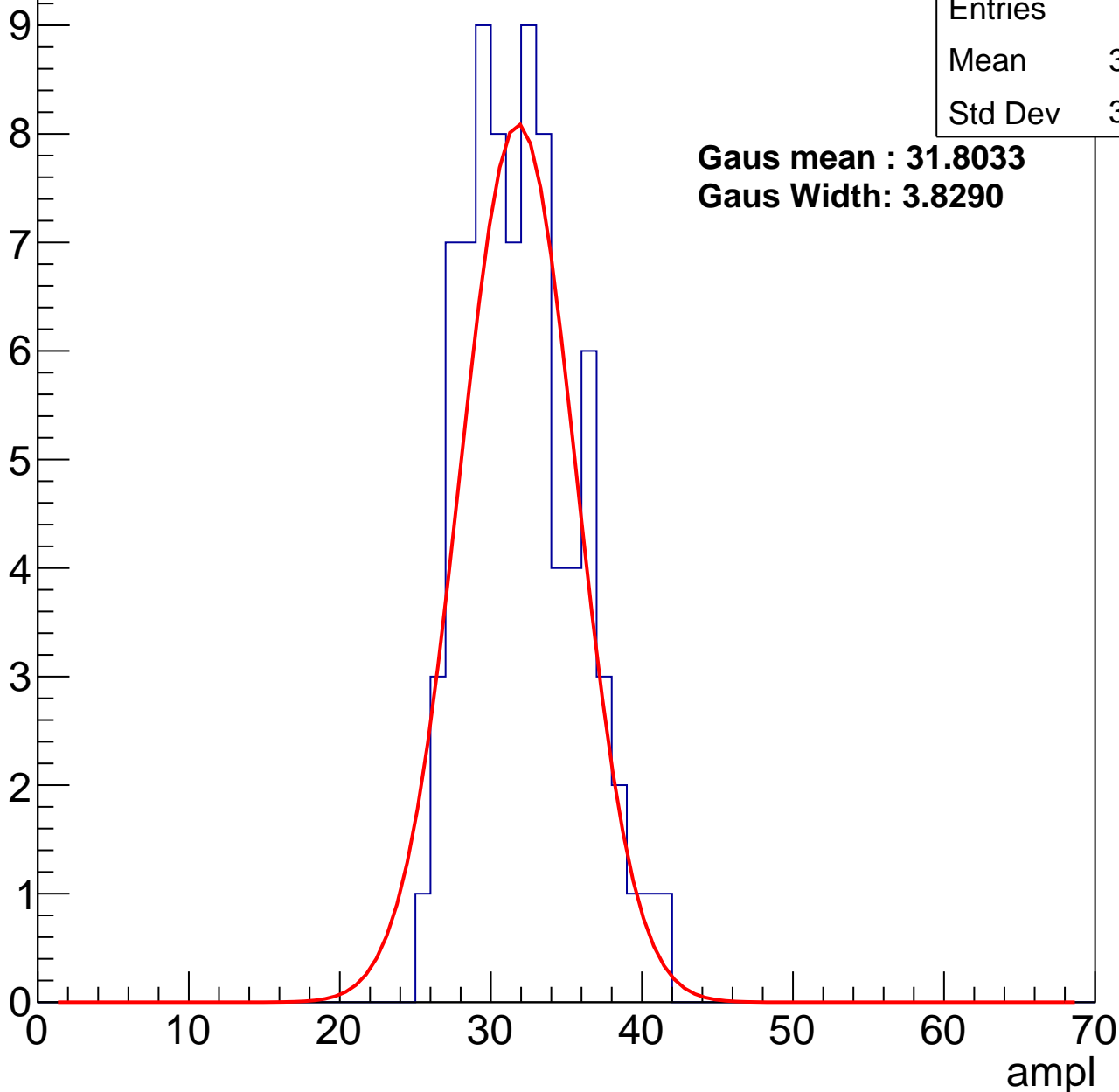
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	31.57
Std Dev	3.593

Gaus mean : 31.8033

Gaus Width: 3.8290



B0L001S, U23-ch69, adc1

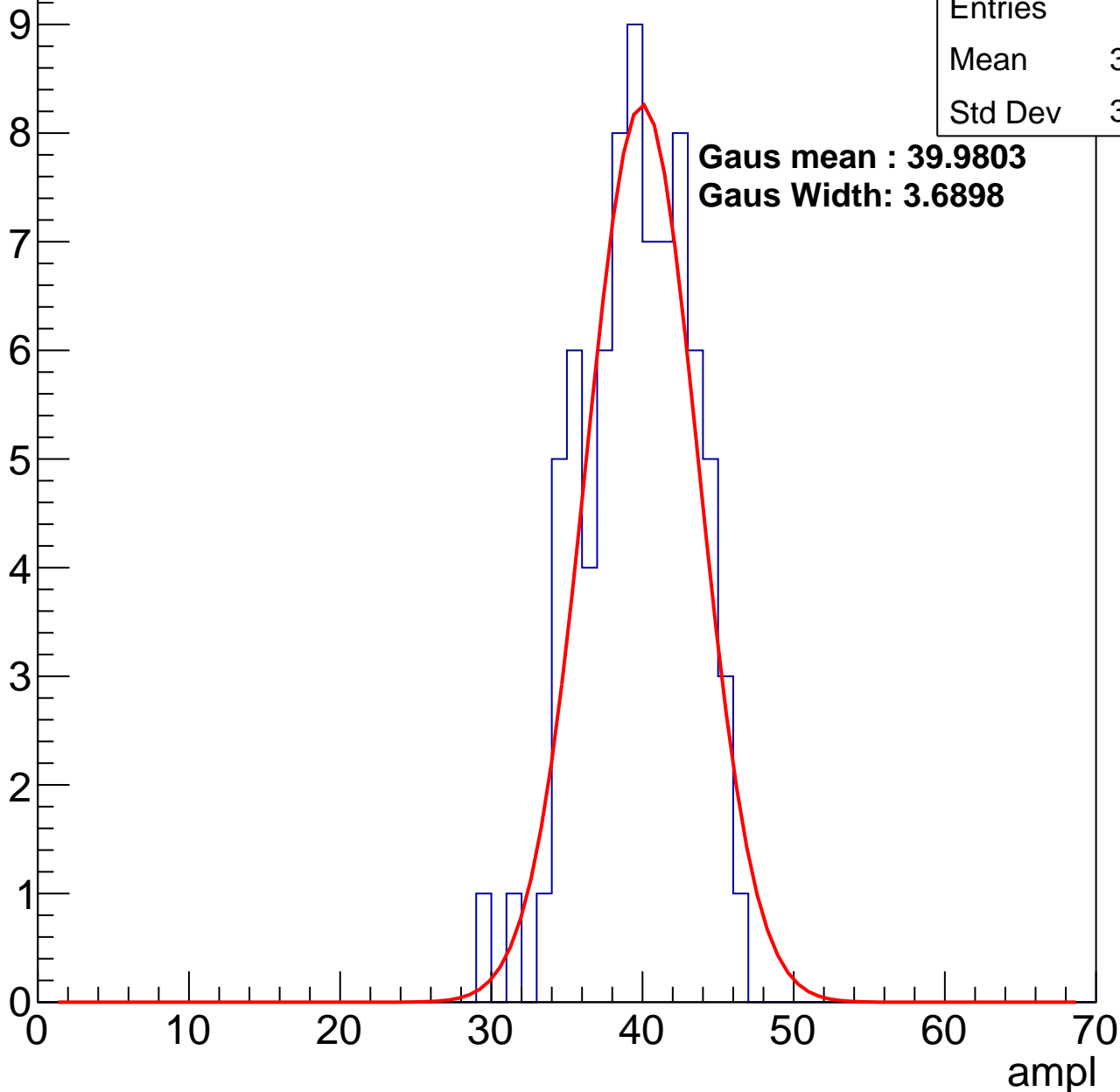
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	39.18
Std Dev	3.529

Gaus mean : 39.9803

Gaus Width: 3.6898



B0L001S, U23-ch69, adc2

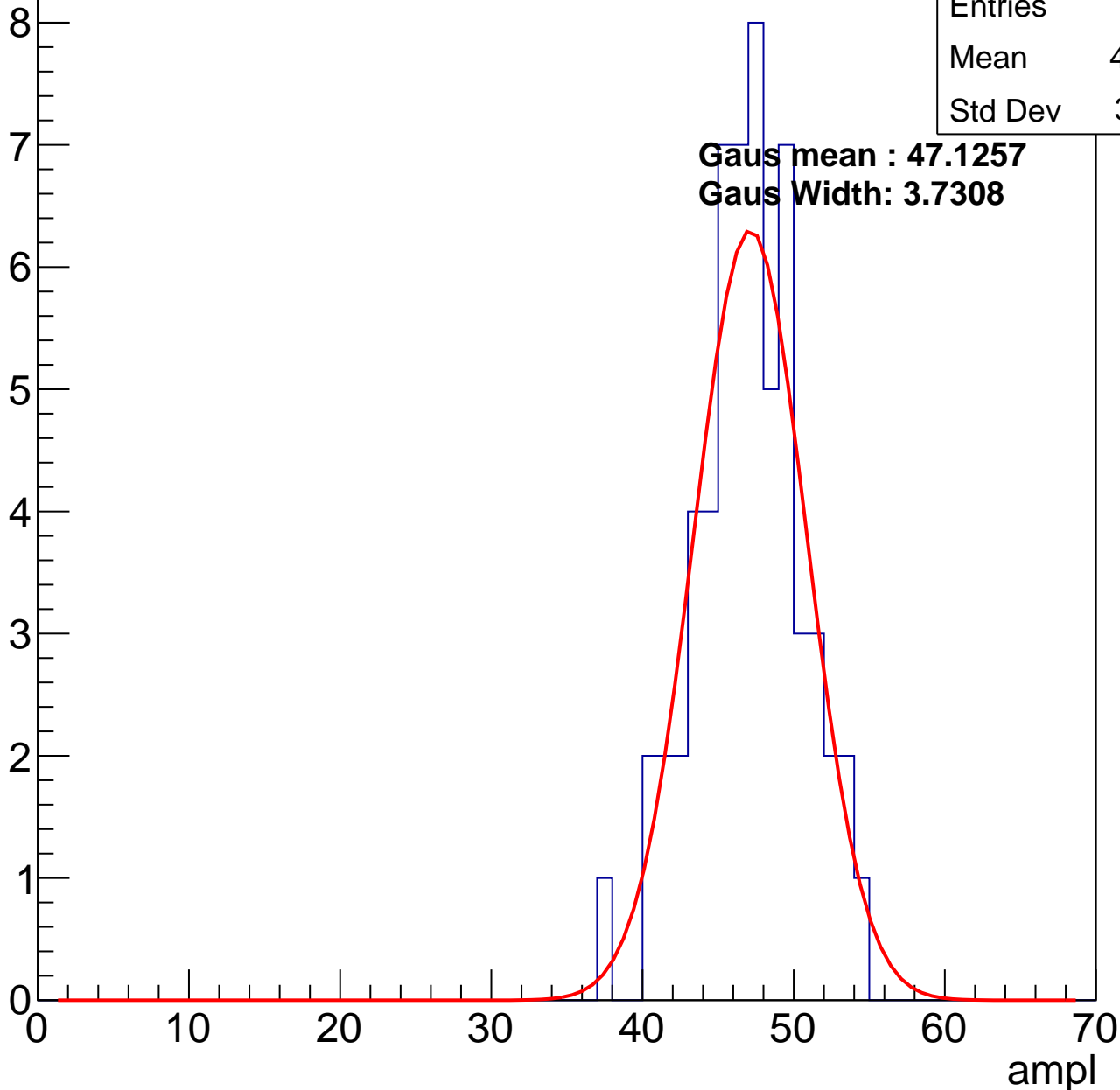
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	46.57
Std Dev	3.471

Gaus mean : 47.1257

Gaus Width: 3.7308

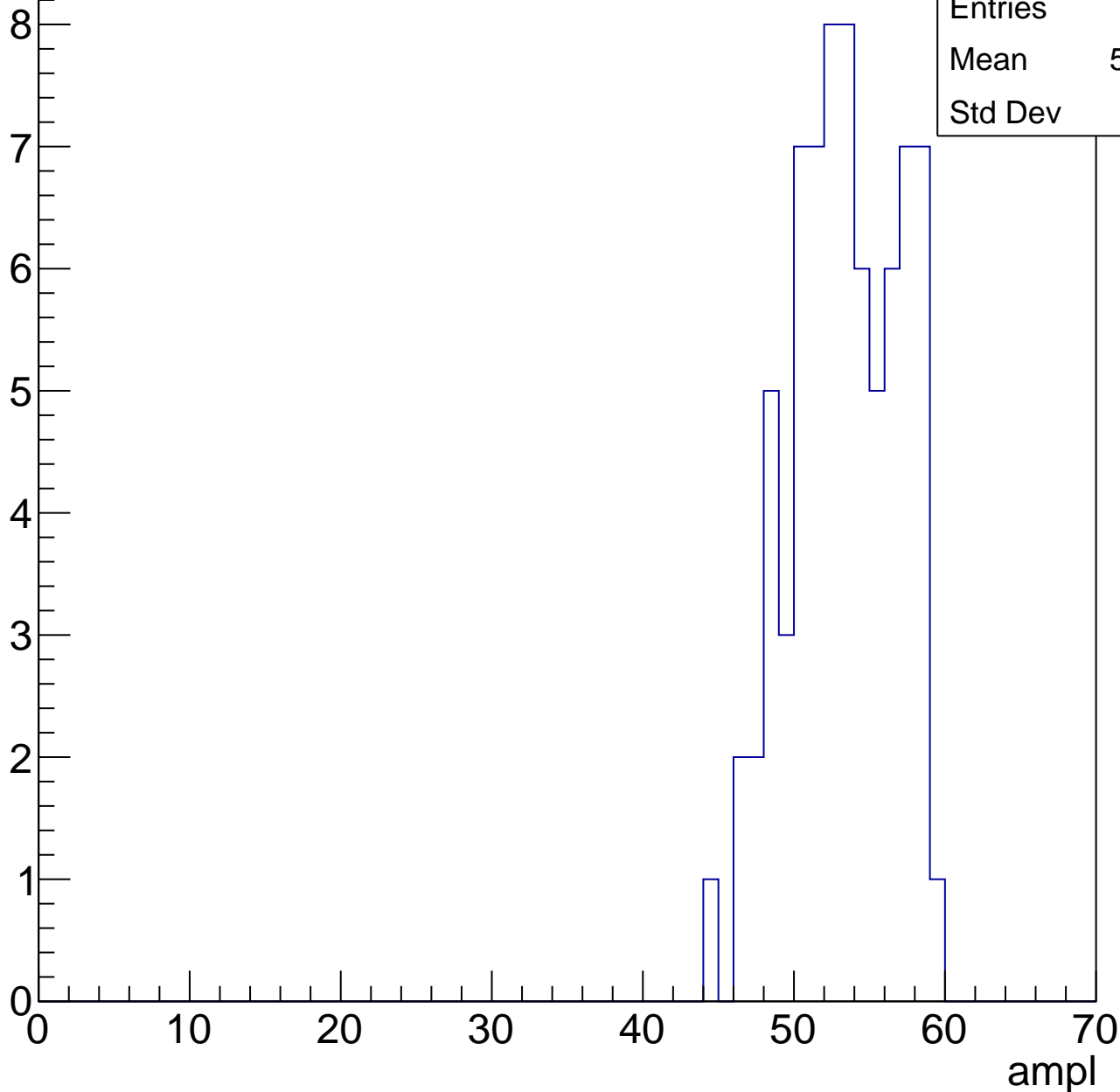


B0L001S, U23-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	52.84
Std Dev	3.51

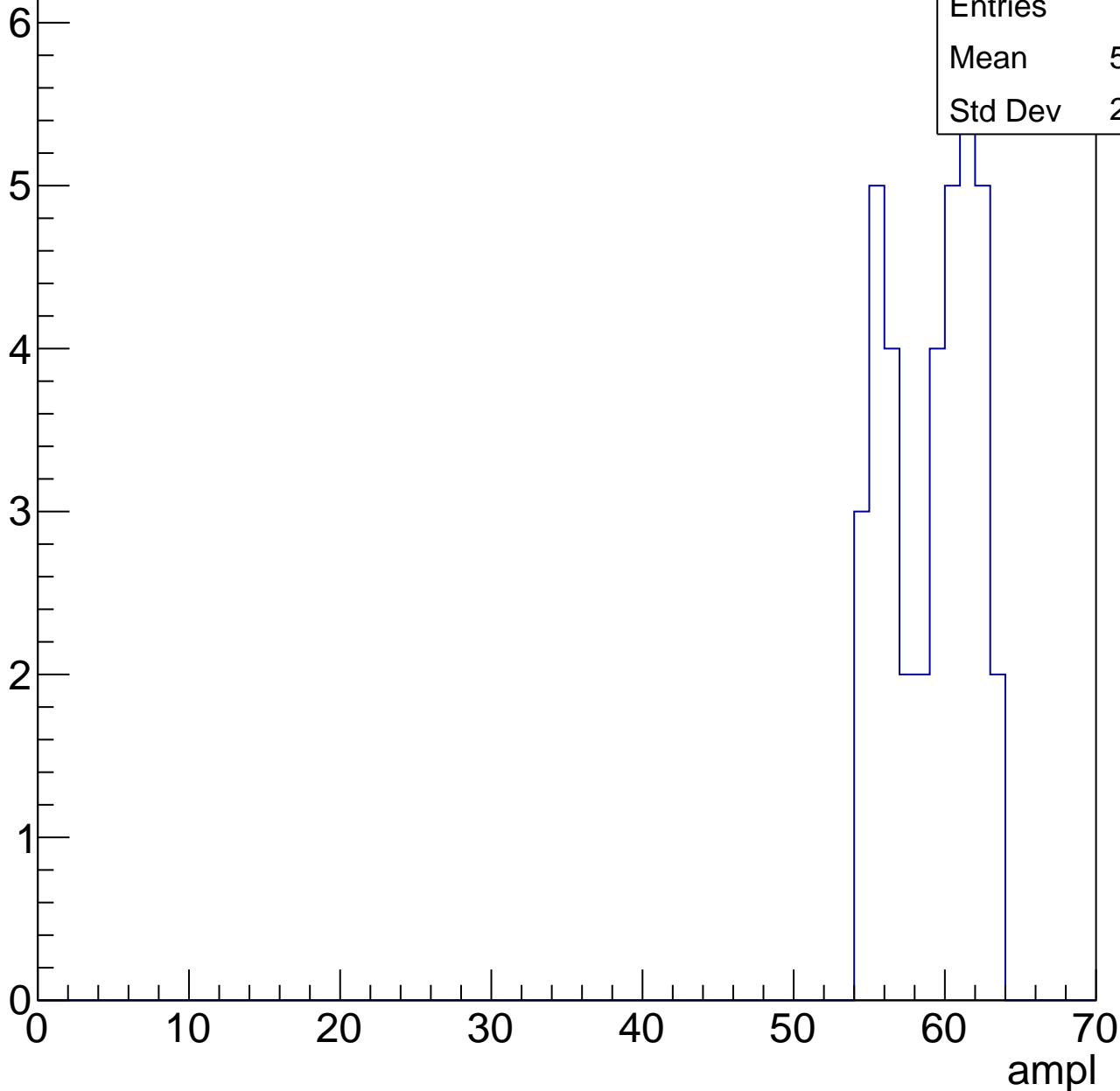


B0L001S, U23-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.66
Std Dev	2.822

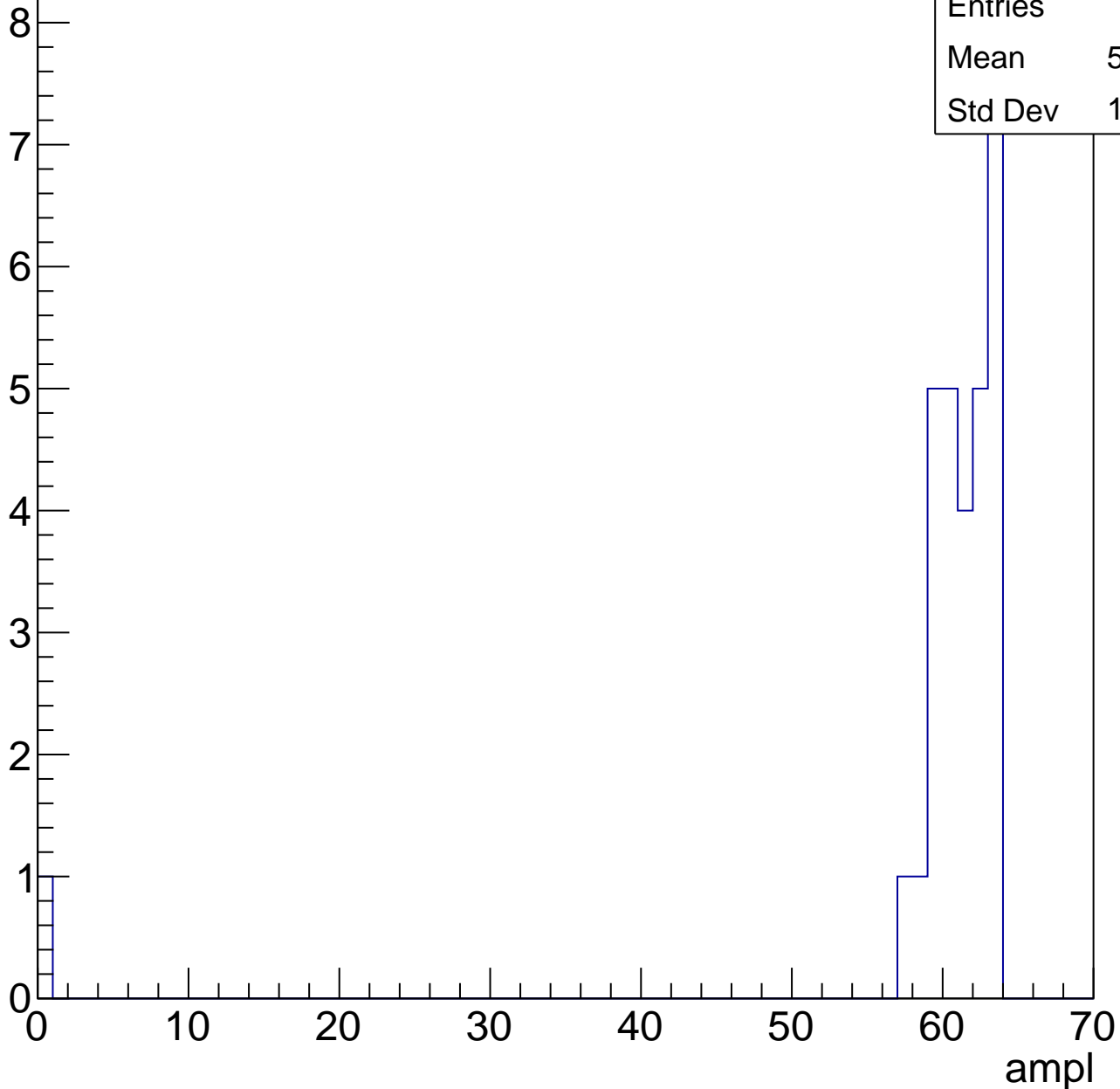


B0L001S, U23-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.93
Std Dev	11.08



B0L001S, U23-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch70, adc0

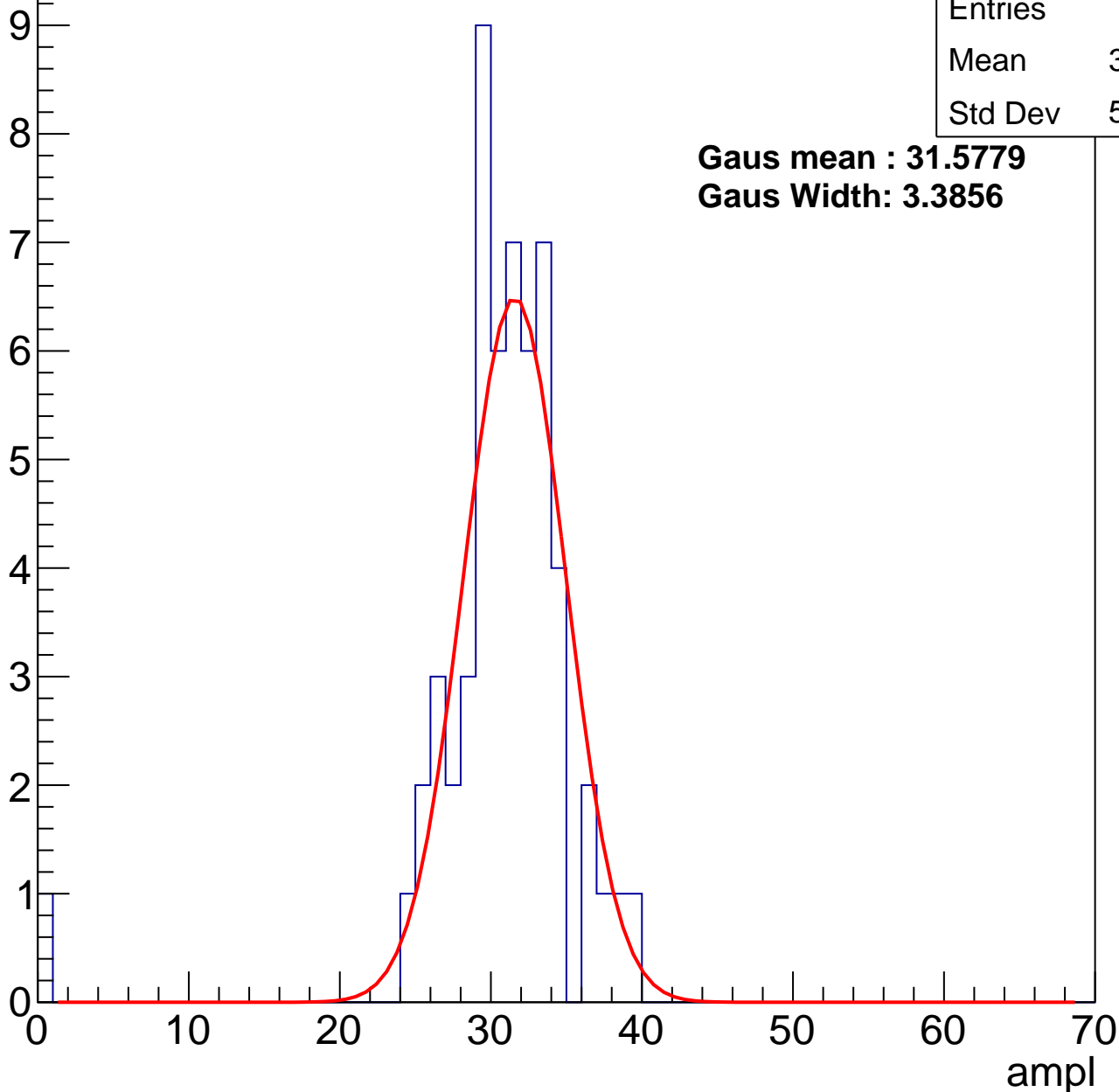
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.23
Std Dev	5.165

Gaus mean : 31.5779

Gaus Width: 3.3856



B0L001S, U23-ch70, adc1

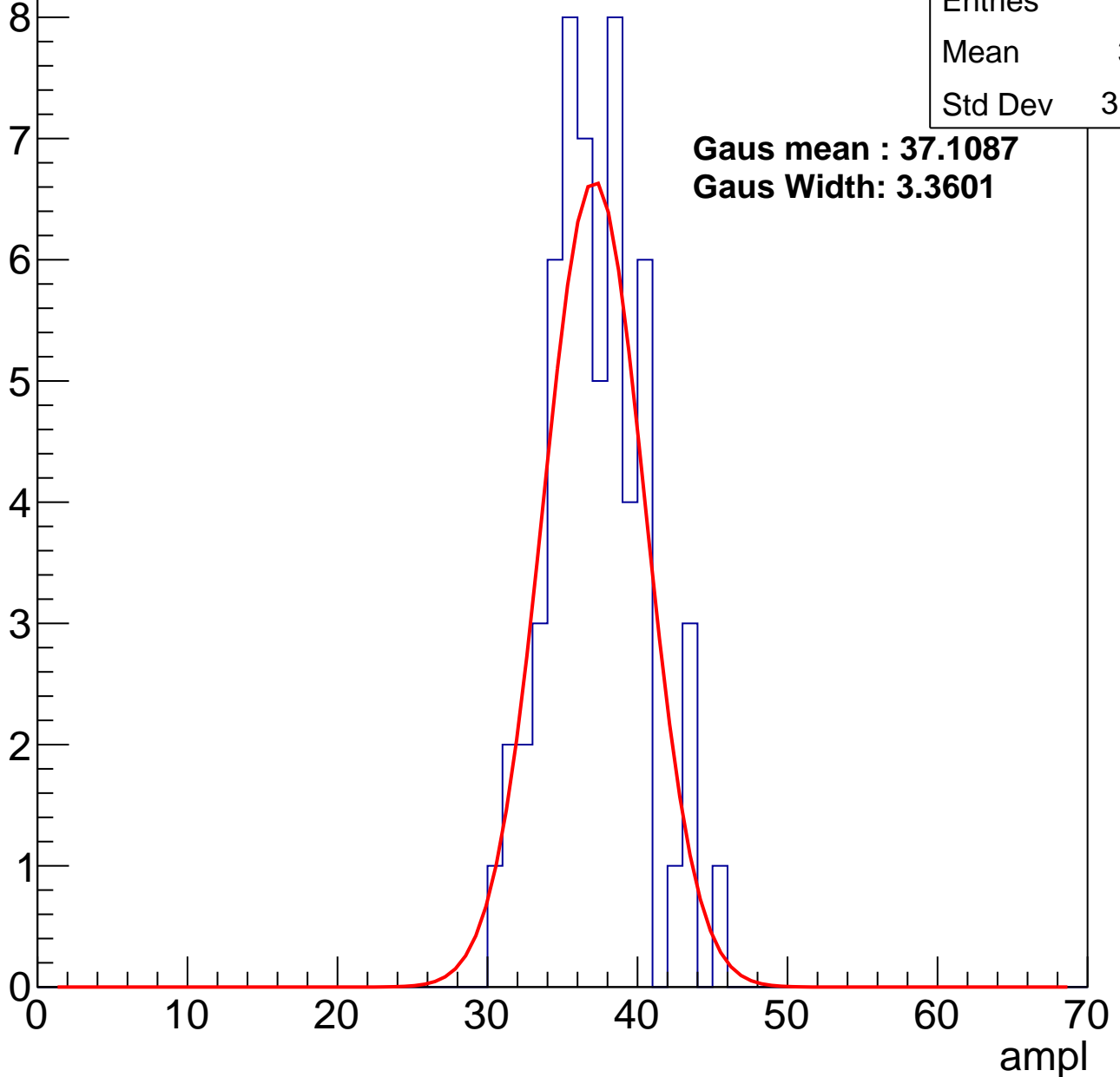
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	36.7
Std Dev	3.195

Gaus mean : 37.1087

Gaus Width: 3.3601



B0L001S, U23-ch70, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	43.46
Std Dev	3.863

Gaus mean : 44.5353

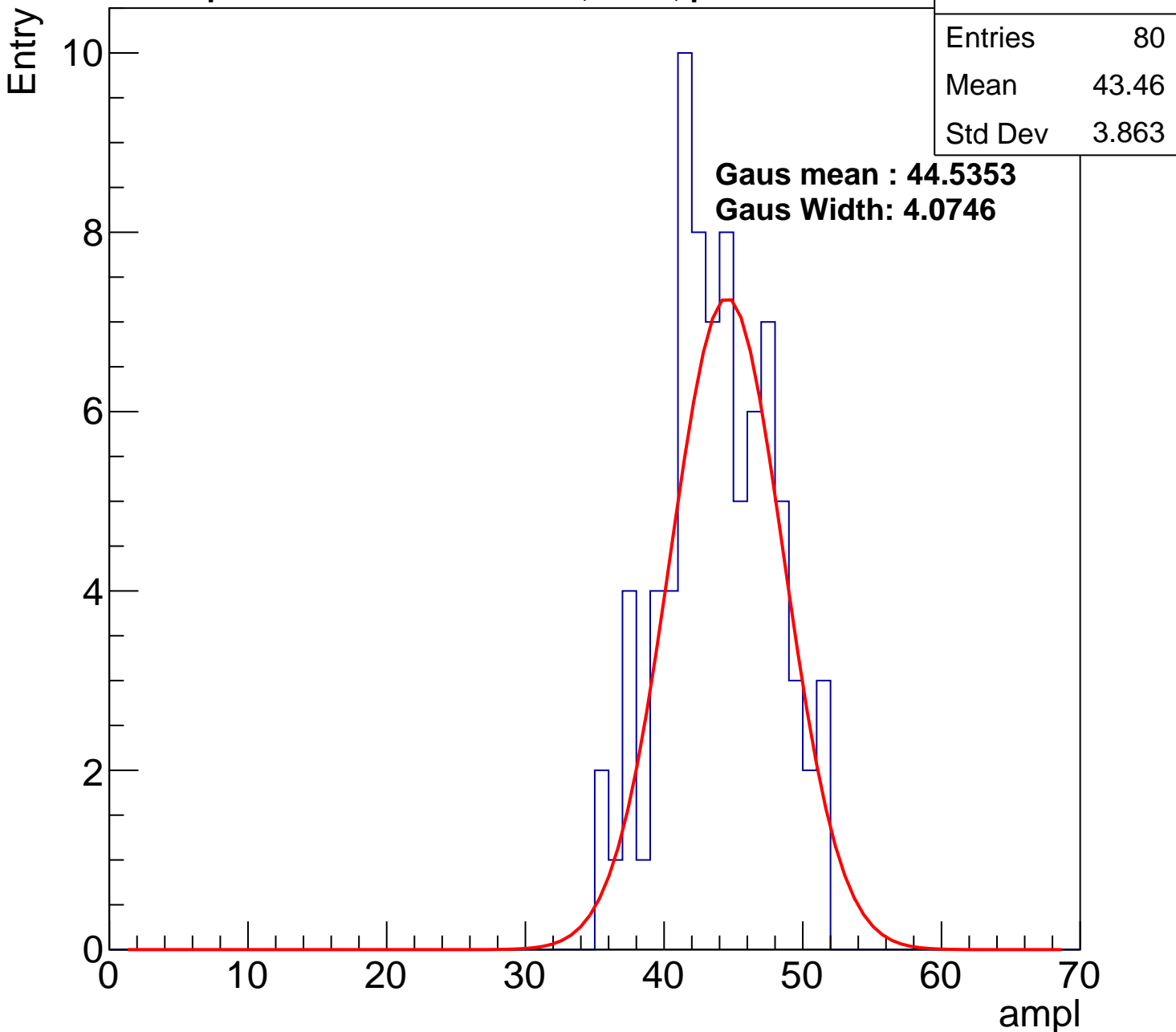
Gaus Width: 4.0746

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

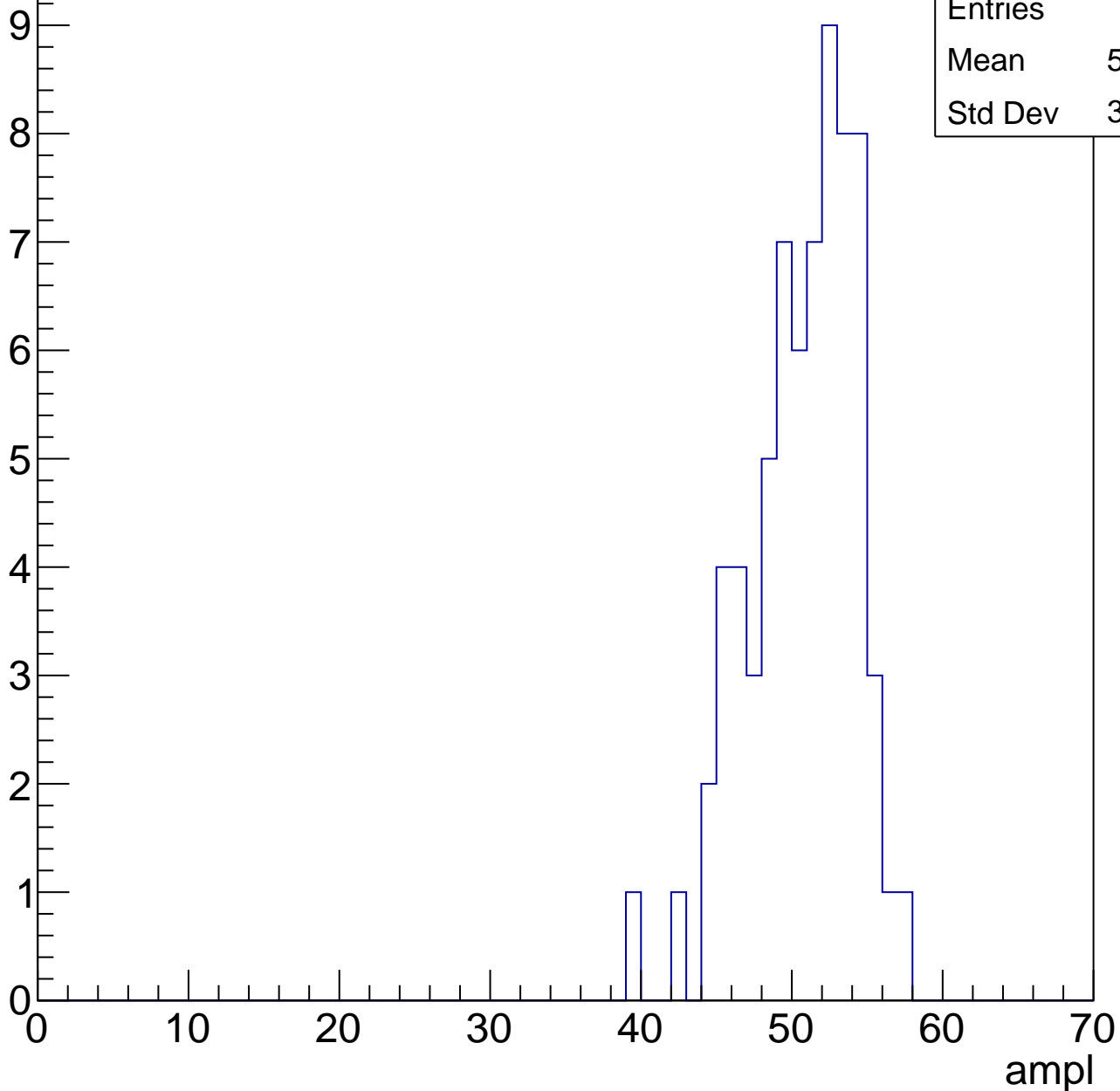


B0L001S, U23-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.23
Std Dev	3.534

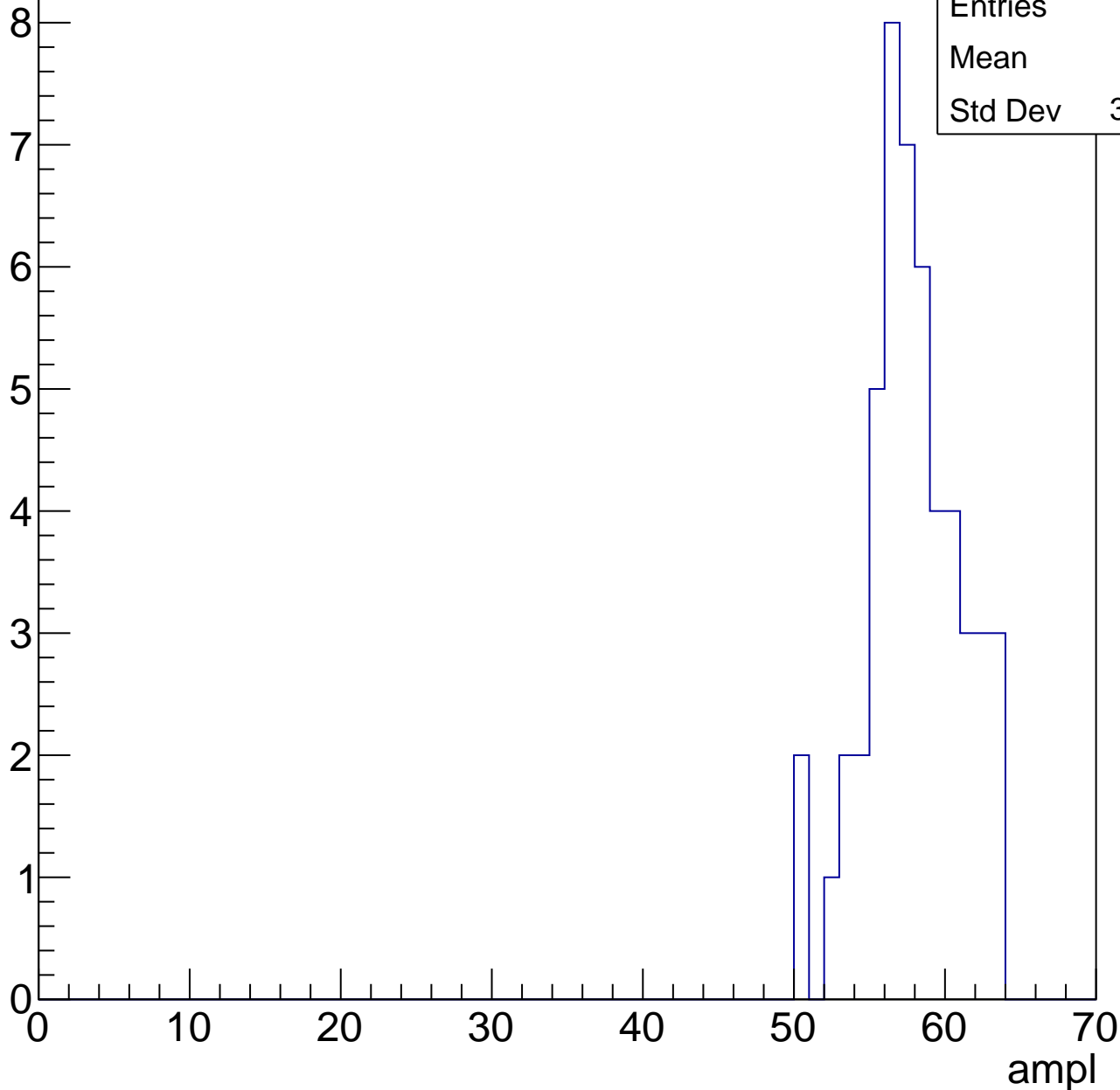


B0L001S, U23-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.4
Std Dev	3.105



B0L001S, U23-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 39

Mean 58.41

Std Dev 9.71

ampl

0

10

20

30

40

50

60

70

B0L001S, U23-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch71, adc0

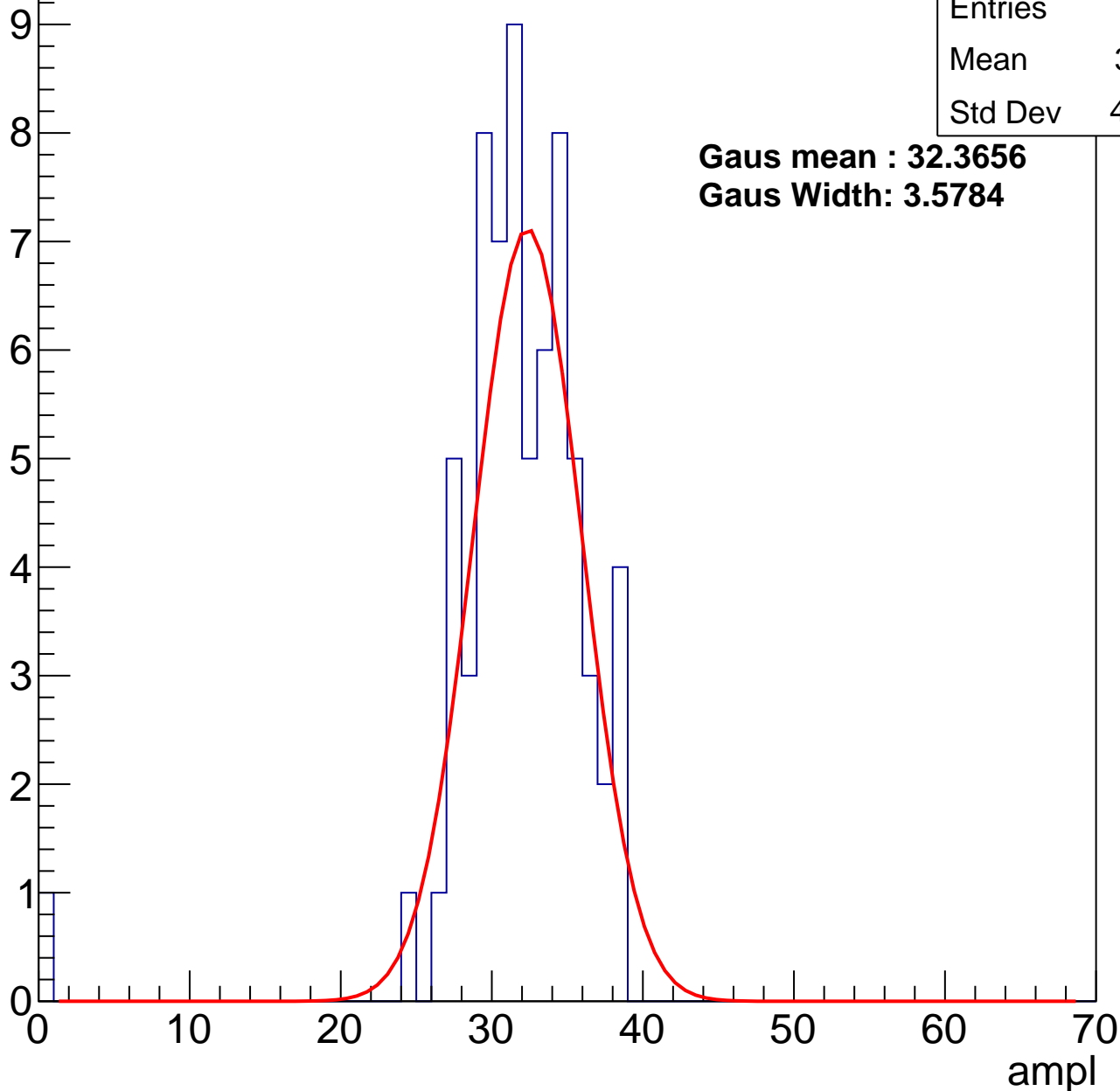
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.31
Std Dev	4.998

Gaus mean : 32.3656

Gaus Width: 3.5784



B0L001S, U23-ch71, adc1

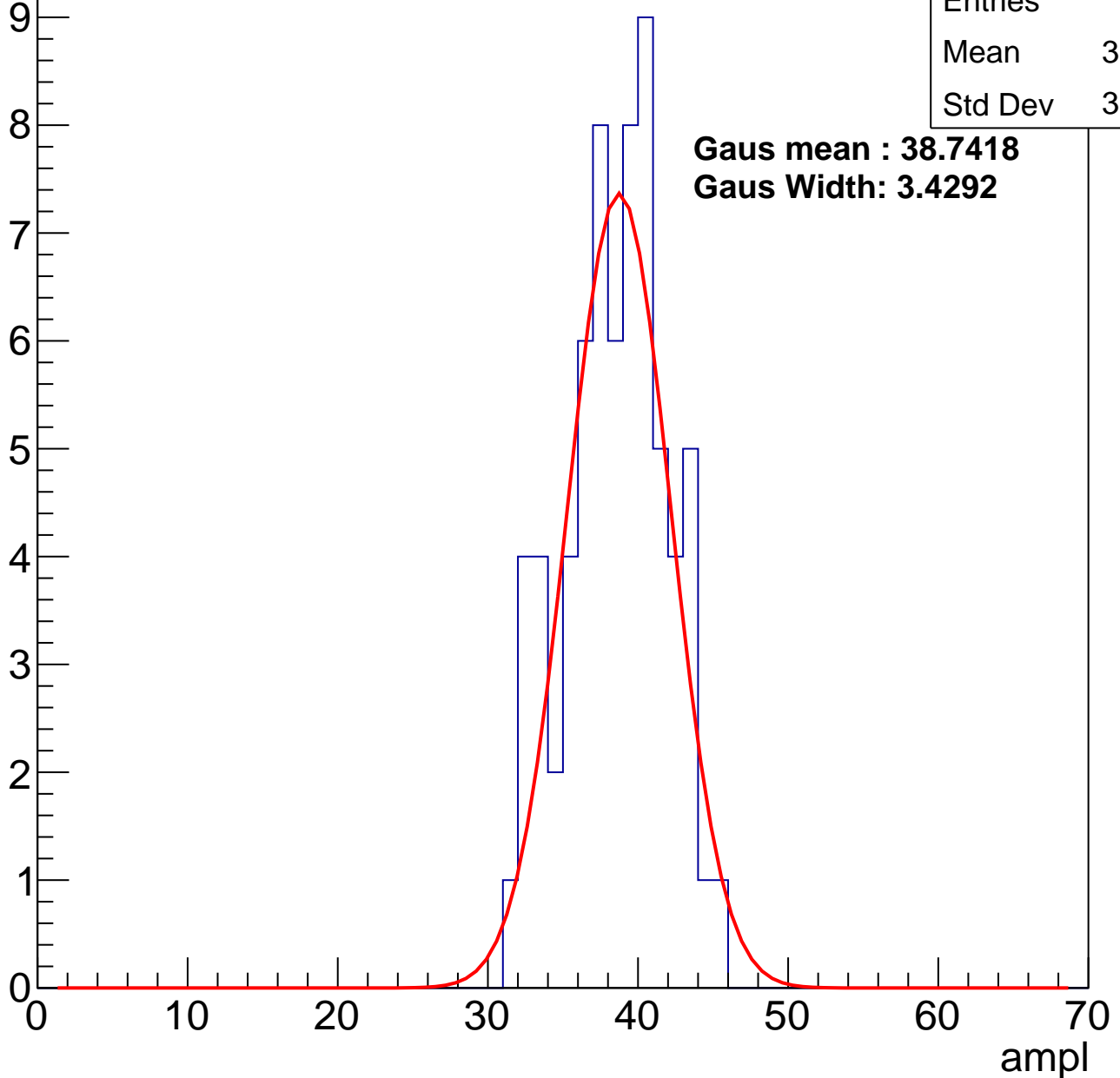
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.06
Std Dev	3.334

Gaus mean : 38.7418

Gaus Width: 3.4292



B0L001S, U23-ch71, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	44.91
Std Dev	3.412

Gaus mean : 45.2404

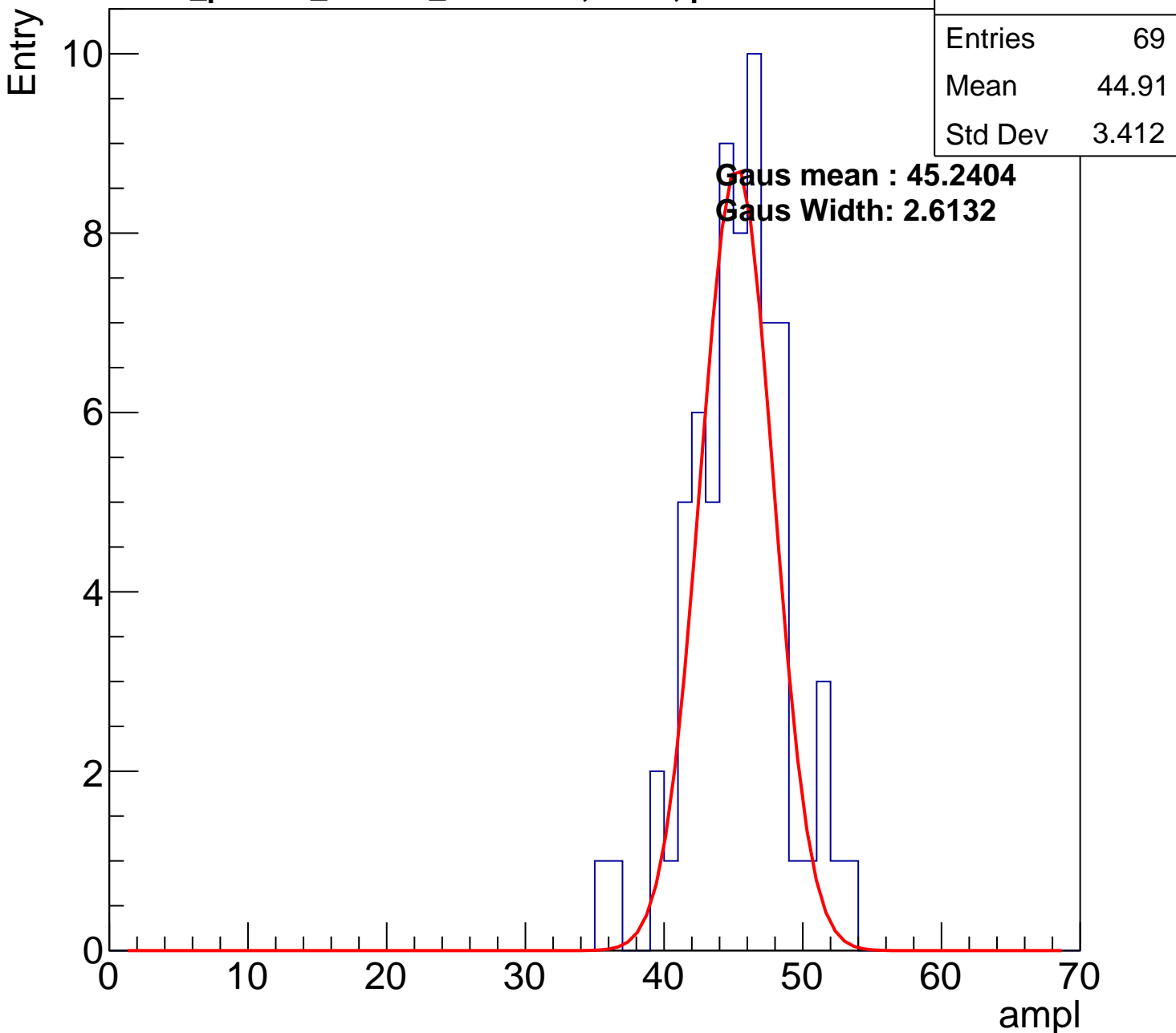
Gaus Width: 2.6132

Entry

10
8
6
4
2
0

ampl

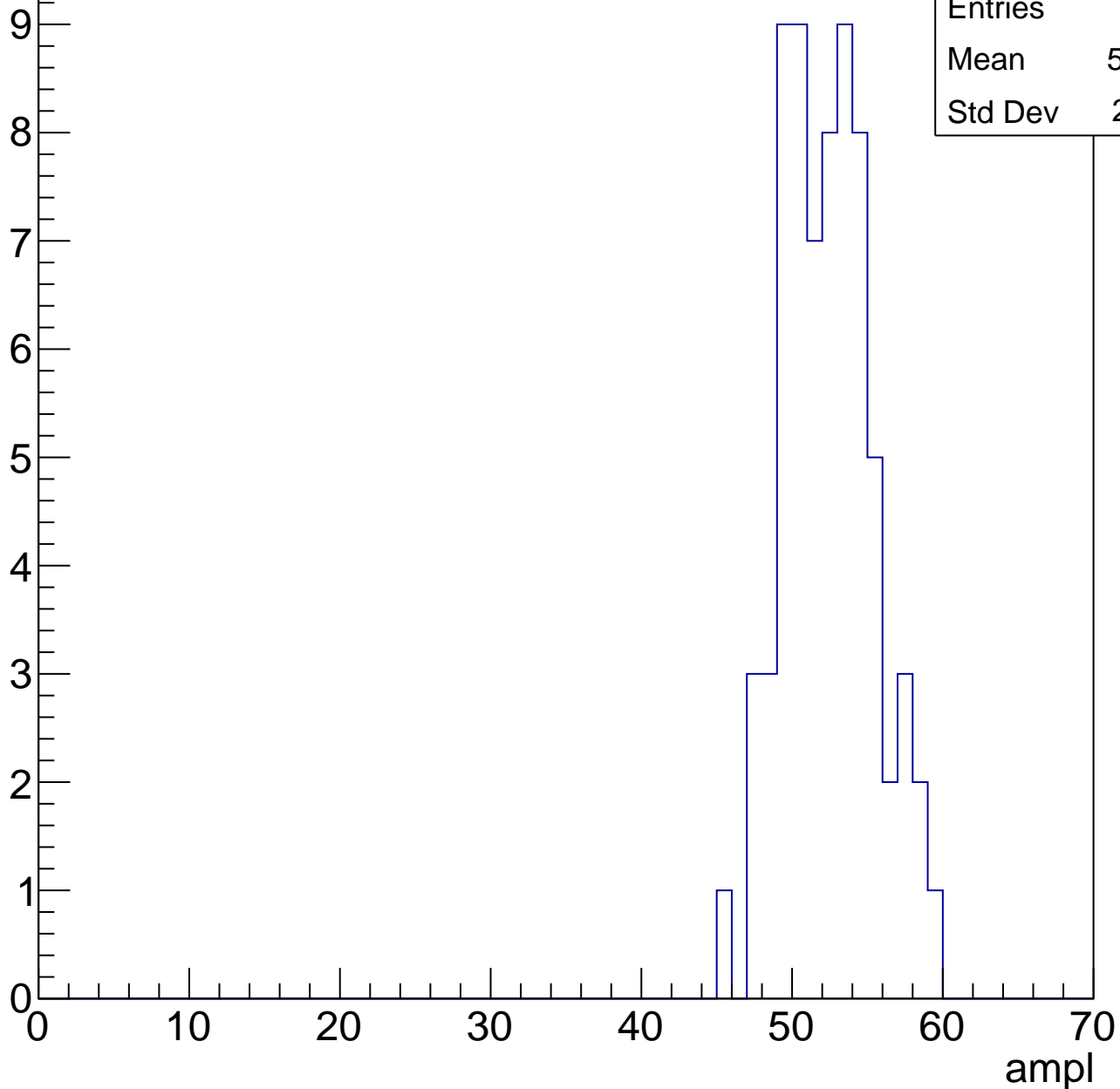
0 10 20 30 40 50 60 70



B0L001S, U23-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

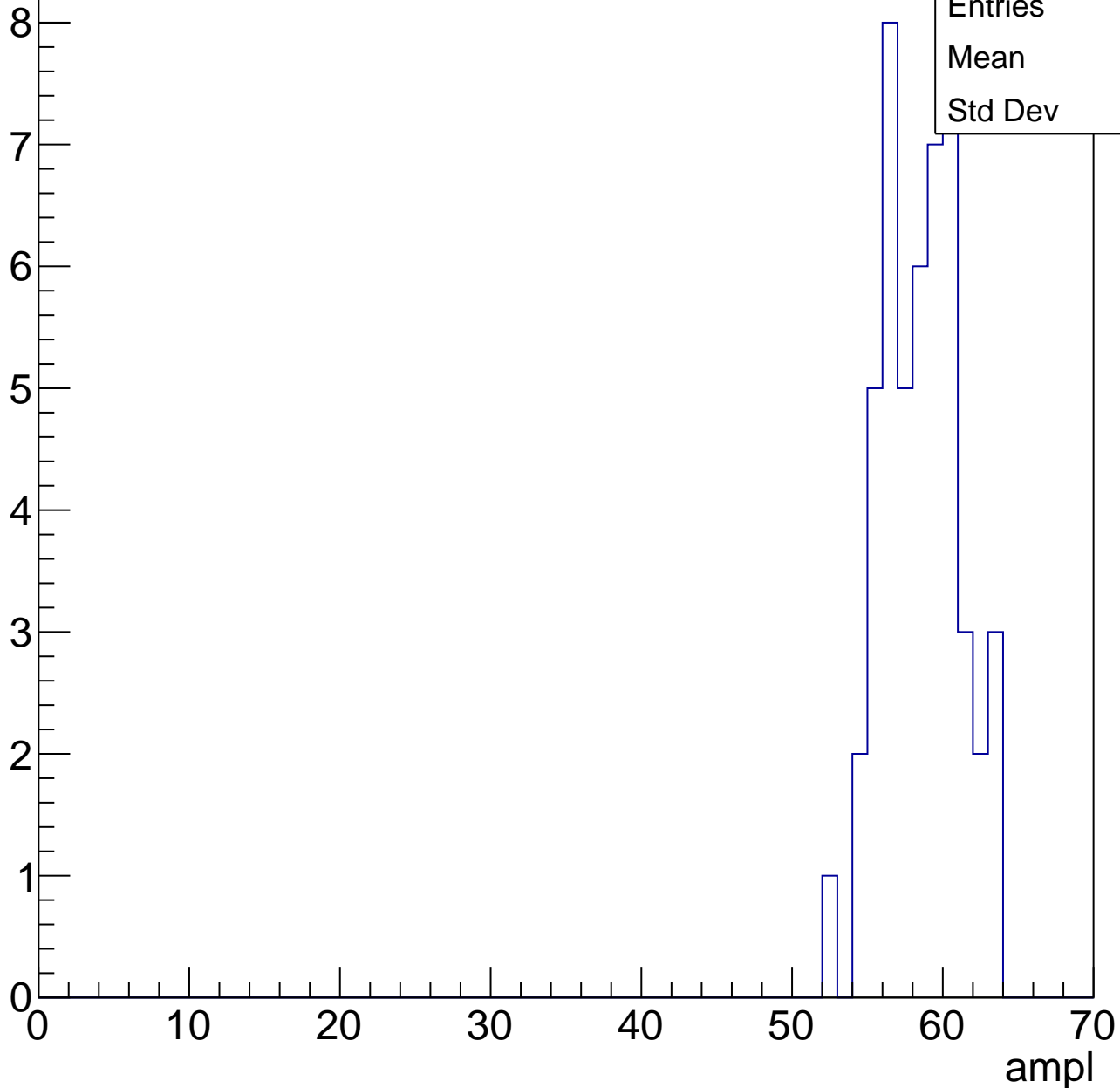


Entries	70
Mean	51.94
Std Dev	2.951

B0L001S, U23-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



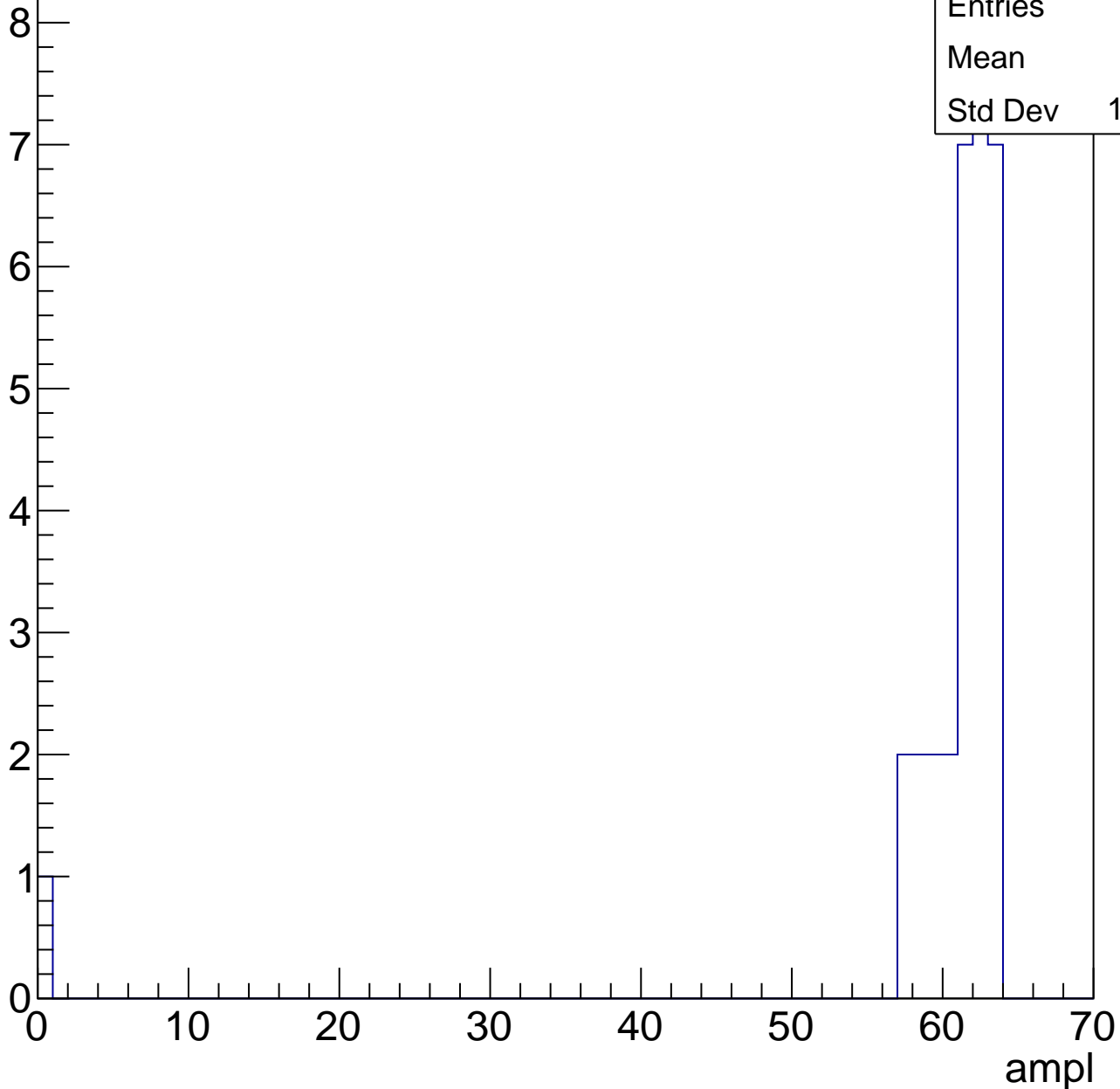
Entries	50
Mean	58.1
Std Dev	2.54

B0L001S, U23-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.1
Std Dev	10.93



B0L001S, U23-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch72, adc0

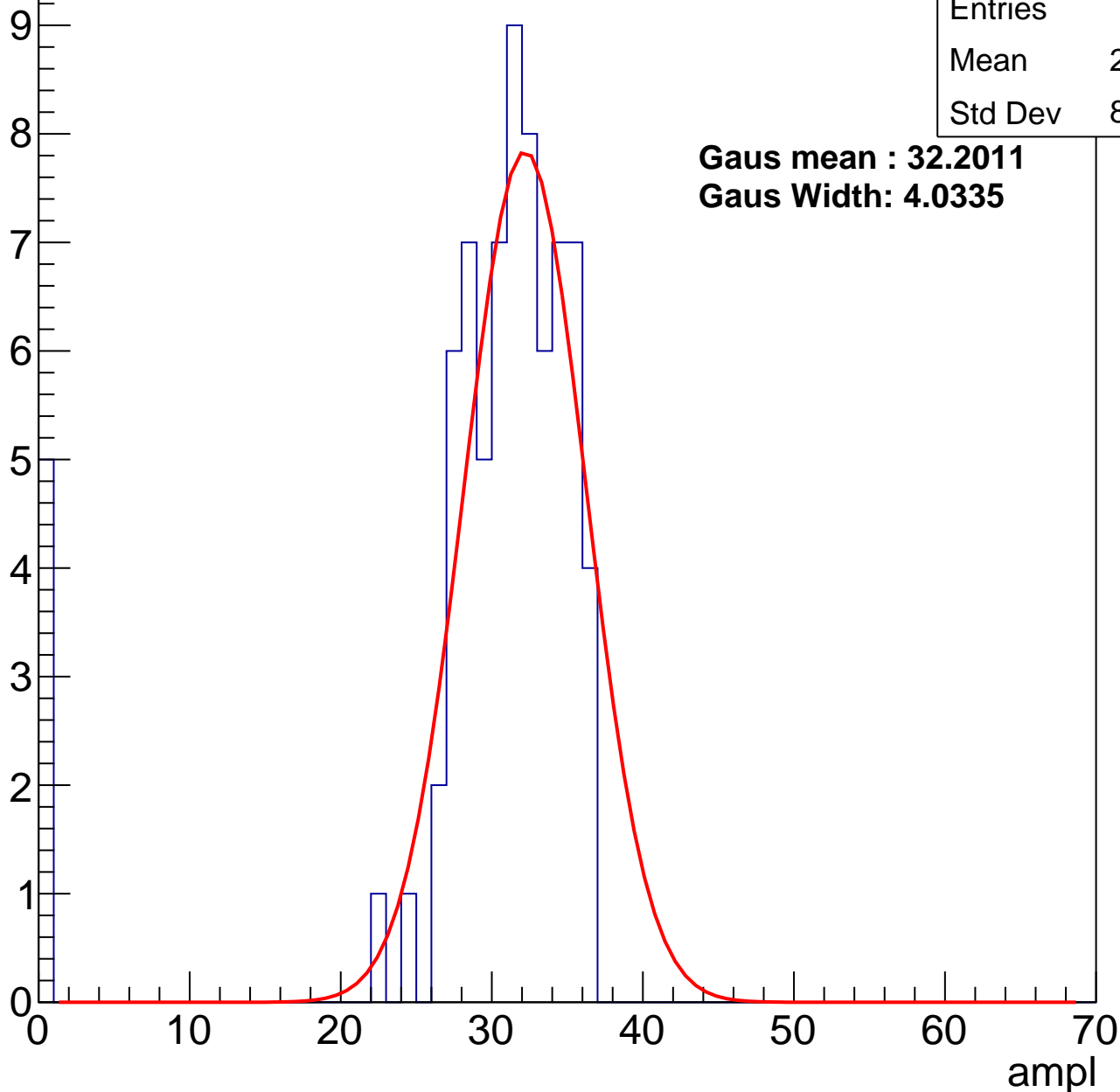
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	28.95
Std Dev	8.296

Gaus mean : 32.2011

Gaus Width: 4.0335



B0L001S, U23-ch72, adc1

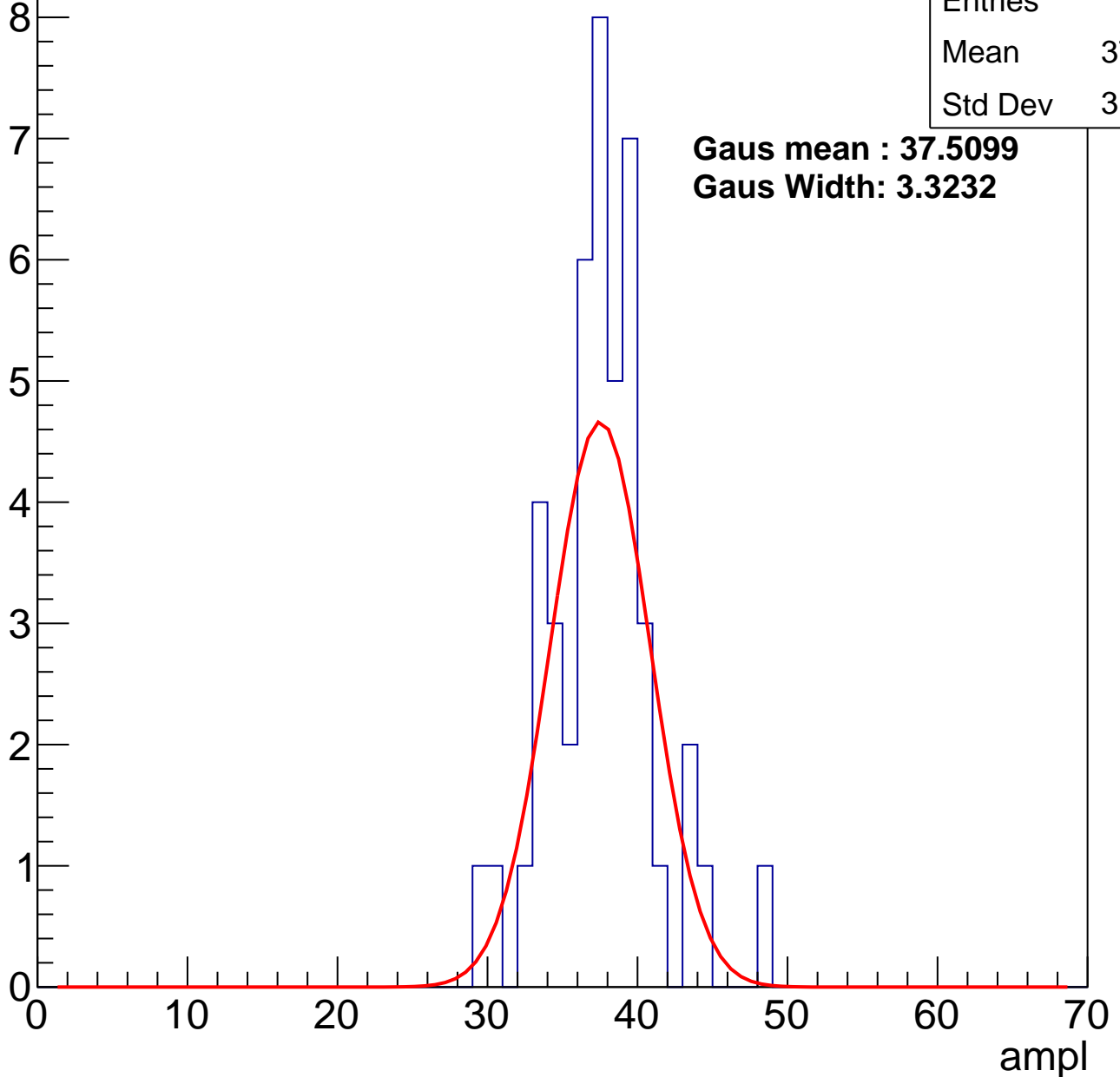
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	37.15
Std Dev	3.489

Gaus mean : 37.5099

Gaus Width: 3.3232



B0L001S, U23-ch72, adc2

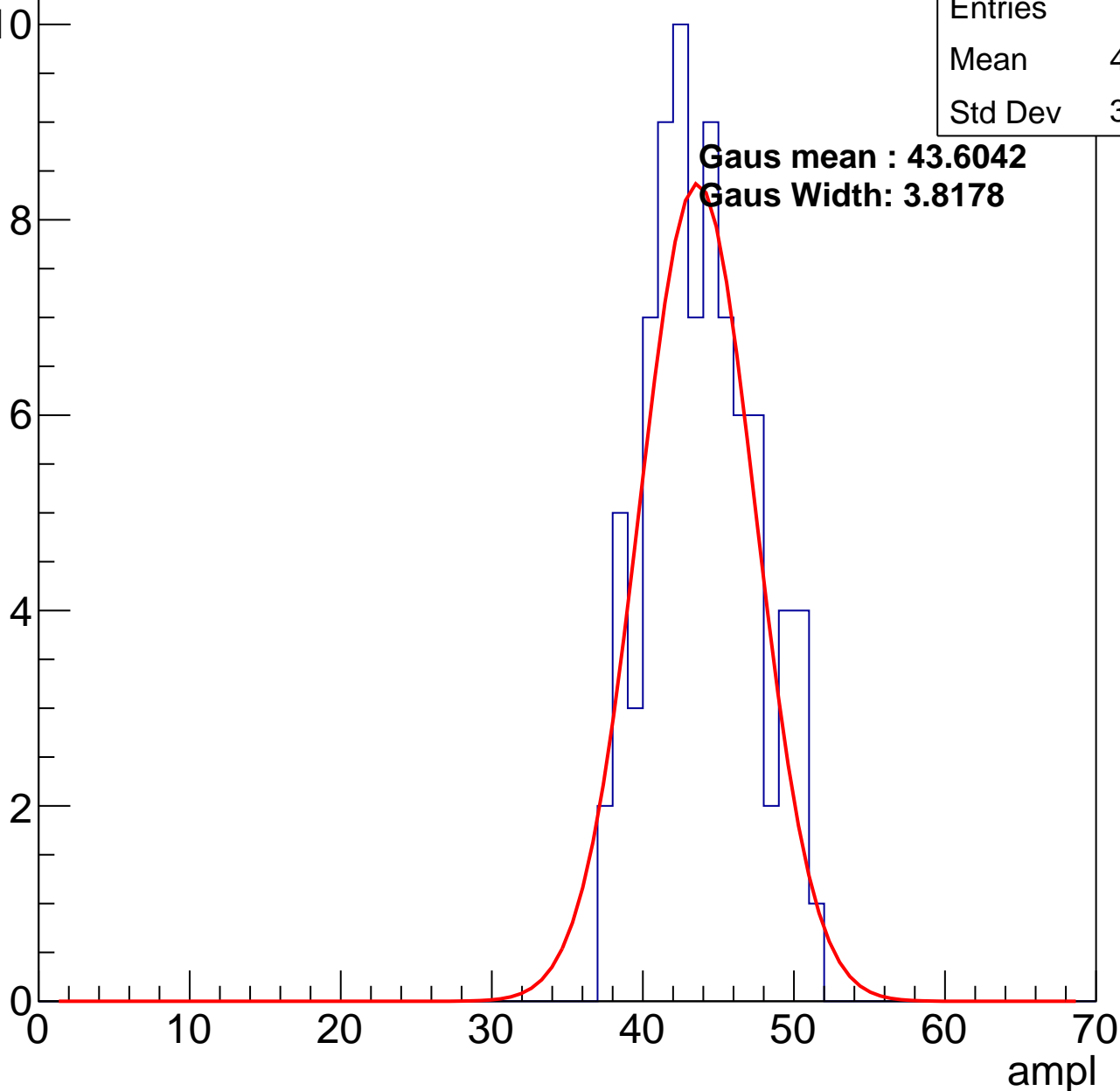
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	43.45
Std Dev	3.454

Gaus mean : 43.6042

Gaus Width: 3.8178



B0L001S, U23-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	51.08
Std Dev	3.395

Entry

10

8

6

4

2

0

0

10

20

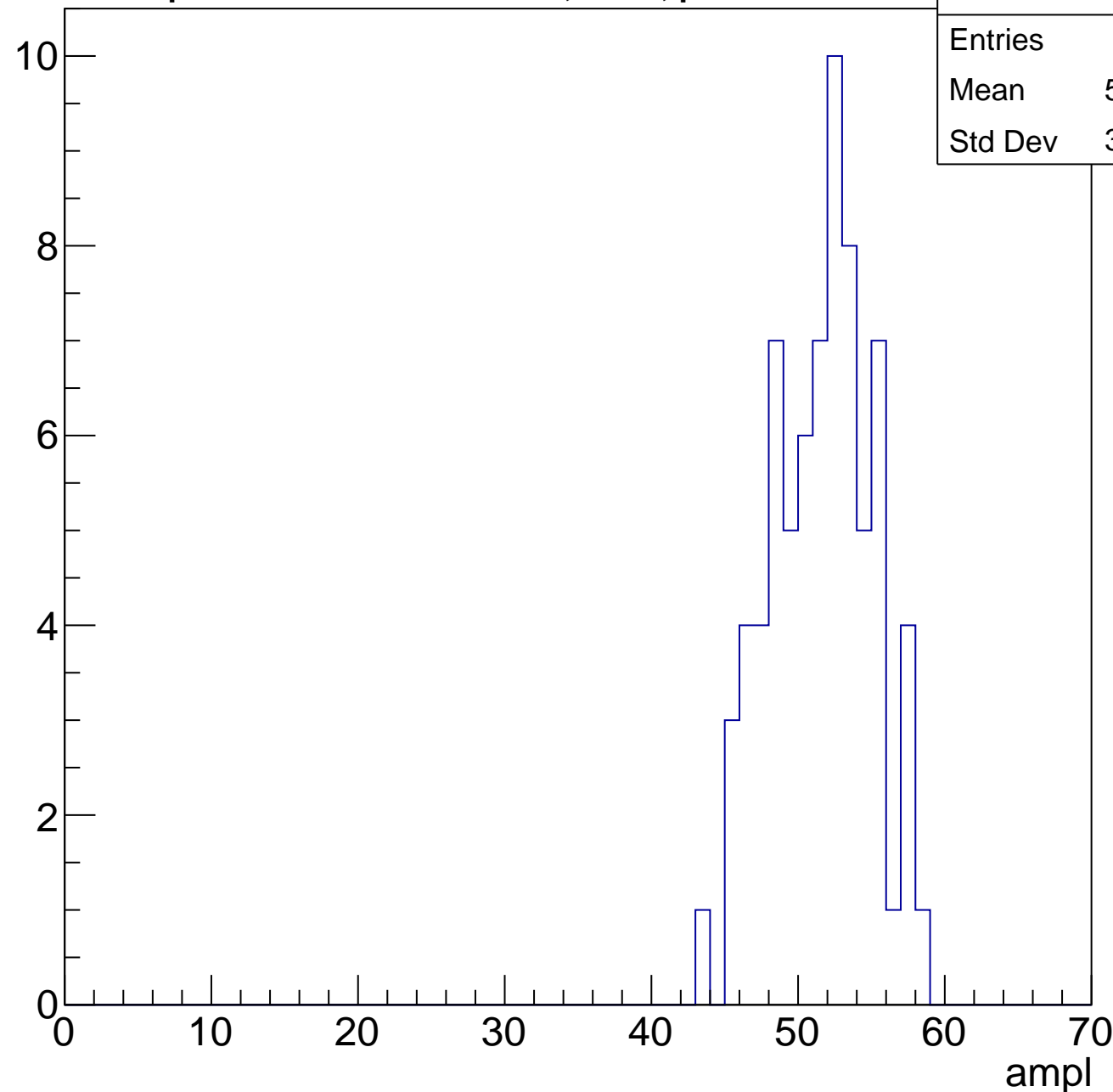
30

40

50

60

ampl

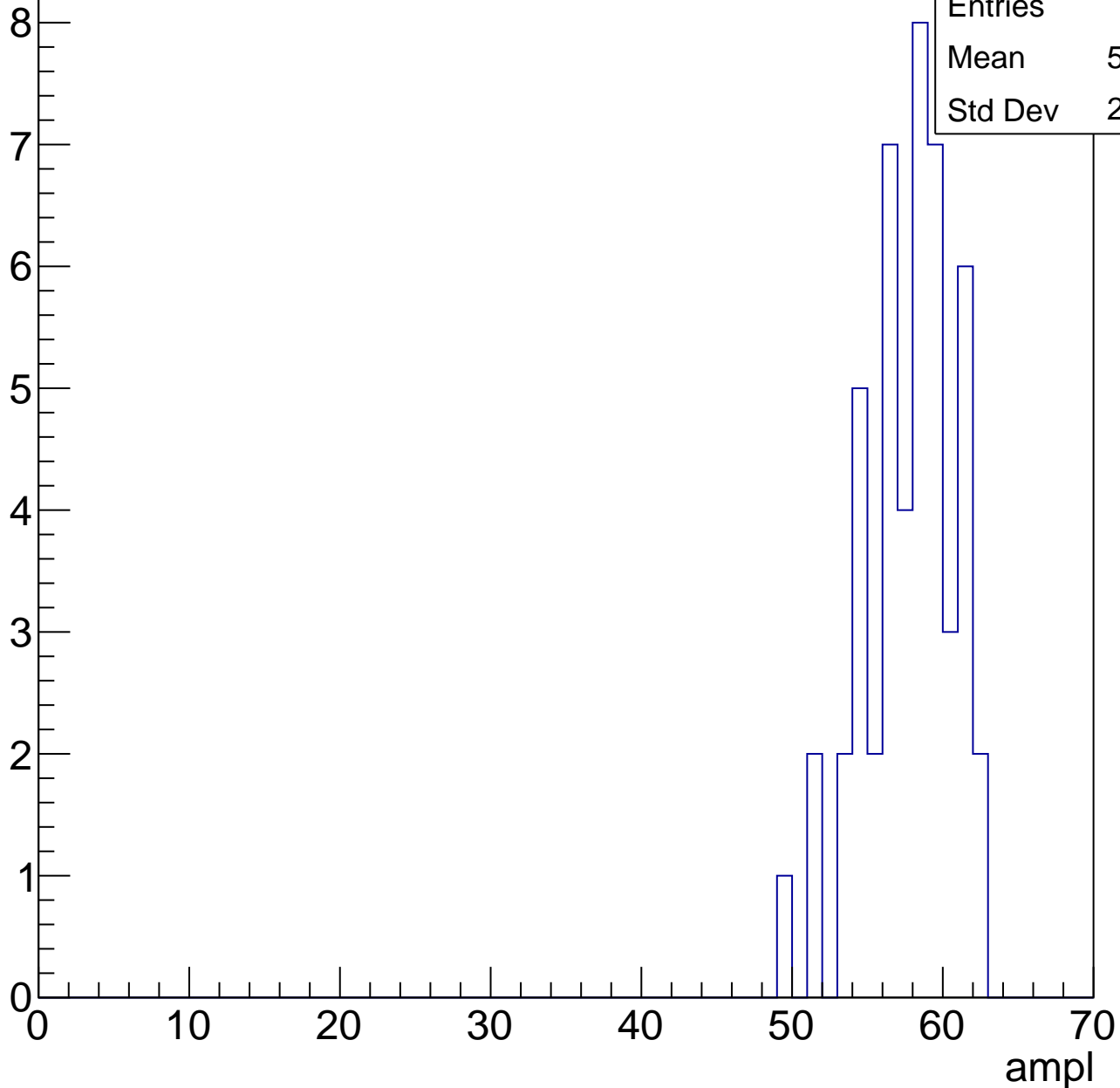


B0L001S, U23-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	57.22
Std Dev	2.978

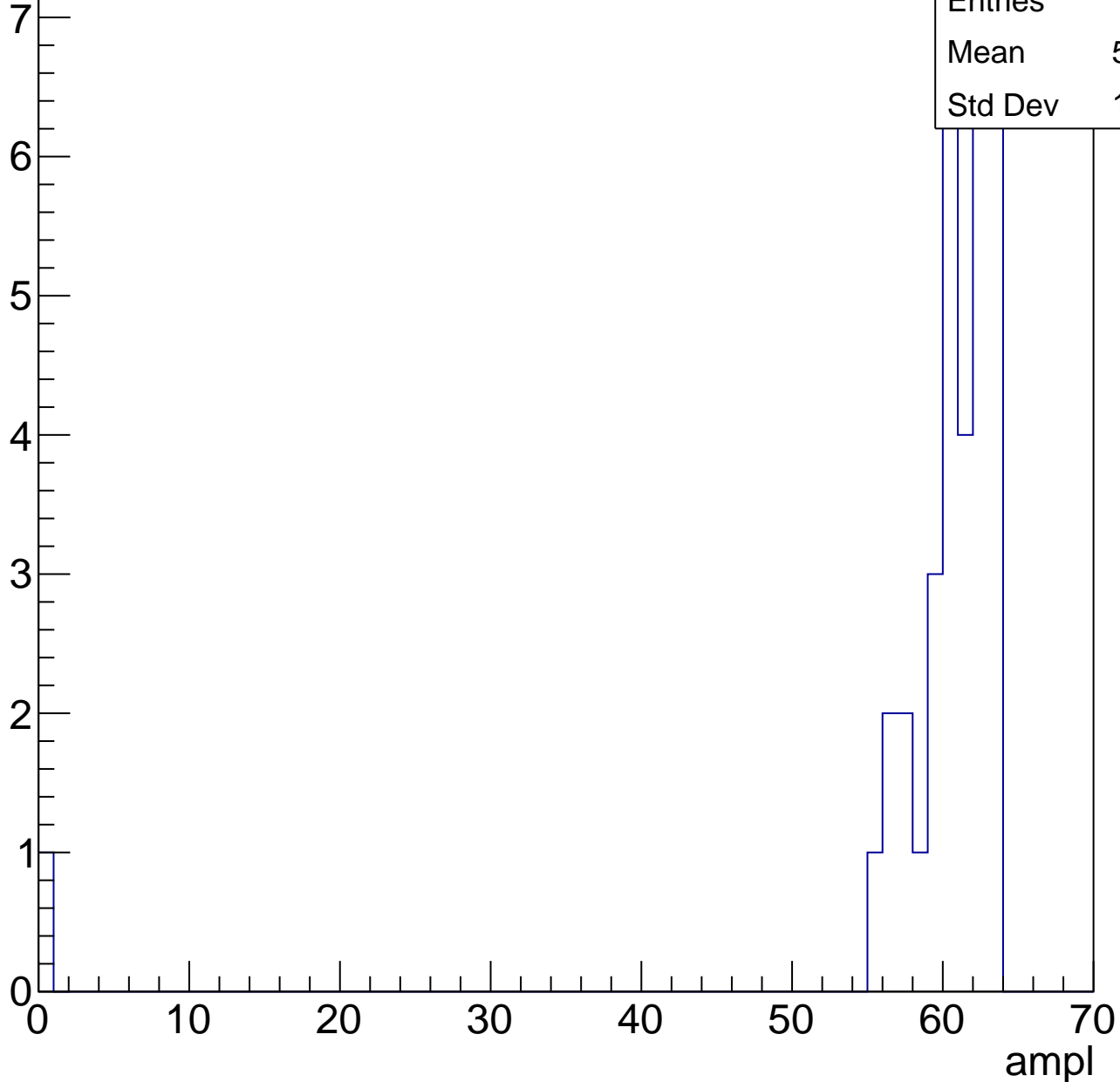


B0L001S, U23-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.71
Std Dev	10.31



B0L001S, U23-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch73, adc0

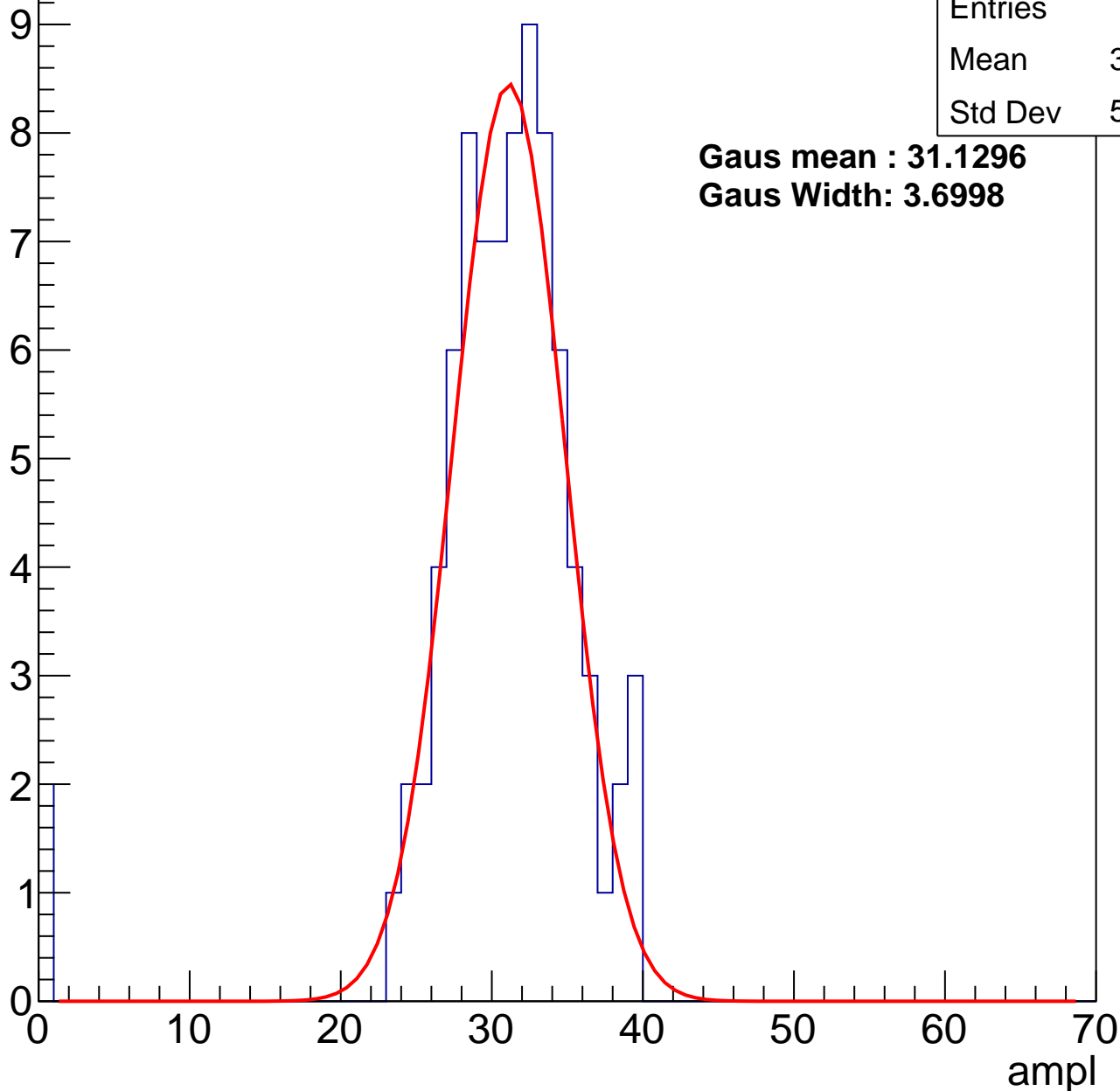
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	30.19
Std Dev	5.977

Gaus mean : 31.1296

Gaus Width: 3.6998



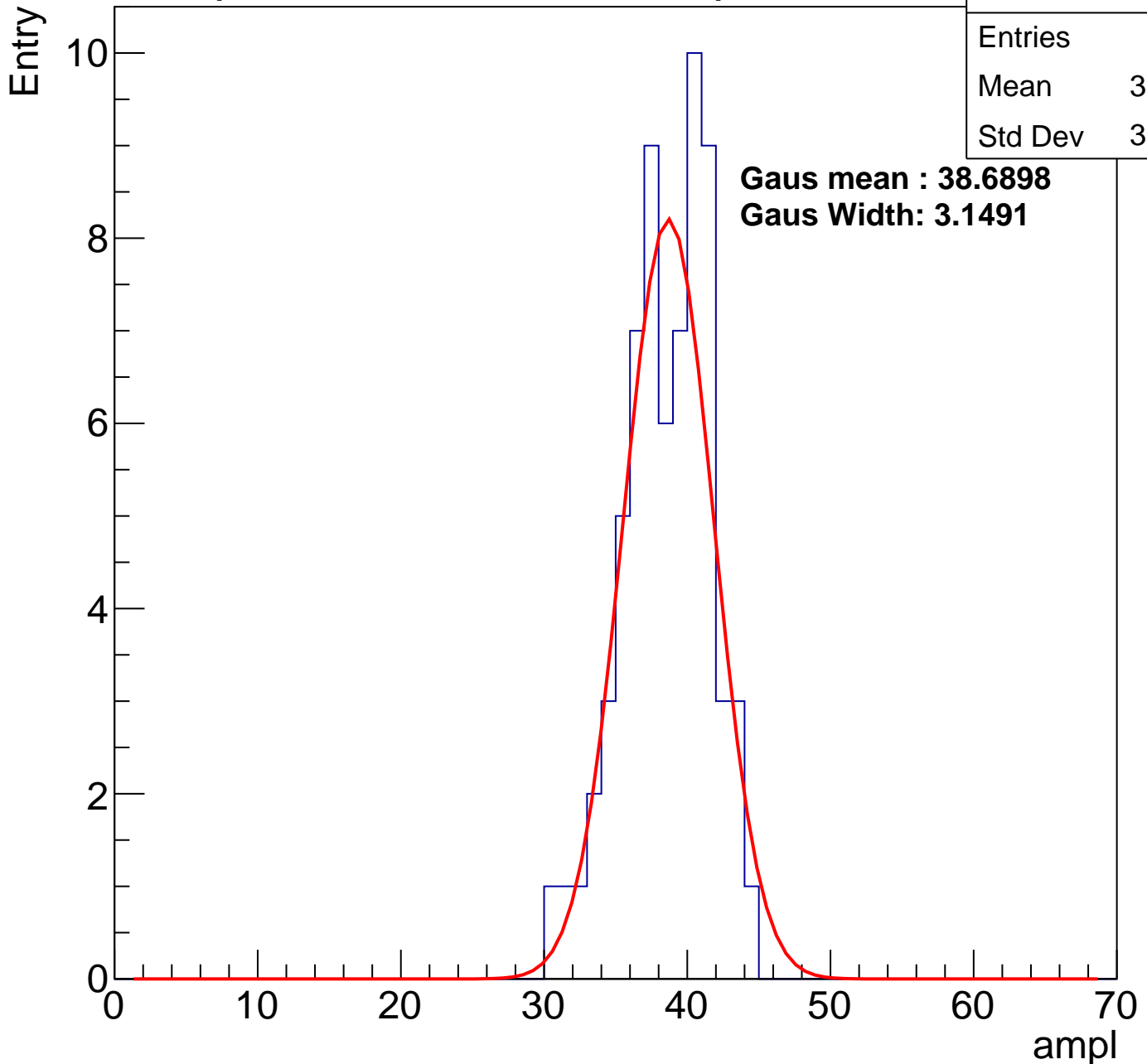
B0L001S, U23-ch73, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	38.09
Std Dev	3.008

Gaus mean : 38.6898

Gaus Width: 3.1491



B0L001S, U23-ch73, adc2

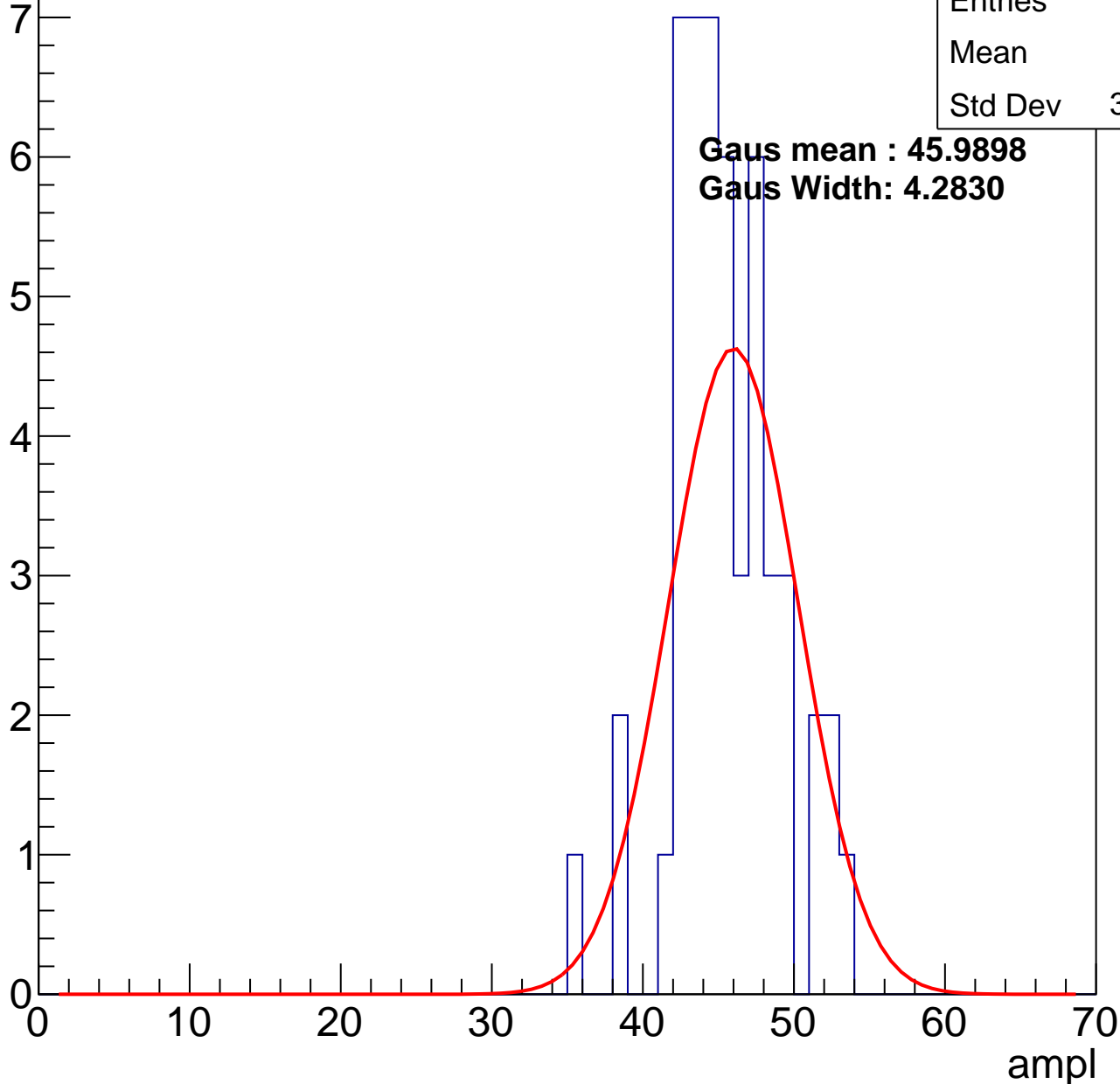
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	45
Std Dev	3.565

Gaus mean : 45.9898

Gaus Width: 4.2830

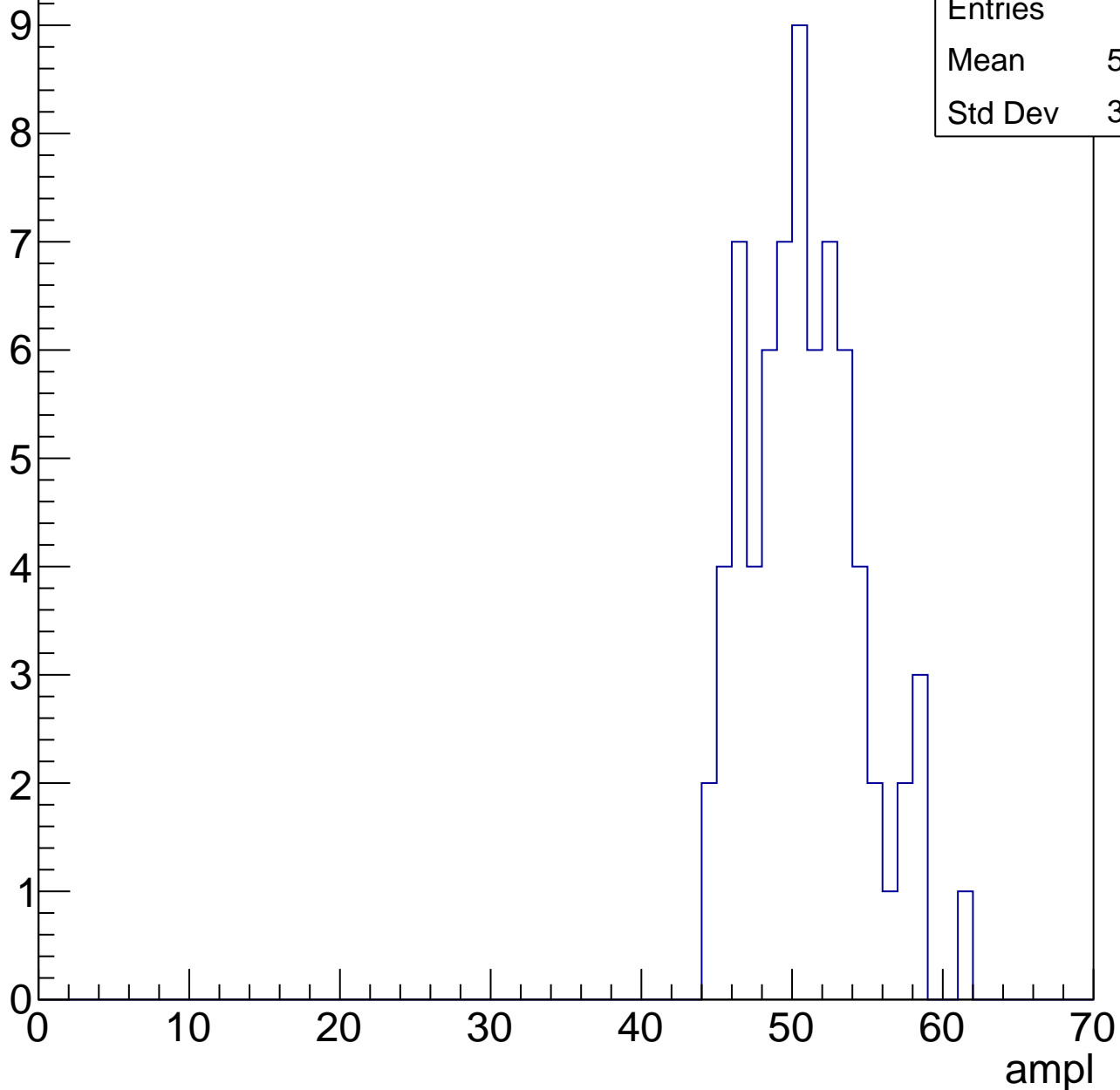


B0L001S, U23-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

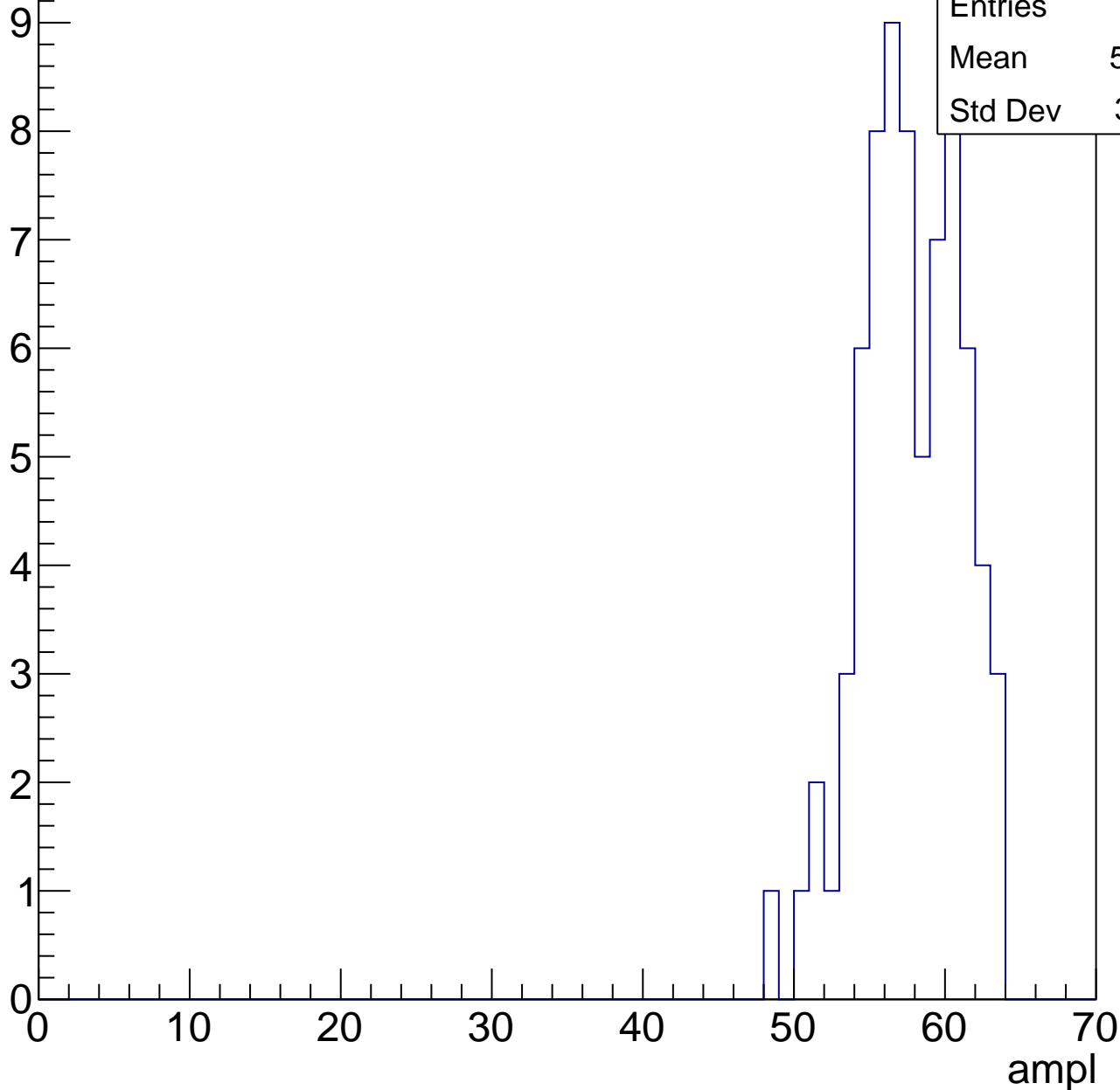
Entries	71
Mean	50.39
Std Dev	3.732



B0L001S, U23-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

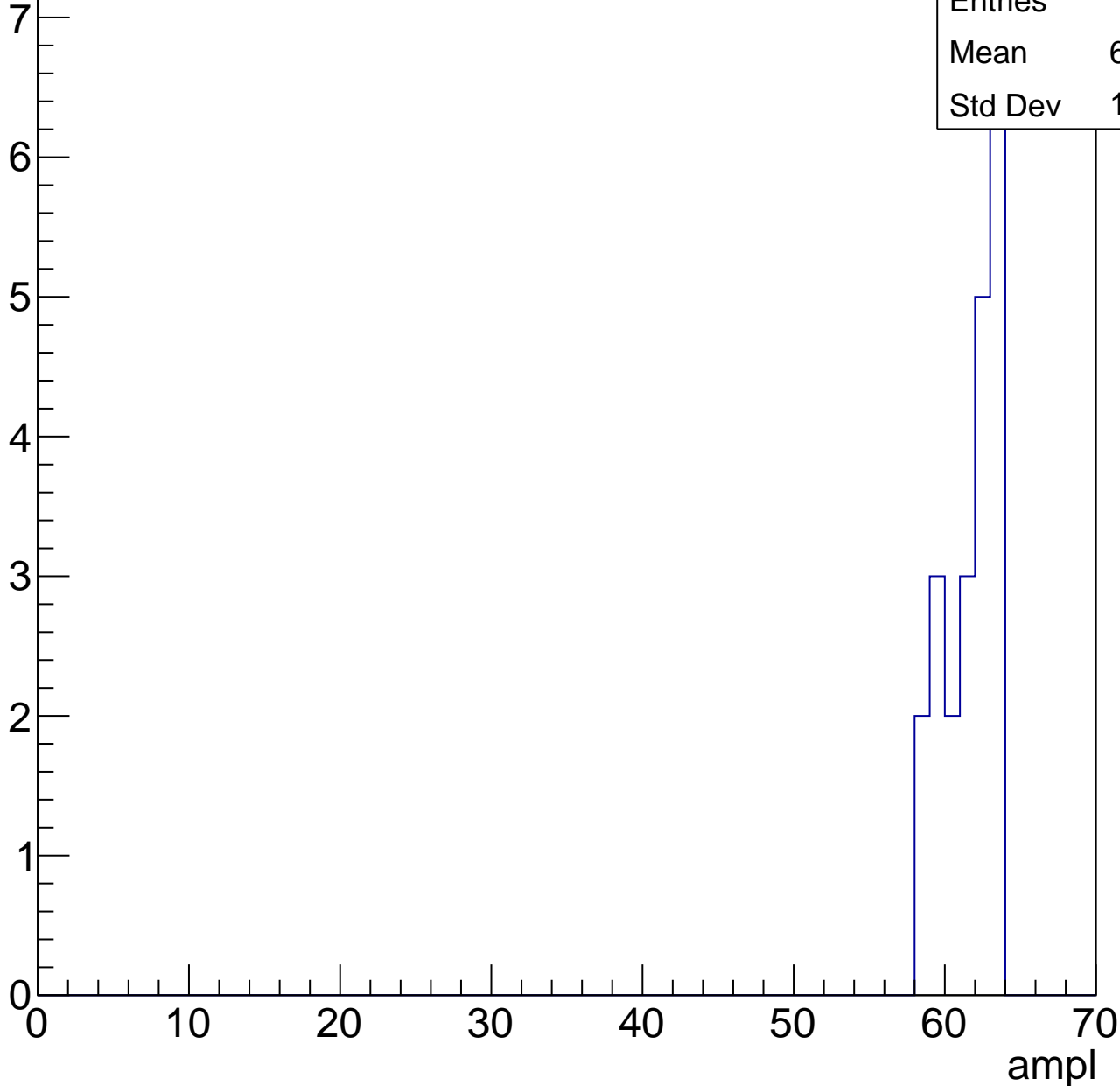


B0L001S, U23-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	61.23
Std Dev	1.704



B0L001S, U23-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch74, adc0

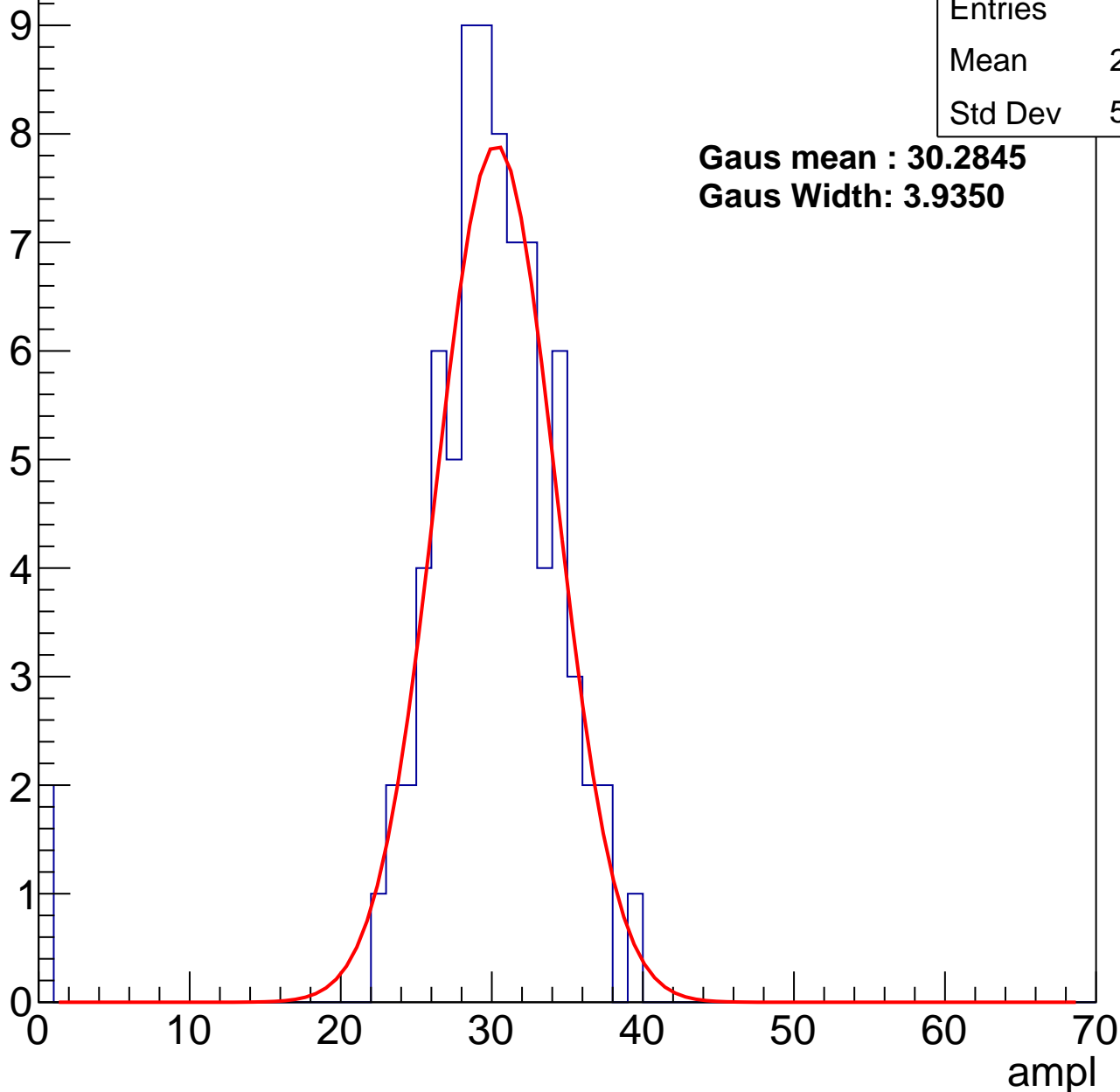
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	29.09
Std Dev	5.859

Gaus mean : 30.2845

Gaus Width: 3.9350



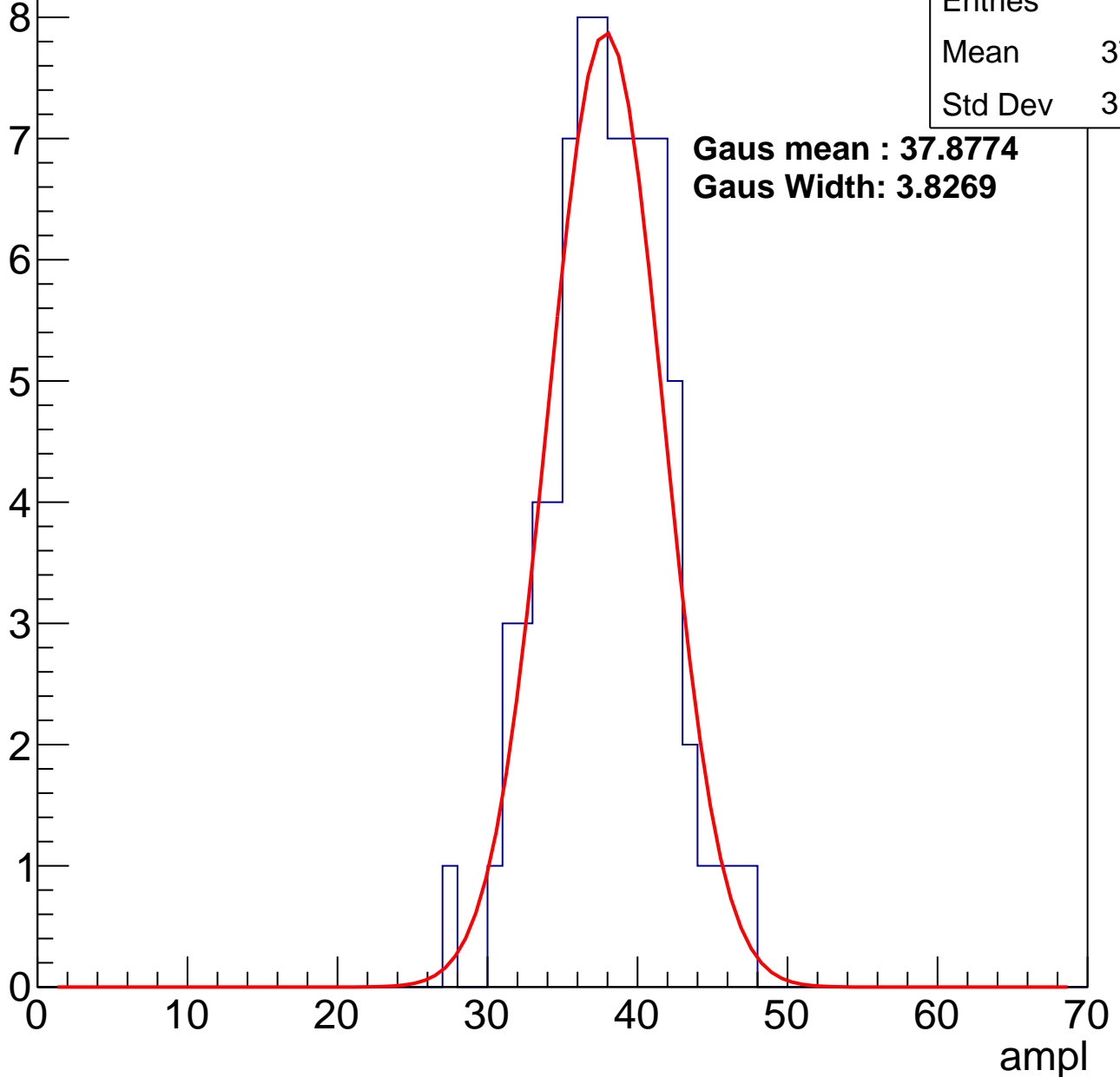
B0L001S, U23-ch74, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.53
Std Dev	3.835

Gaus mean : 37.8774
Gaus Width: 3.8269



B0L001S, U23-ch74, adc2

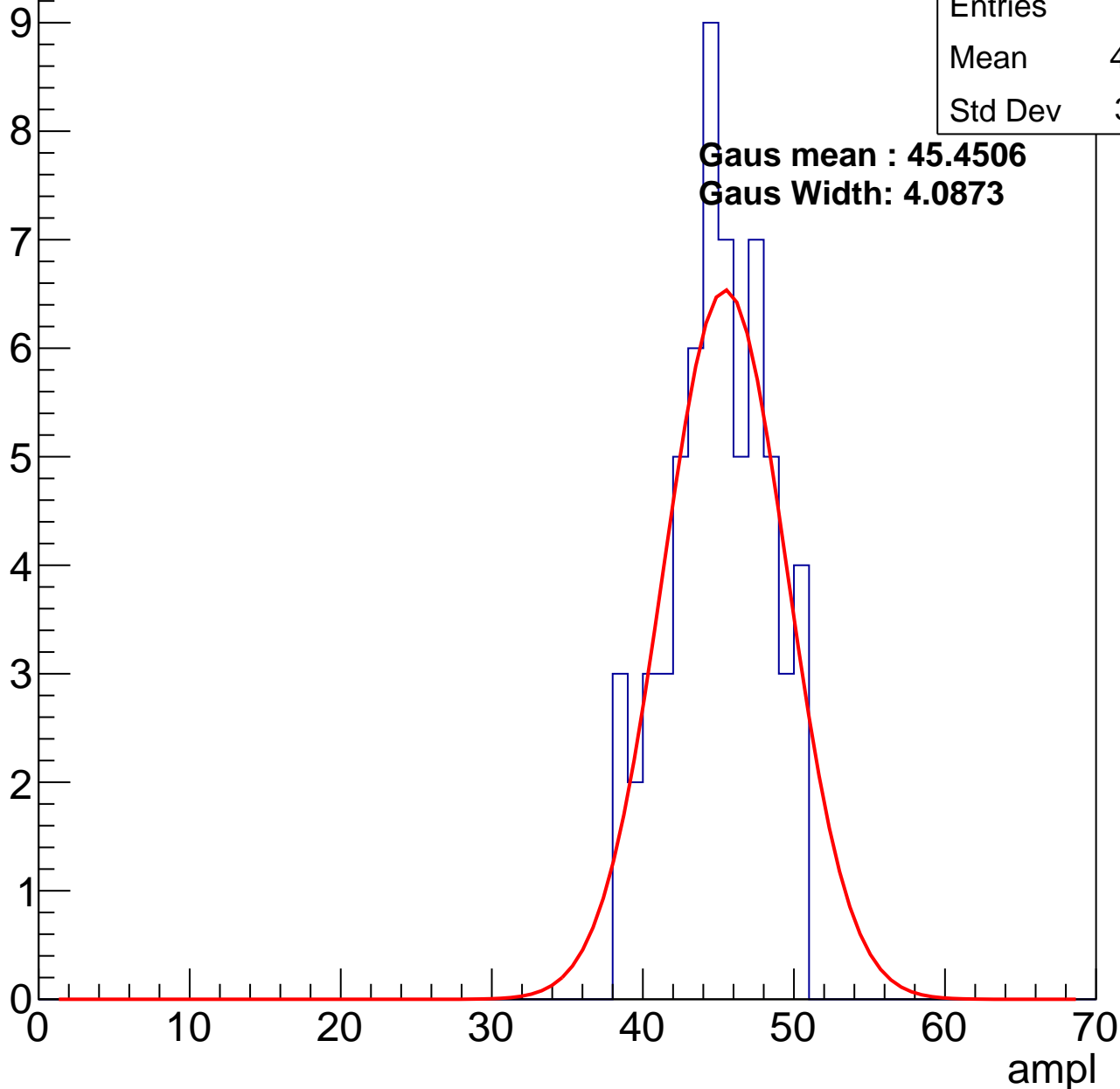
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.52
Std Dev	3.191

Gaus mean : 45.4506

Gaus Width: 4.0873

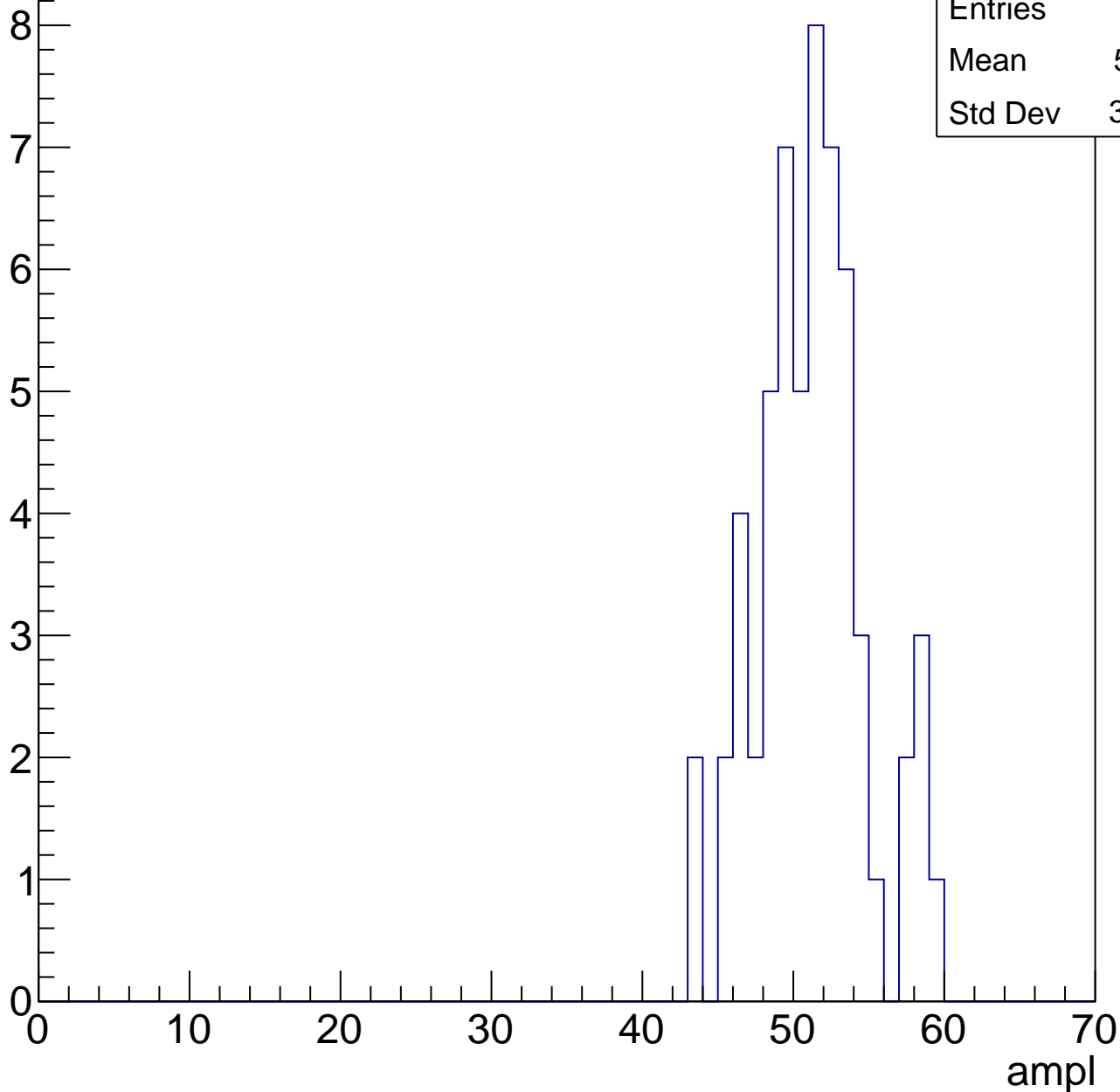


B0L001S, U23-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

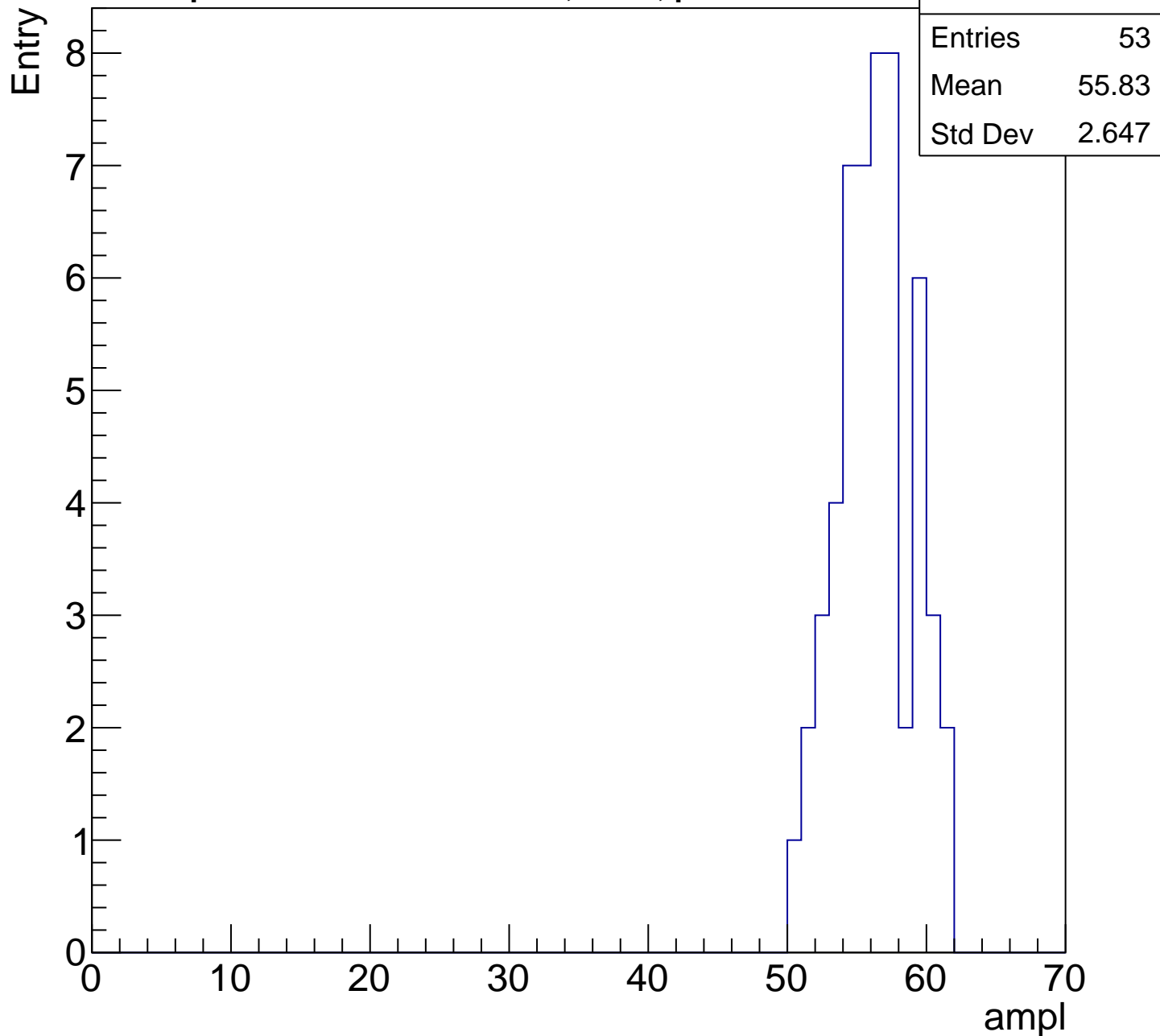
Entry

Entries	58
Mean	50.71
Std Dev	3.625



B0L001S, U23-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

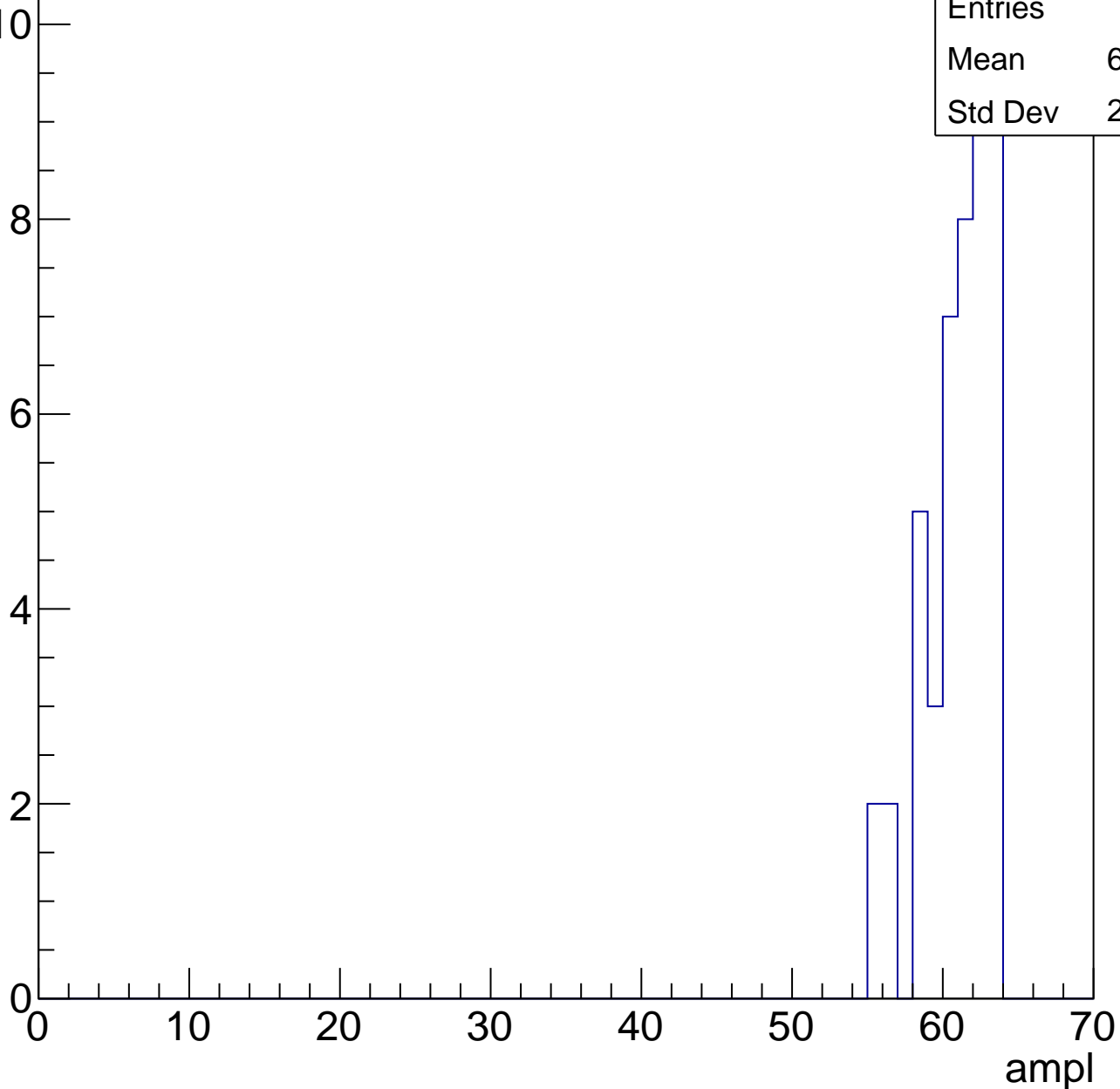


B0L001S, U23-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	60.52
Std Dev	2.194



B0L001S, U23-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch75, adc0

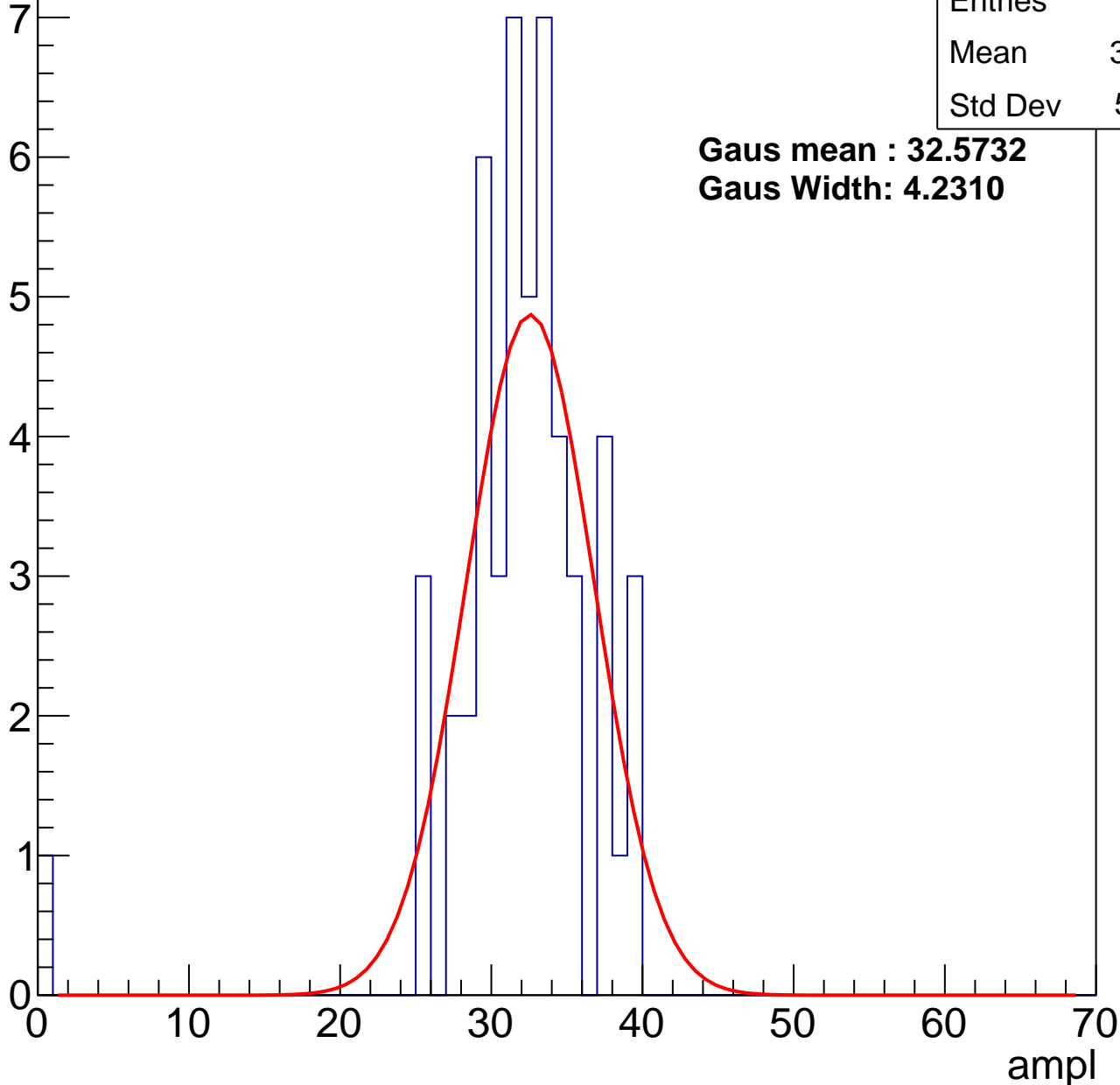
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	31.39
Std Dev	5.671

Gaus mean : 32.5732

Gaus Width: 4.2310



B0L001S, U23-ch75, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	37.37
Std Dev	3.567

Gaus mean : 37.9688

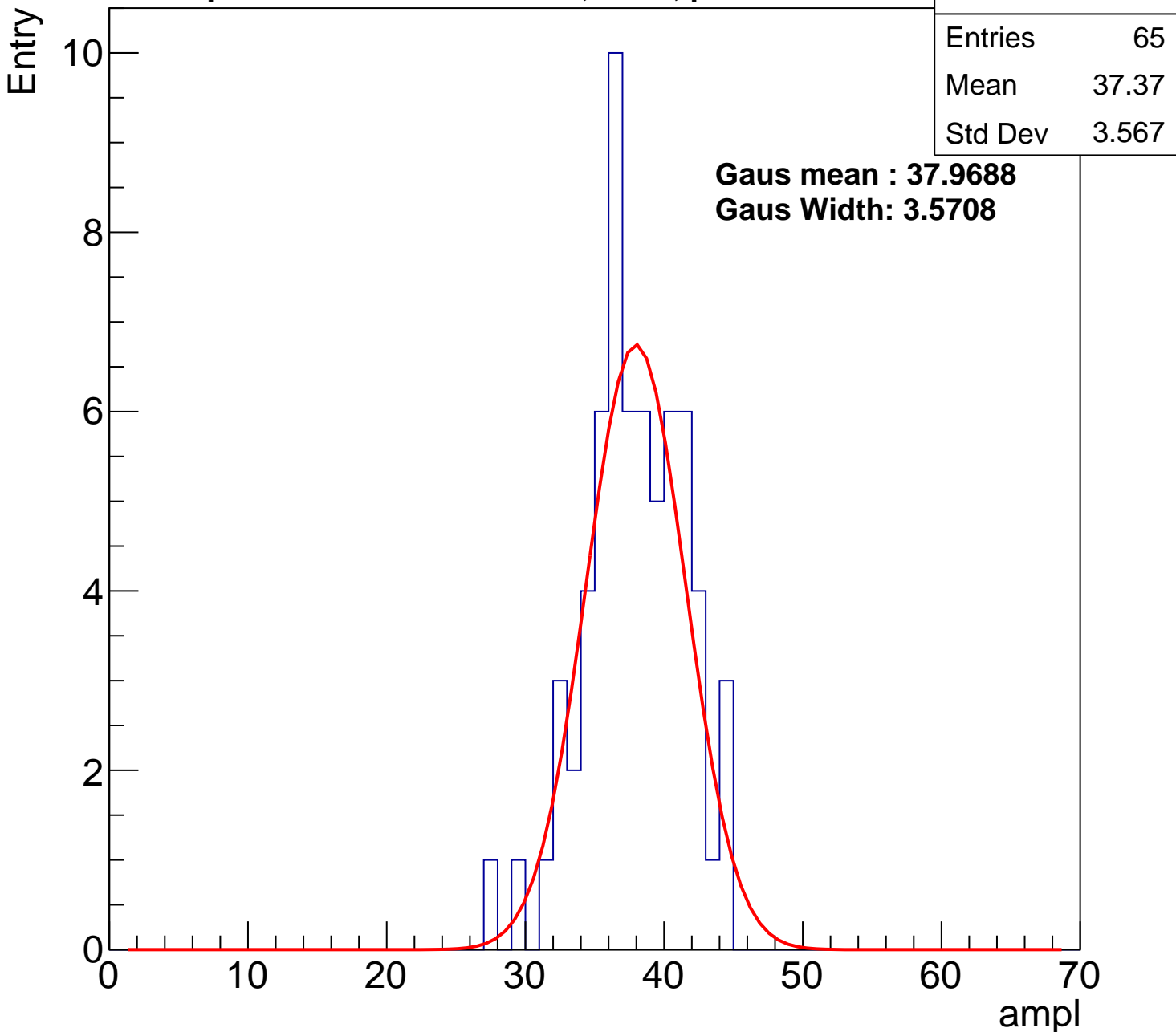
Gaus Width: 3.5708

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch75, adc2

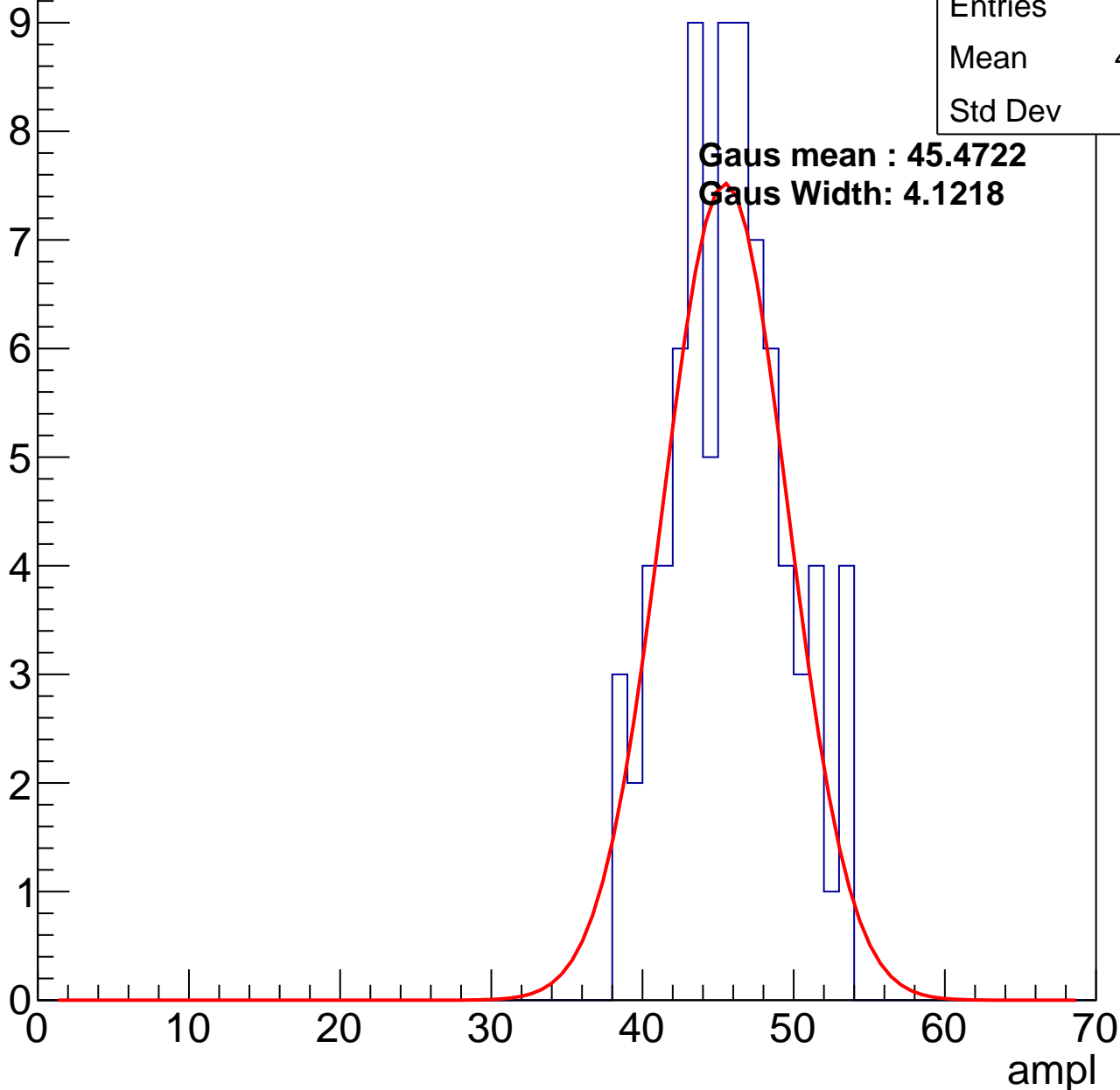
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	45.31
Std Dev	3.79

Gaus mean : 45.4722

Gaus Width: 4.1218

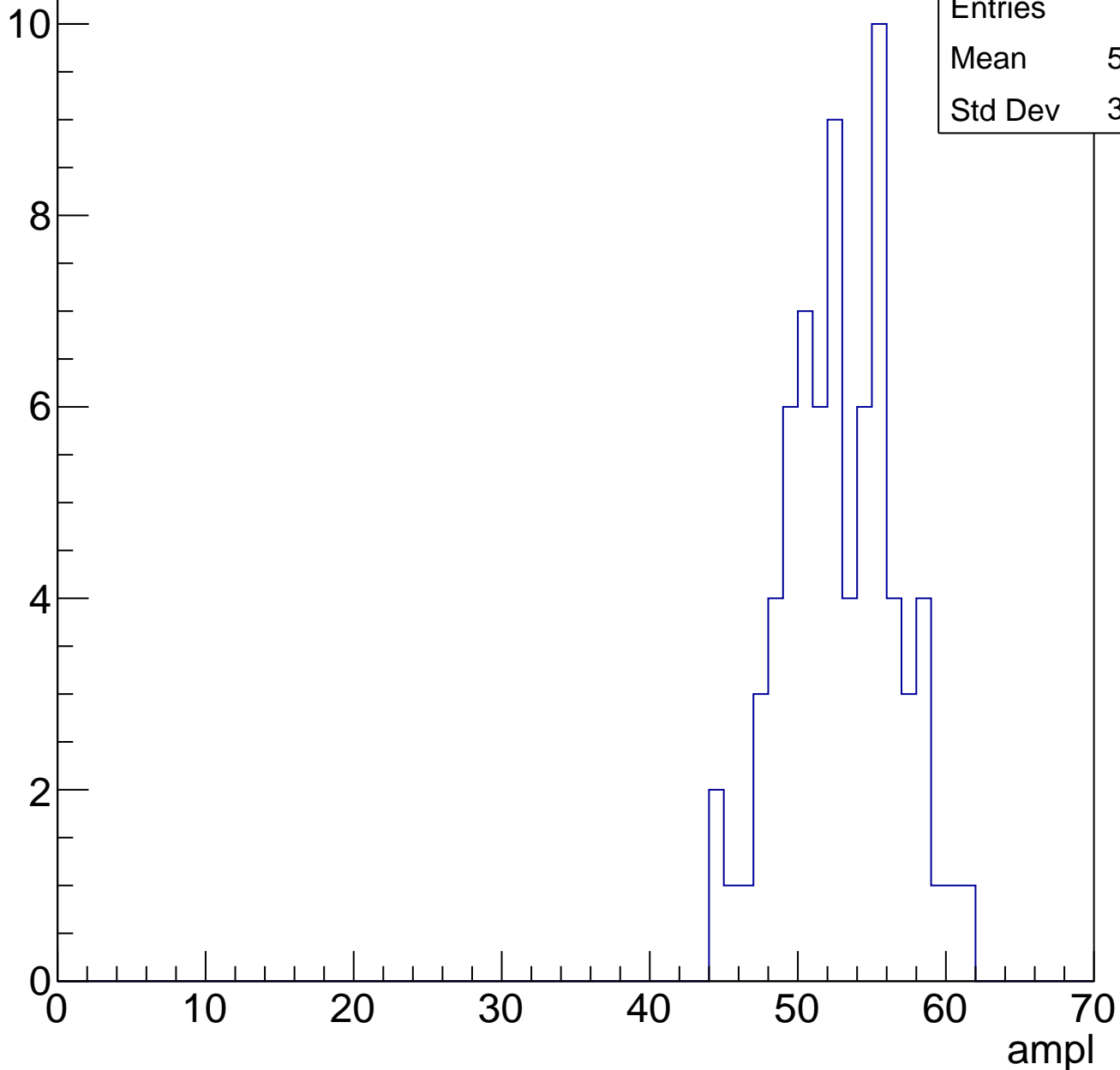


B0L001S, U23-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	52.37
Std Dev	3.747

Entry

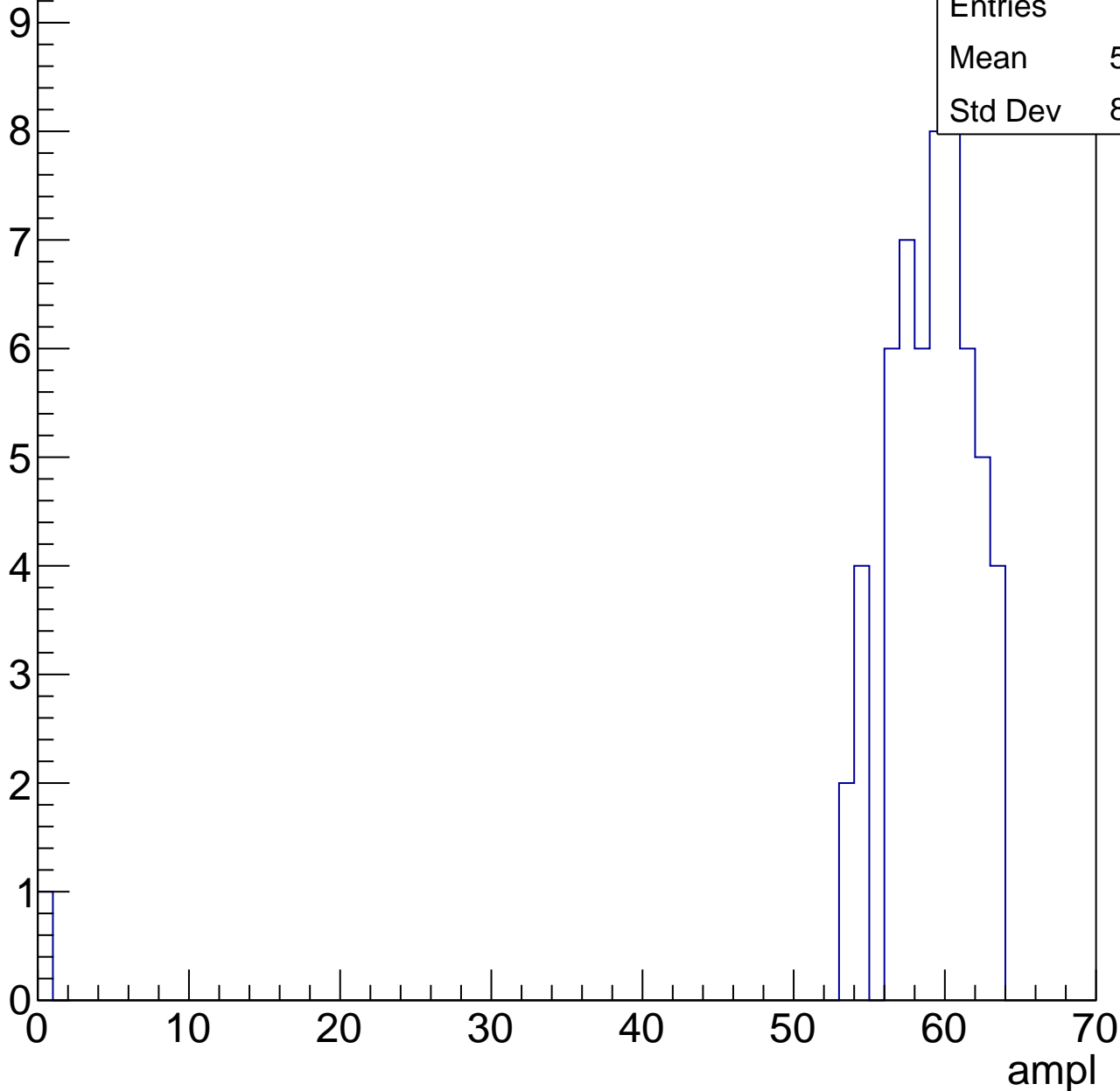


B0L001S, U23-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.67
Std Dev	8.074

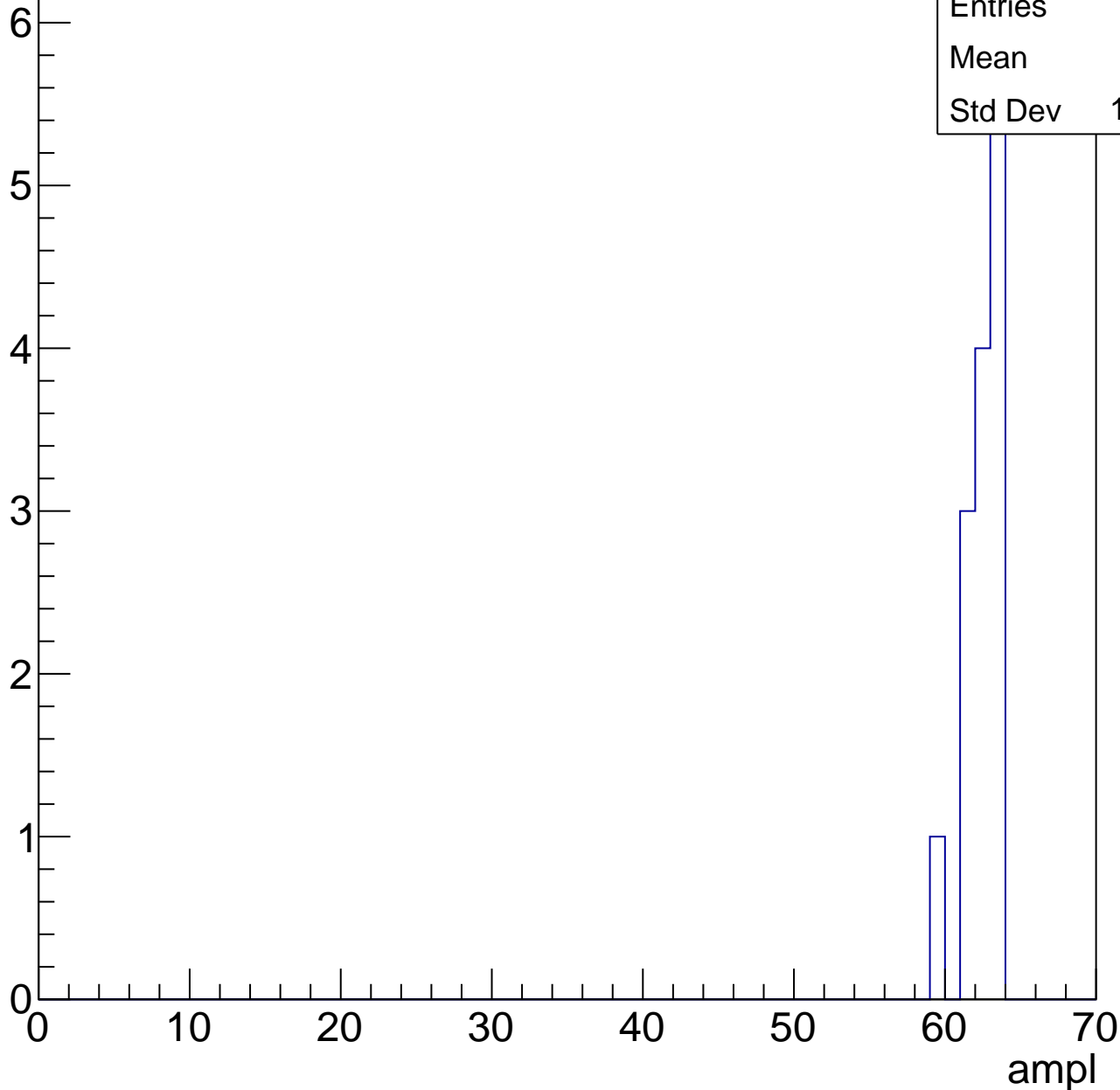


B0L001S, U23-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	62
Std Dev	1.134



B0L001S, U23-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch76, adc0

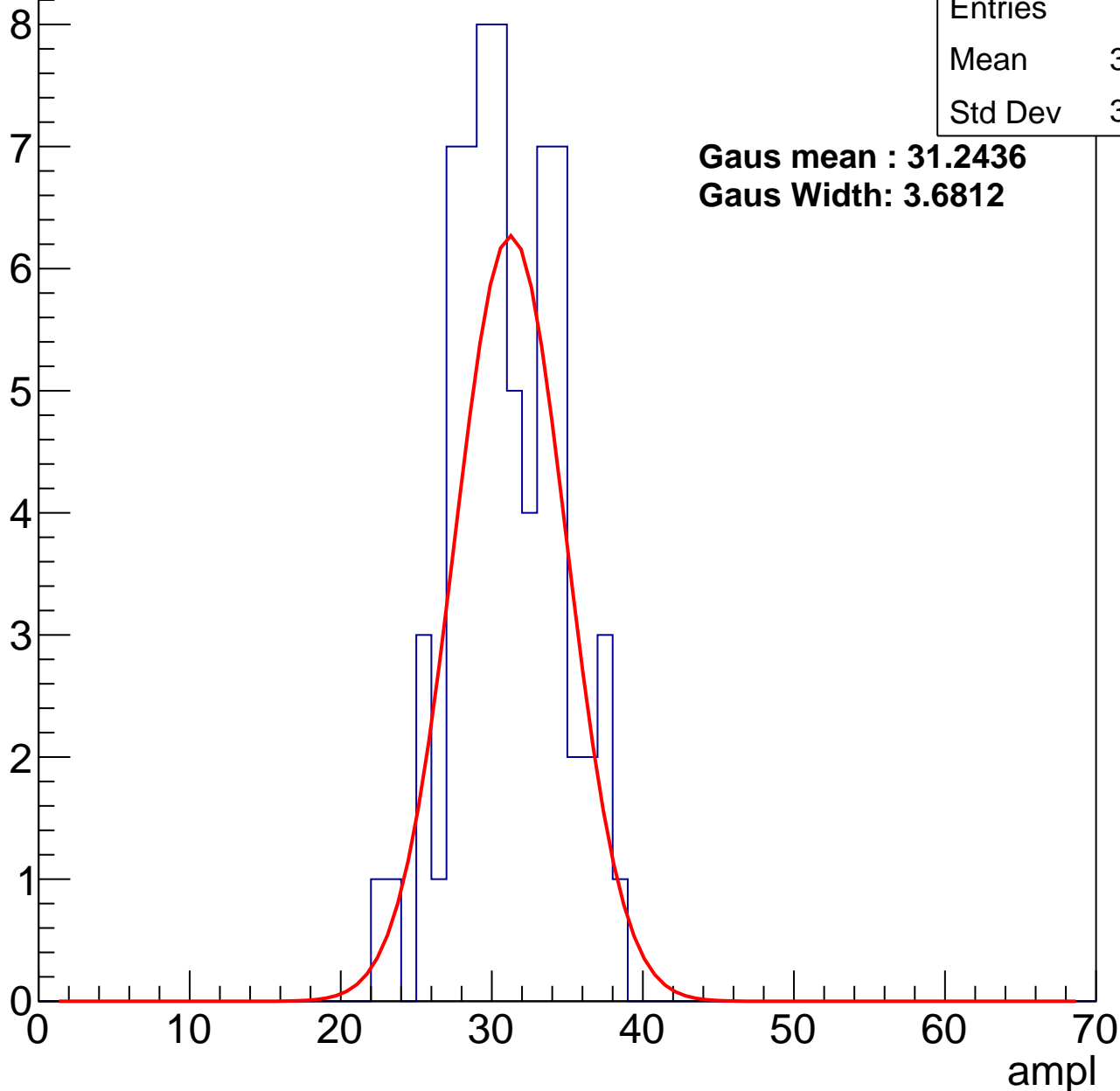
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.54
Std Dev	3.483

Gaus mean : 31.2436

Gaus Width: 3.6812



B0L001S, U23-ch76, adc1

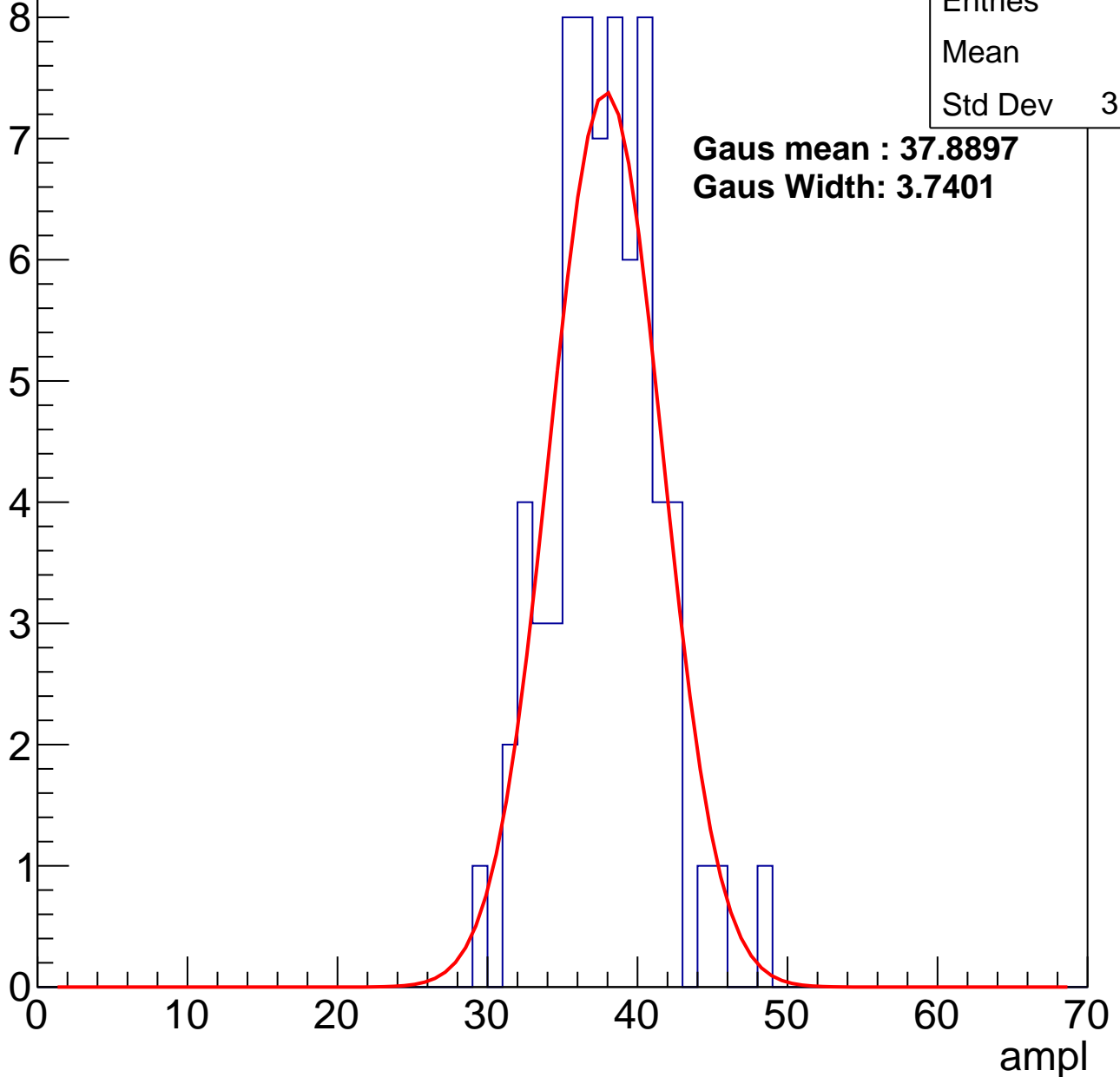
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.3
Std Dev	3.503

Gaus mean : 37.8897

Gaus Width: 3.7401



B0L001S, U23-ch76, adc2

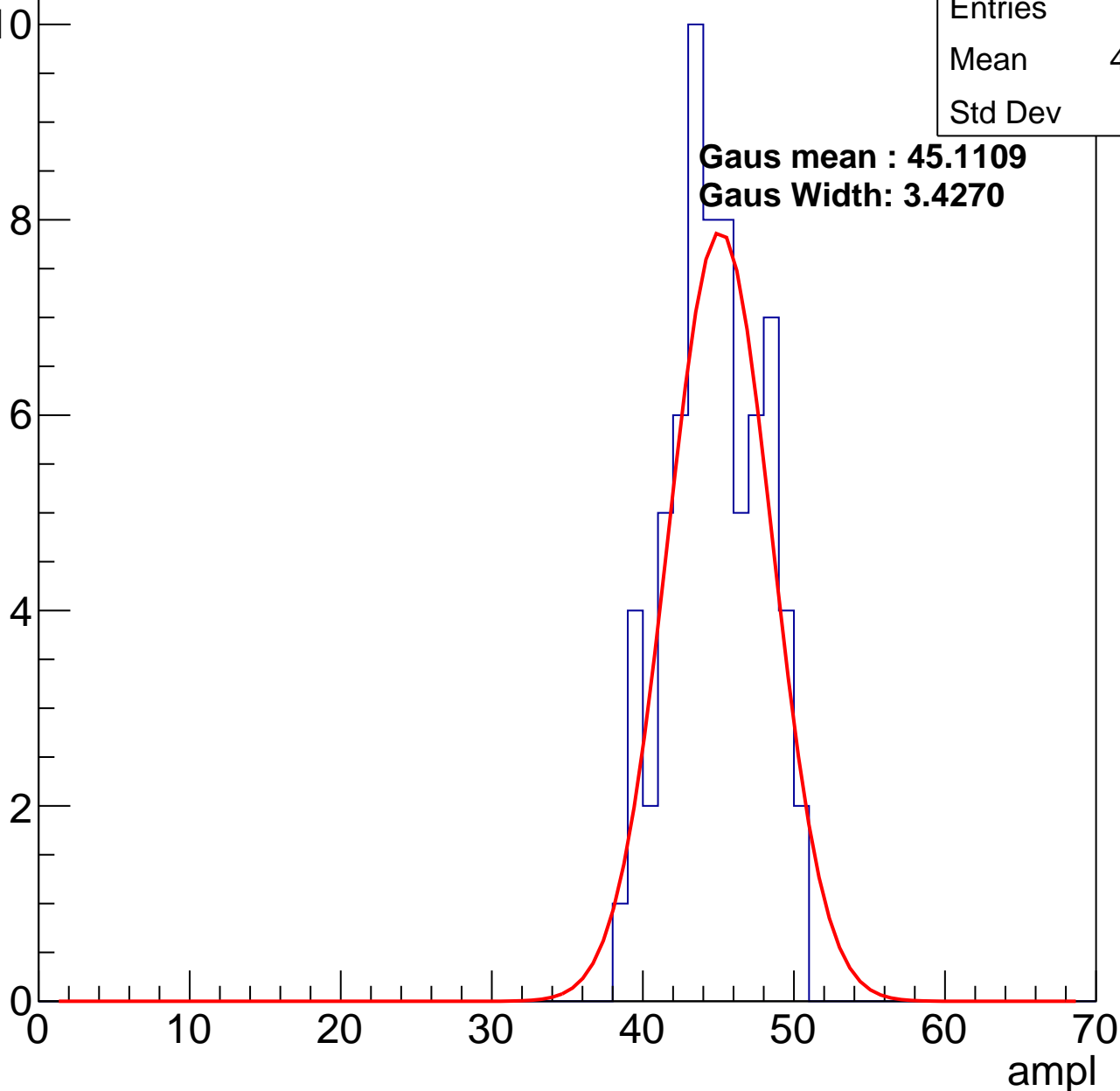
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.37
Std Dev	2.98

Gaus mean : 45.1109

Gaus Width: 3.4270



B0L001S, U23-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	54
Mean	50.7
Std Dev	3.984

Entry

10

8

6

4

2

0

0

10

20

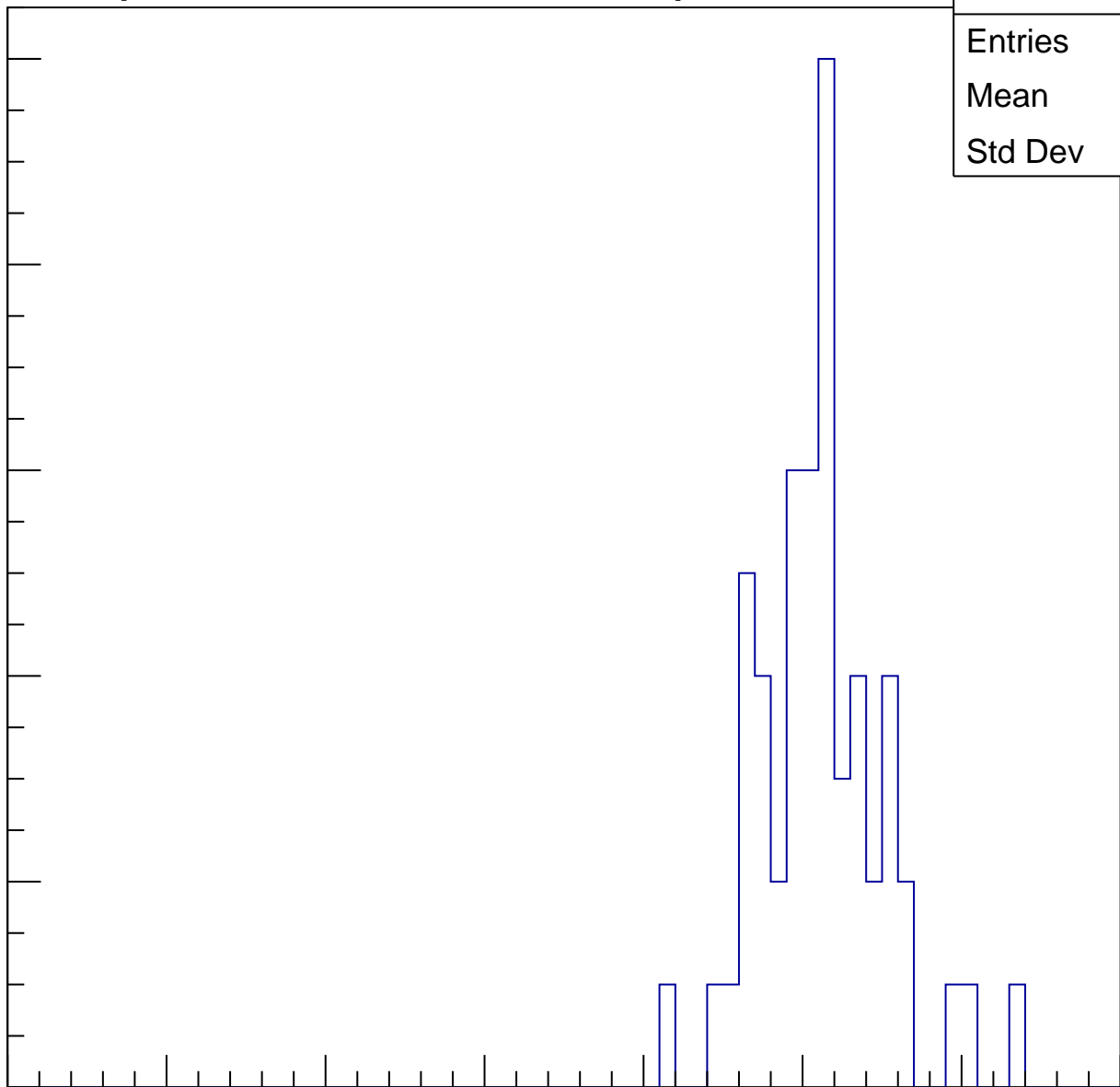
30

40

50

60

ampl



B0L001S, U23-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	56.62
Std Dev	3.221

Entry

10

8

6

4

2

0

0

10

20

30

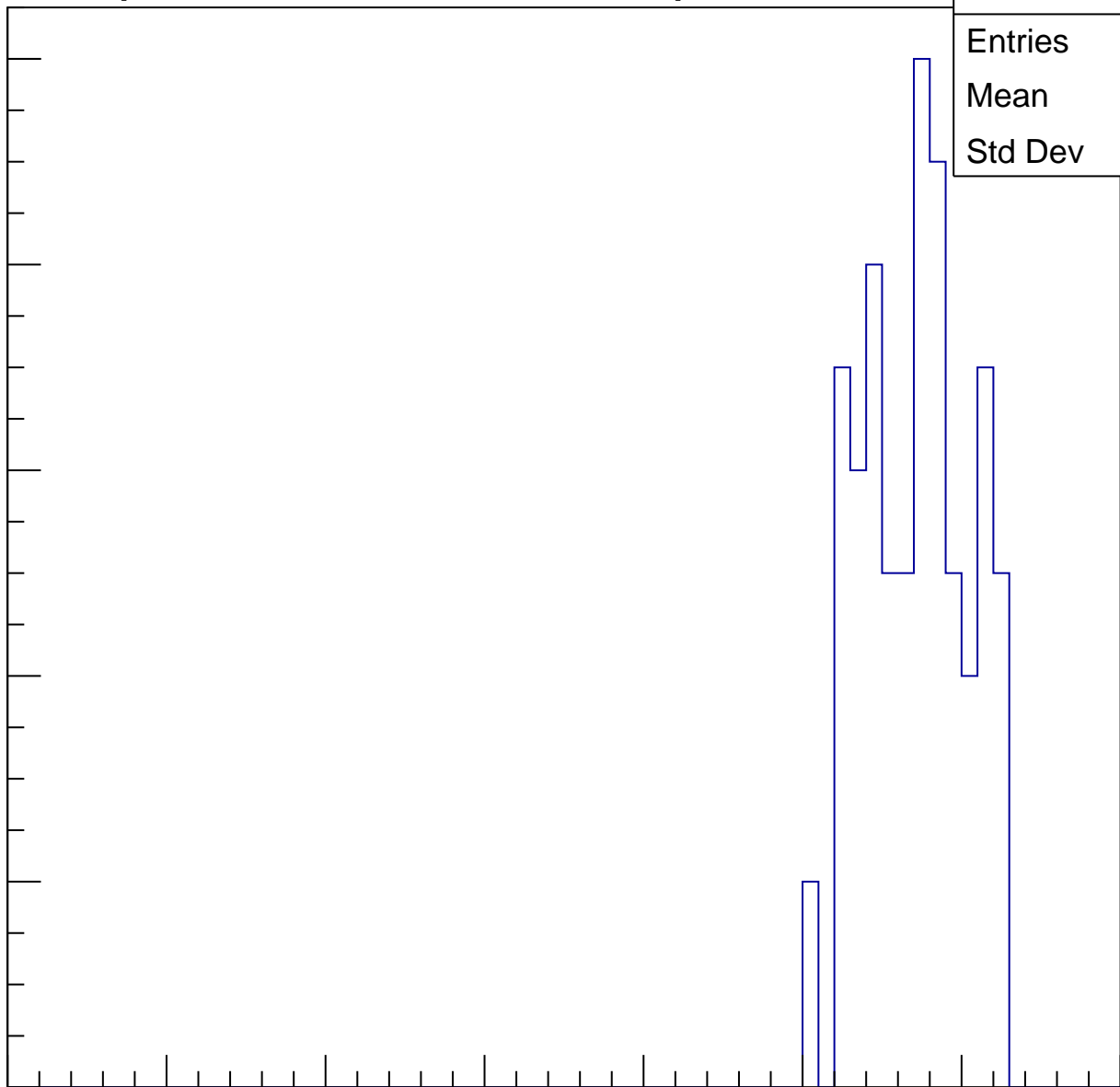
40

50

60

70

ampl

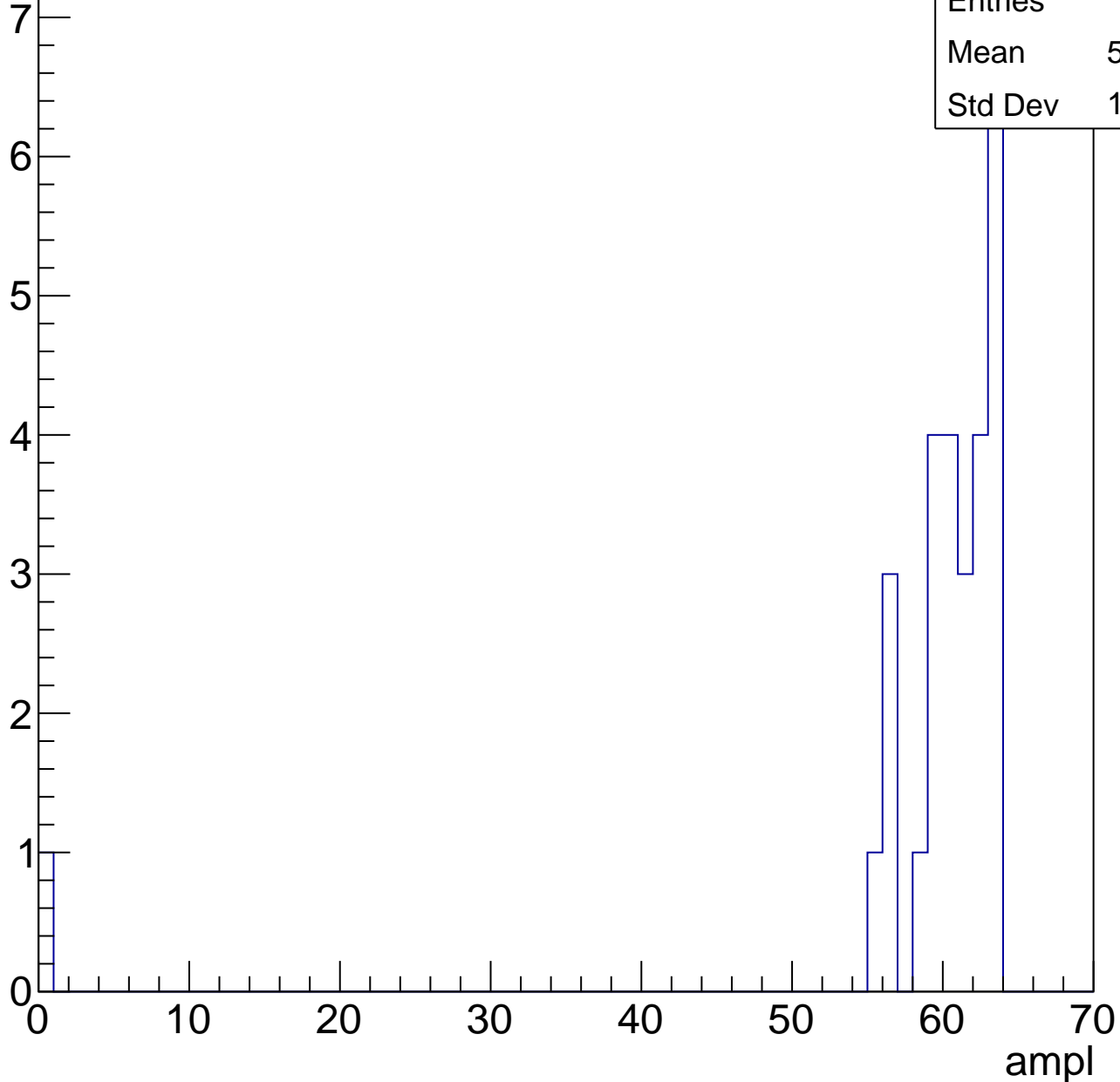


B0L001S, U23-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.18
Std Dev	11.45



B0L001S, U23-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch77, adc0

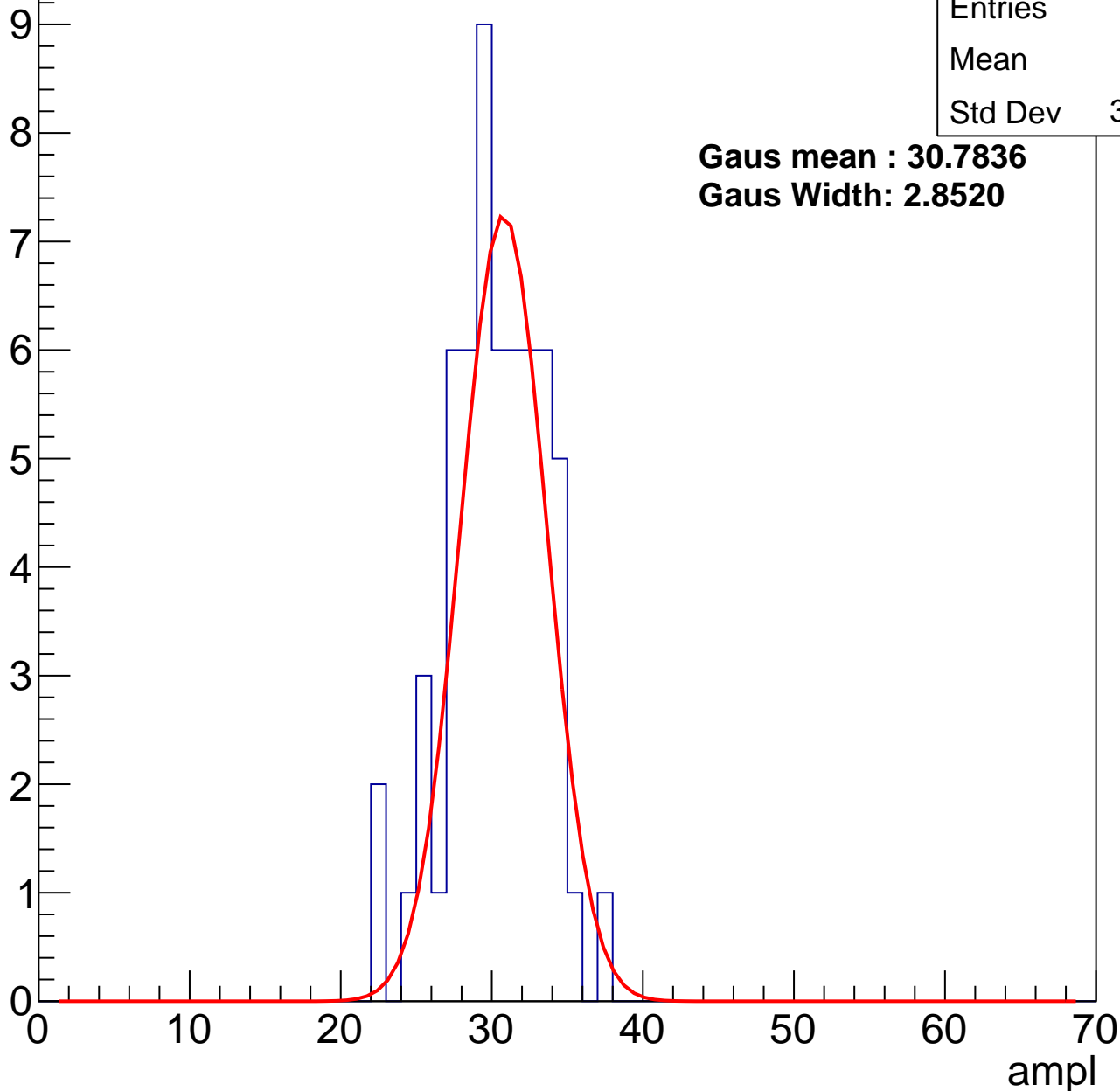
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	29.8
Std Dev	3.129

Gaus mean : 30.7836

Gaus Width: 2.8520

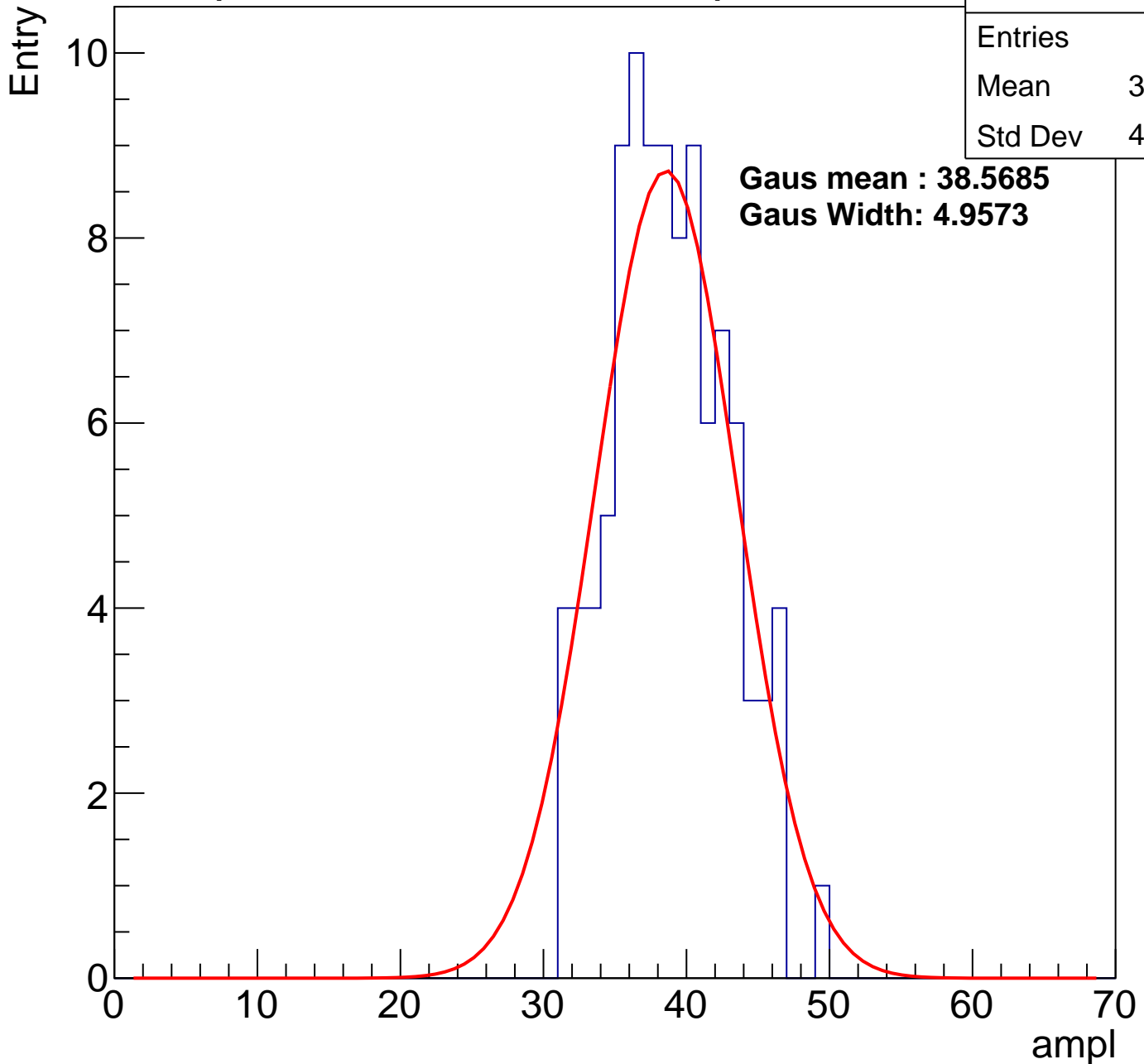


B0L001S, U23-ch77, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	101
Mean	38.36
Std Dev	4.016

Gaus mean : 38.5685
Gaus Width: 4.9573



B0L001S, U23-ch77, adc2

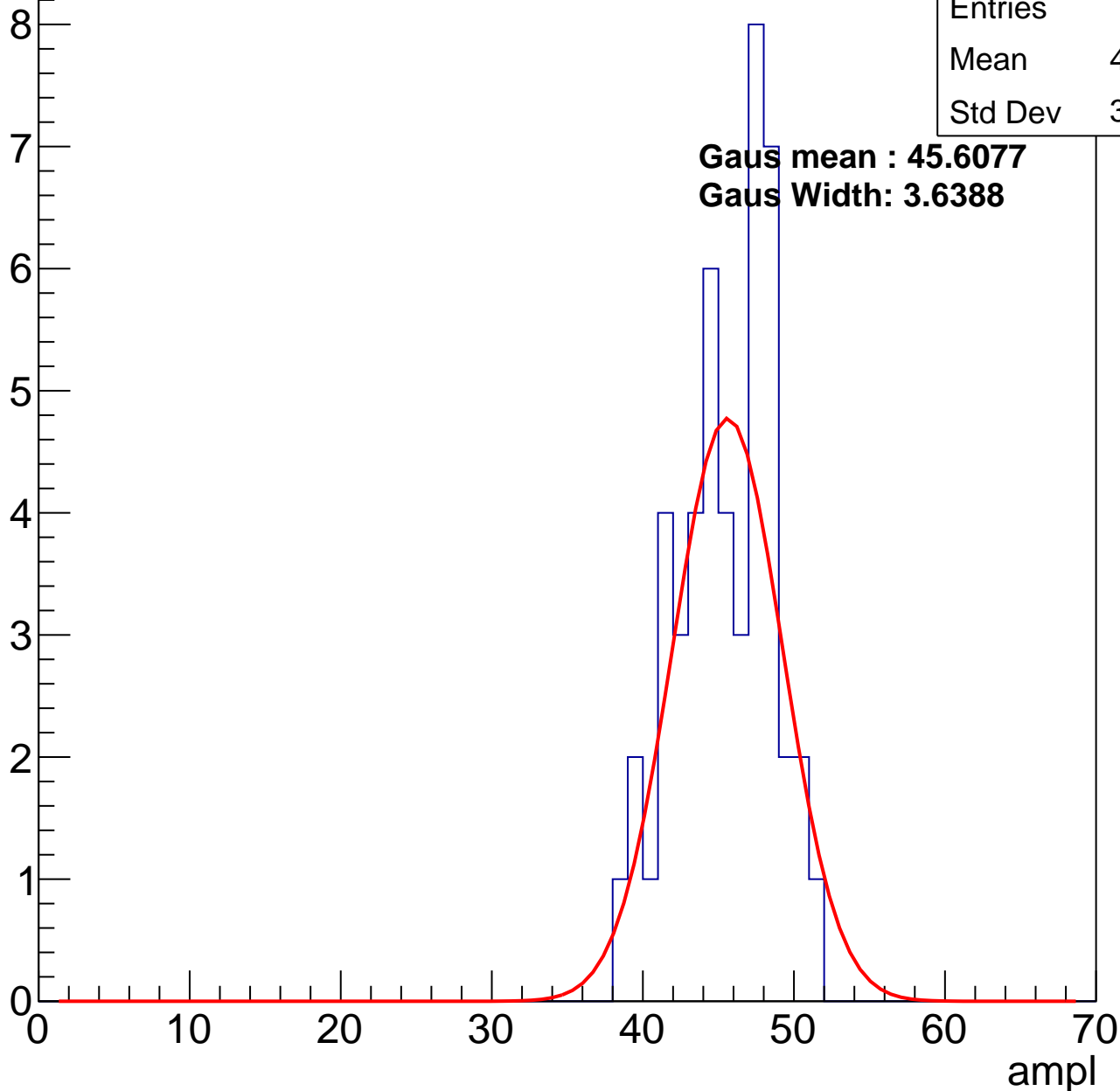
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	45.02
Std Dev	3.146

Gaus mean : 45.6077

Gaus Width: 3.6388

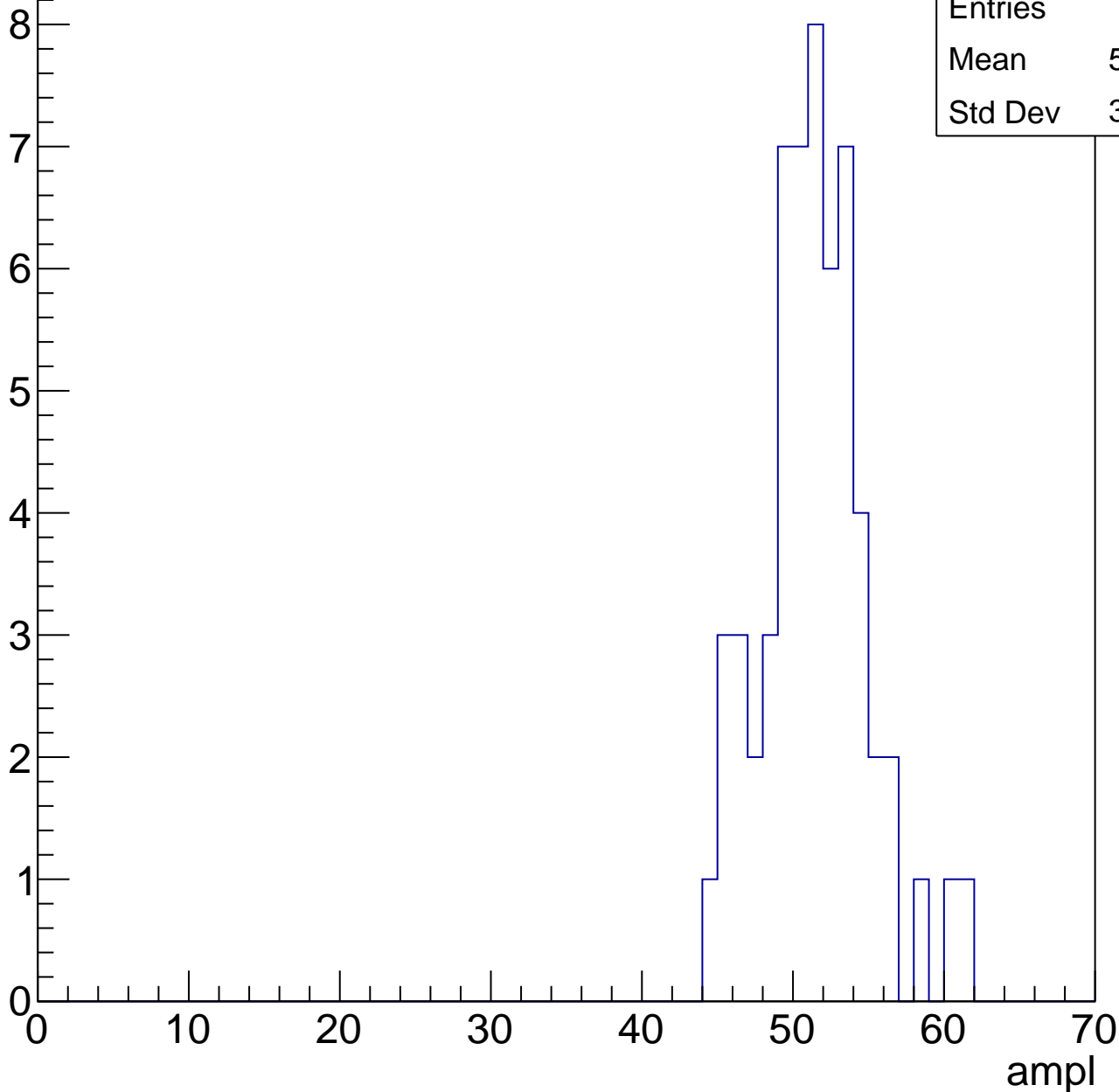


B0L001S, U23-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.97
Std Dev	3.494

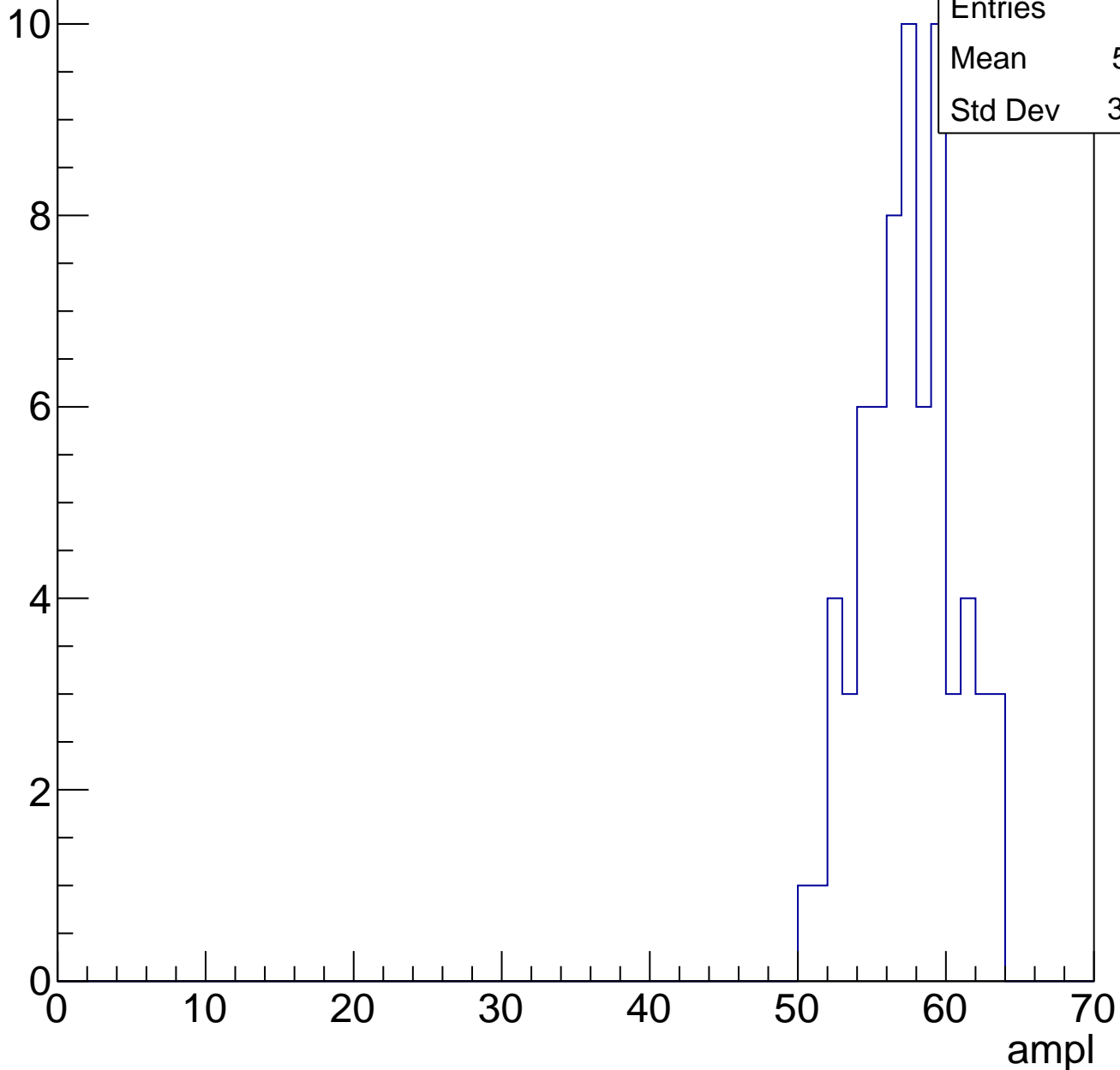


B0L001S, U23-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	57.01
Std Dev	3.065

Entry

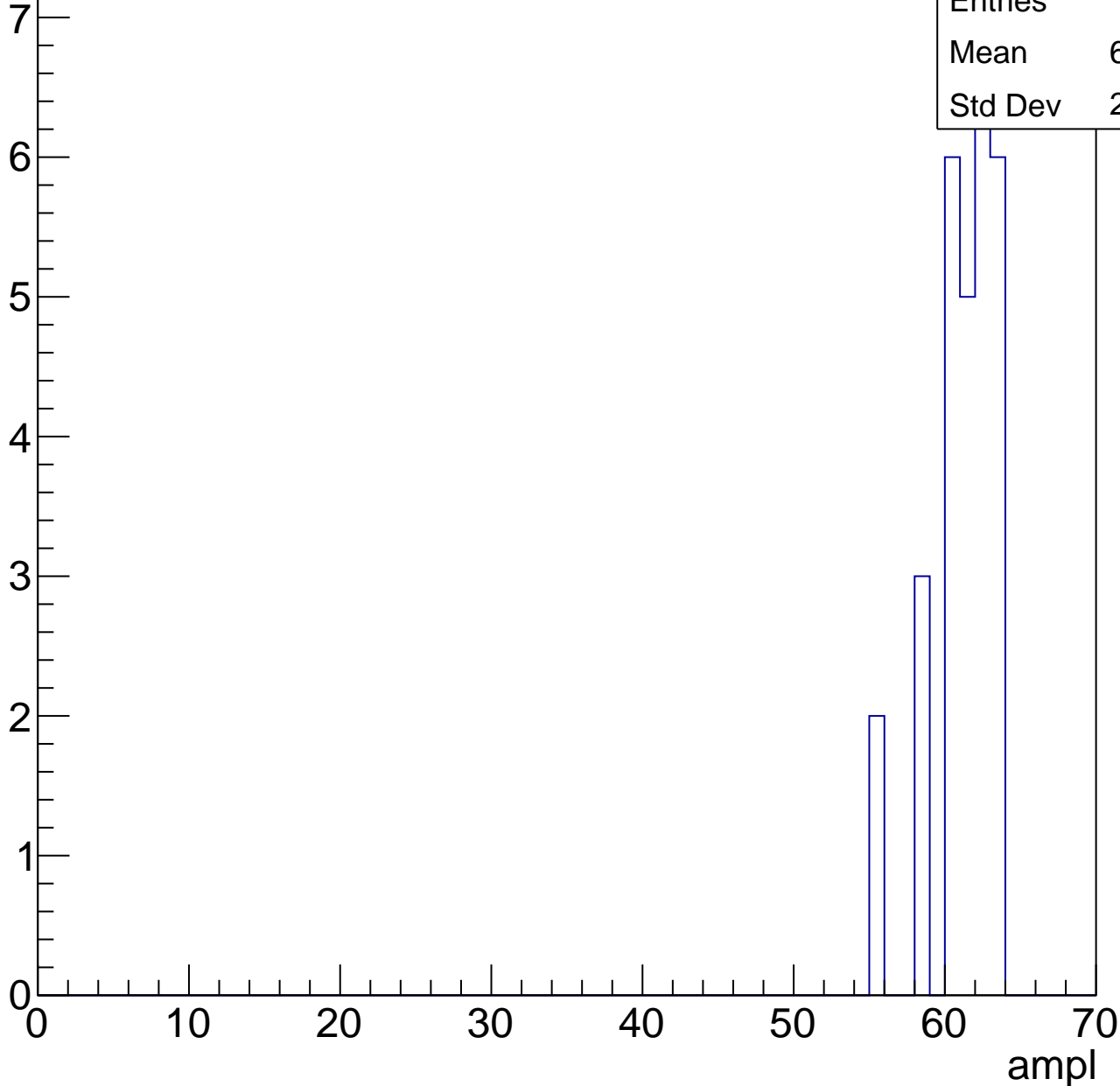


B0L001S, U23-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	60.72
Std Dev	2.148



B0L001S, U23-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U23-ch78, adc0

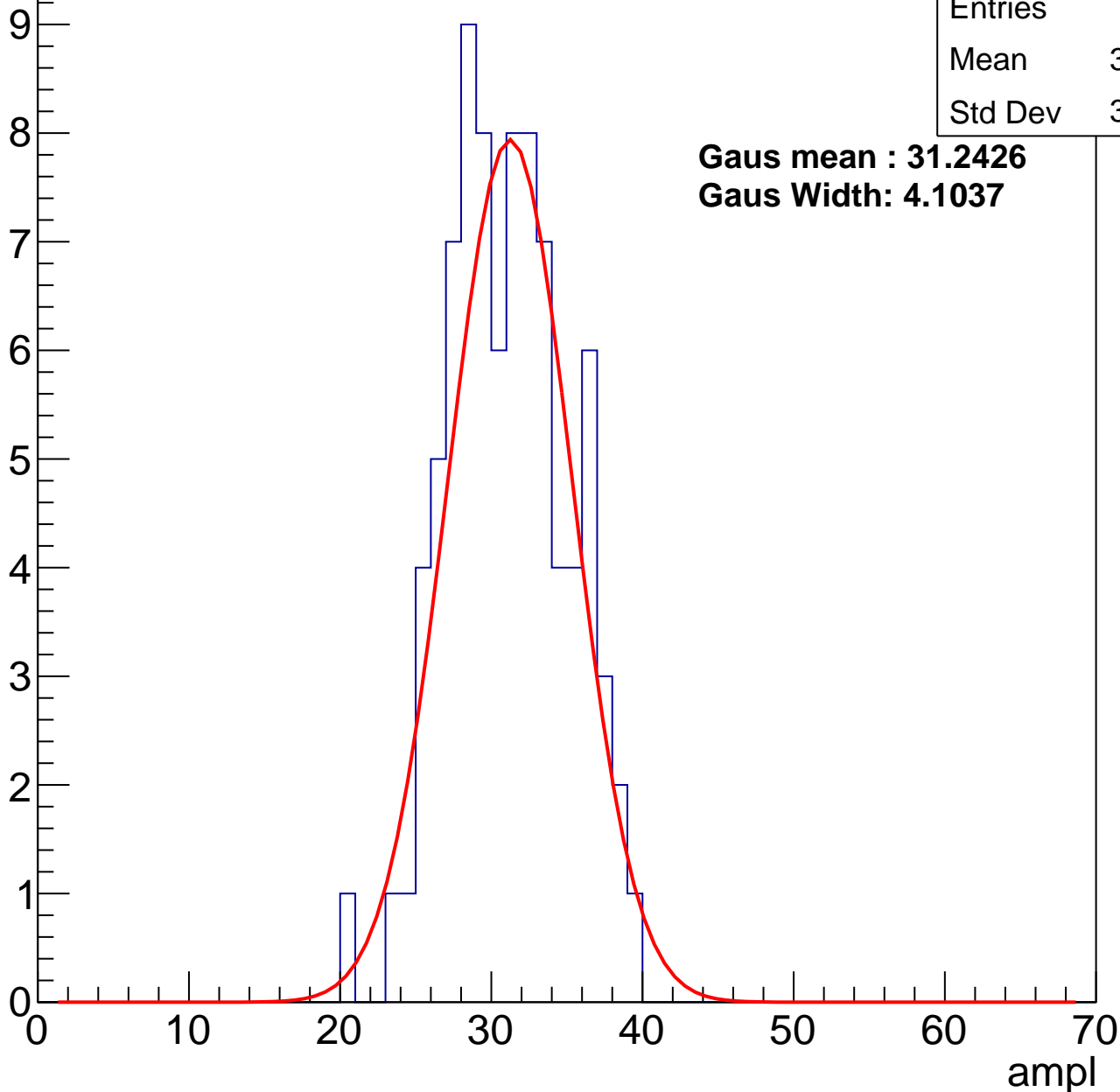
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	30.62
Std Dev	3.877

Gaus mean : 31.2426

Gaus Width: 4.1037



B0L001S, U23-ch78, adc1

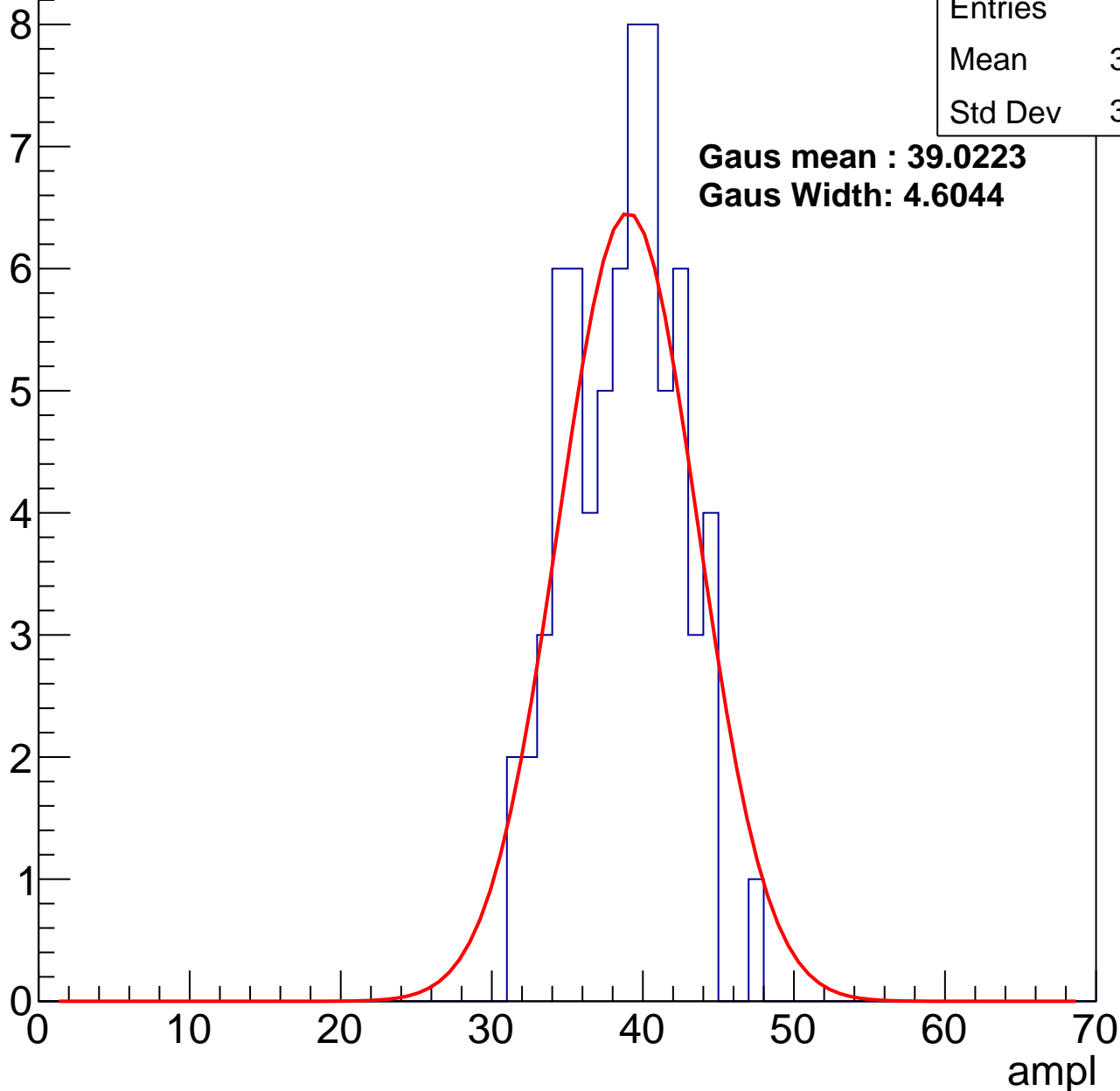
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	38.22
Std Dev	3.599

Gaus mean : 39.0223

Gaus Width: 4.6044



B0L001S, U23-ch78, adc2

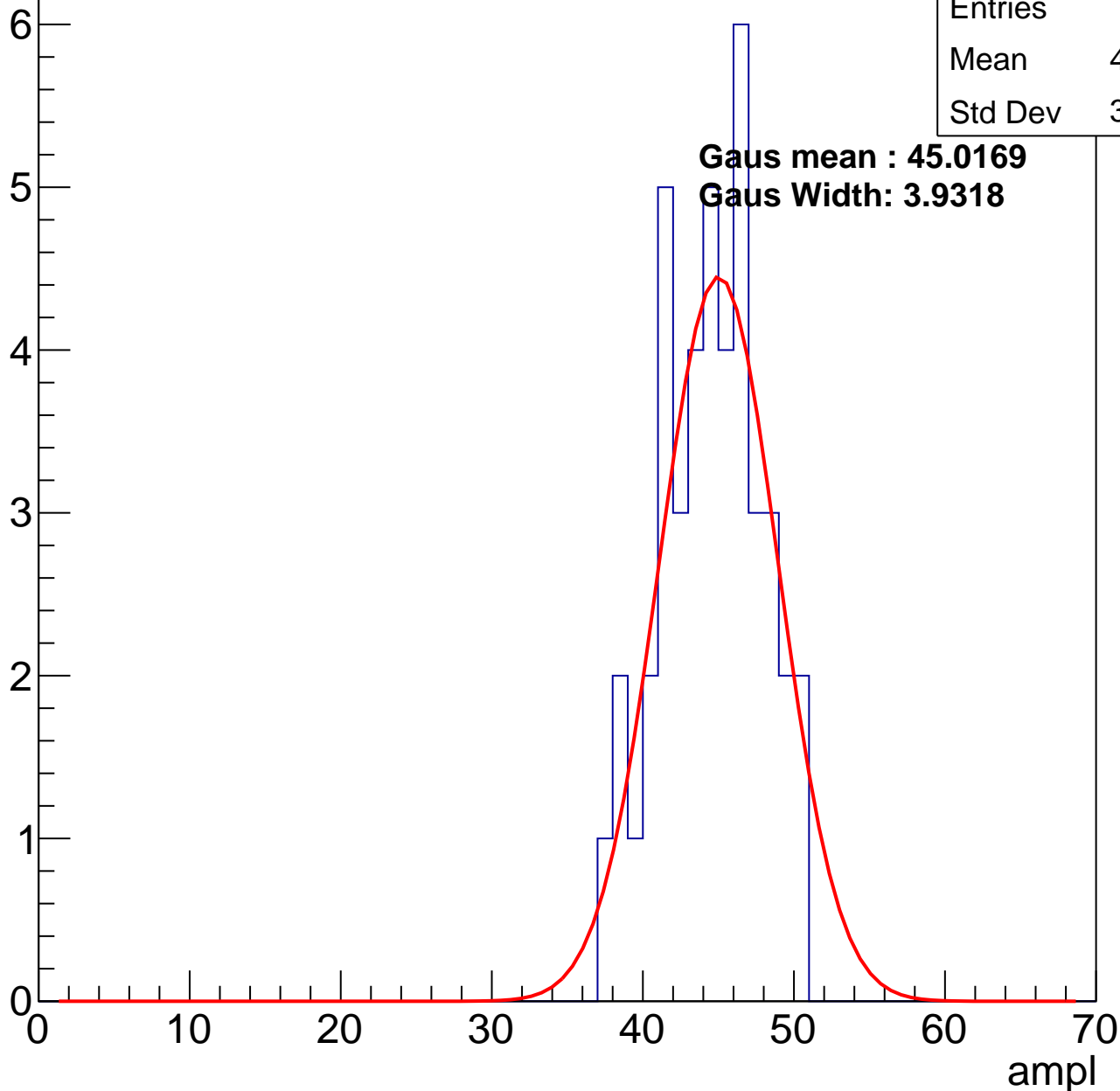
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	44.05
Std Dev	3.285

Gaus mean : 45.0169

Gaus Width: 3.9318

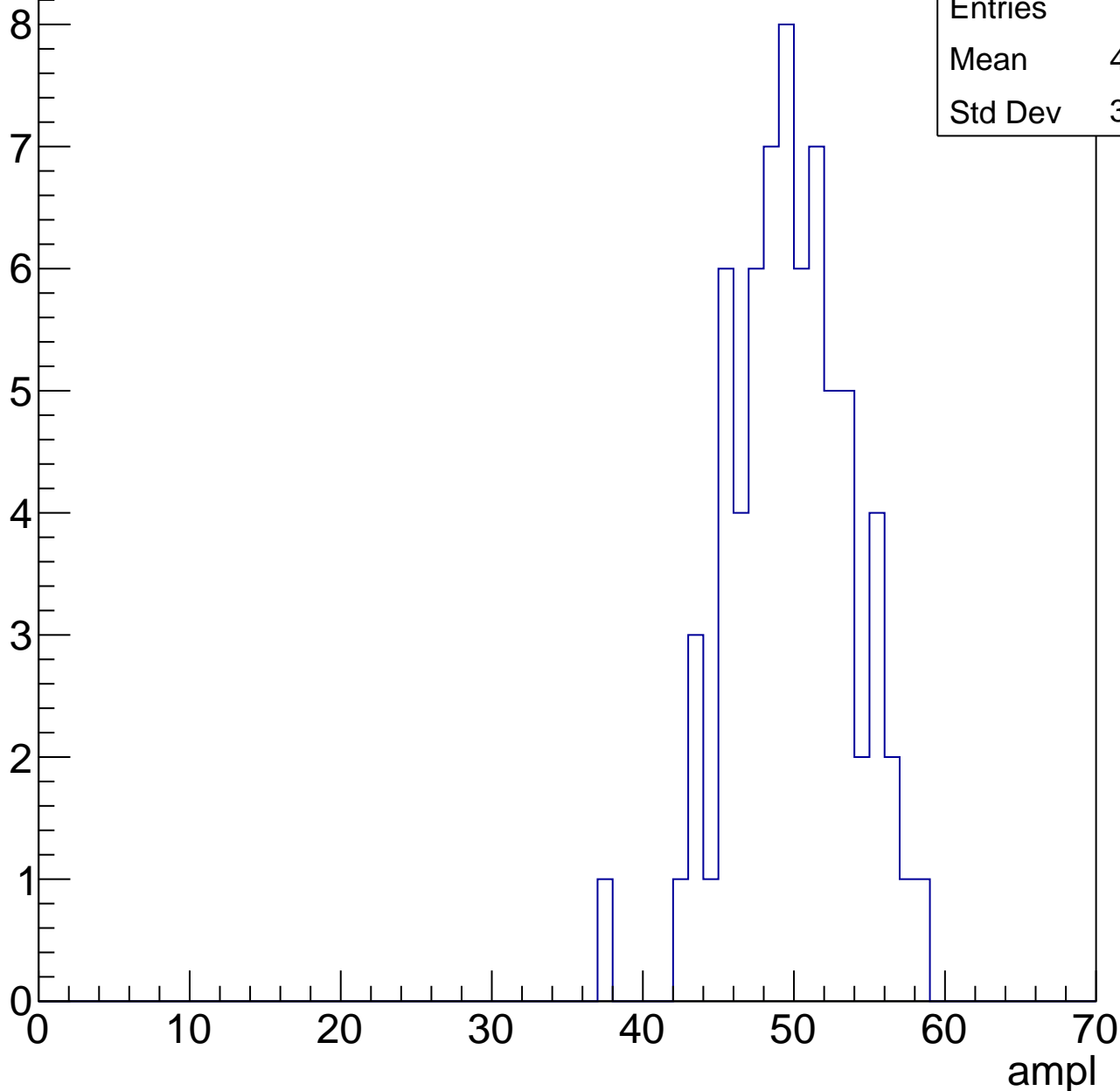


B0L001S, U23-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.33
Std Dev	3.923

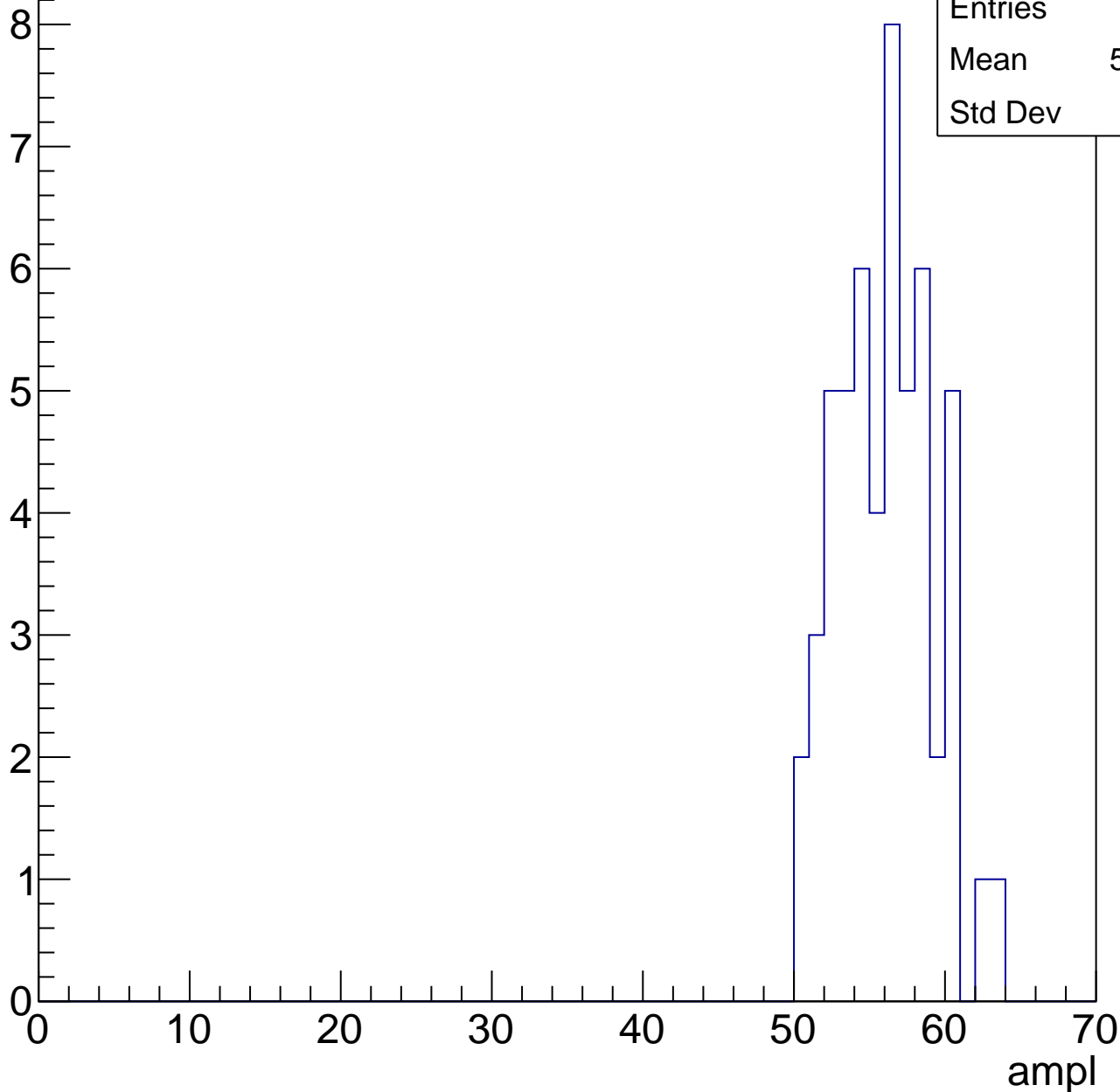


B0L001S, U23-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.58
Std Dev	3.08

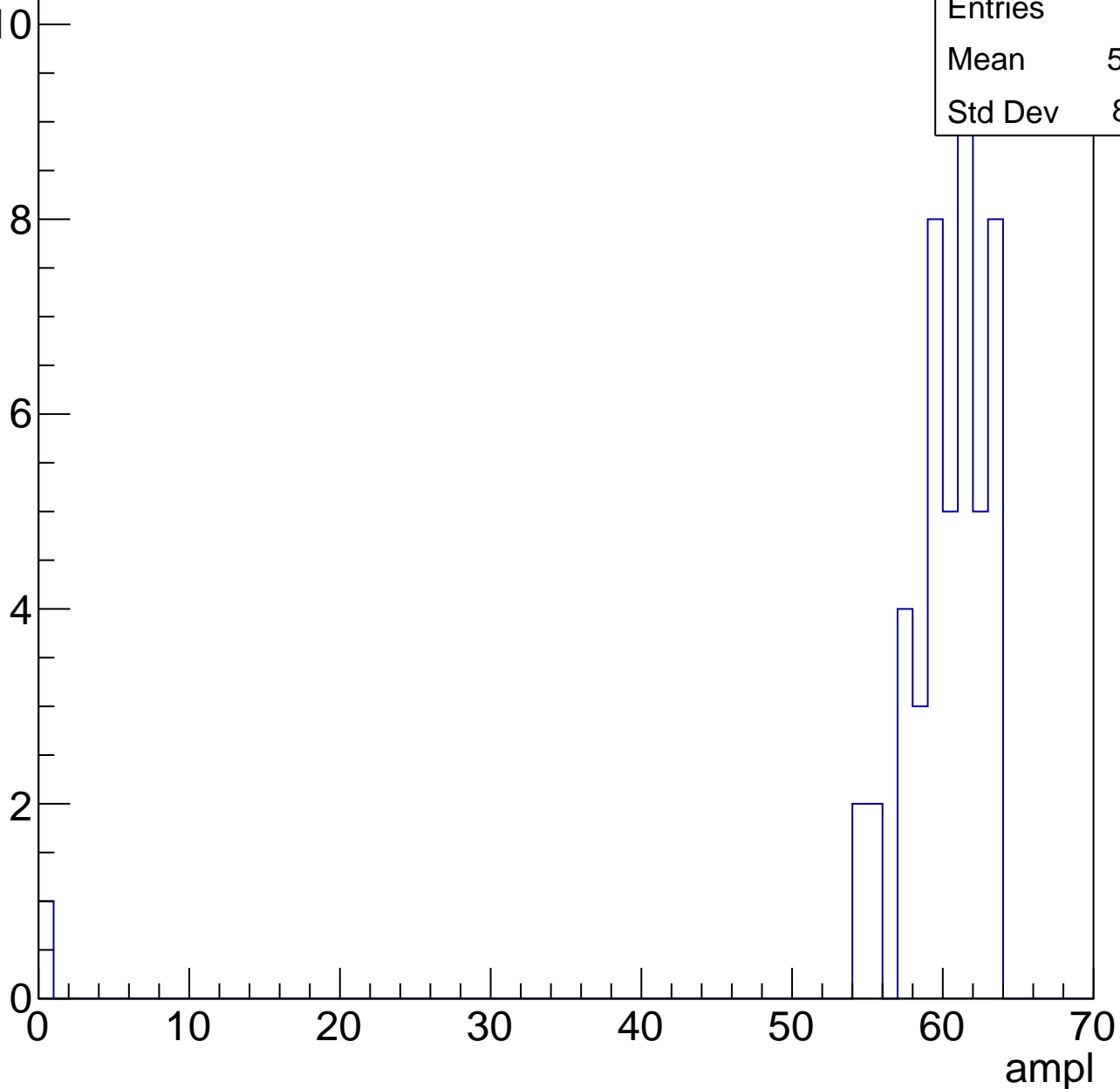


B0L001S, U23-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

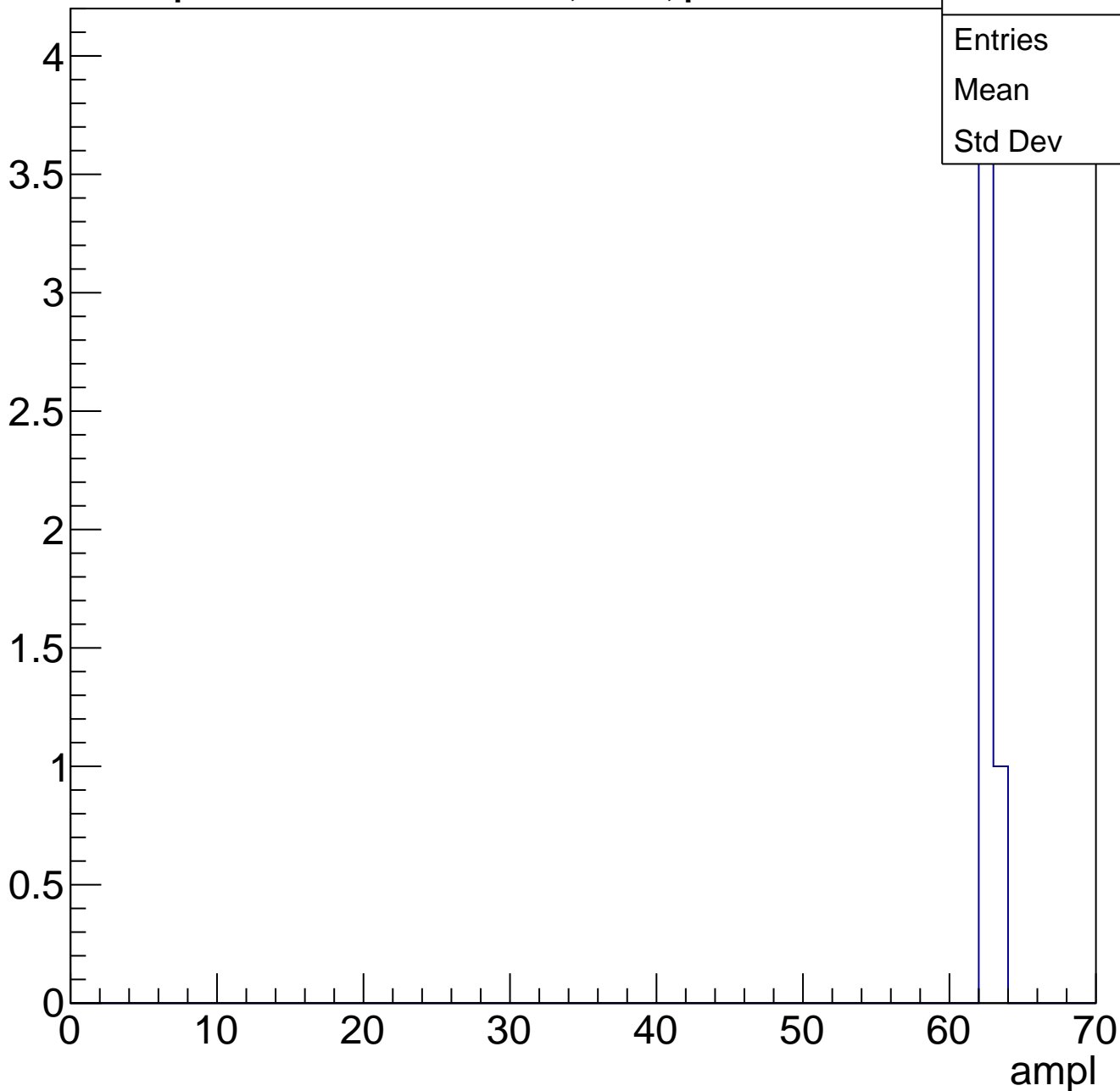
Entries	48
Mean	58.67
Std Dev	8.891



B0L001S, U23-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch79, adc0

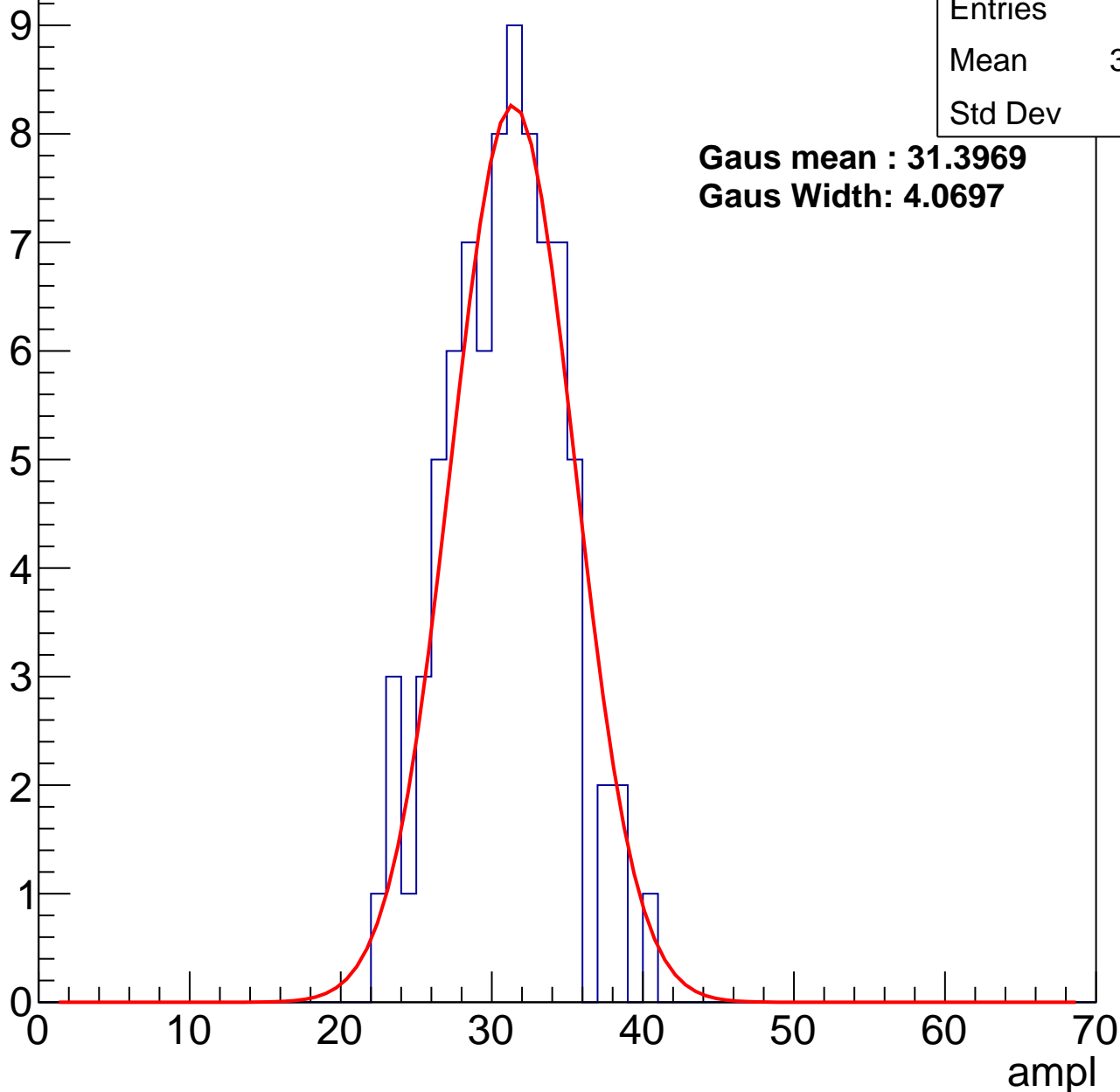
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.38
Std Dev	3.76

Gaus mean : 31.3969

Gaus Width: 4.0697



B0L001S, U23-ch79, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	37.05
Std Dev	3.251

Gaus mean : 37.6400

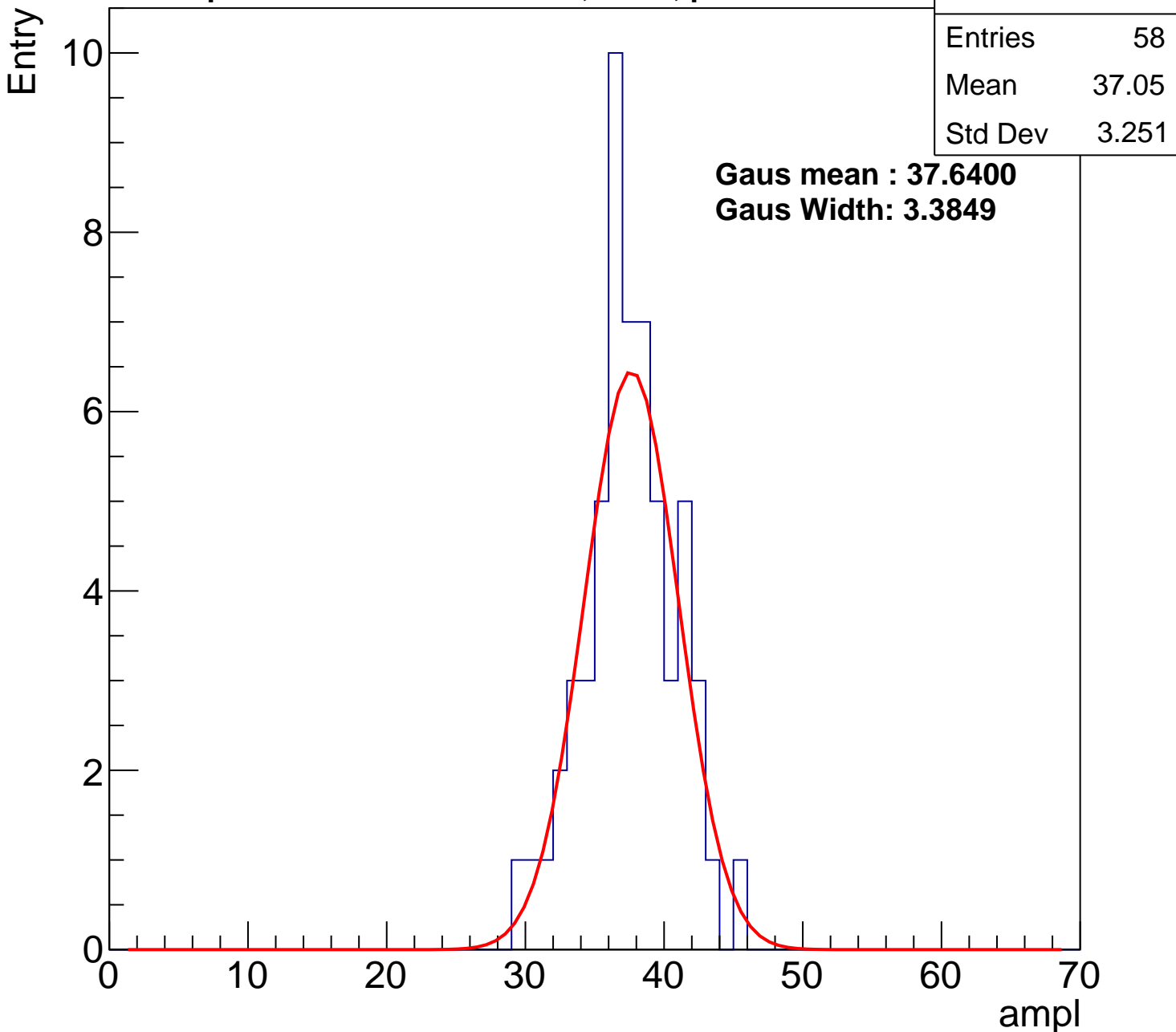
Gaus Width: 3.3849

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch79, adc2

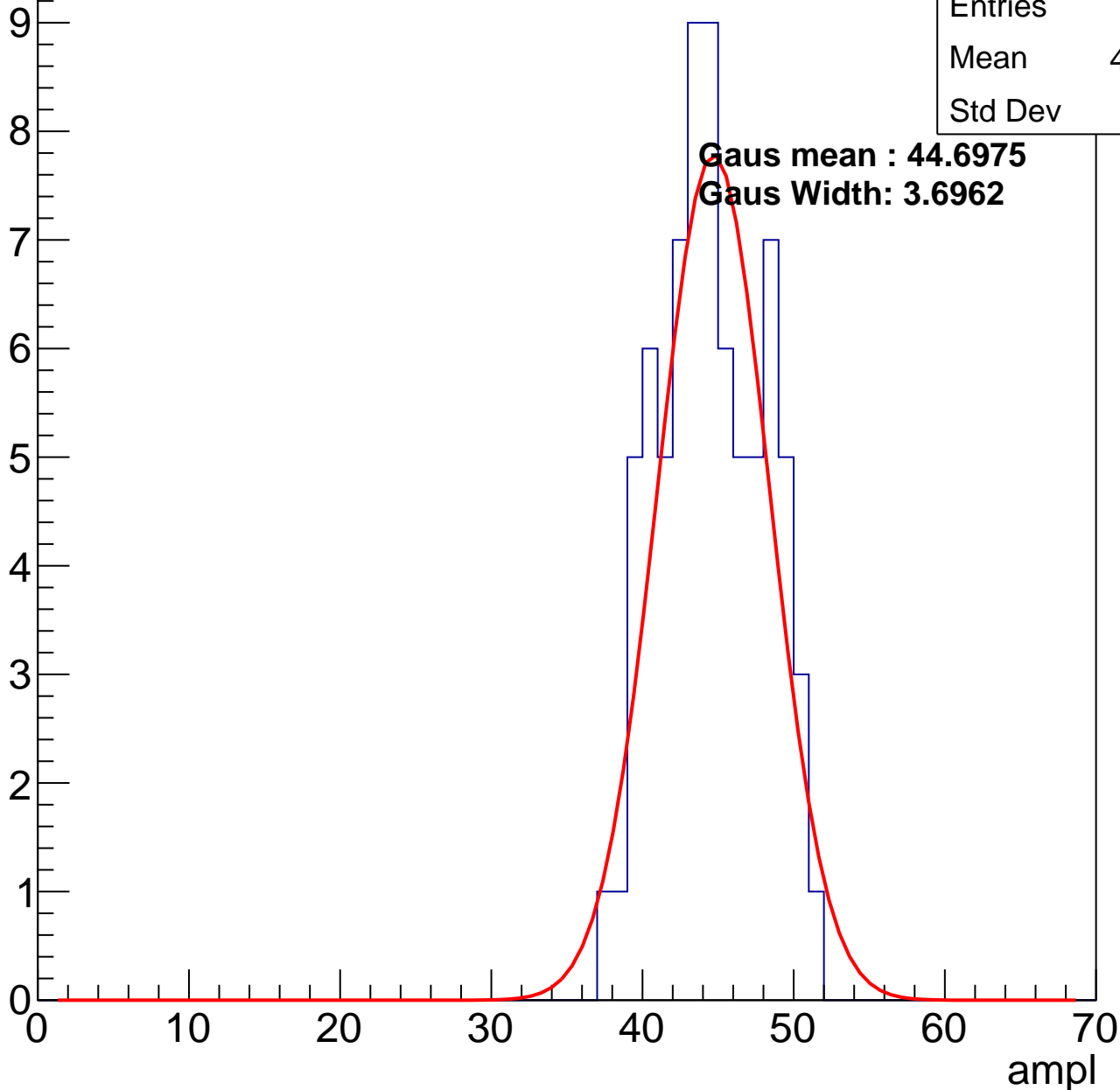
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	44.12
Std Dev	3.37

Gaus mean : 44.6975

Gaus Width: 3.6962

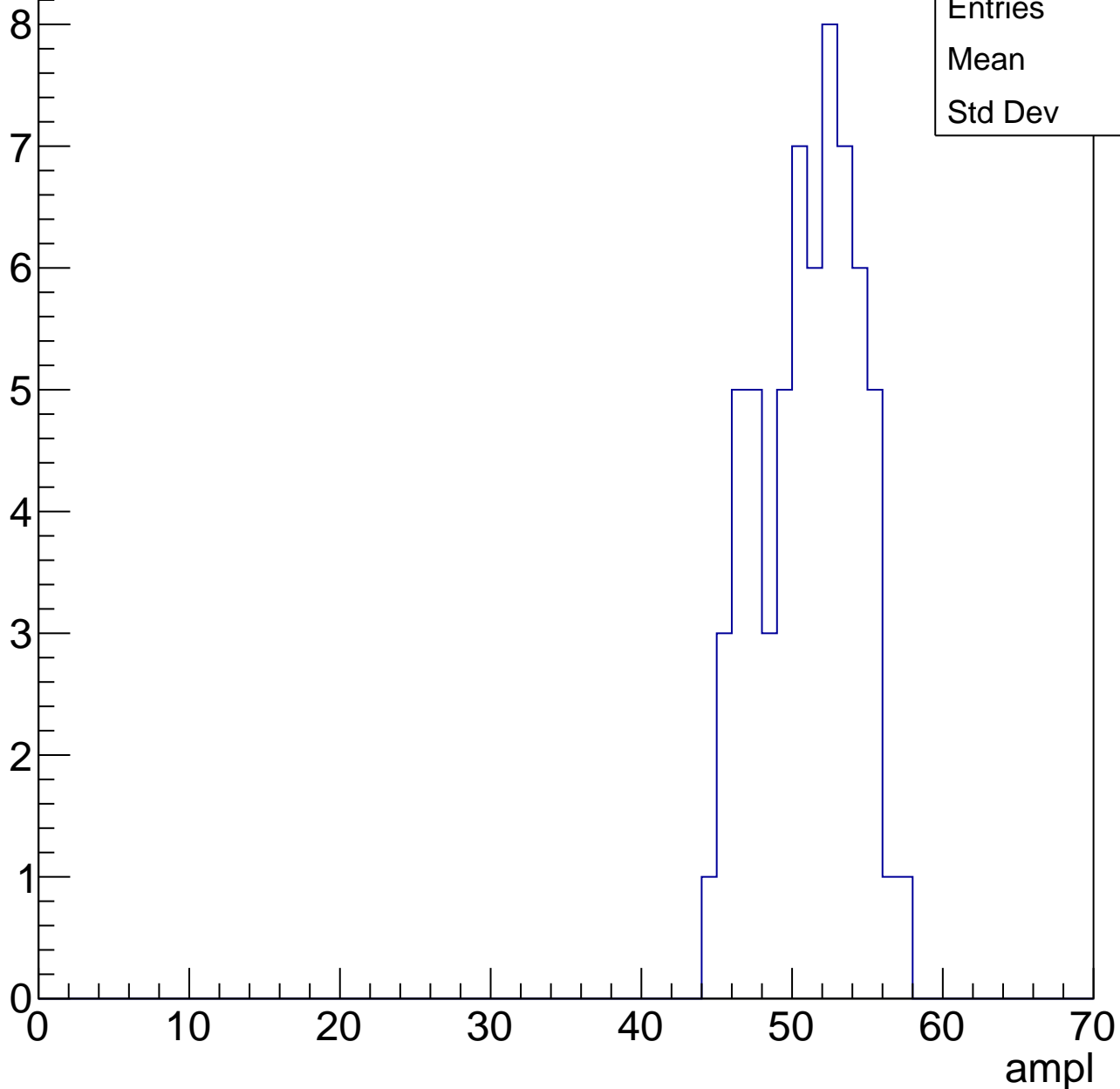


B0L001S, U23-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.6
Std Dev	3.18

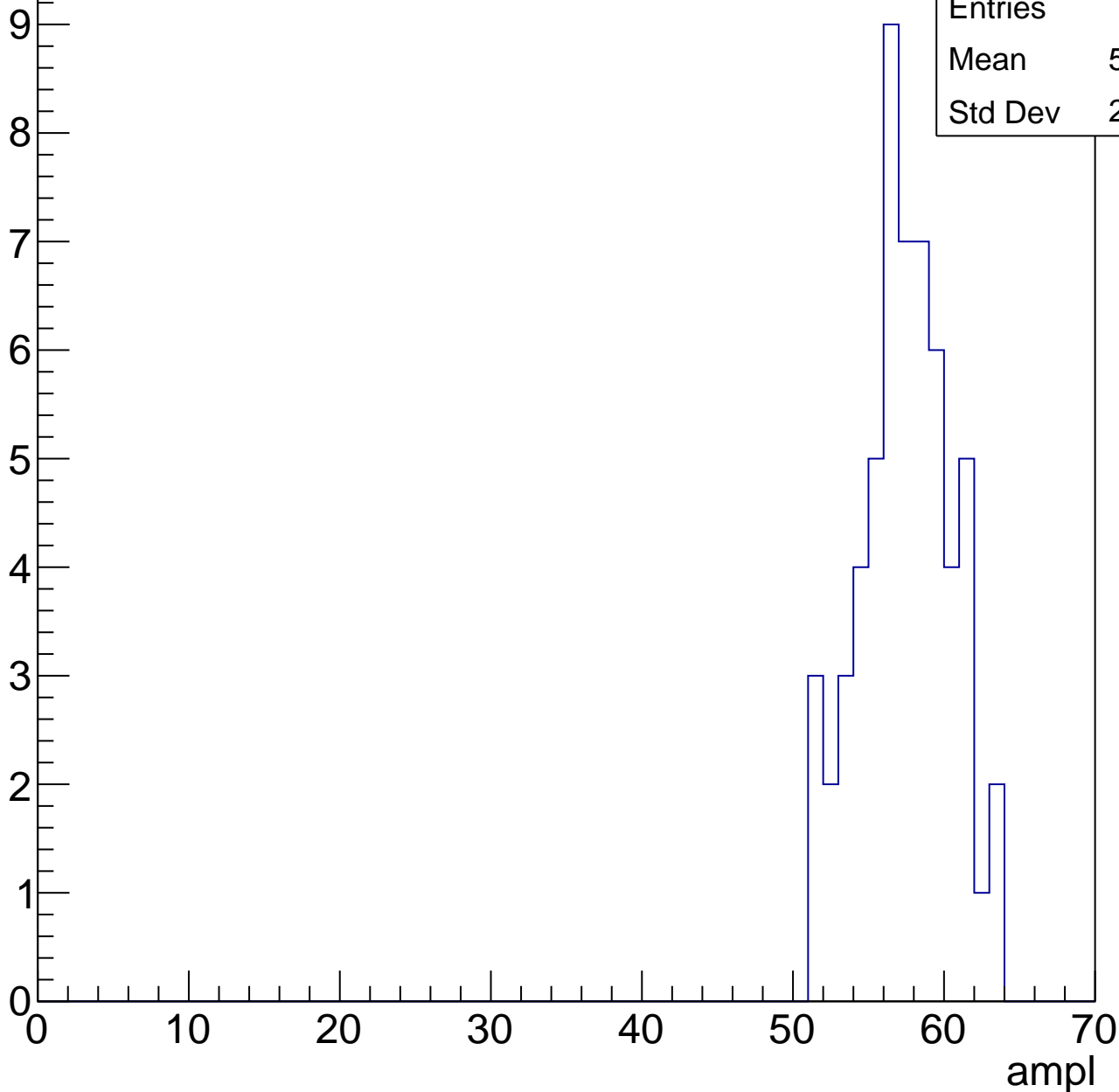


B0L001S, U23-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.95
Std Dev	2.979

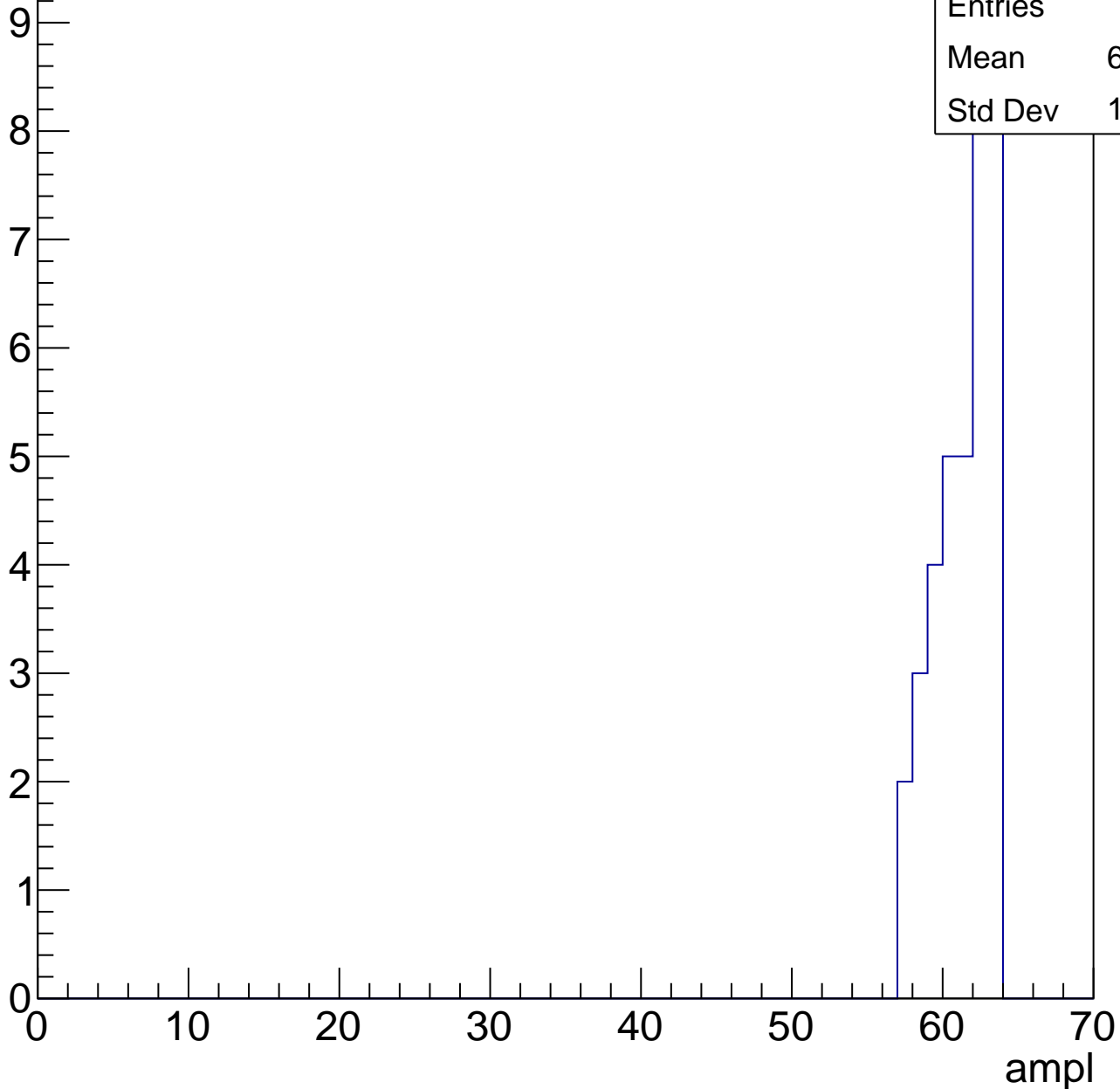


B0L001S, U23-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

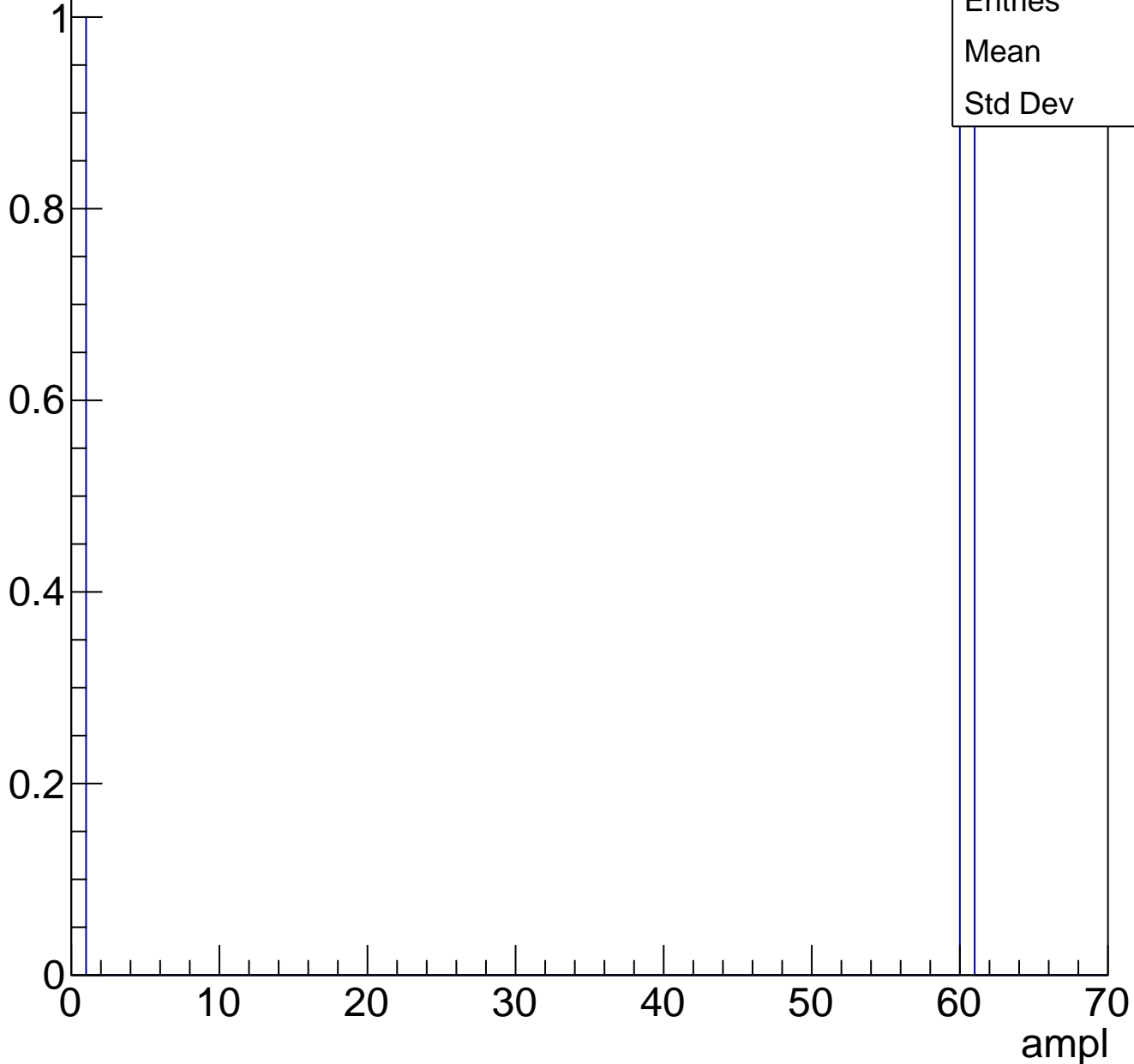
Entries	36
Mean	60.86
Std Dev	1.828



B0L001S, U23-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

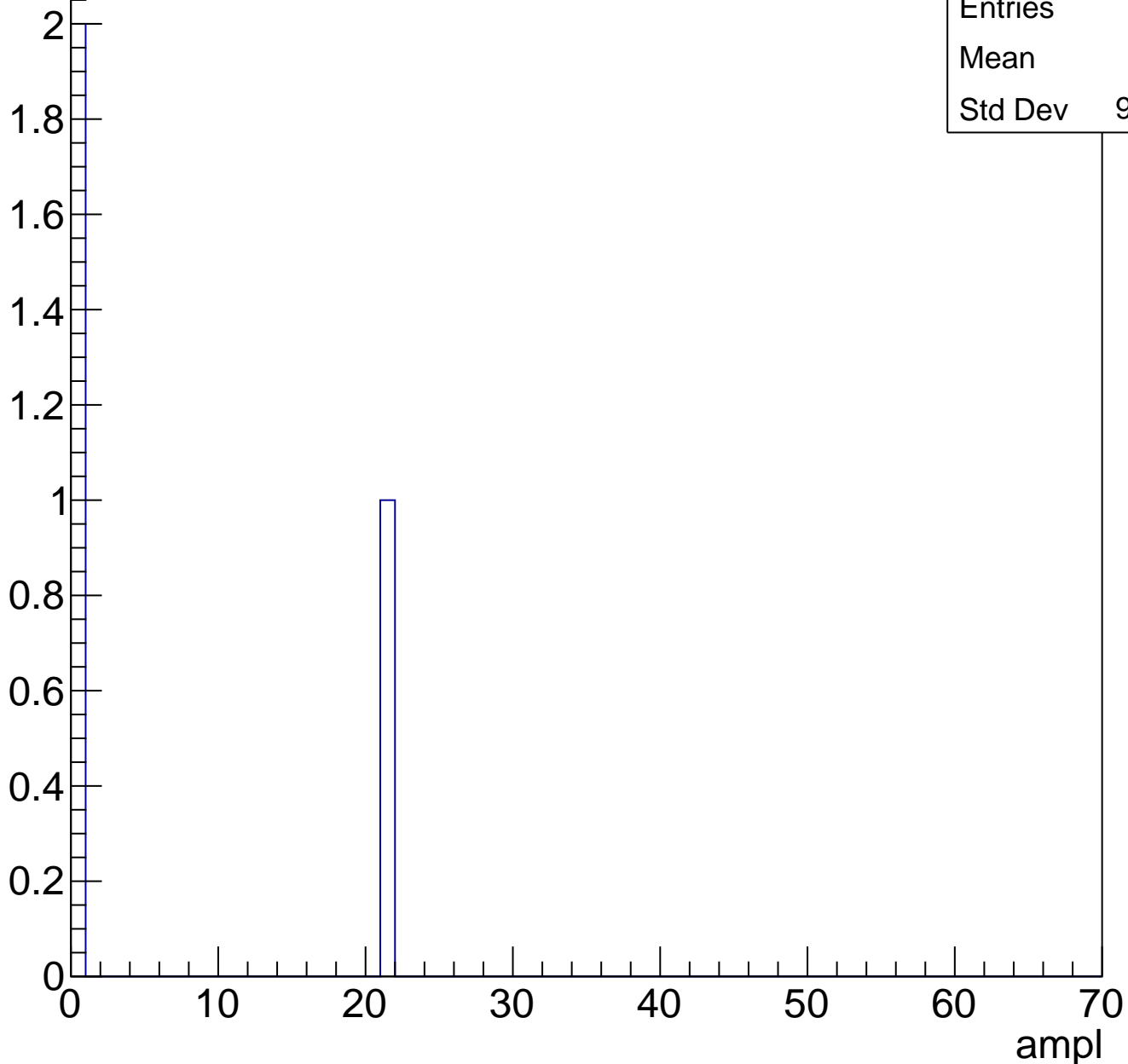
Entry



B0L001S, U23-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L001S, U23-ch80, adc0

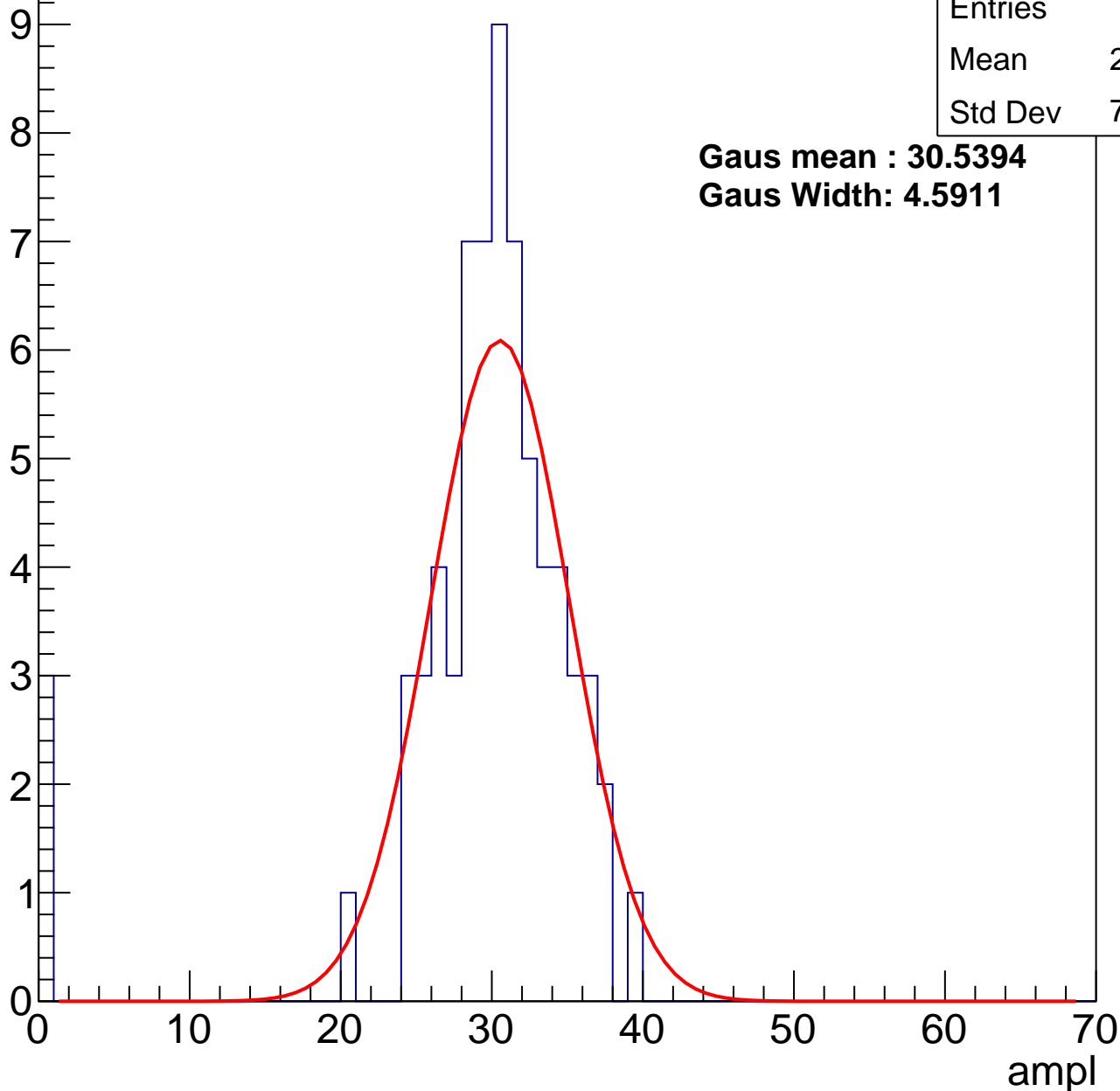
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	28.87
Std Dev	7.132

Gaus mean : 30.5394

Gaus Width: 4.5911



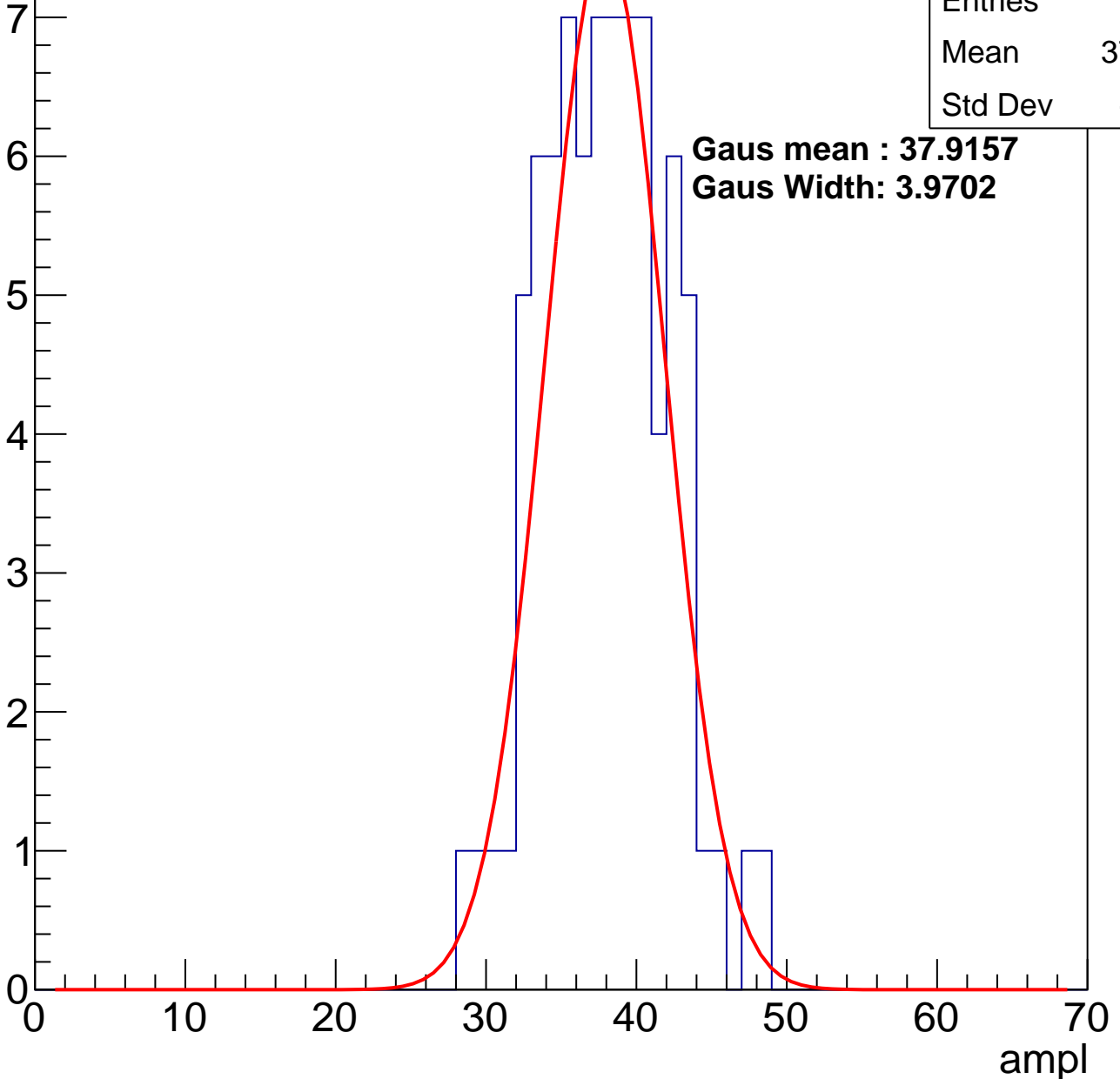
B0L001S, U23-ch80, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.46
Std Dev	4.08

Gaus mean : 37.9157
Gaus Width: 3.9702



B0L001S, U23-ch80, adc2

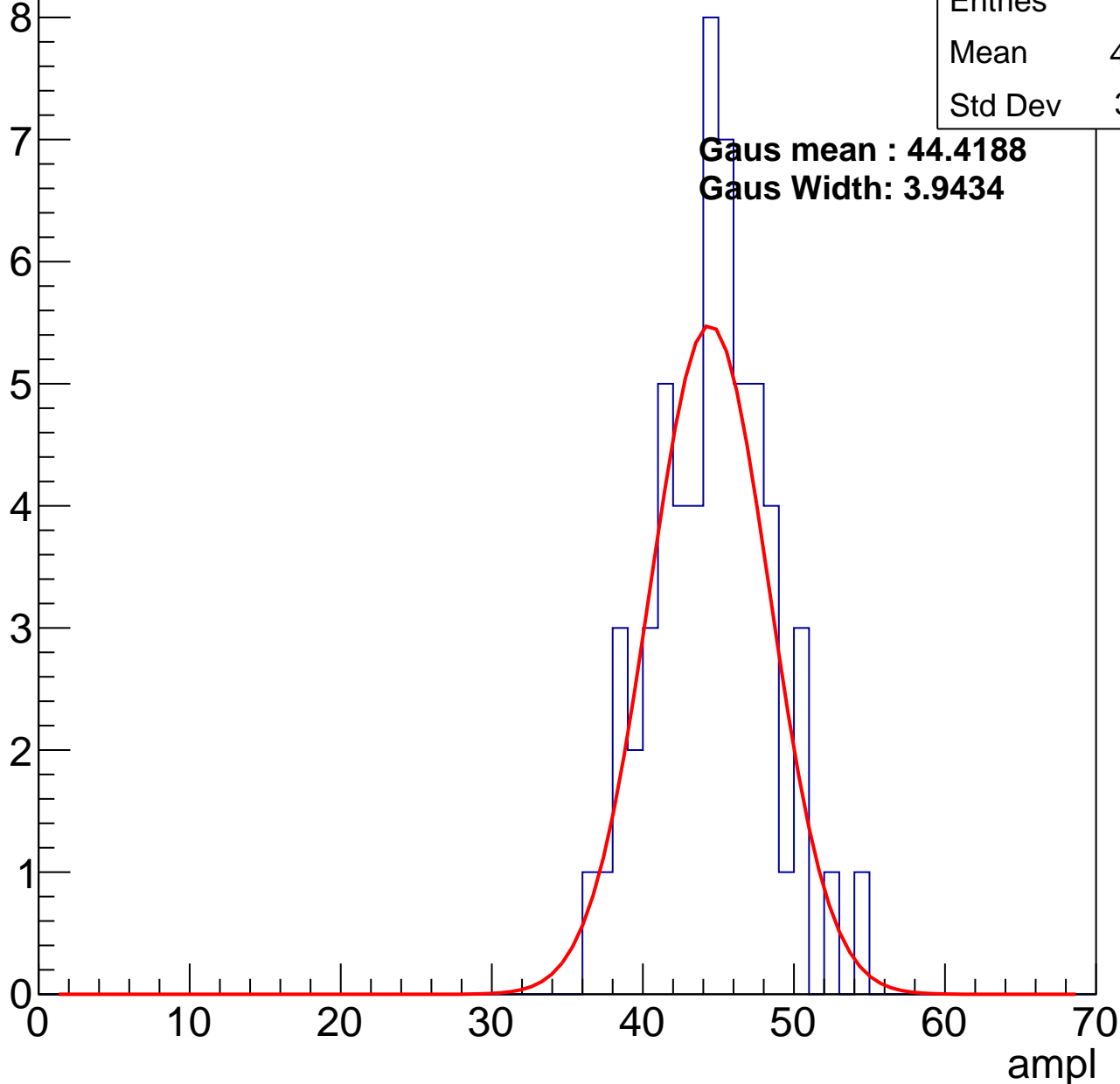
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.12
Std Dev	3.751

Gaus mean : 44.4188

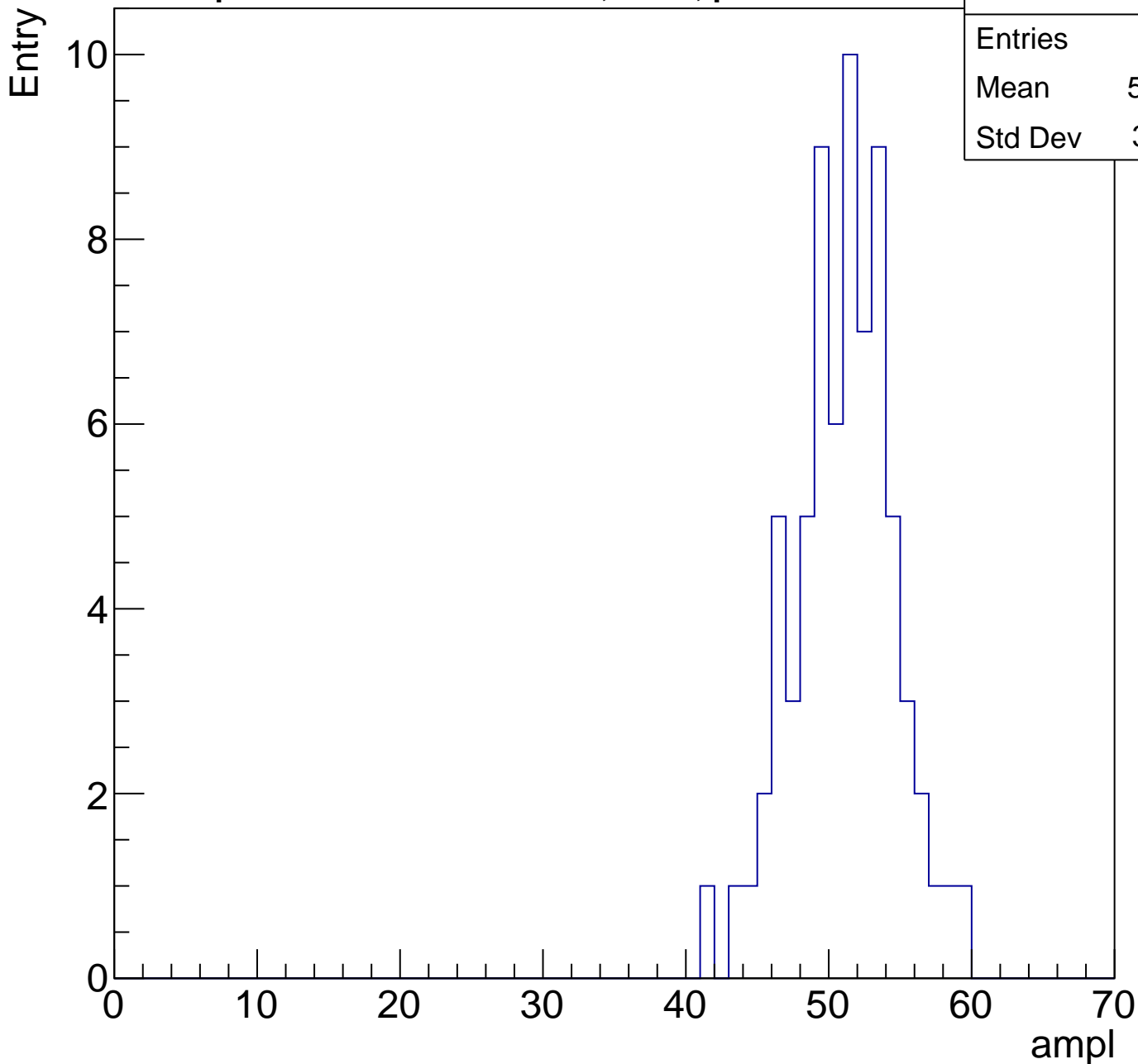
Gaus Width: 3.9434



B0L001S, U23-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	50.58
Std Dev	3.471

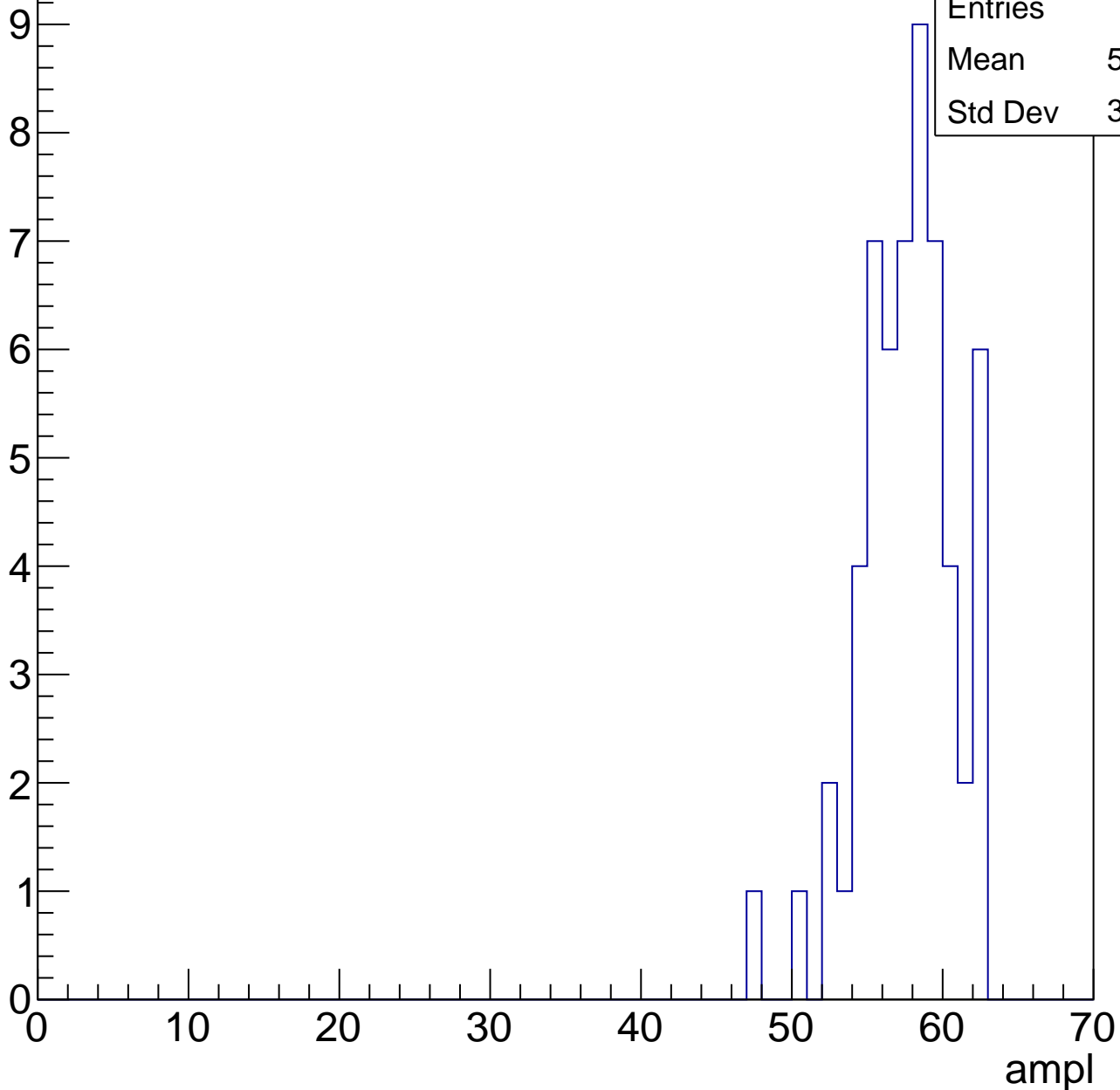


B0L001S, U23-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.18
Std Dev	3.073

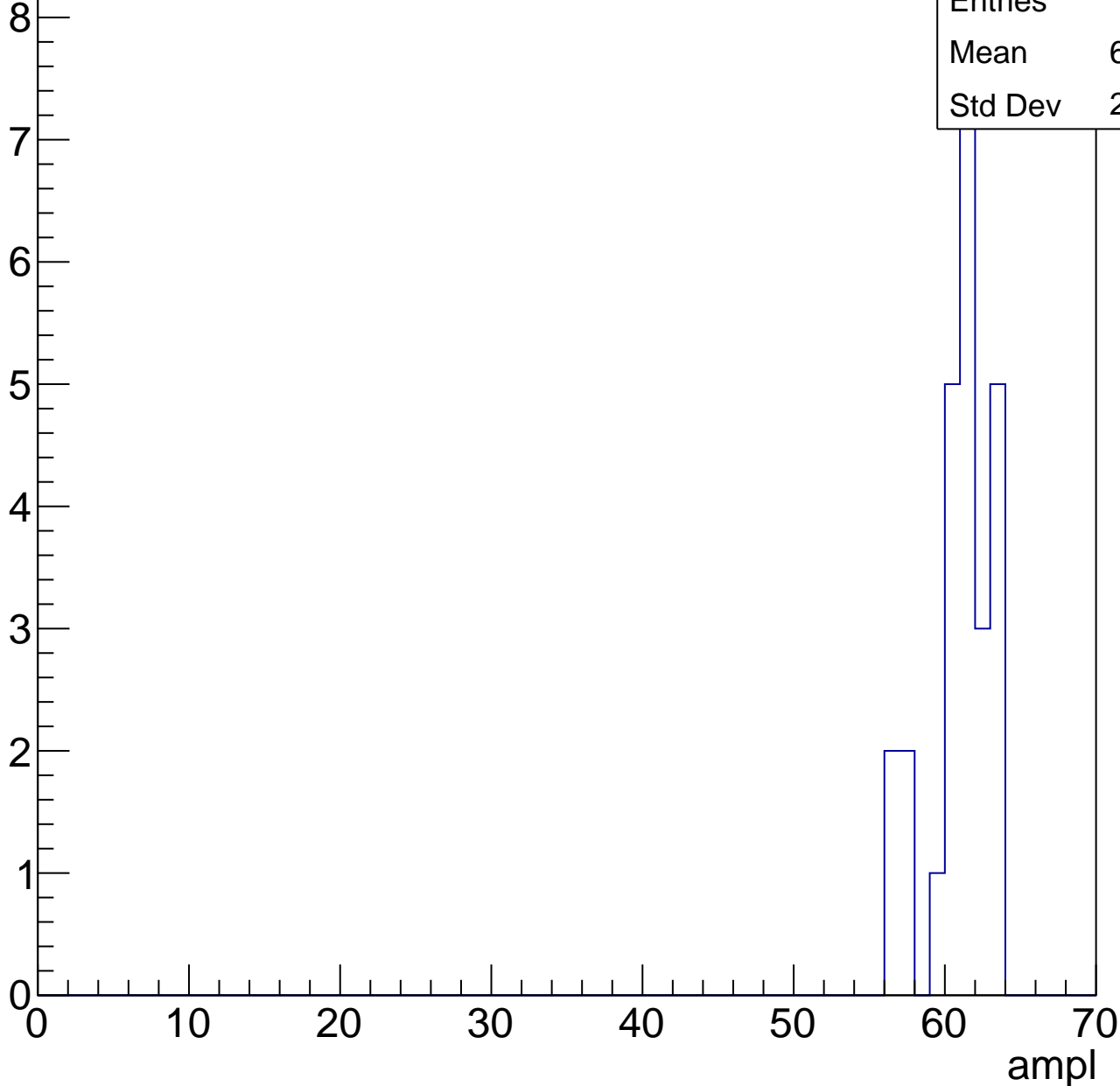


B0L001S, U23-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	60.54
Std Dev	2.042

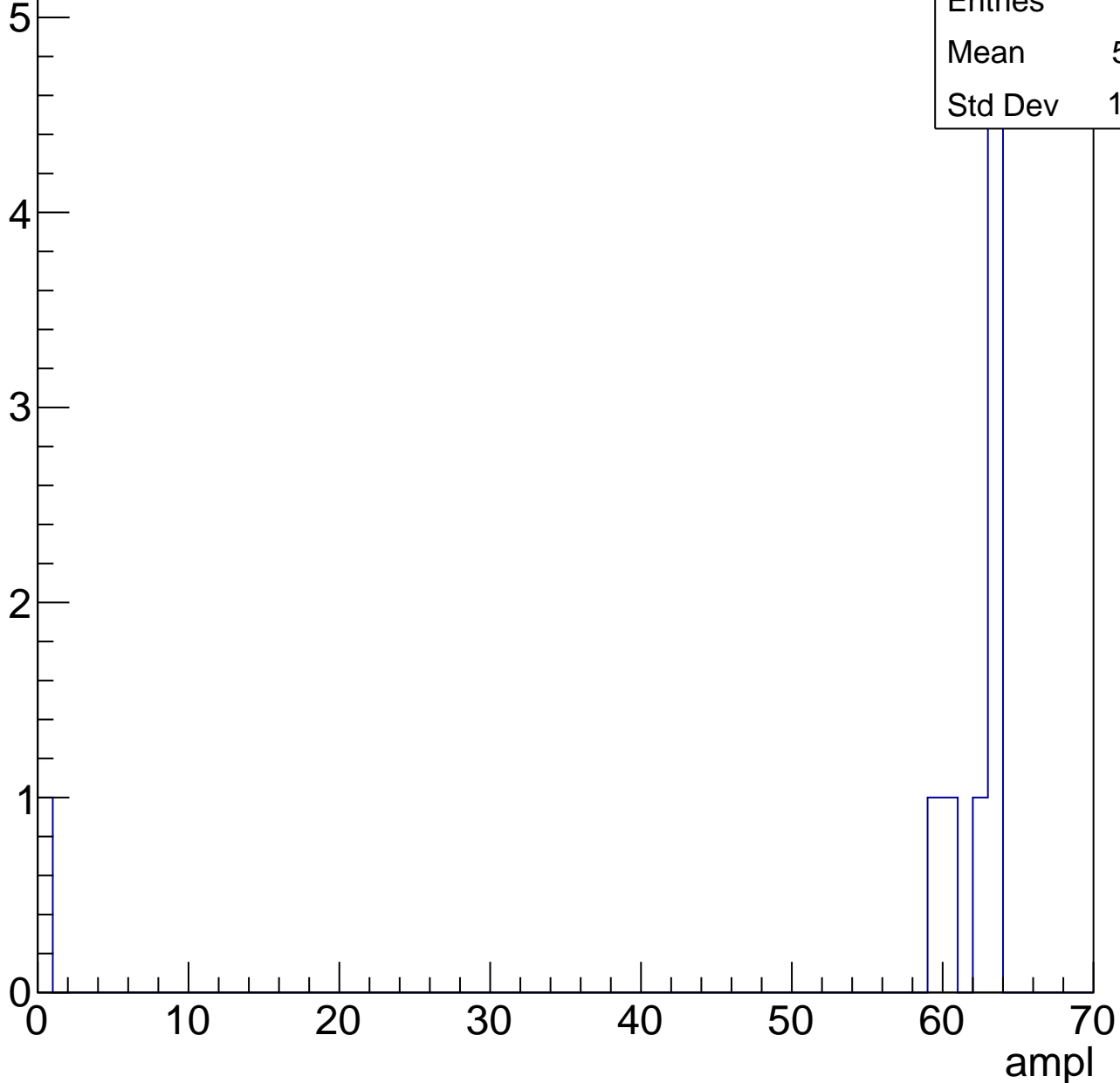


B0L001S, U23-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	55.11
Std Dev	19.54



B0L001S, U23-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch81, adc0

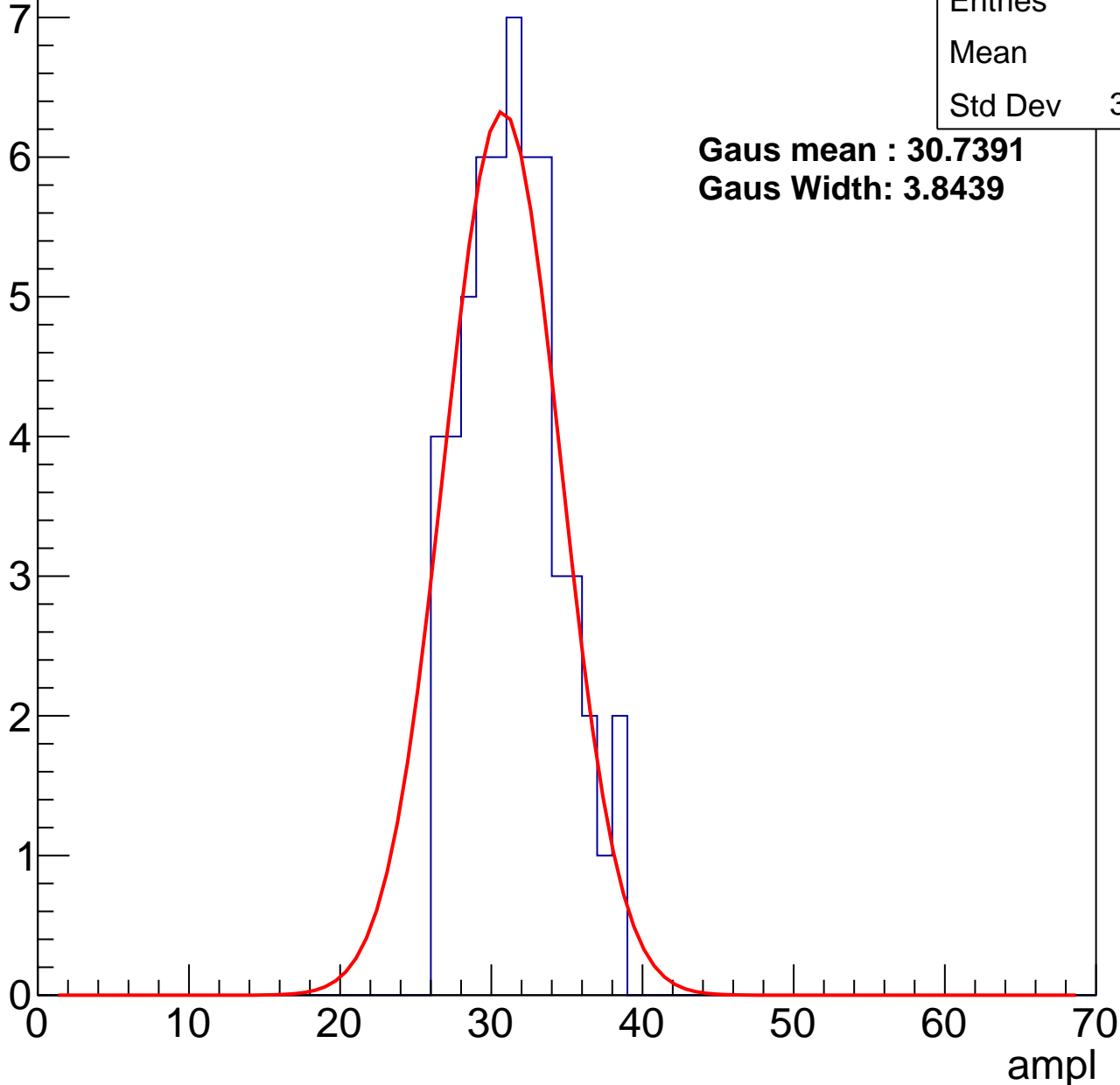
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31
Std Dev	3.098

Gaus mean : 30.7391

Gaus Width: 3.8439



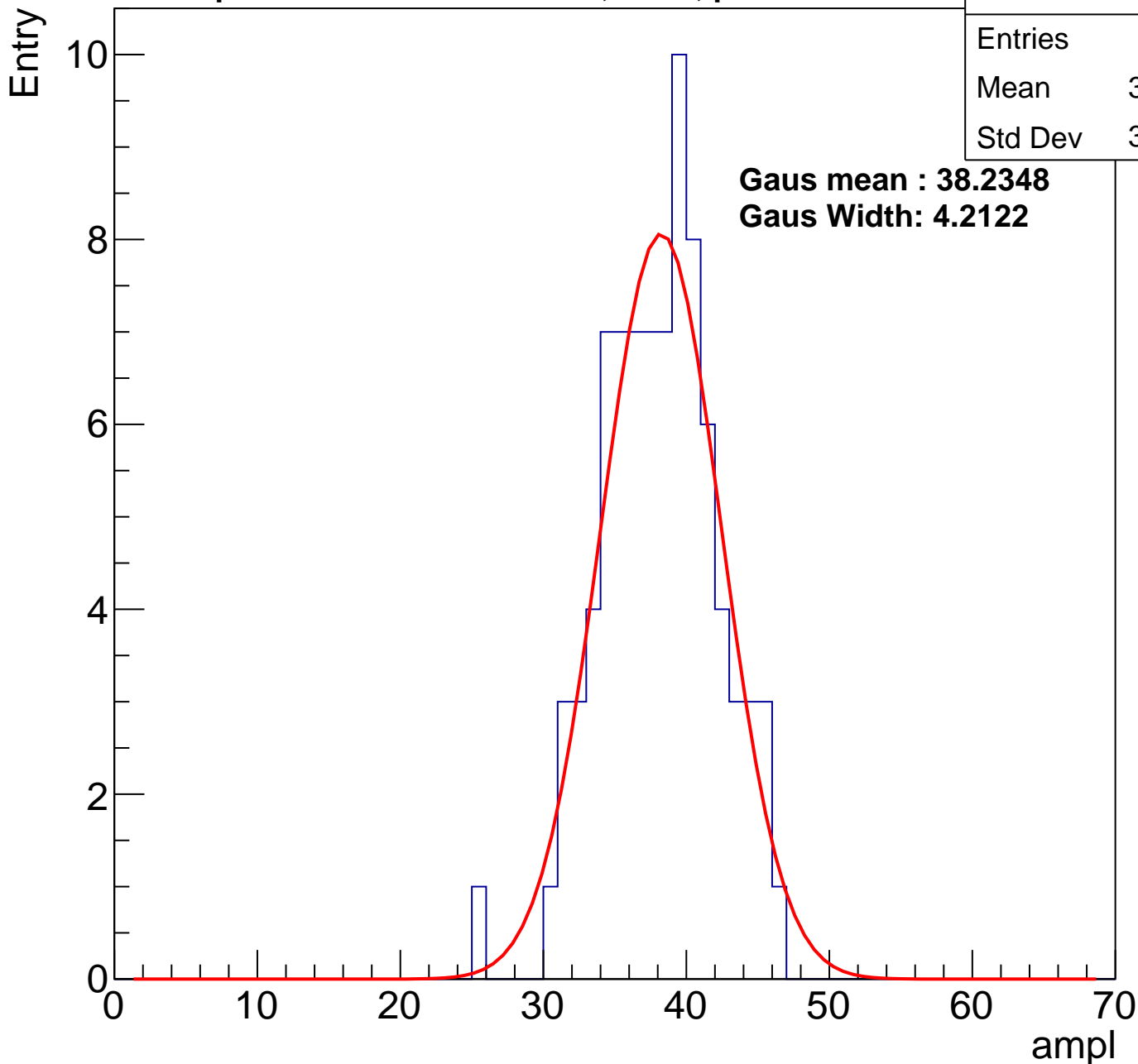
B0L001S, U23-ch81, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	37.67
Std Dev	3.975

Gaus mean : 38.2348

Gaus Width: 4.2122



B0L001S, U23-ch81, adc2

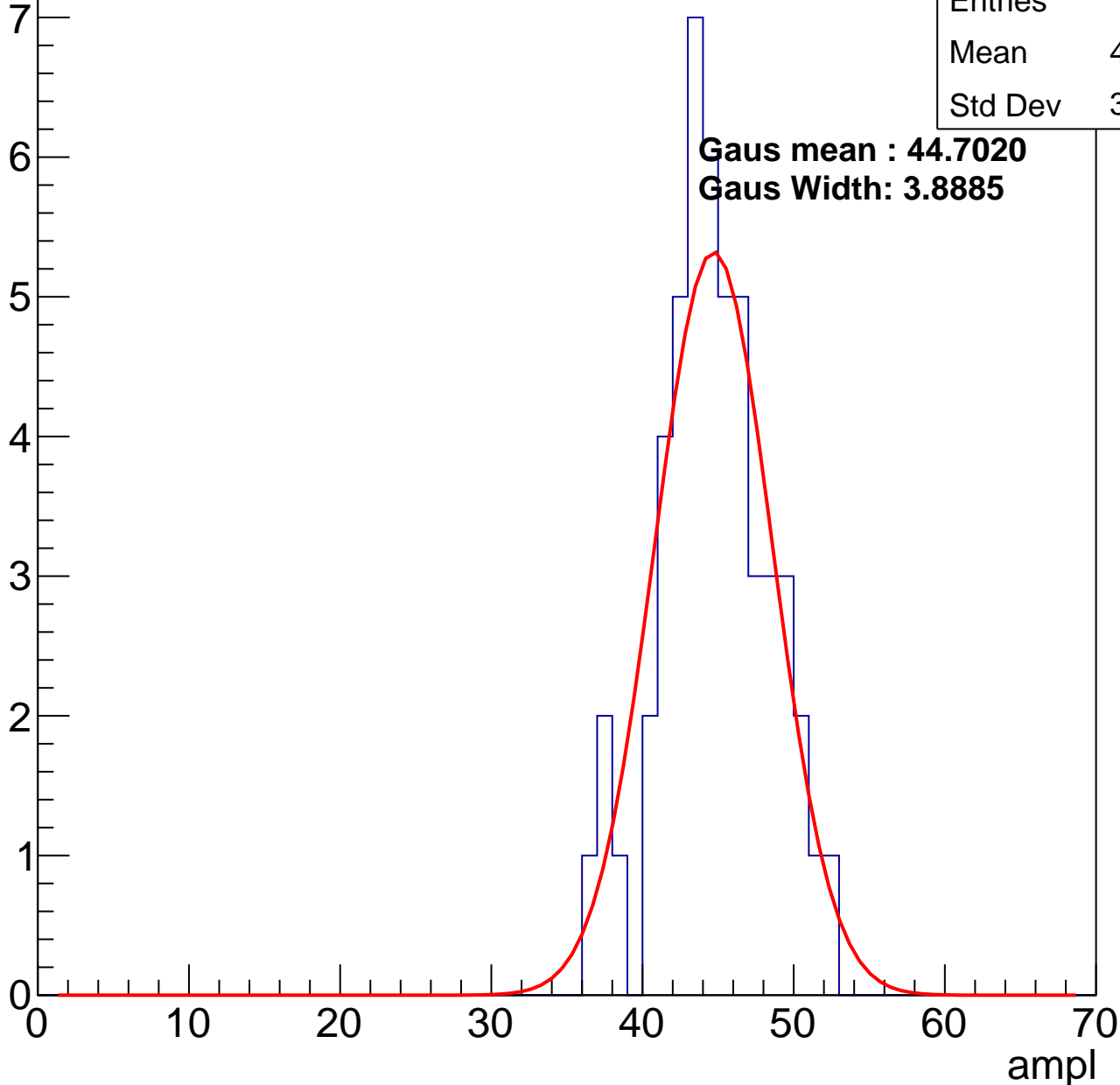
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.25
Std Dev	3.569

Gaus mean : 44.7020

Gaus Width: 3.8885



B0L001S, U23-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	93
Mean	51.33
Std Dev	3.557

Entry

10

8

6

4

2

0

0

10

20

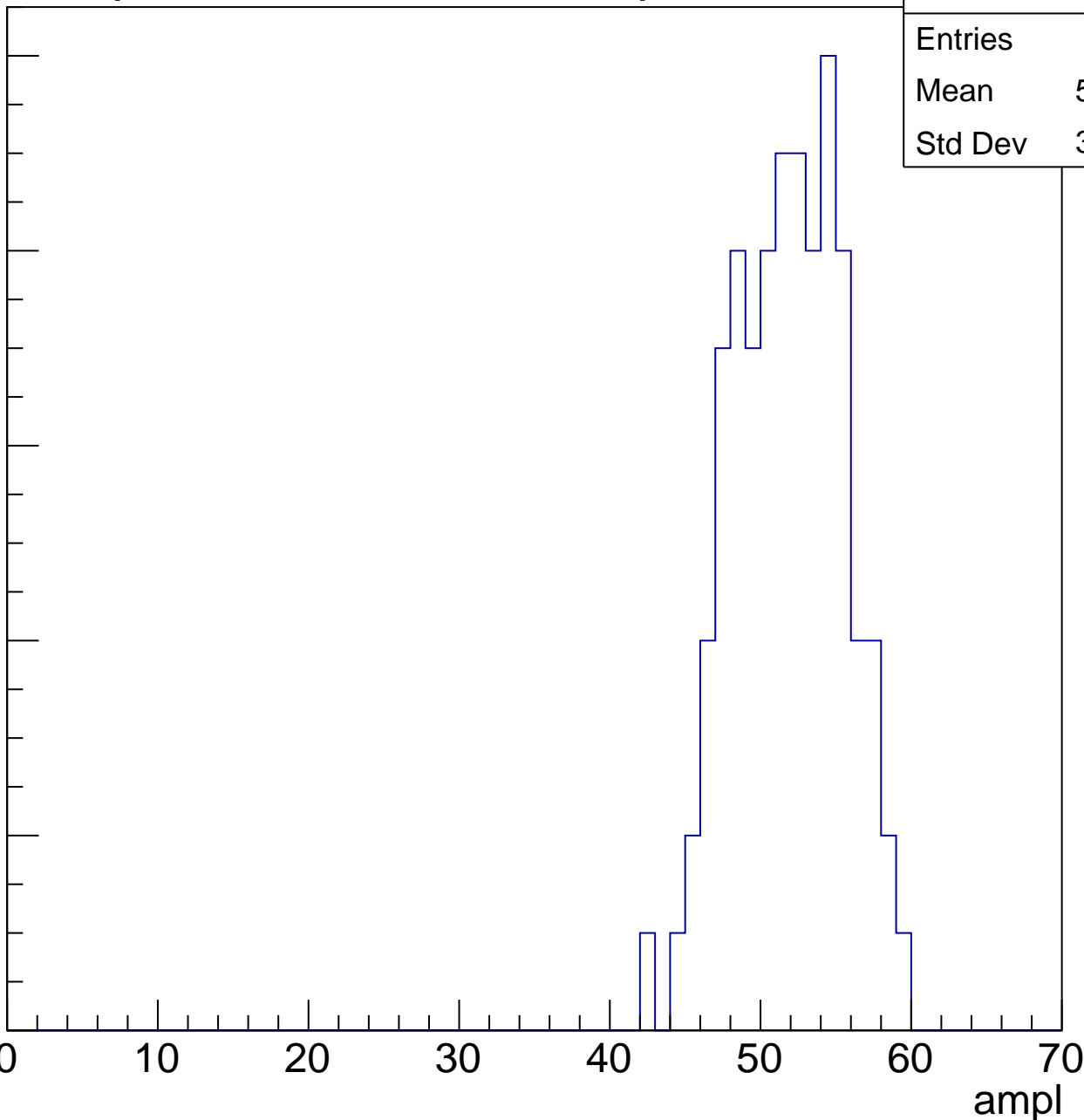
30

40

50

60

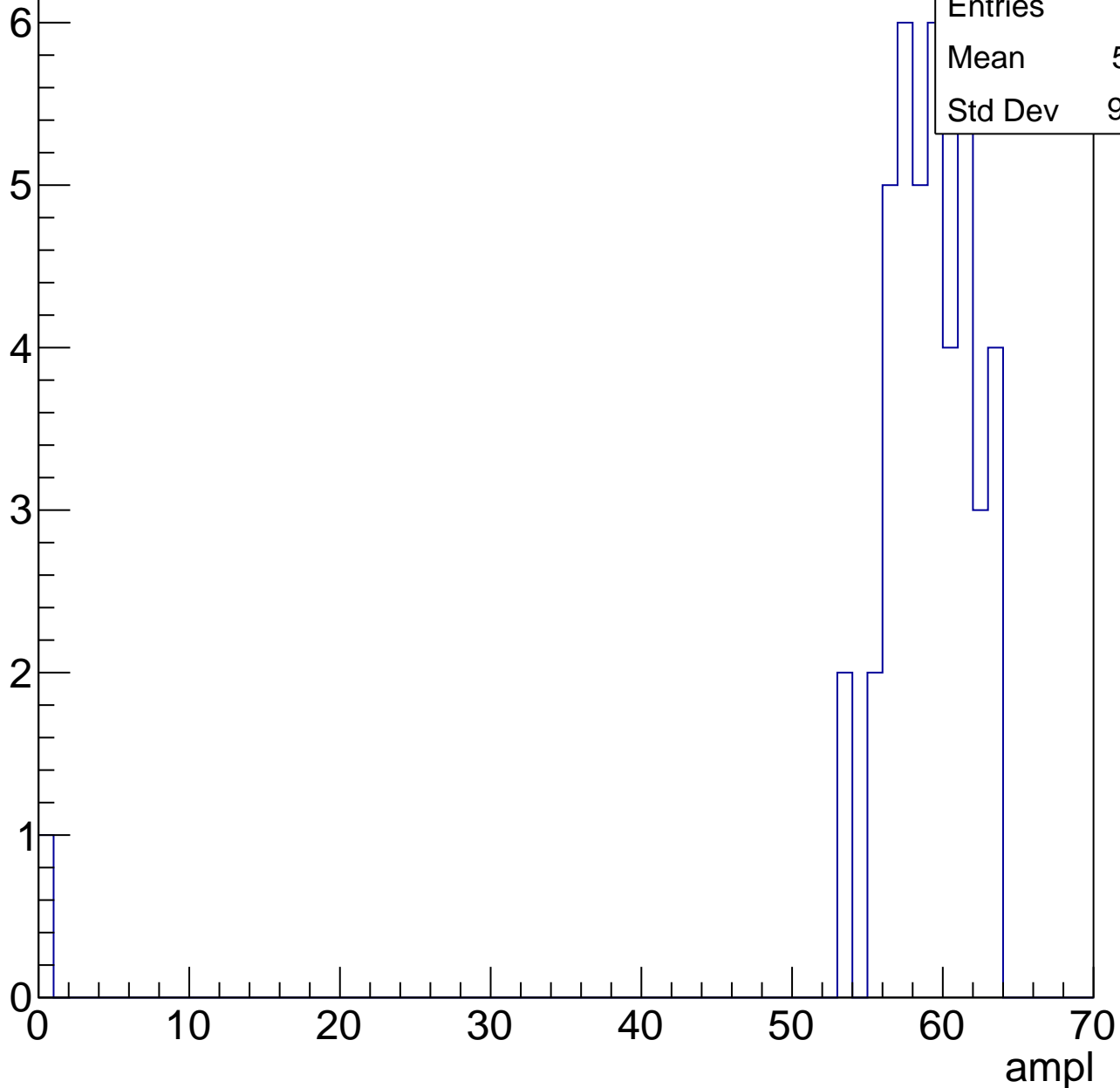
ampl



B0L001S, U23-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

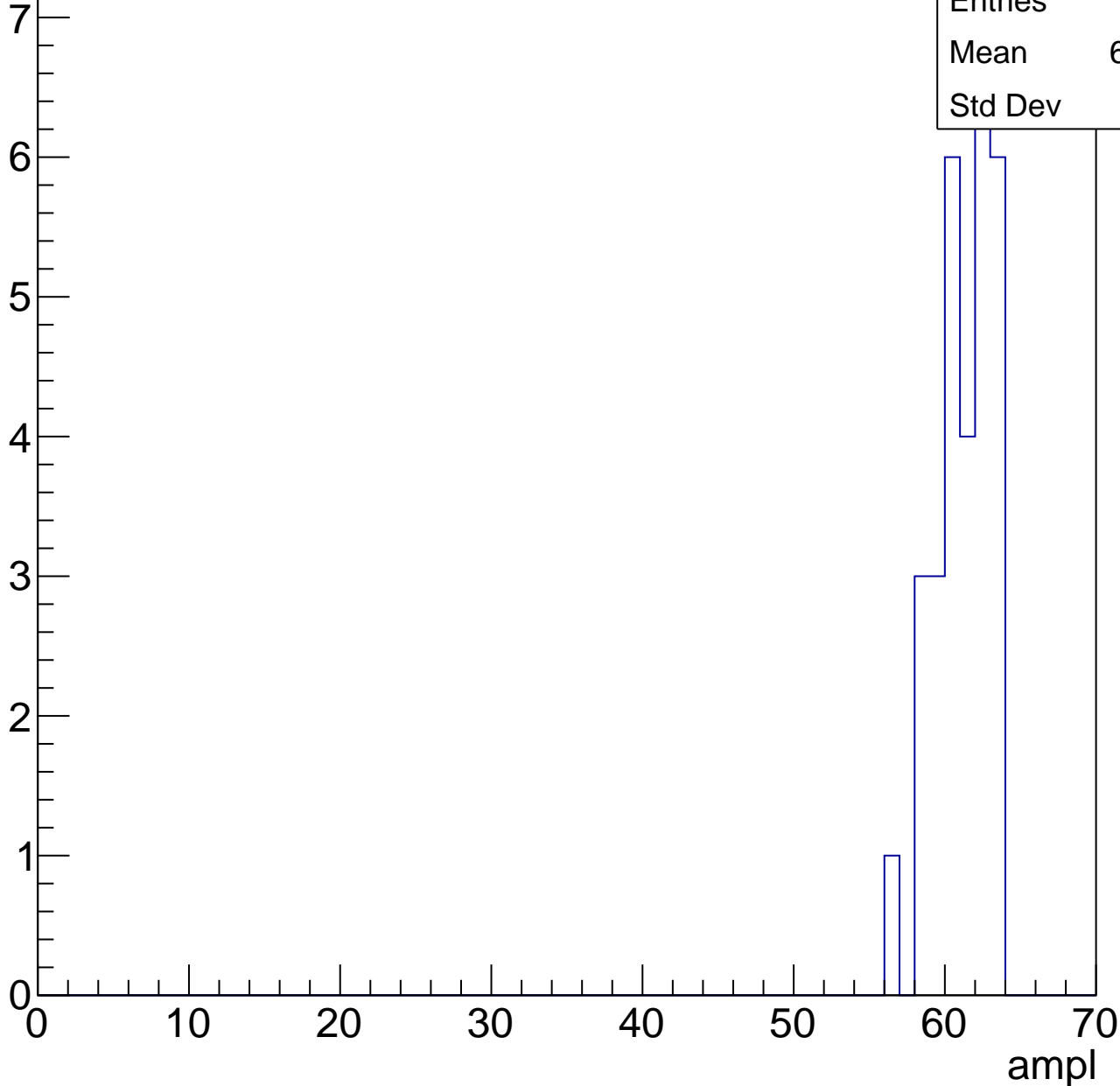


B0L001S, U23-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	60.77
Std Dev	1.82



B0L001S, U23-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch82, adc0

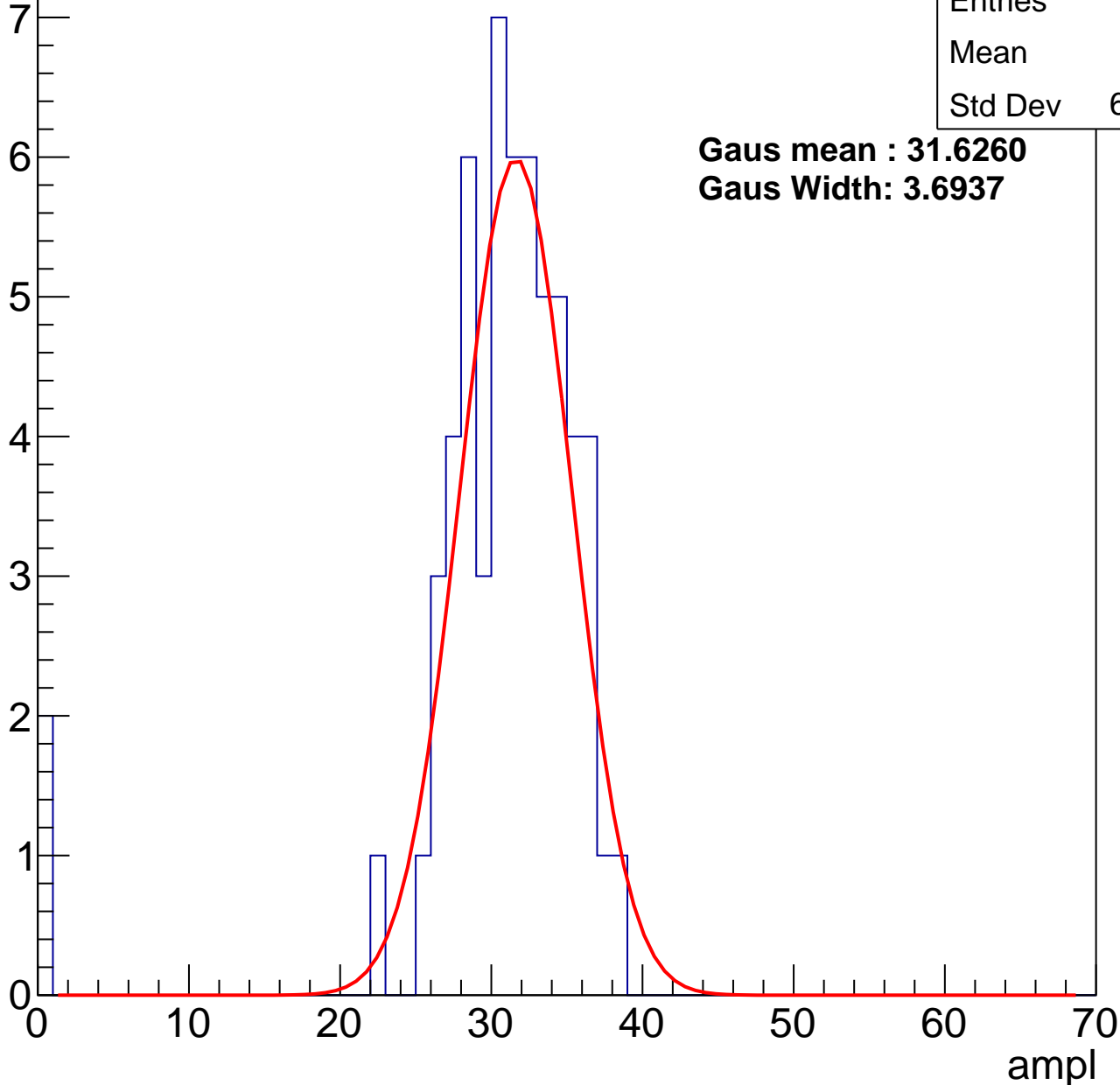
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	30
Std Dev	6.525

Gaus mean : 31.6260

Gaus Width: 3.6937



B0L001S, U23-ch82, adc1

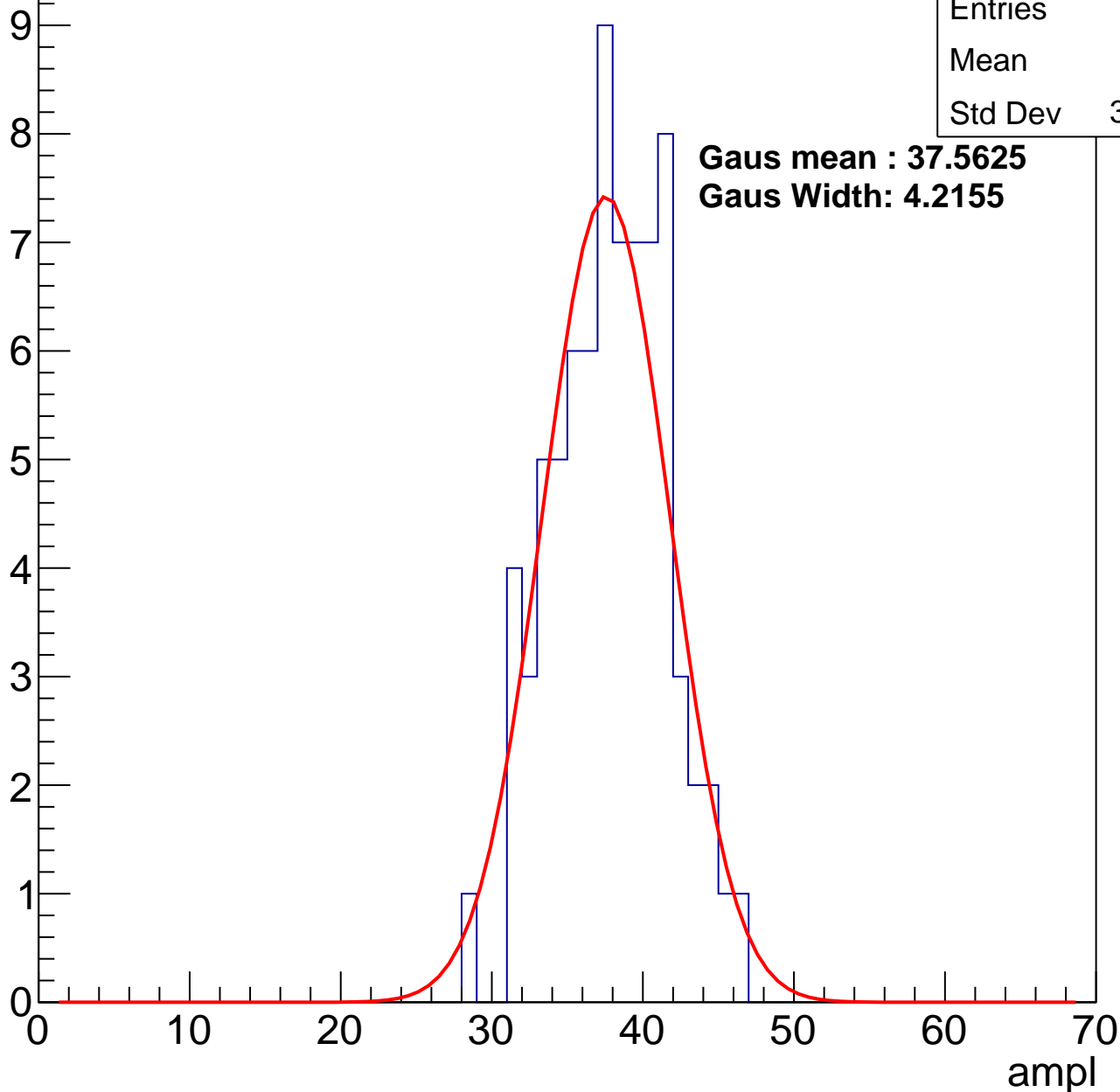
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.4
Std Dev	3.708

Gaus mean : 37.5625

Gaus Width: 4.2155



B0L001S, U23-ch82, adc2

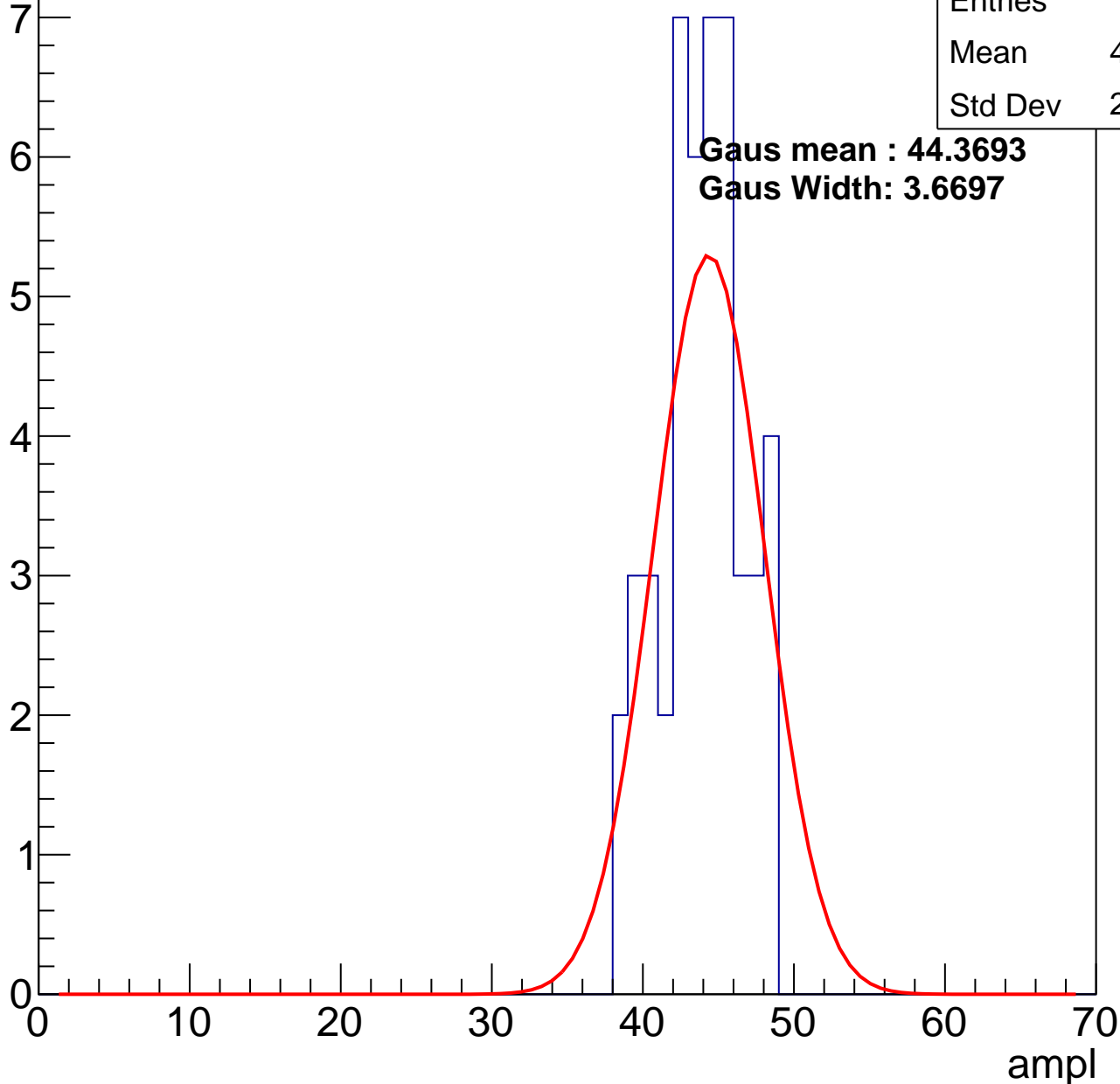
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	43.43
Std Dev	2.696

Gaus mean : 44.3693

Gaus Width: 3.6697



B0L001S, U23-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	49.88
Std Dev	3.1

Entry

10

8

6

4

2

0

0

10

20

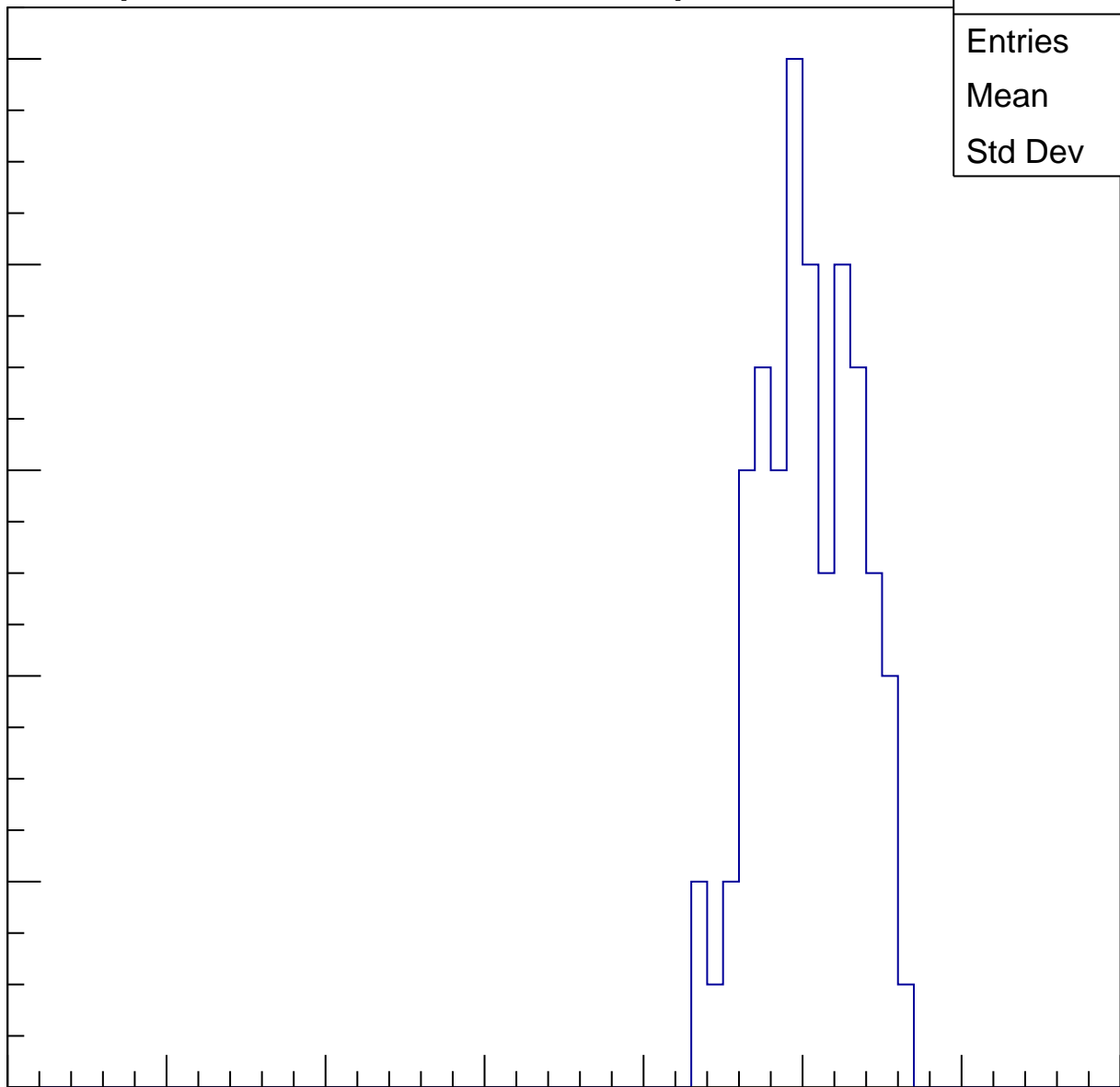
30

40

50

60

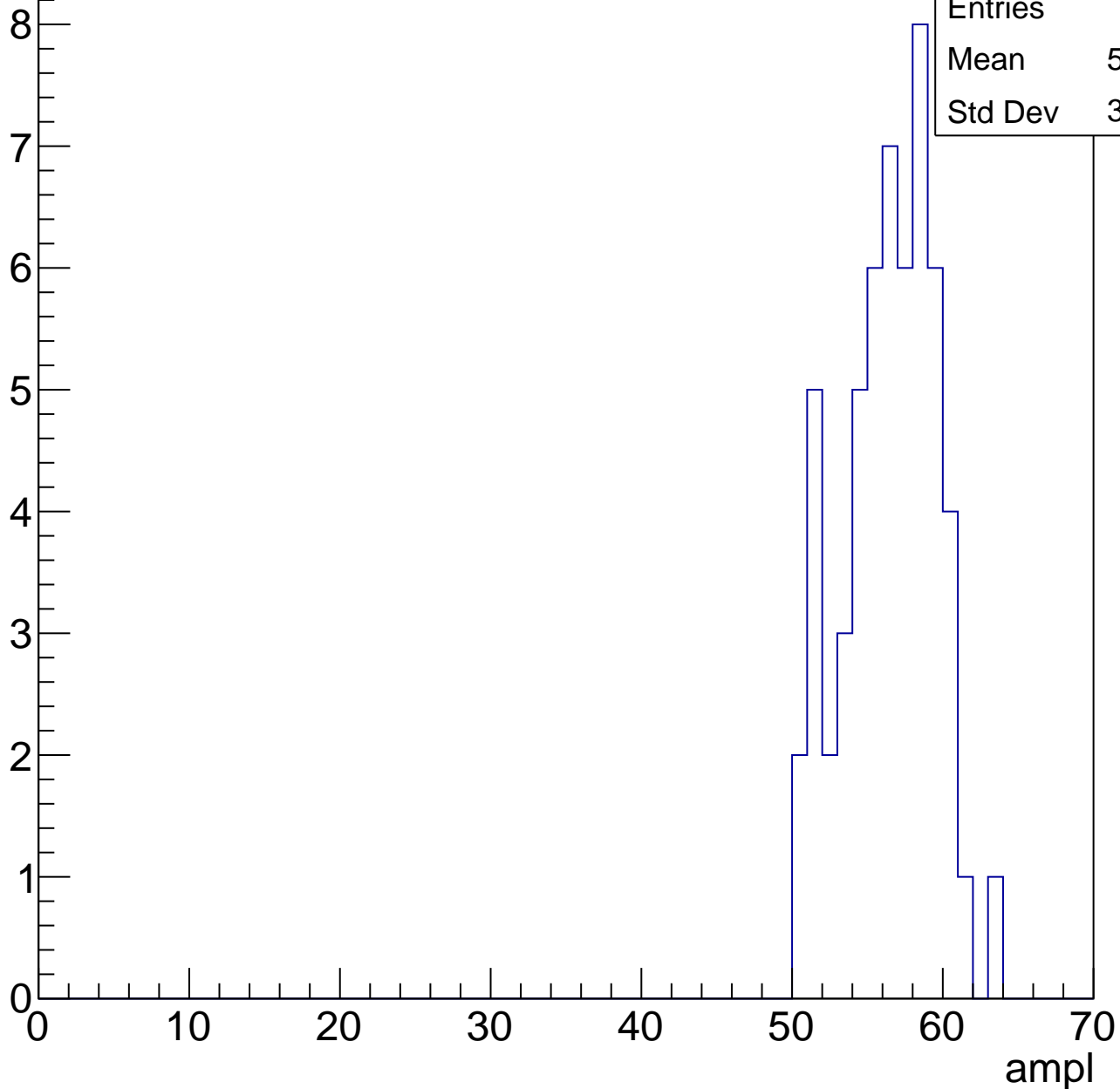
ampl



B0L001S, U23-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

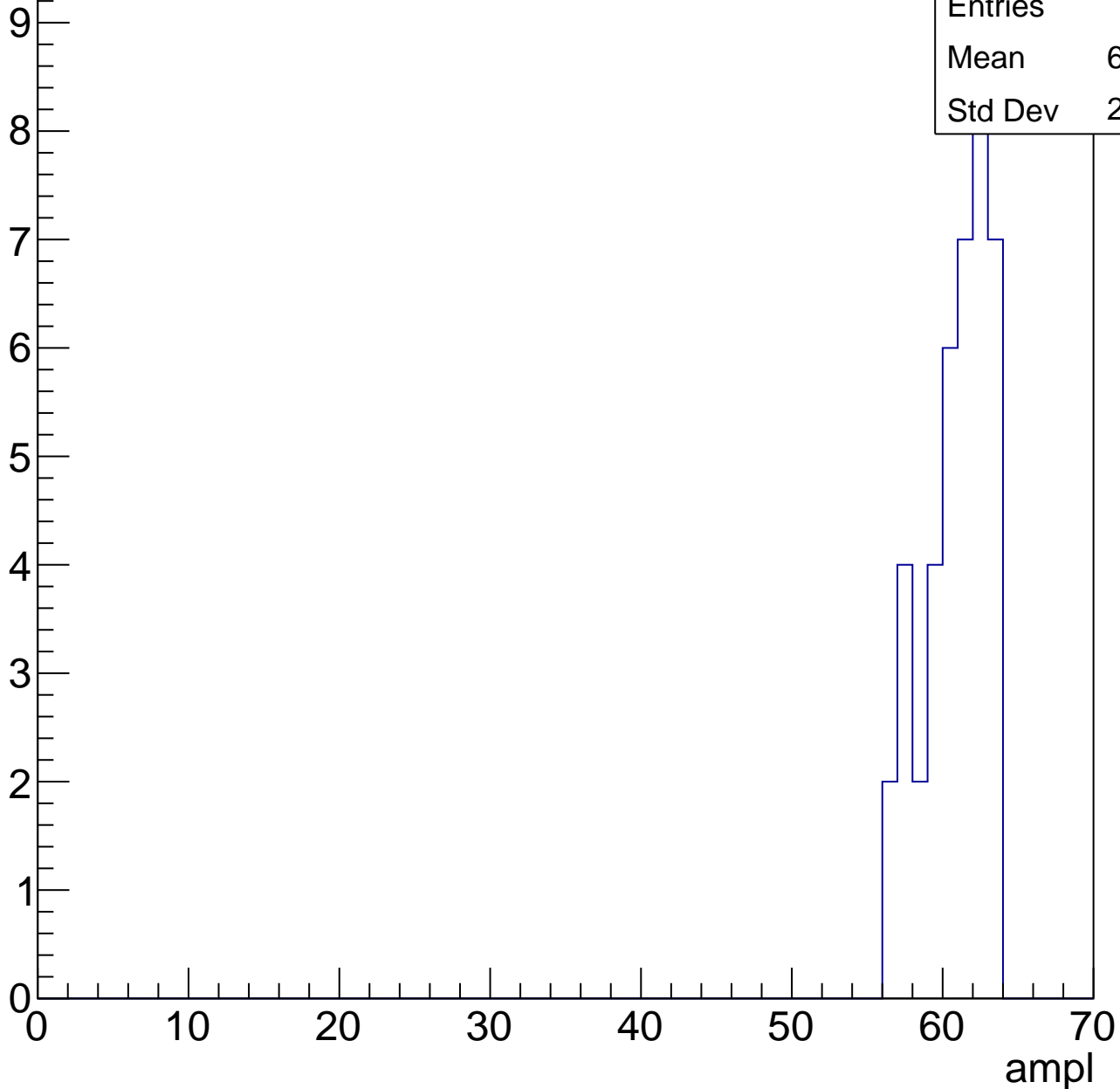


B0L001S, U23-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	60.44
Std Dev	2.084



B0L001S, U23-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

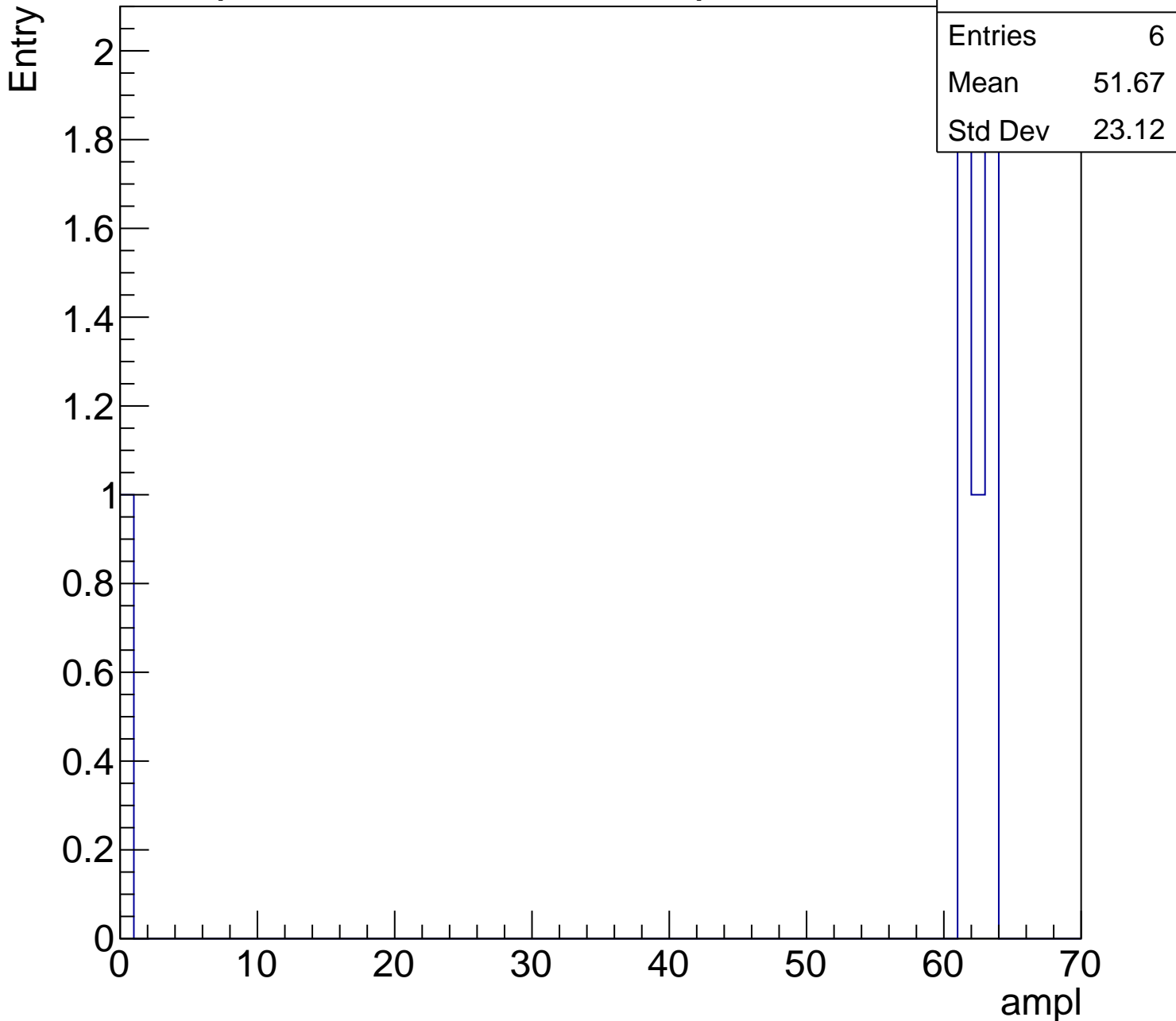
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.12

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch83, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	32.23
Std Dev	5.095

Gaus mean : 33.1505

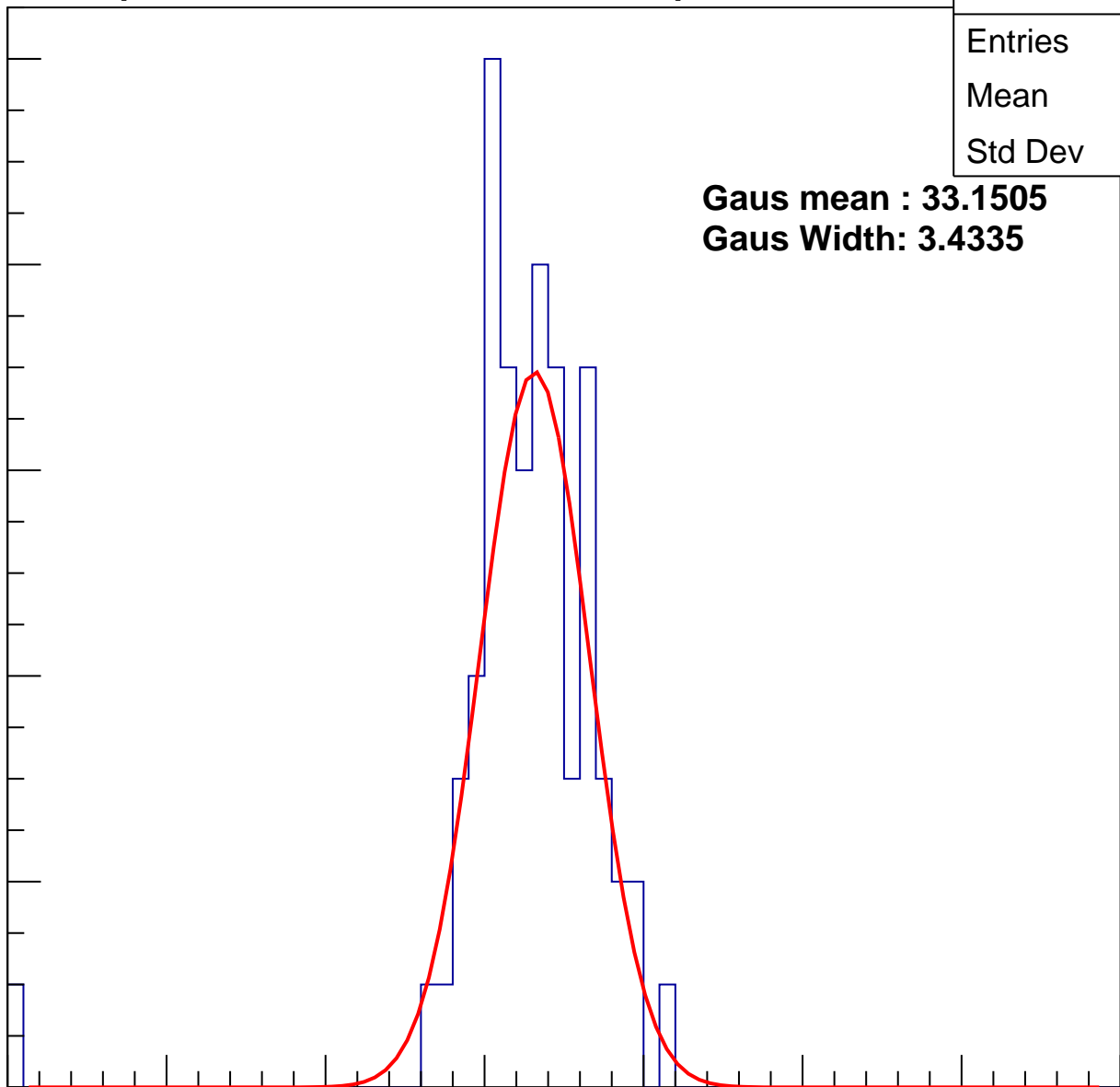
Gaus Width: 3.4335

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch83, adc1

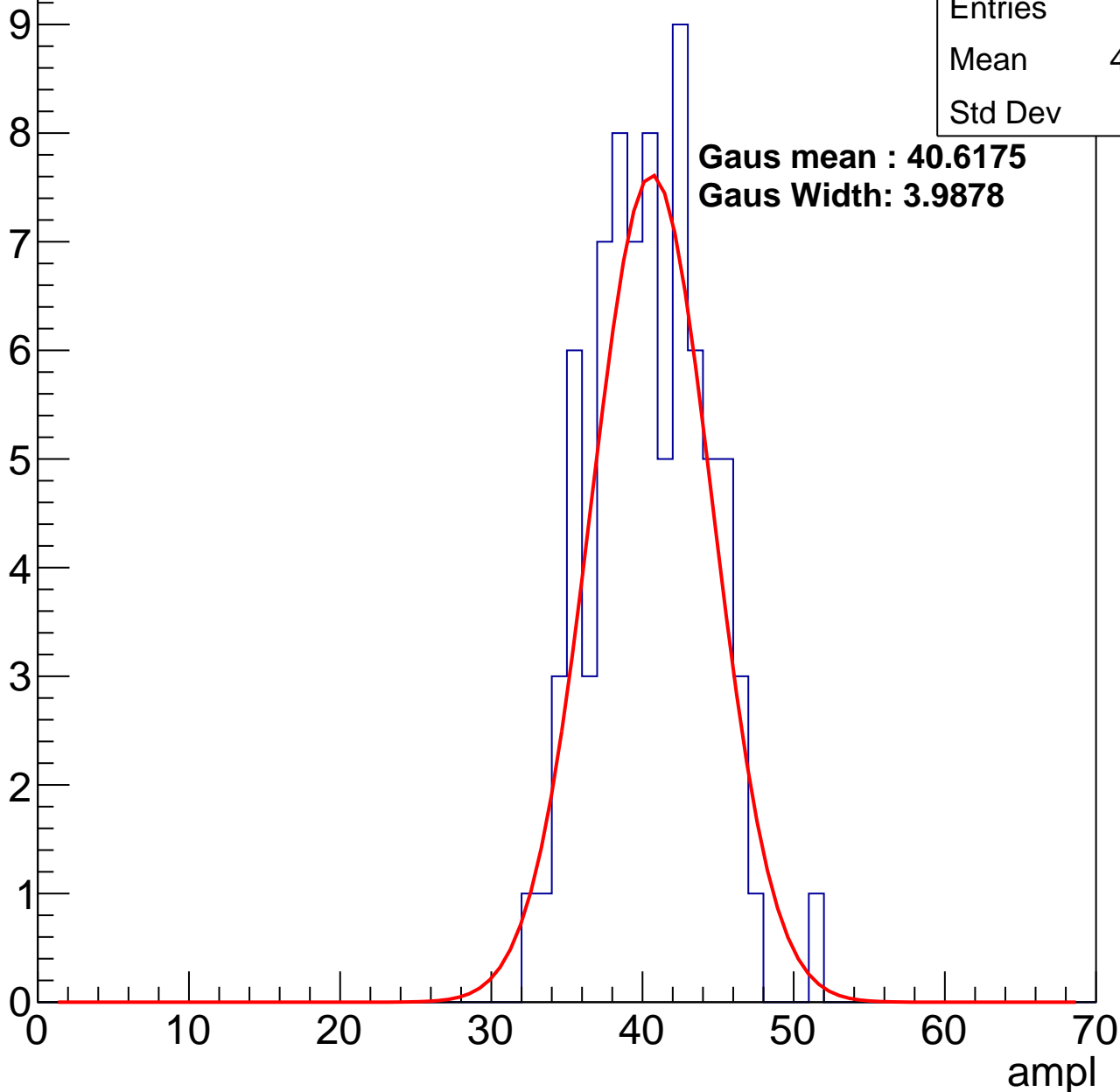
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	40.04
Std Dev	3.73

Gaus mean : 40.6175

Gaus Width: 3.9878



B0L001S, U23-ch83, adc2

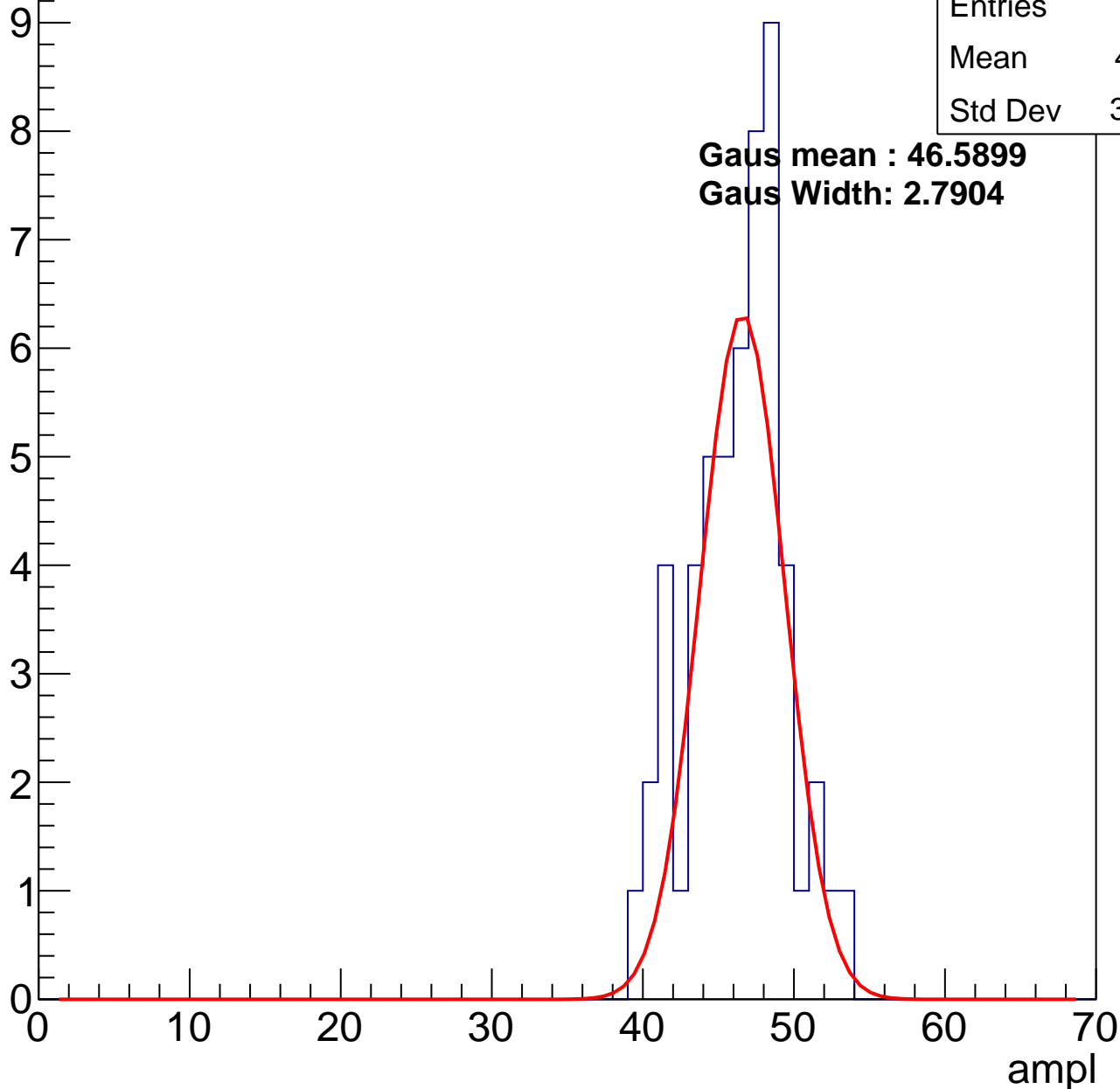
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	45.91
Std Dev	3.129

Gaus mean : 46.5899

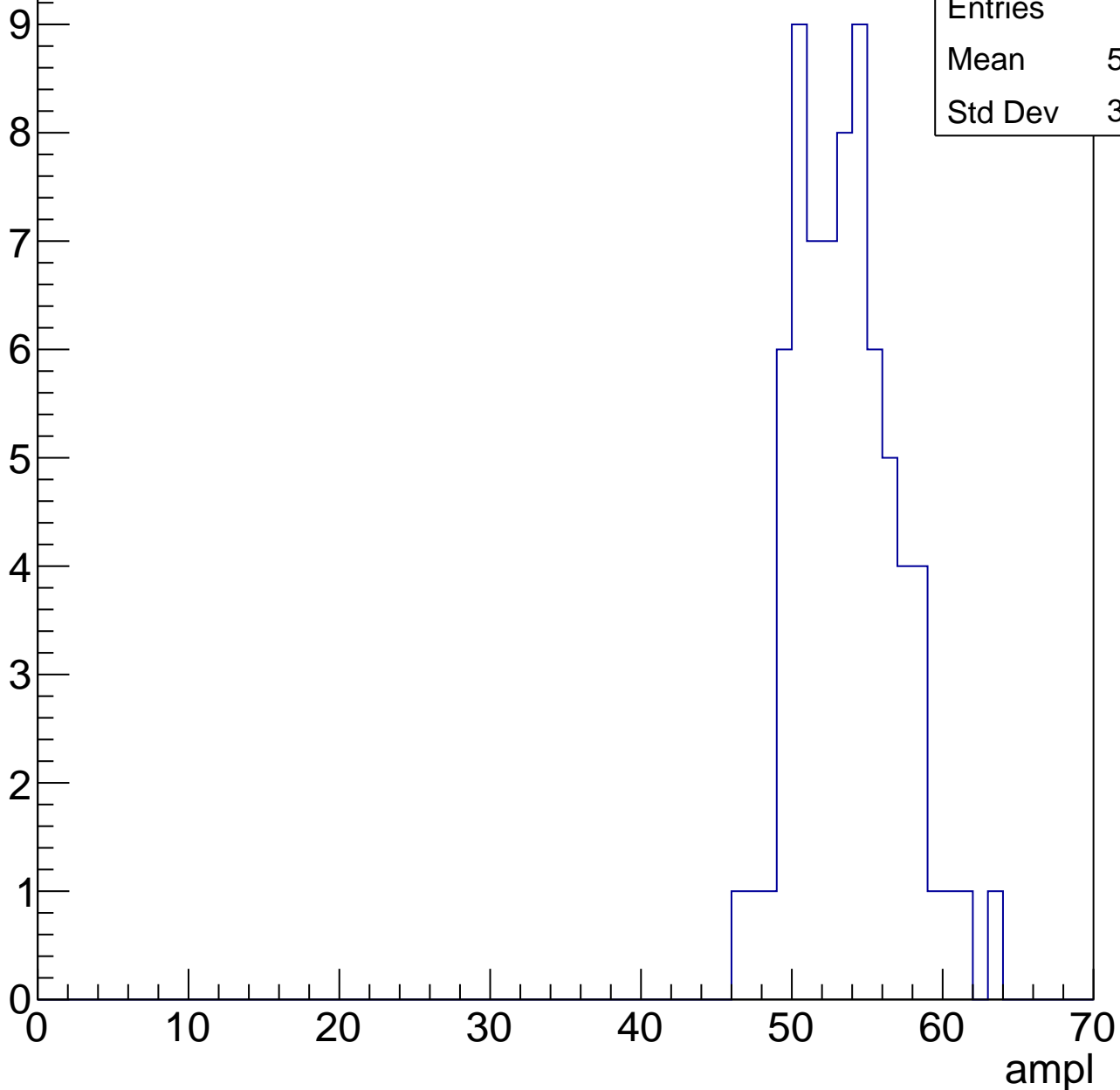
Gaus Width: 2.7904



B0L001S, U23-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	72
Mean	53.18
Std Dev	3.355

B0L001S, U23-ch83, adc4

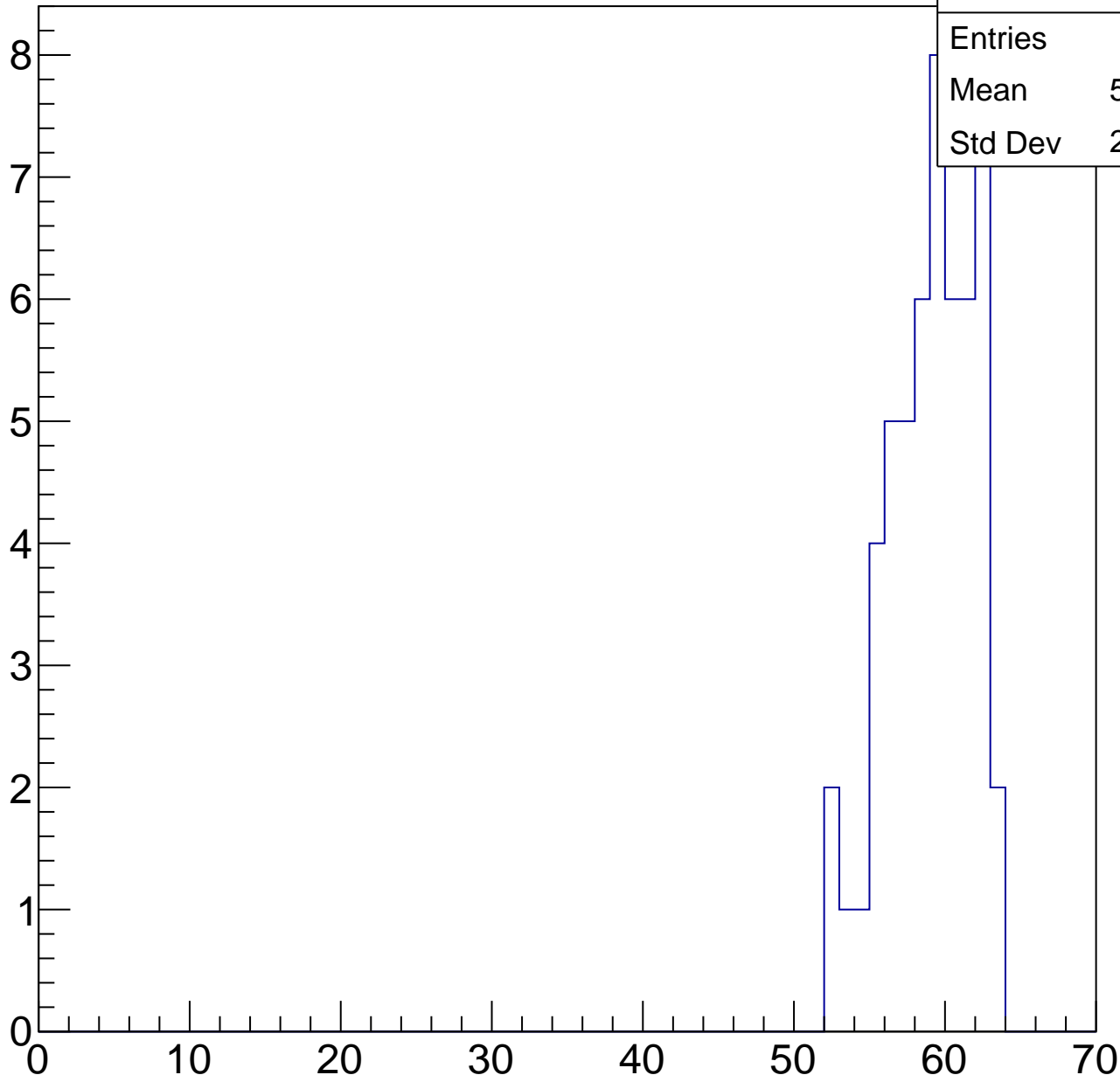
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.59
Std Dev	2.786

ampl

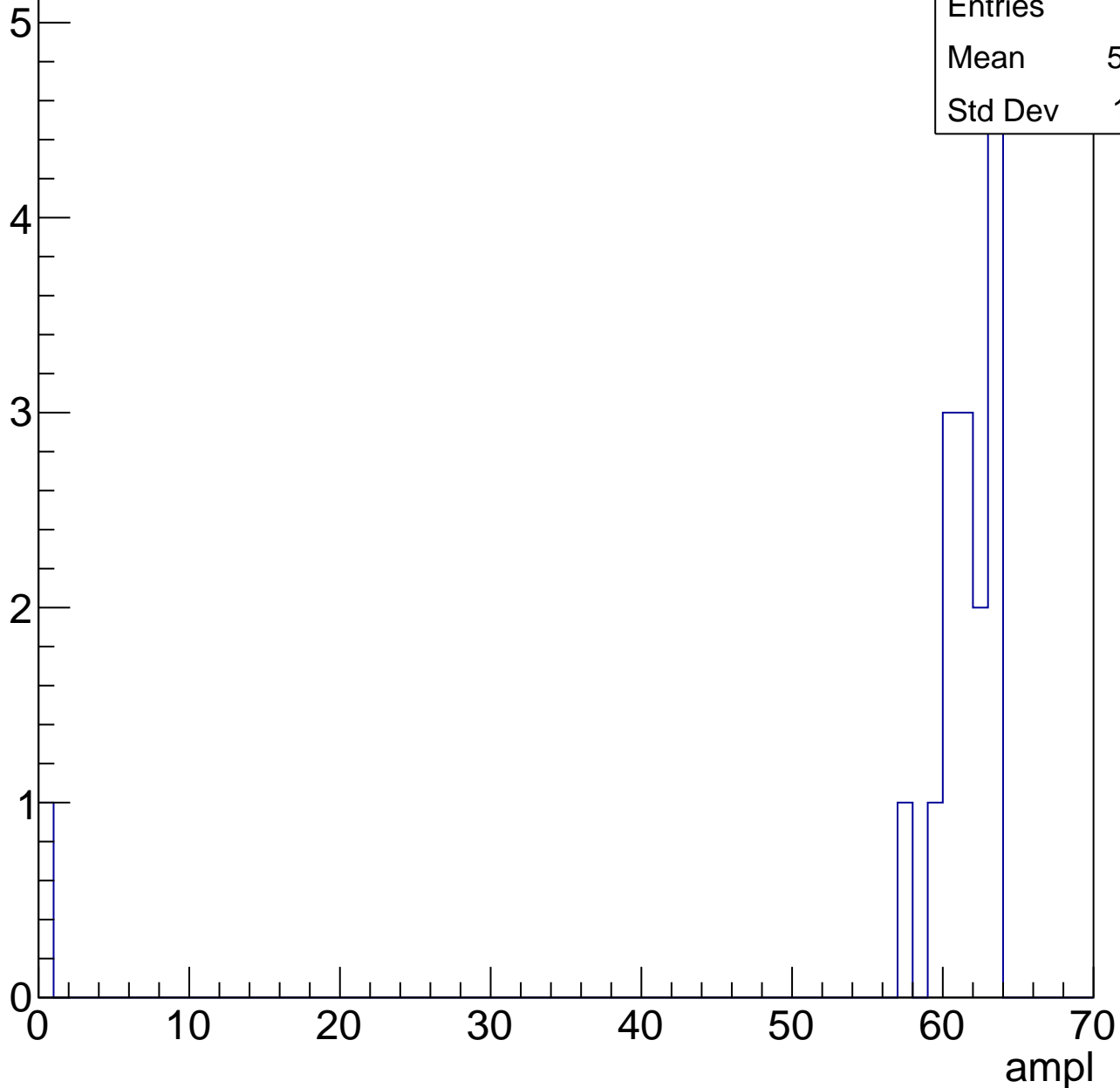


B0L001S, U23-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	57.38
Std Dev	14.91



B0L001S, U23-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L001S, U23-ch84, adc0

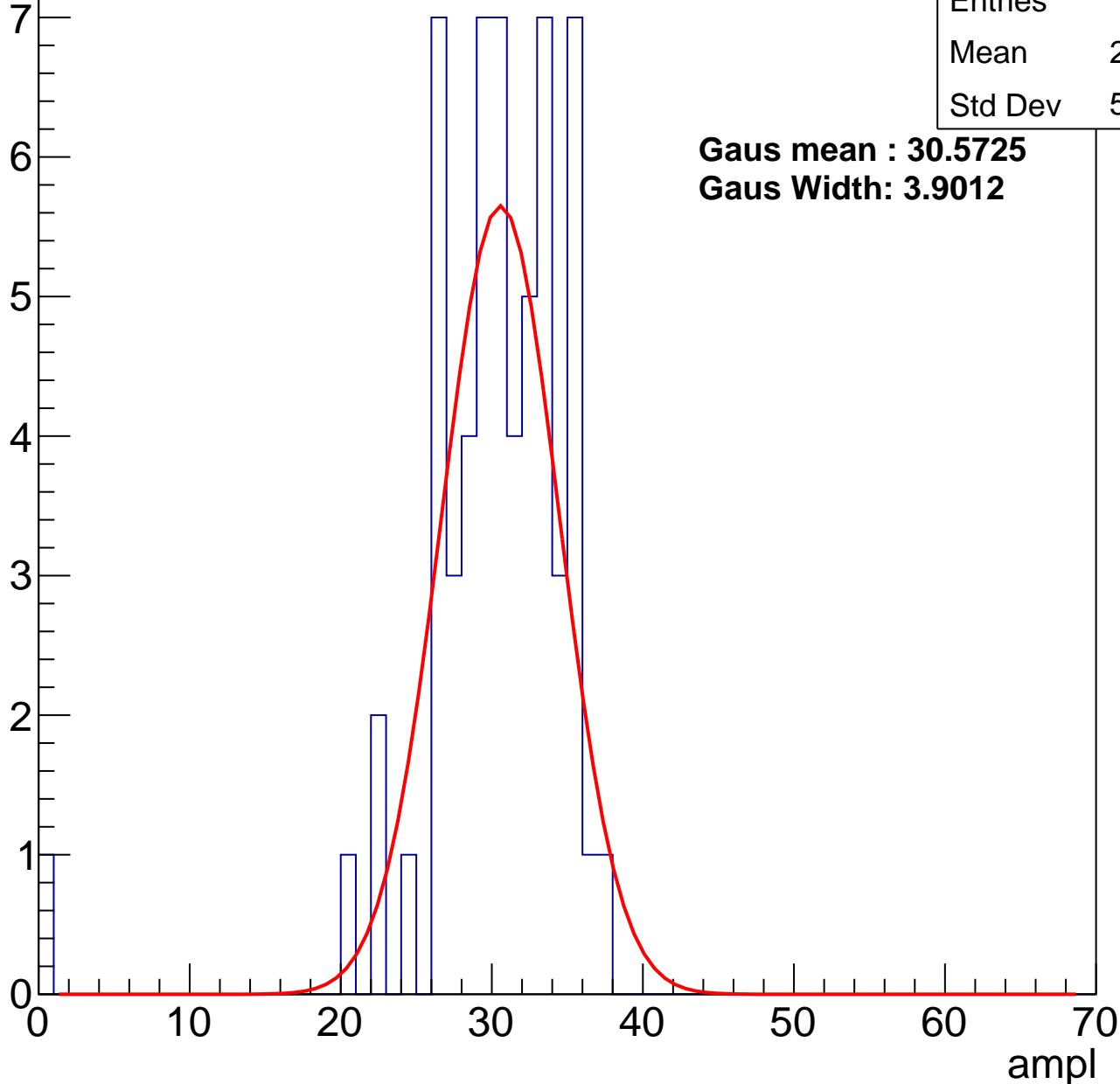
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	29.69
Std Dev	5.302

Gaus mean : 30.5725

Gaus Width: 3.9012



B0L001S, U23-ch84, adc1

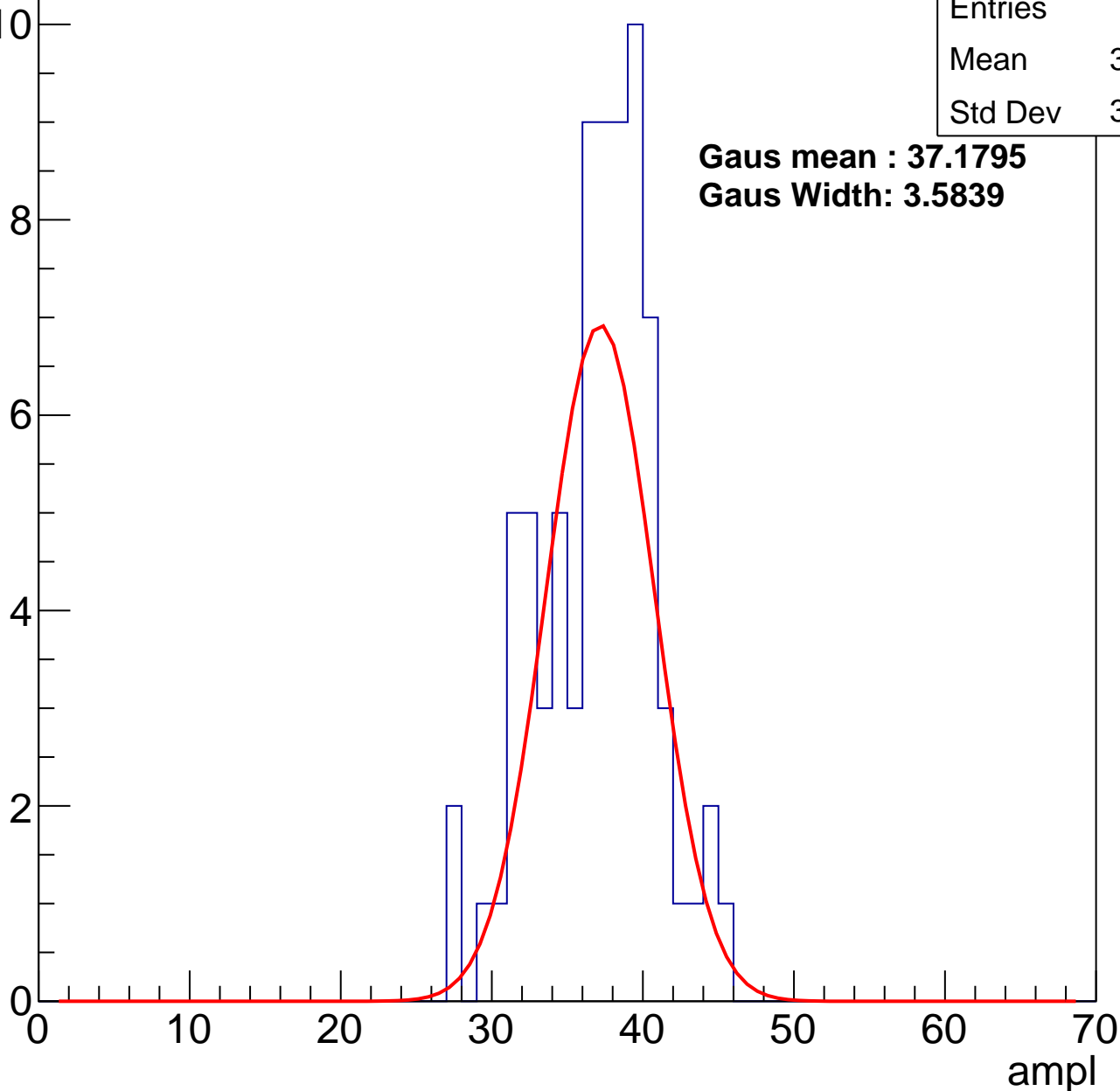
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.52
Std Dev	3.775

Gaus mean : 37.1795

Gaus Width: 3.5839



B0L001S, U23-ch84, adc2

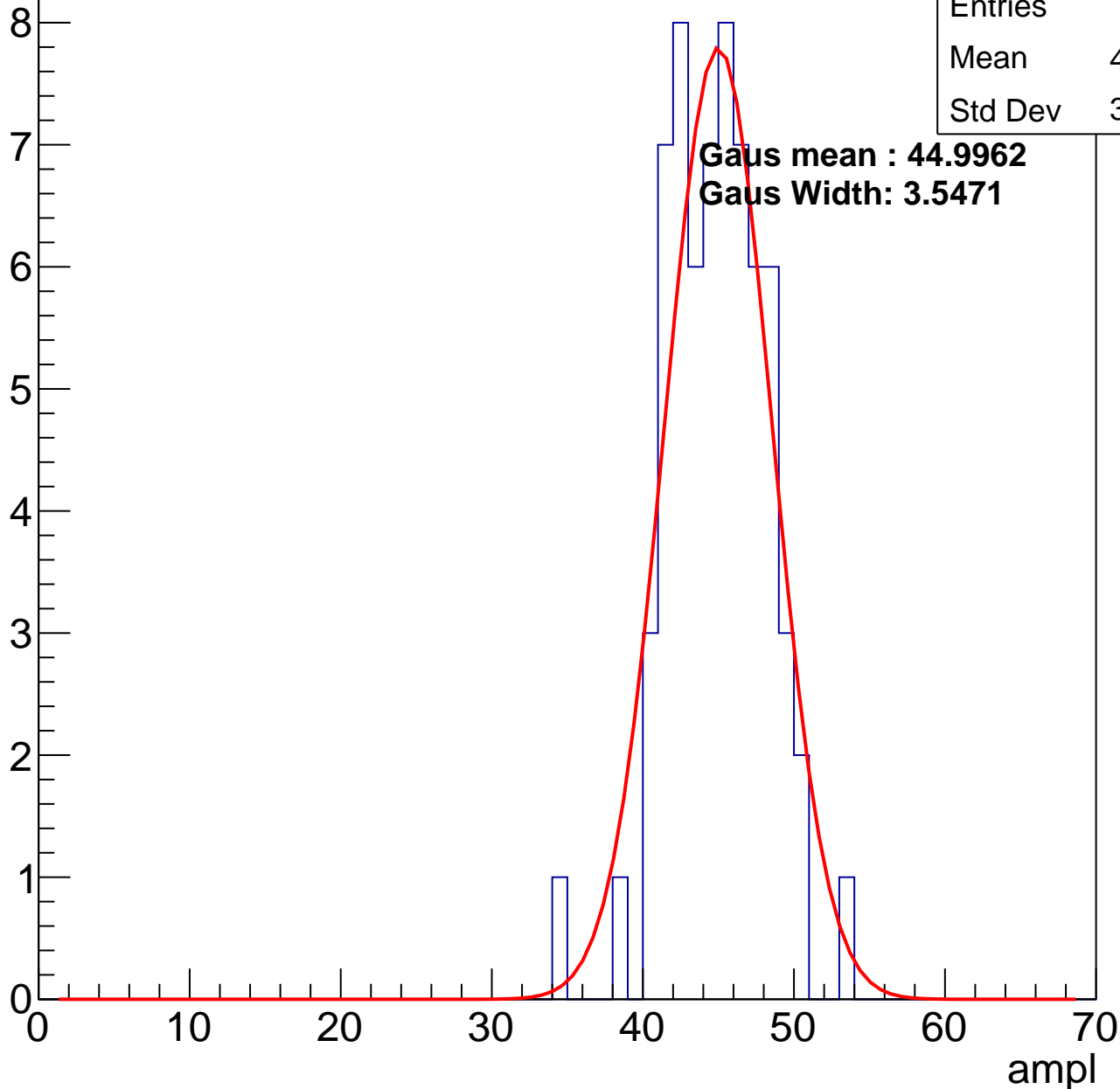
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.44
Std Dev	3.225

Gaus mean : 44.9962

Gaus Width: 3.5471

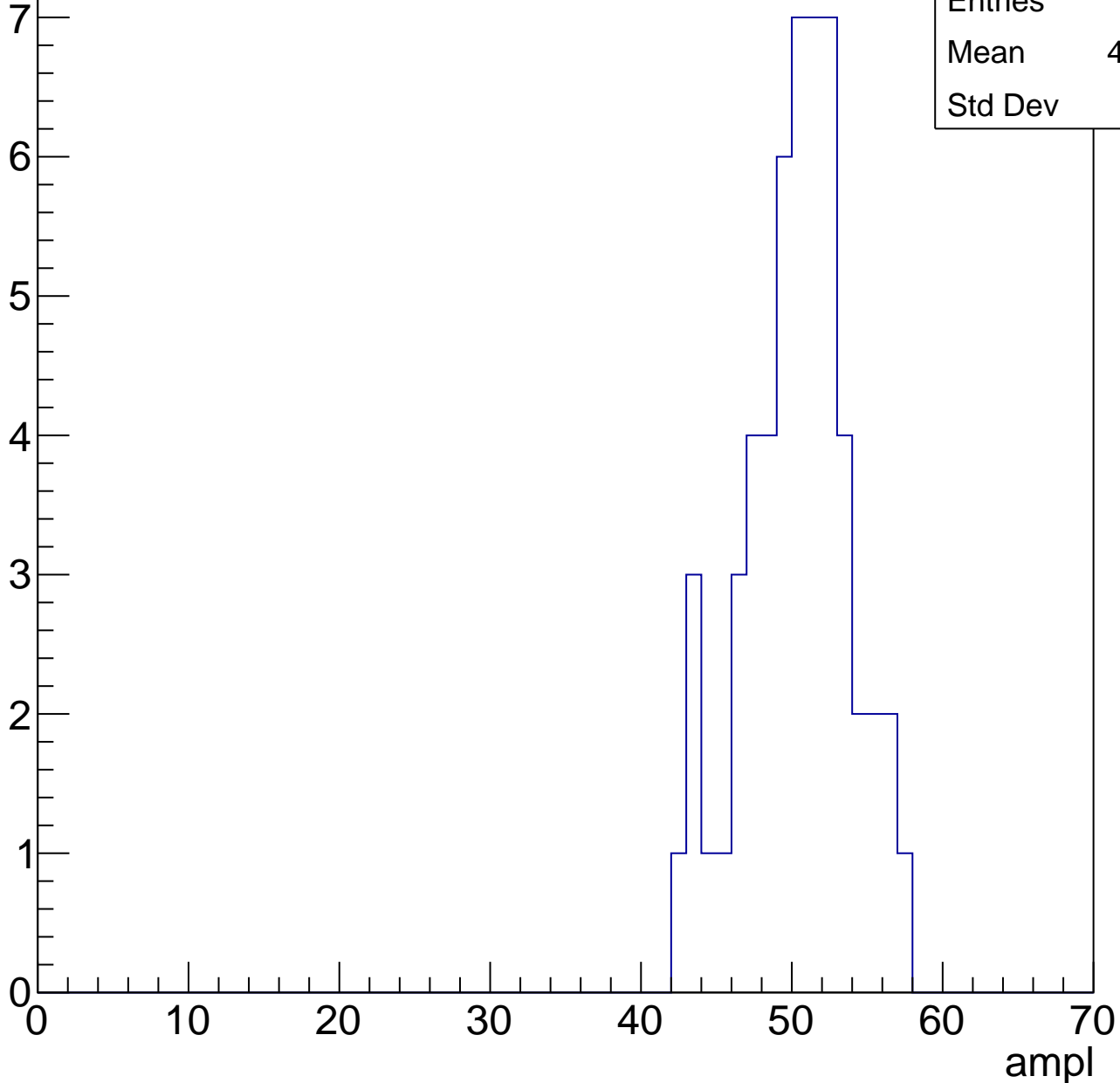


B0L001S, U23-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.85
Std Dev	3.44

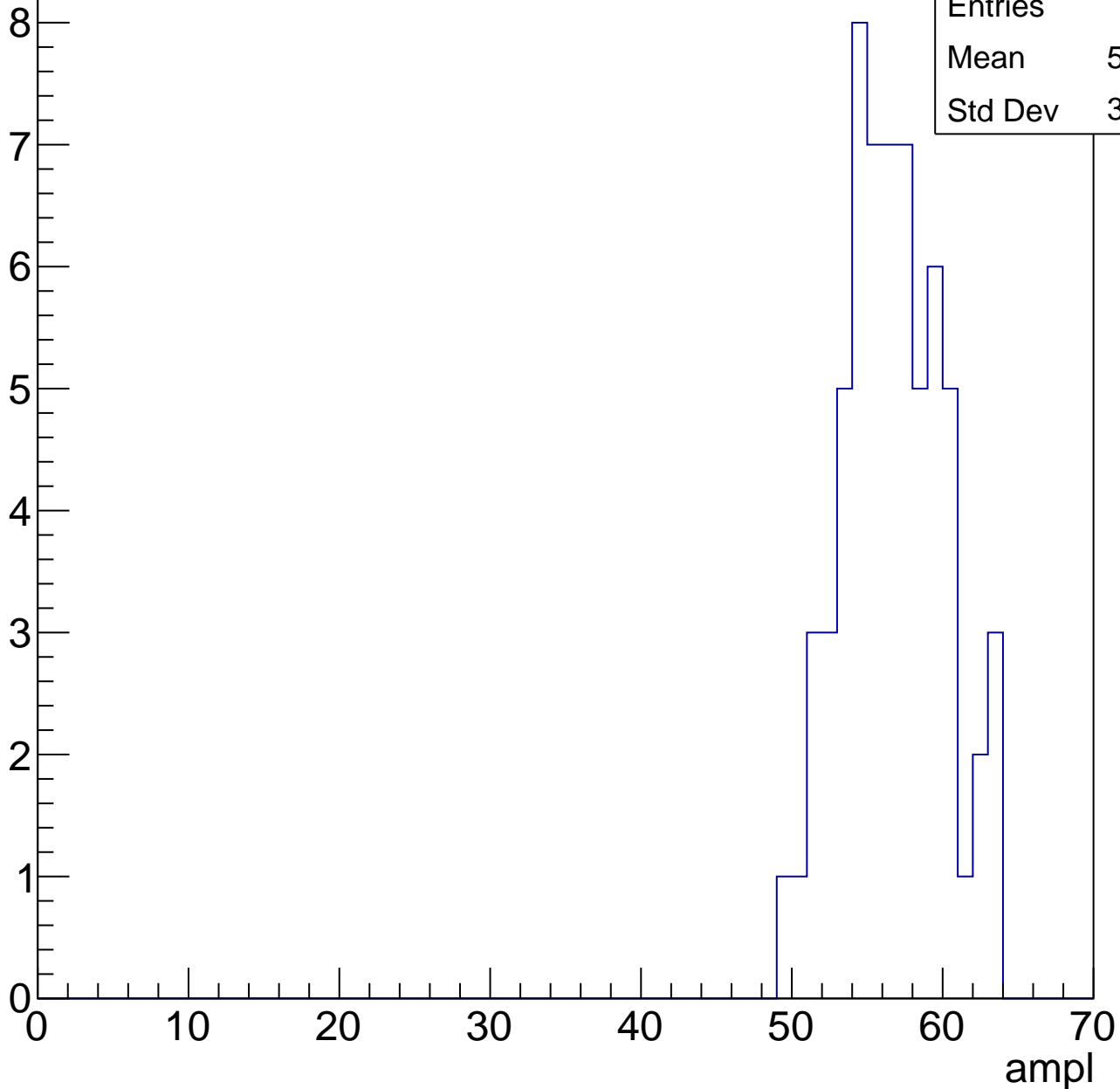


B0L001S, U23-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.23
Std Dev	3.292

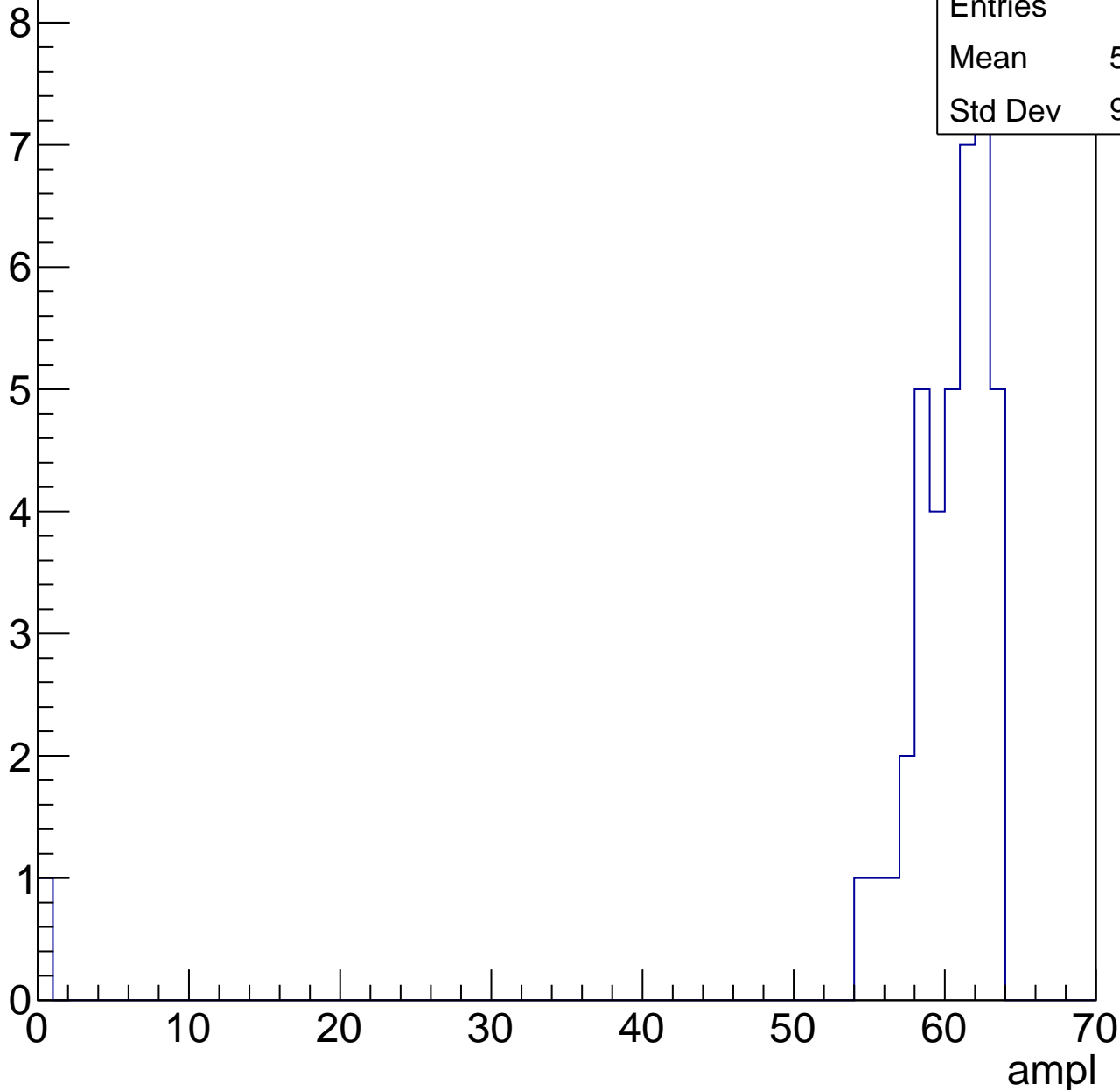


B0L001S, U23-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.58
Std Dev	9.646



B0L001S, U23-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

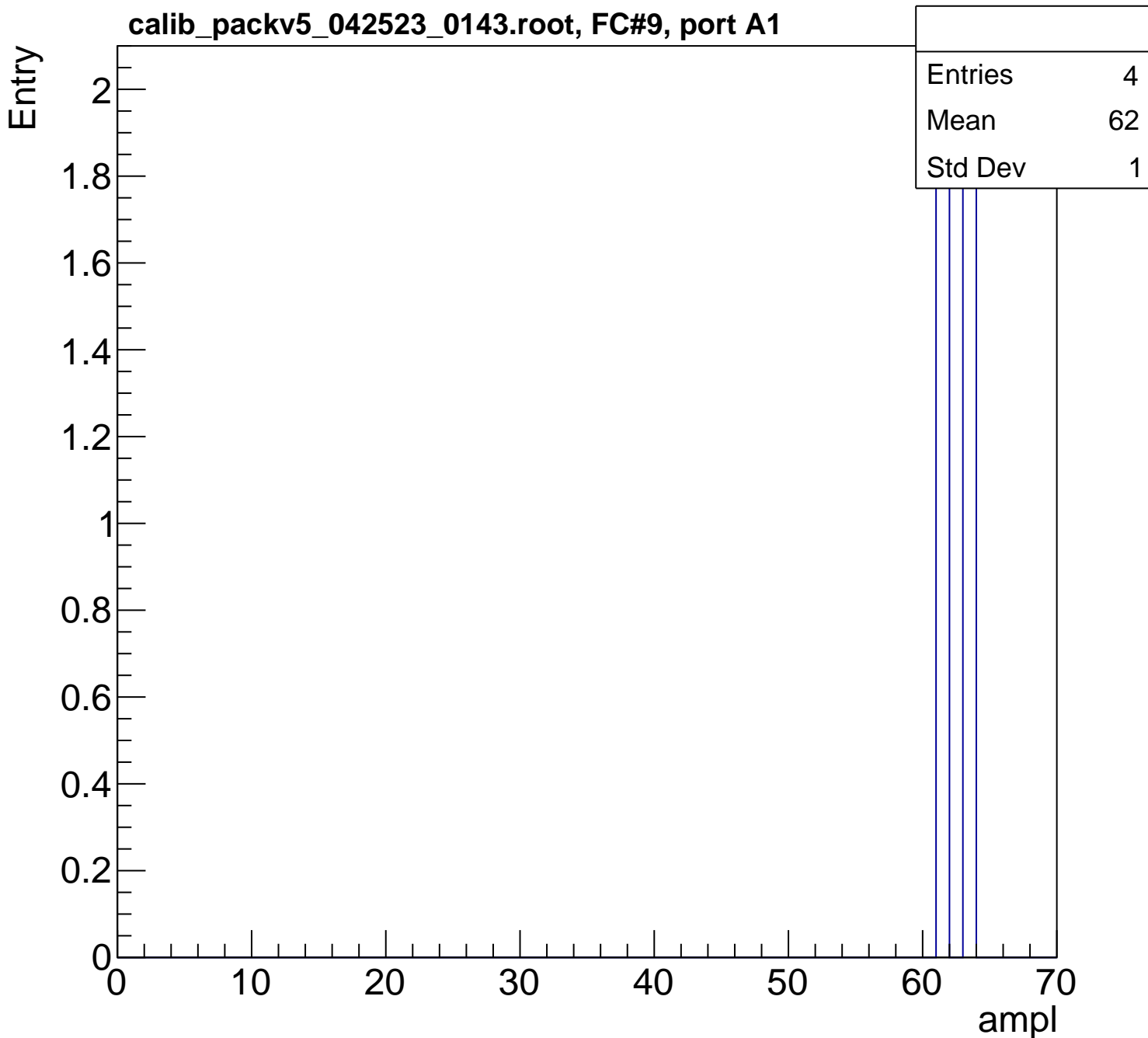
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch85, adc0

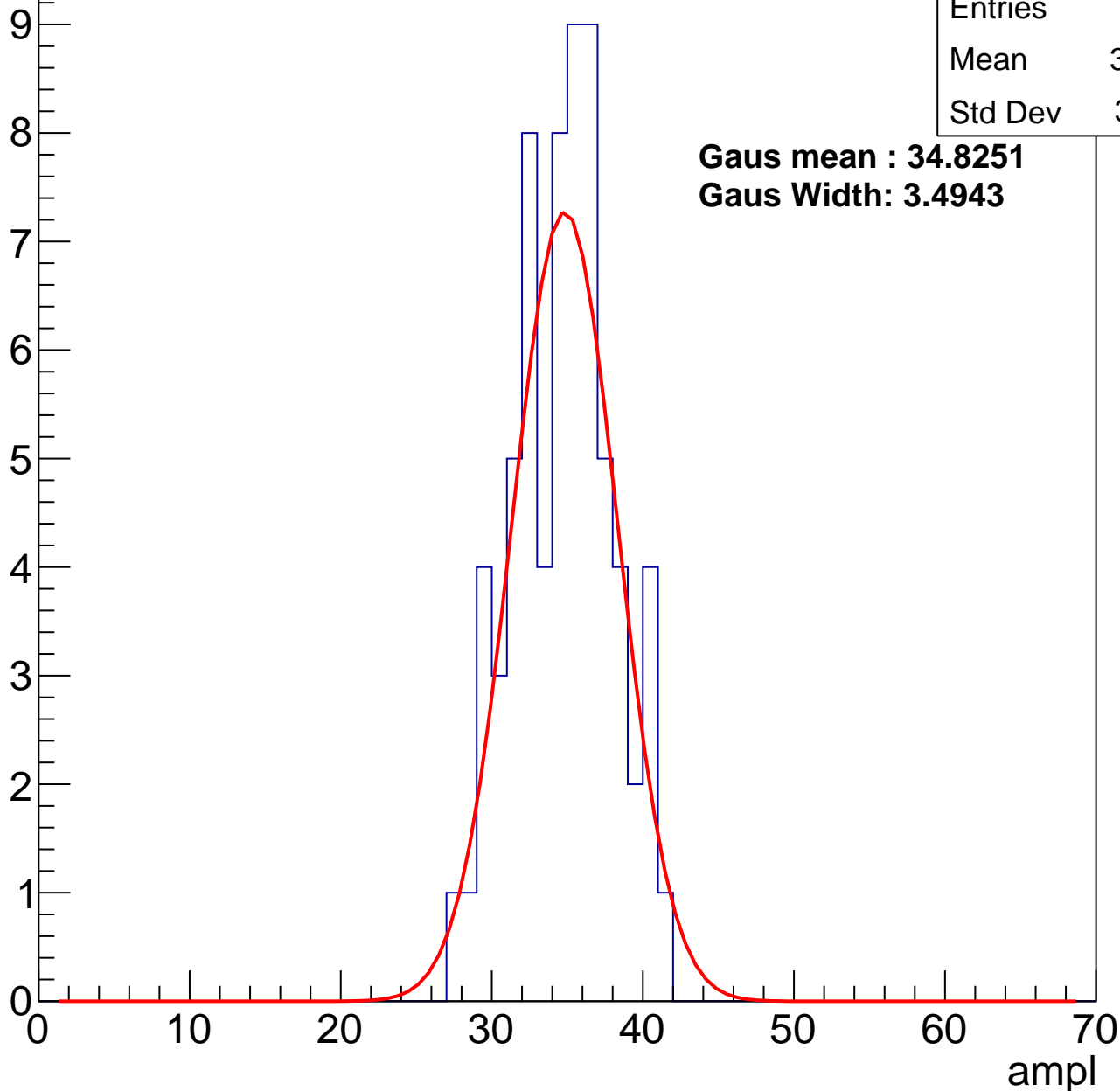
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	34.28
Std Dev	3.221

Gaus mean : 34.8251

Gaus Width: 3.4943



B0L001S, U23-ch85, adc1

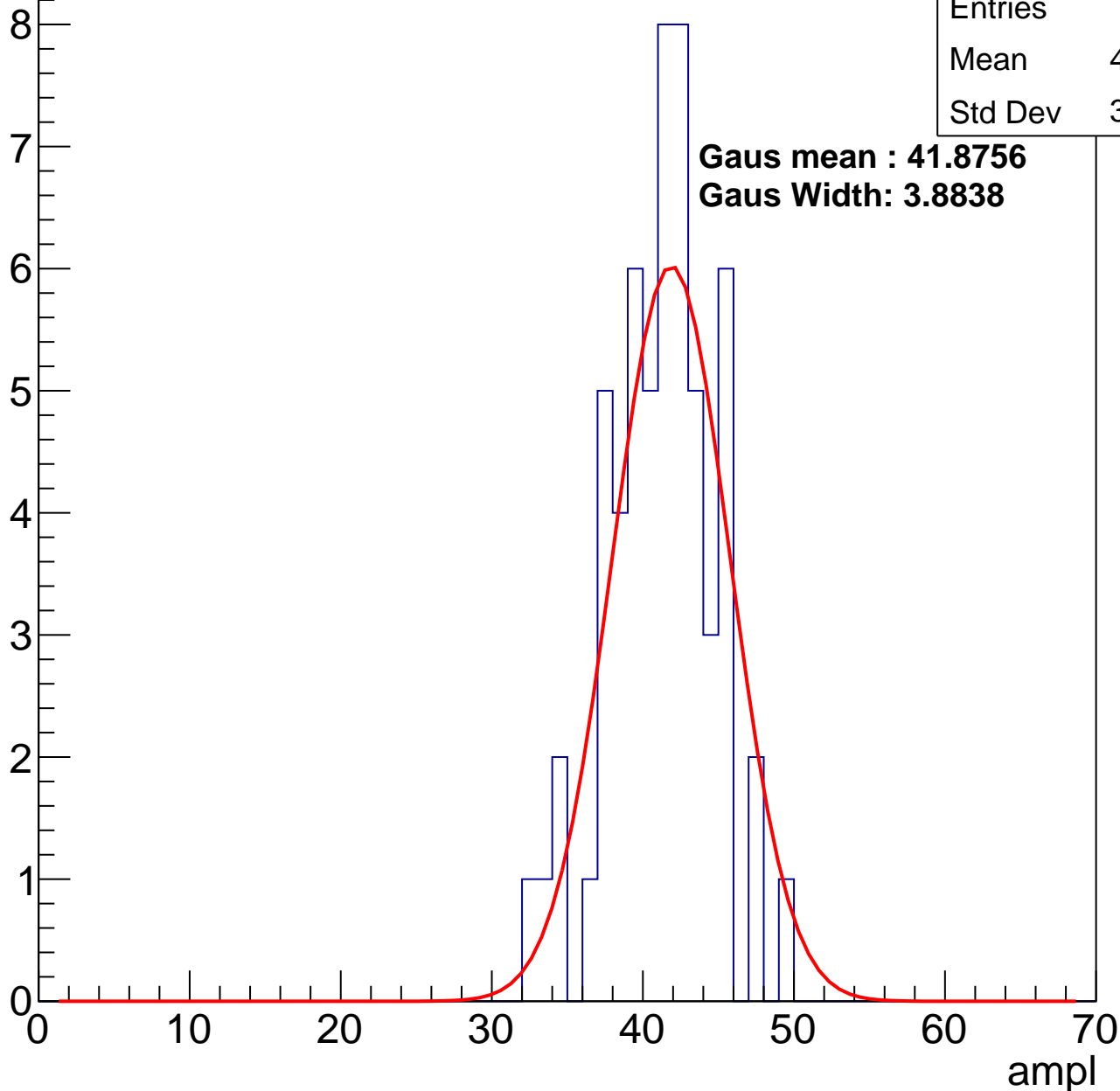
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	40.76
Std Dev	3.466

Gaus mean : 41.8756

Gaus Width: 3.8838



B0L001S, U23-ch85, adc2

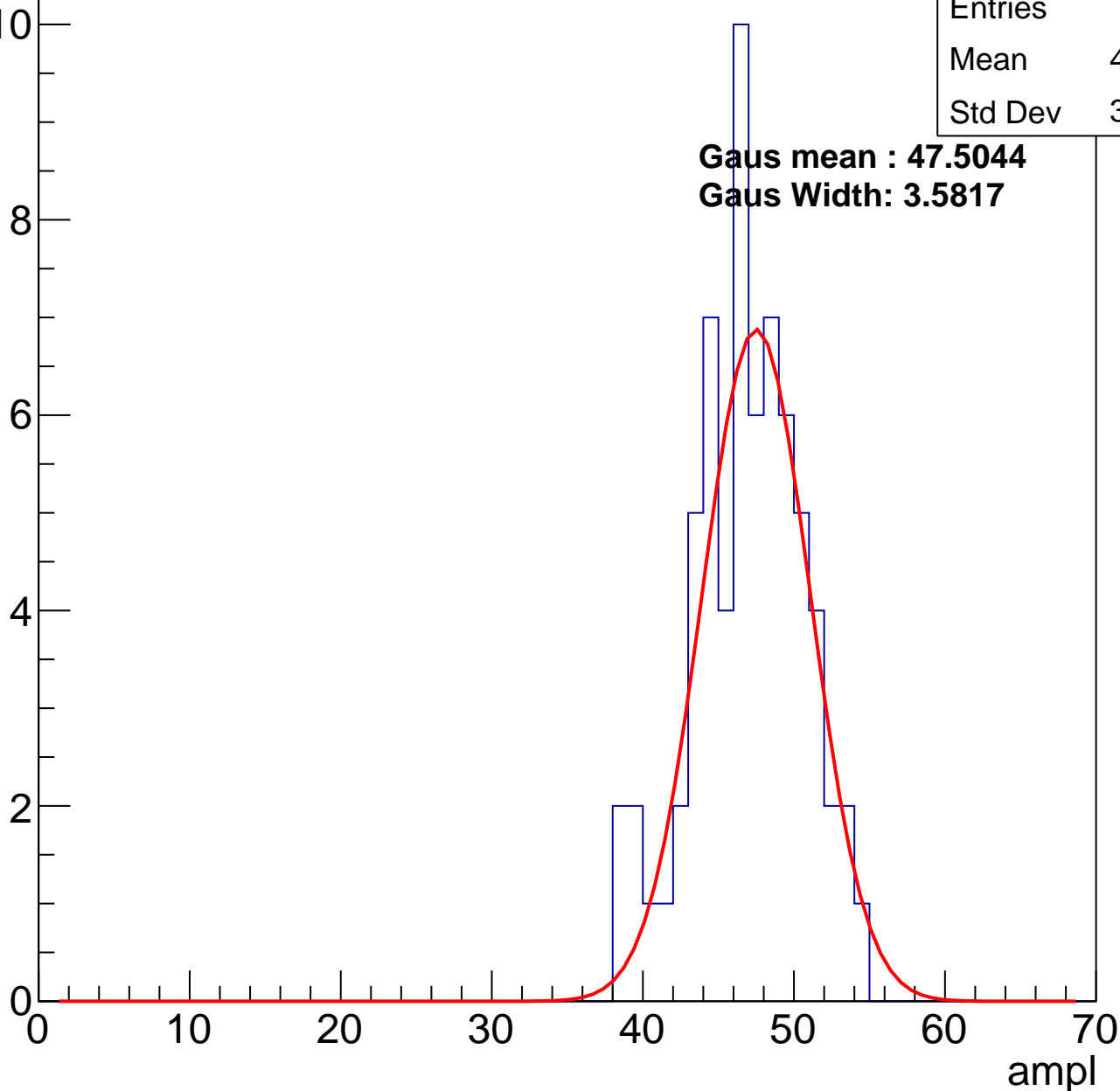
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	46.45
Std Dev	3.634

Gaus mean : 47.5044

Gaus Width: 3.5817

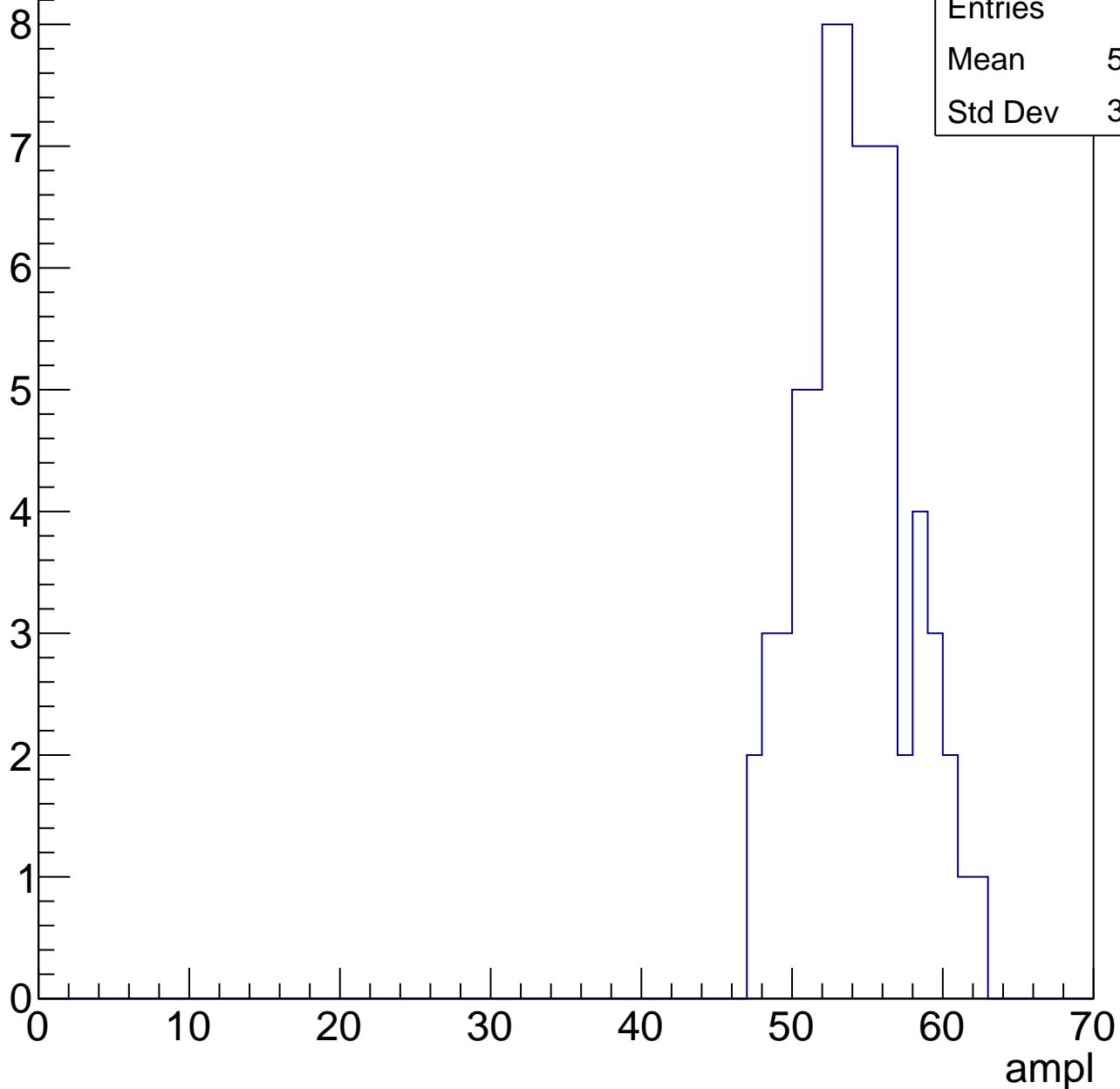


B0L001S, U23-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	53.69
Std Dev	3.465

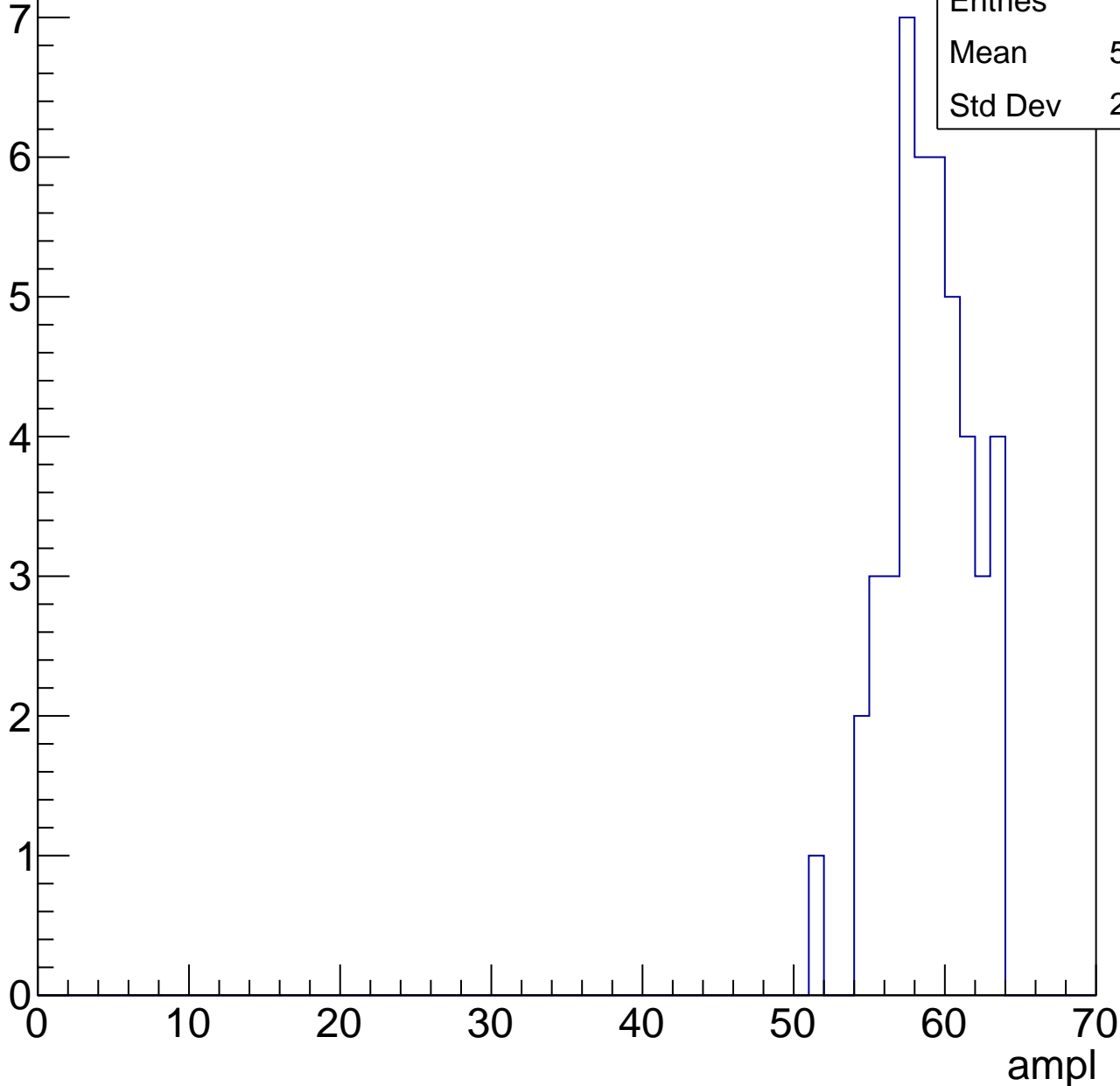


B0L001S, U23-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

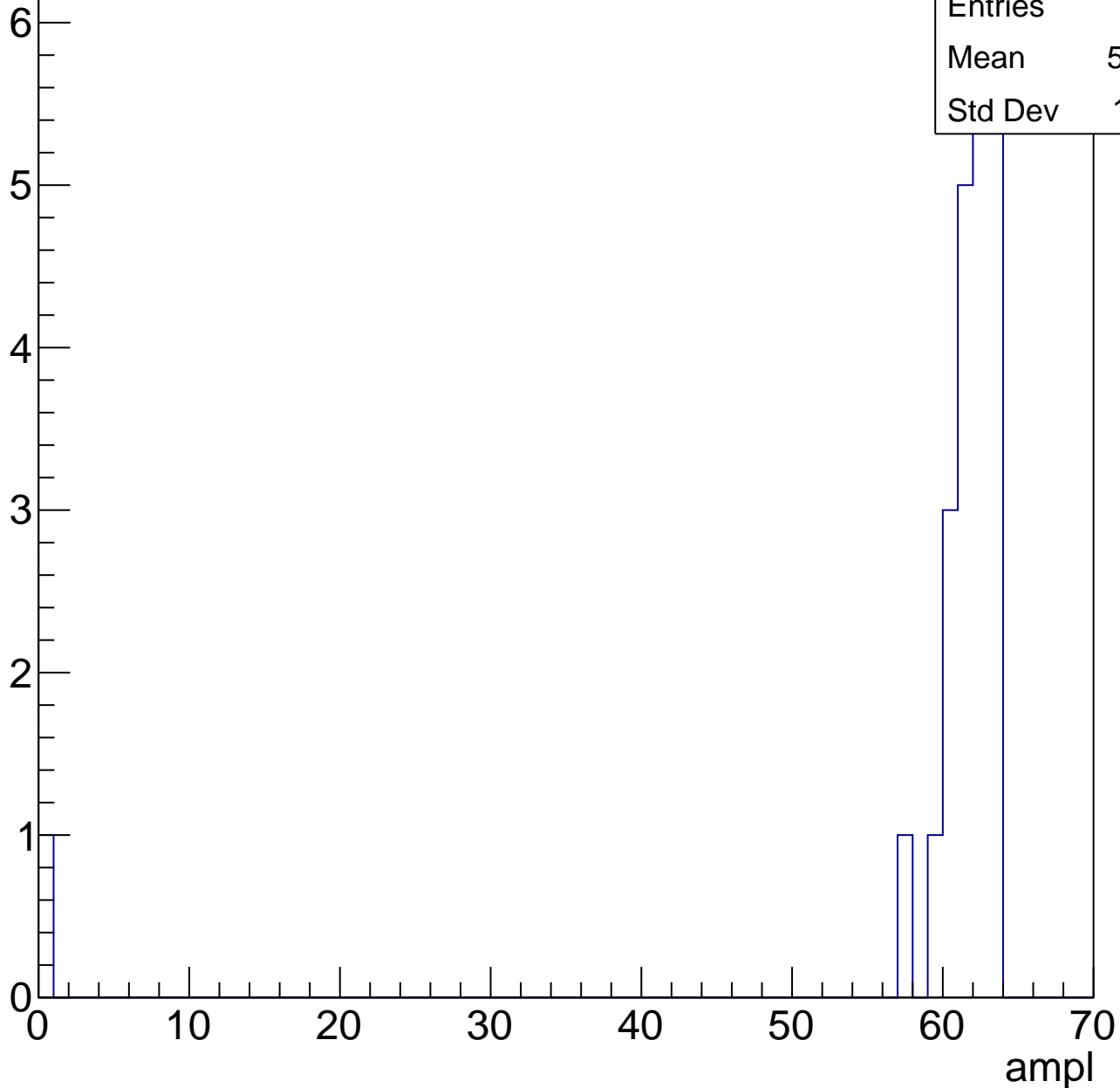
Entries	44
Mean	58.52
Std Dev	2.718



B0L001S, U23-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

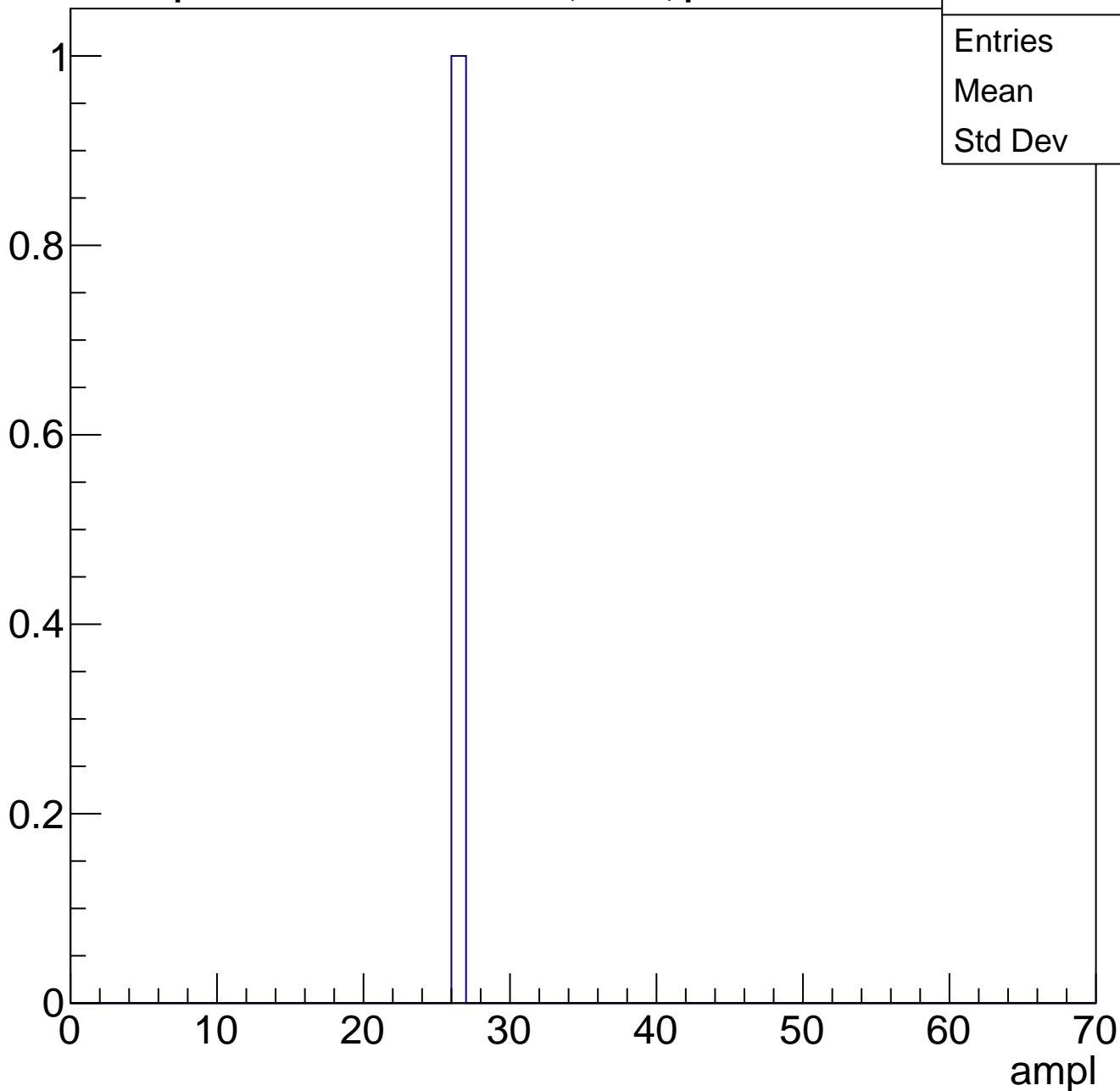


Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	26
Std Dev	0

B0L001S, U23-ch86, adc0

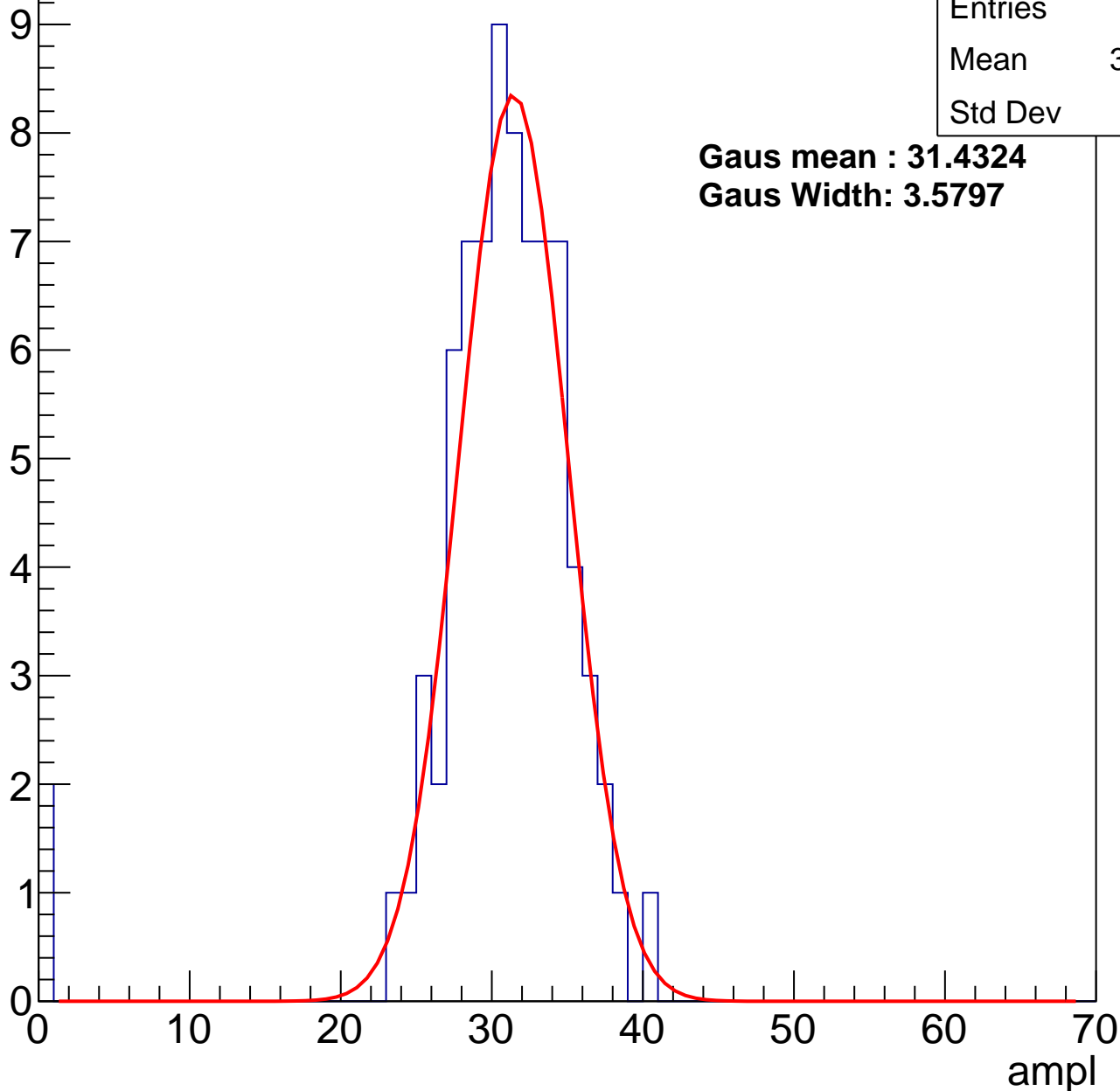
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.08
Std Dev	5.95

Gaus mean : 31.4324

Gaus Width: 3.5797



B0L001S, U23-ch86, adc1

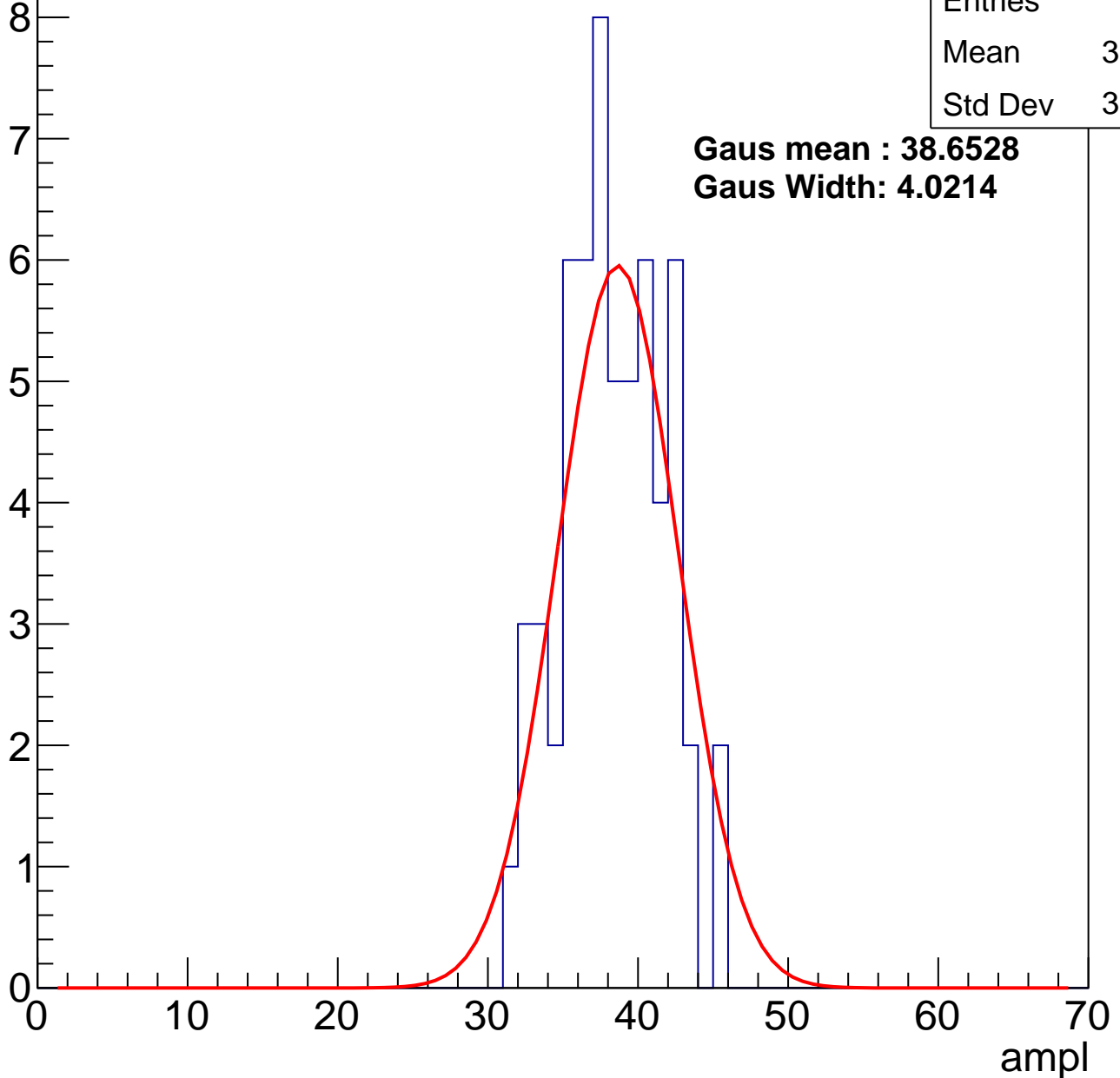
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.85
Std Dev	3.339

Gaus mean : 38.6528

Gaus Width: 4.0214



B0L001S, U23-ch86, adc2

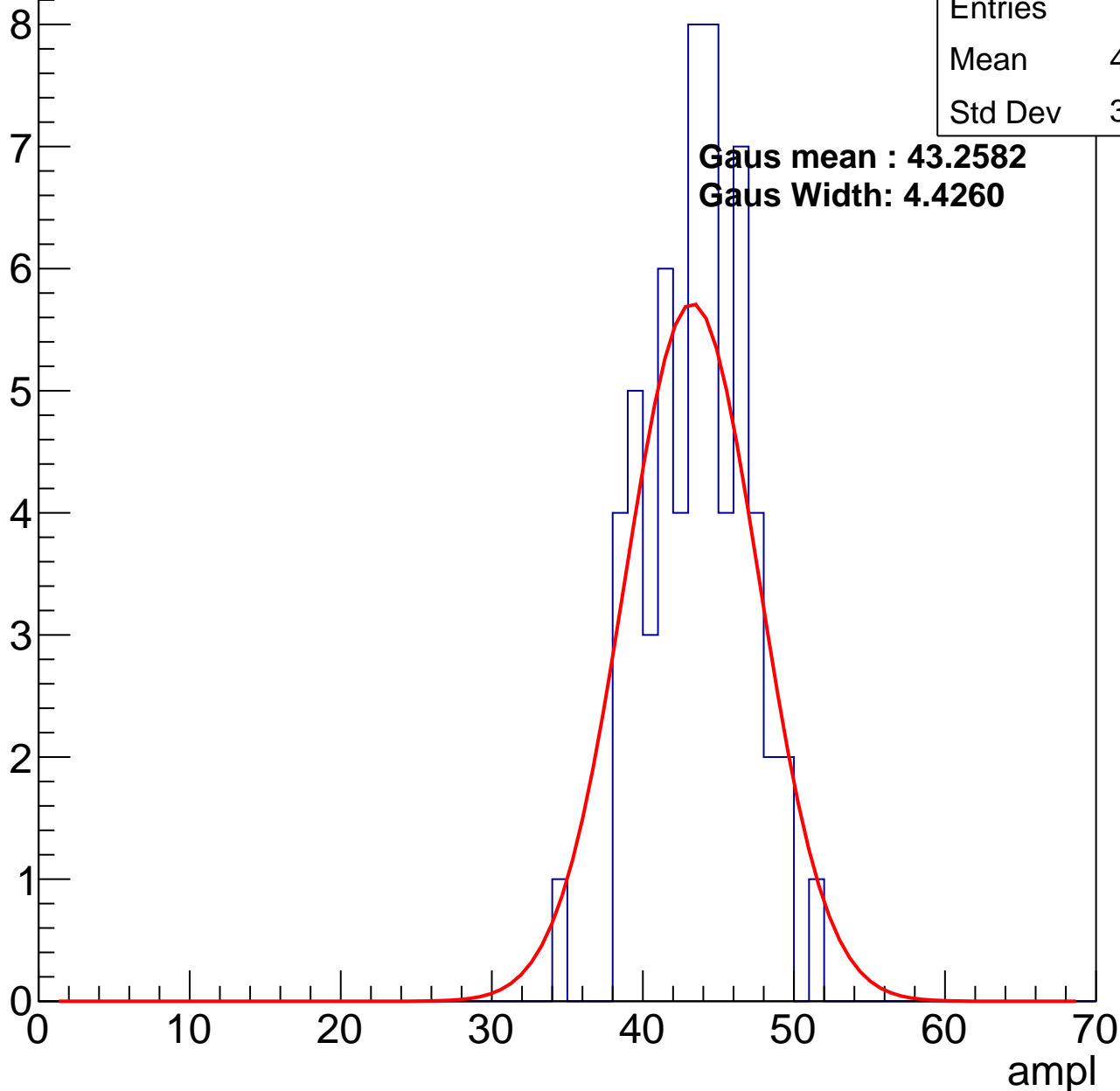
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.15
Std Dev	3.323

Gaus mean : 43.2582

Gaus Width: 4.4260

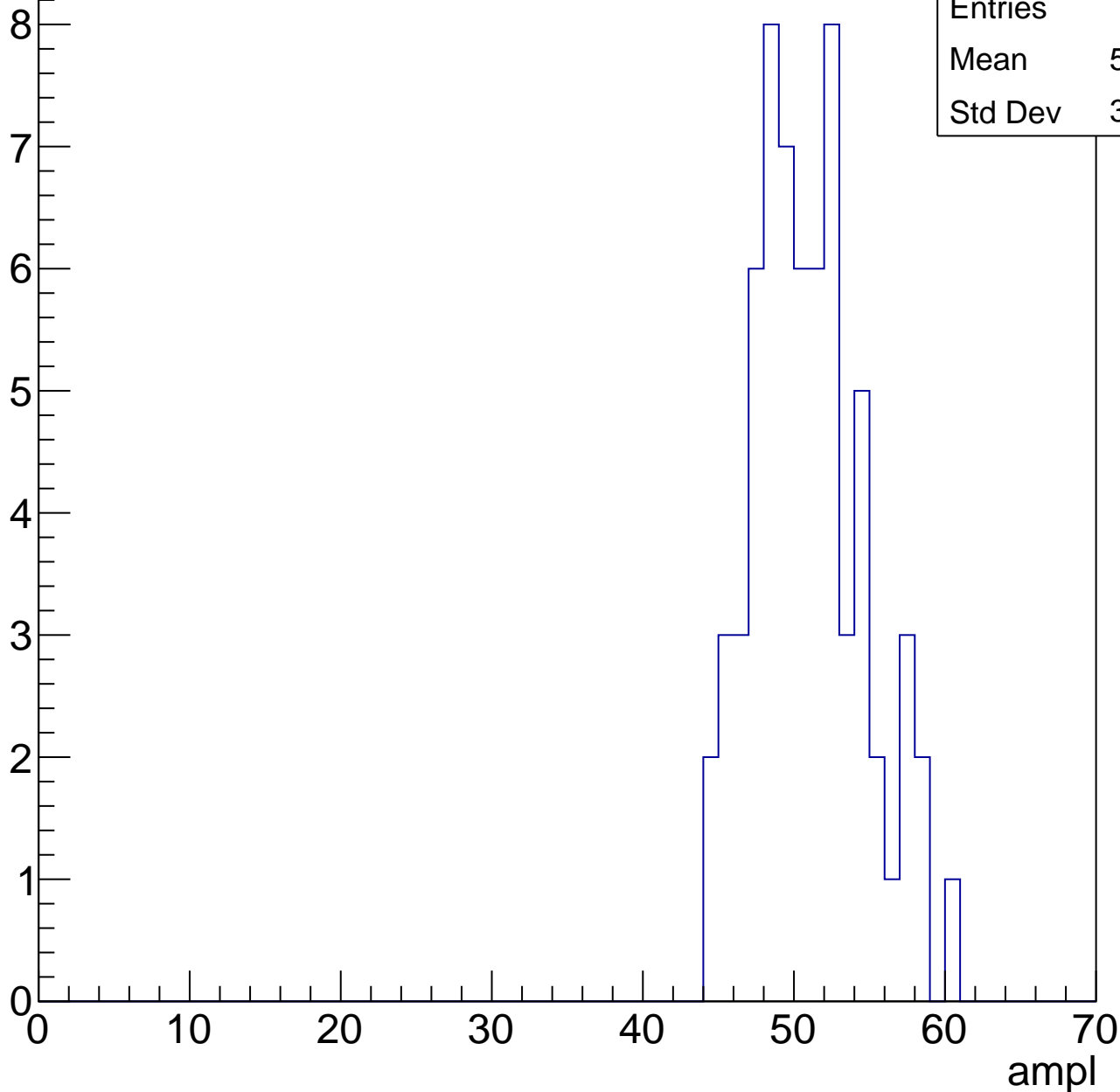


B0L001S, U23-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.52
Std Dev	3.644

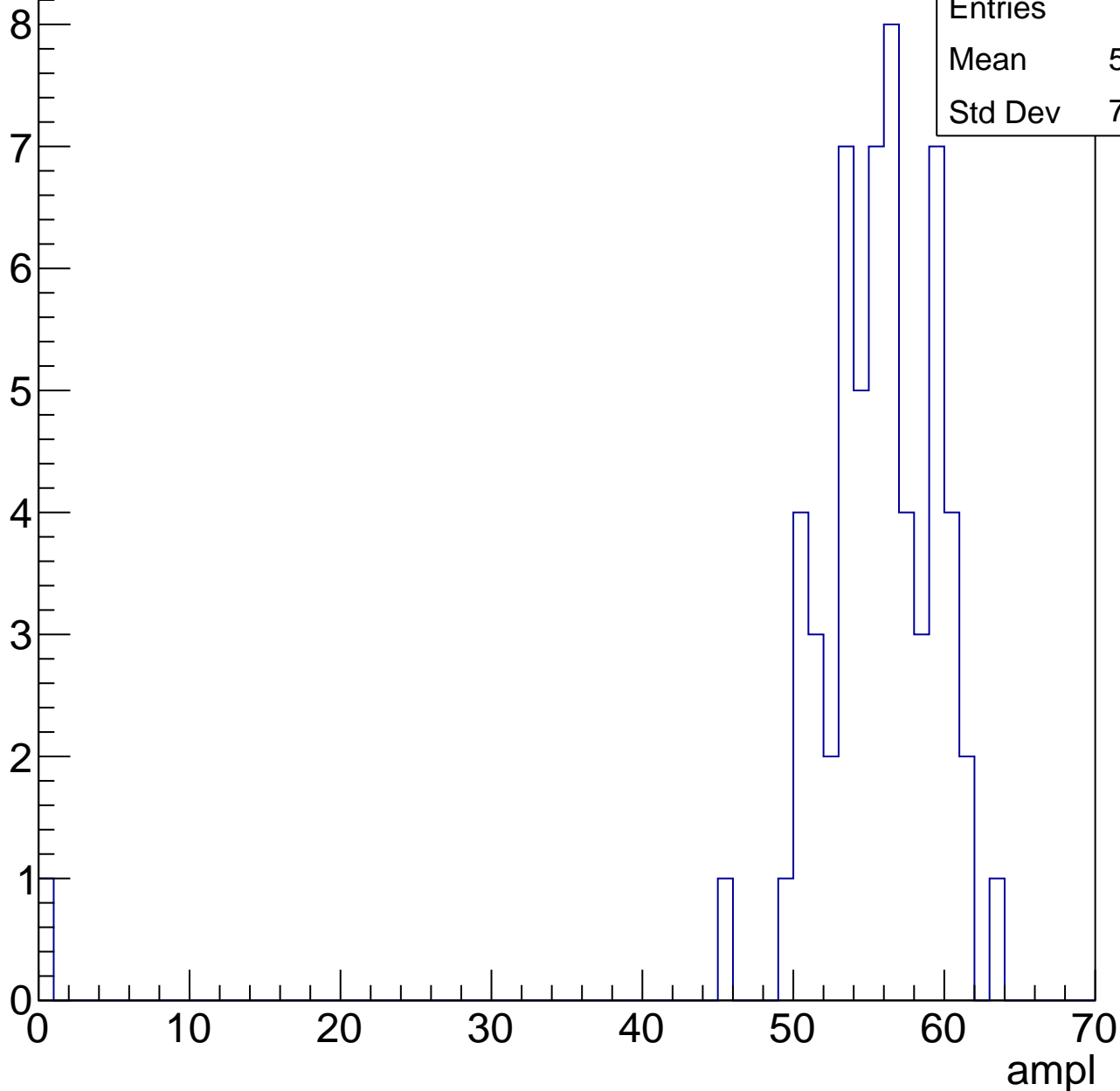


B0L001S, U23-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	54.42
Std Dev	7.894

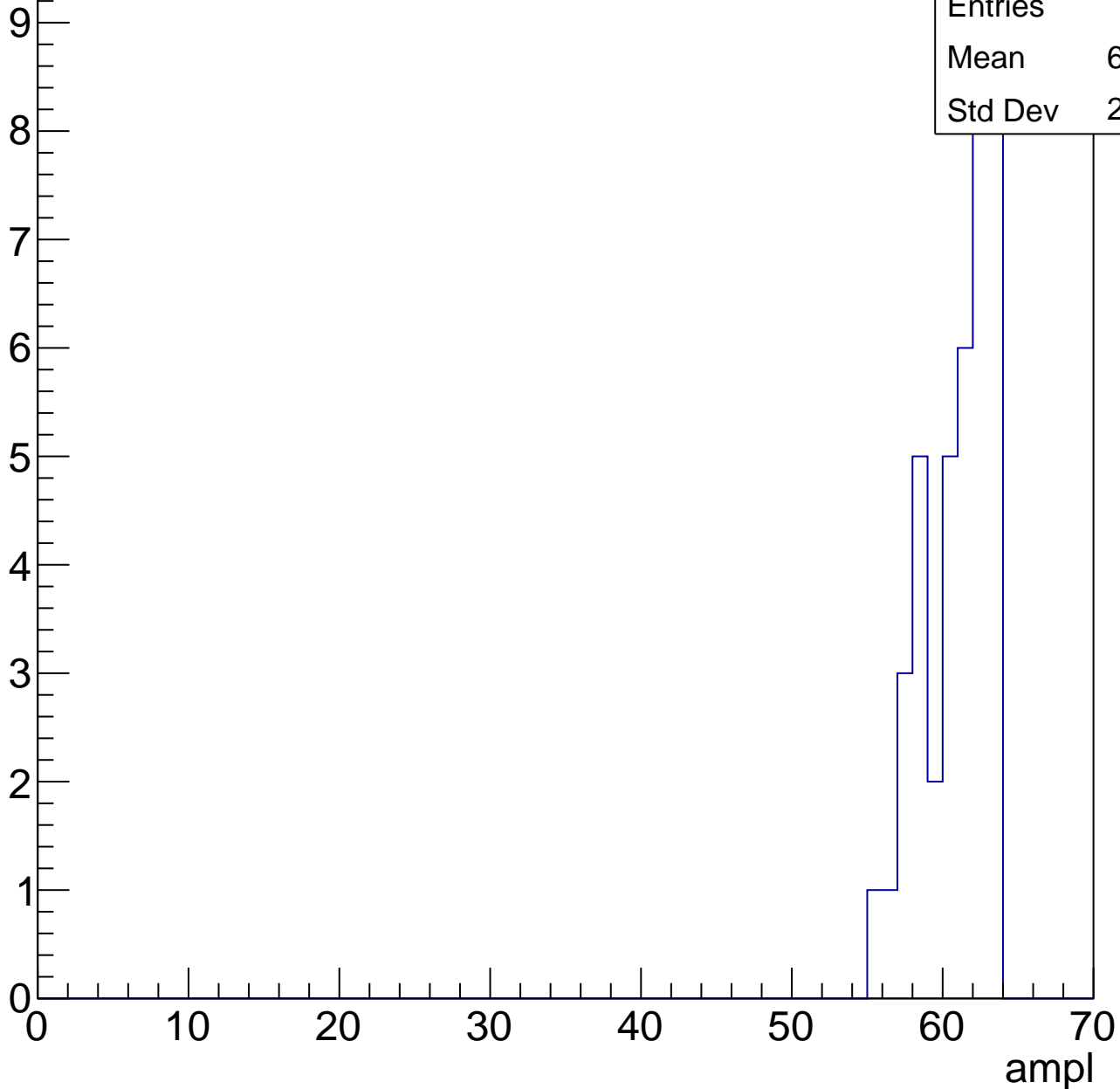


B0L001S, U23-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

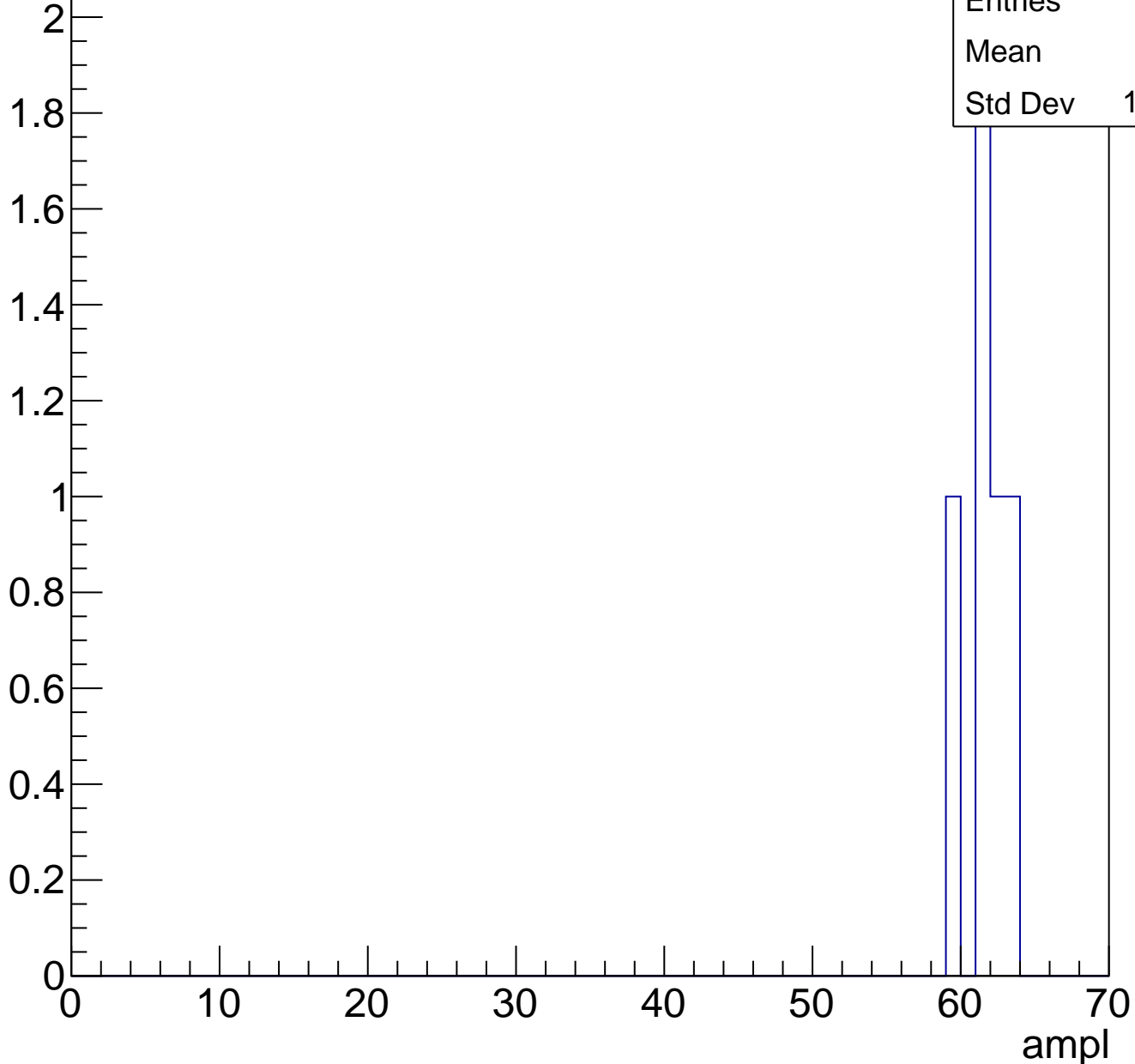
Entries	40
Mean	60.45
Std Dev	2.213



B0L001S, U23-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch87, adc0

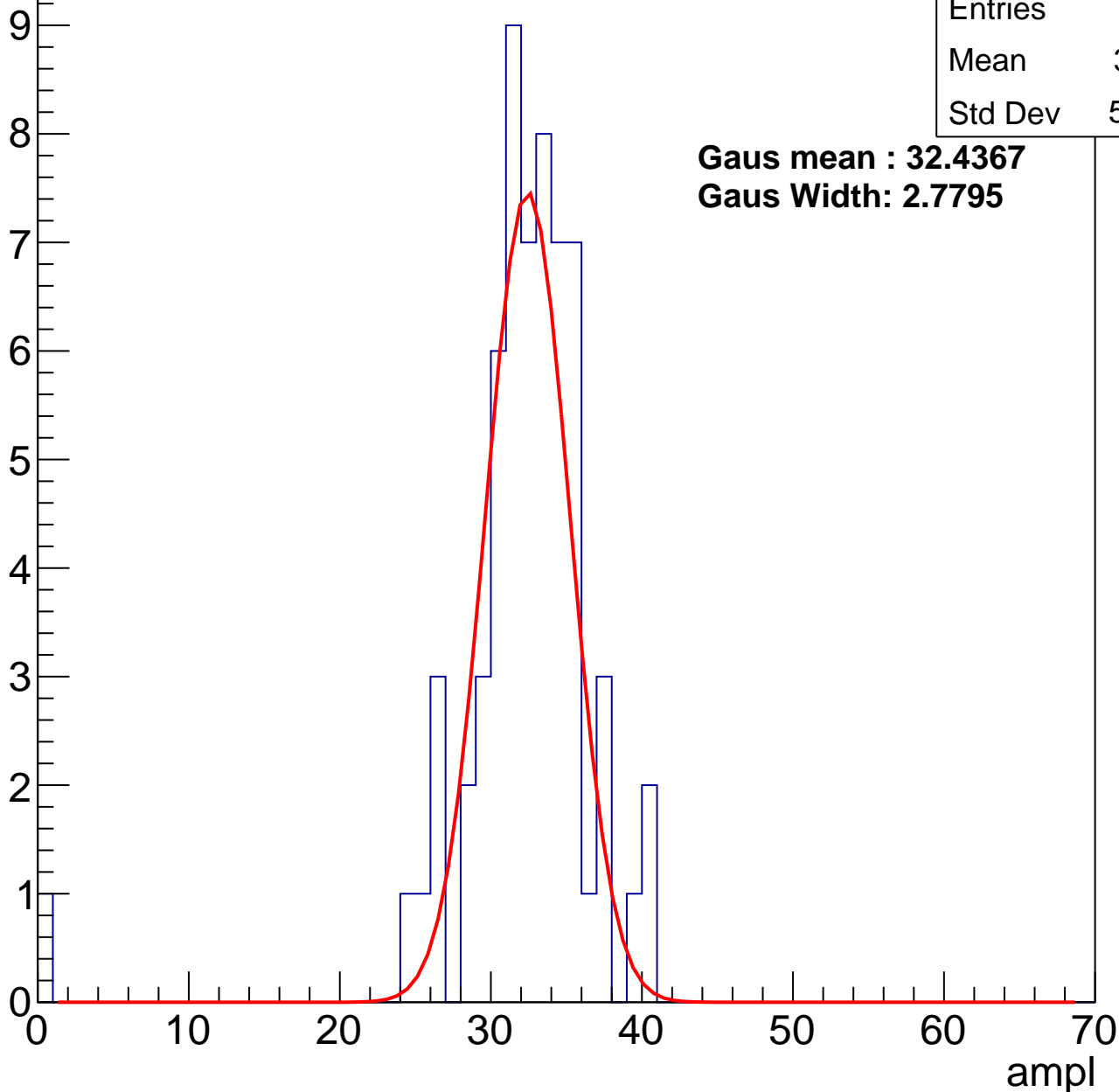
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.71
Std Dev	5.234

Gaus mean : 32.4367

Gaus Width: 2.7795



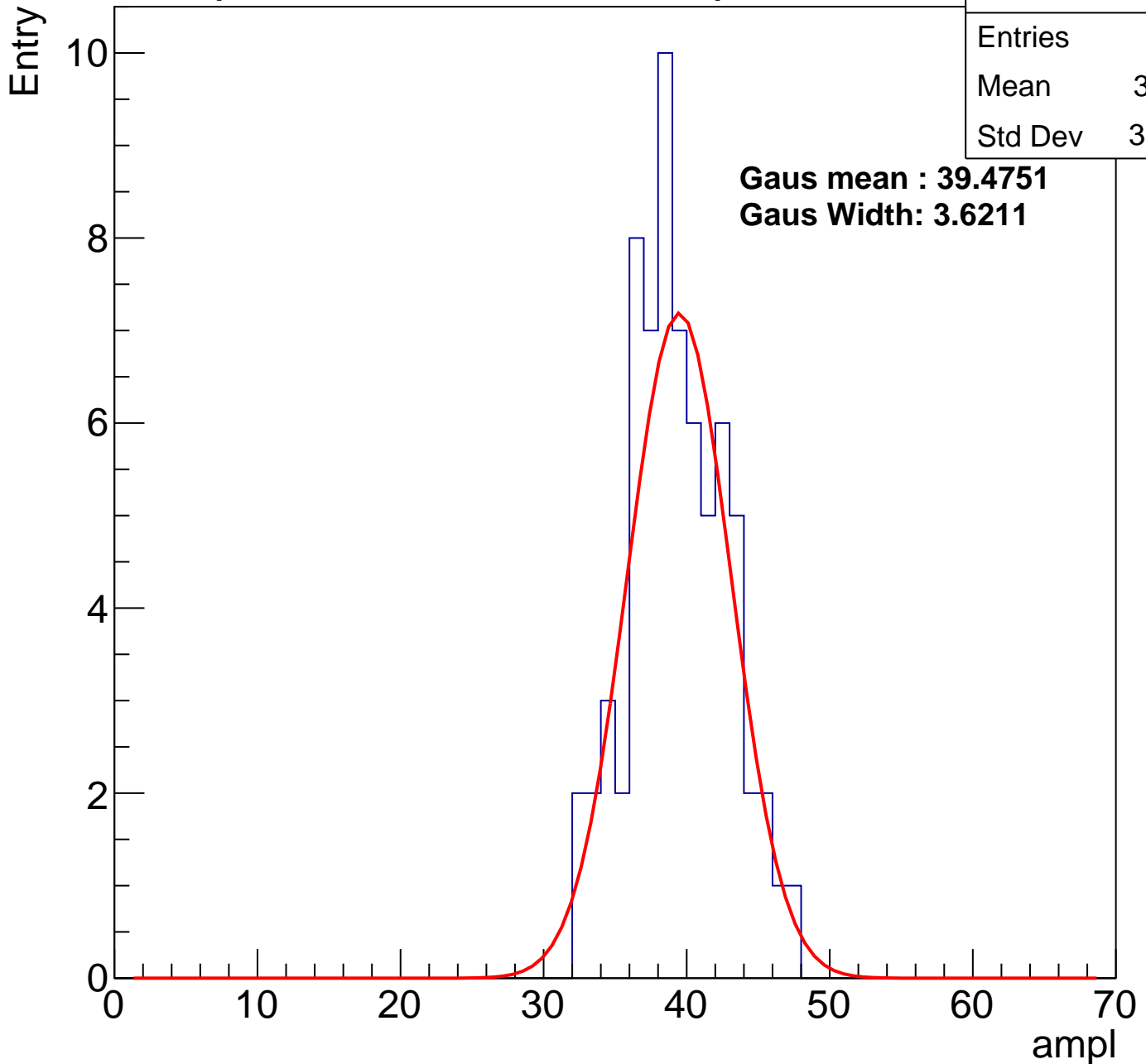
B0L001S, U23-ch87, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	38.91
Std Dev	3.365

Gaus mean : 39.4751

Gaus Width: 3.6211



B0L001S, U23-ch87, adc2

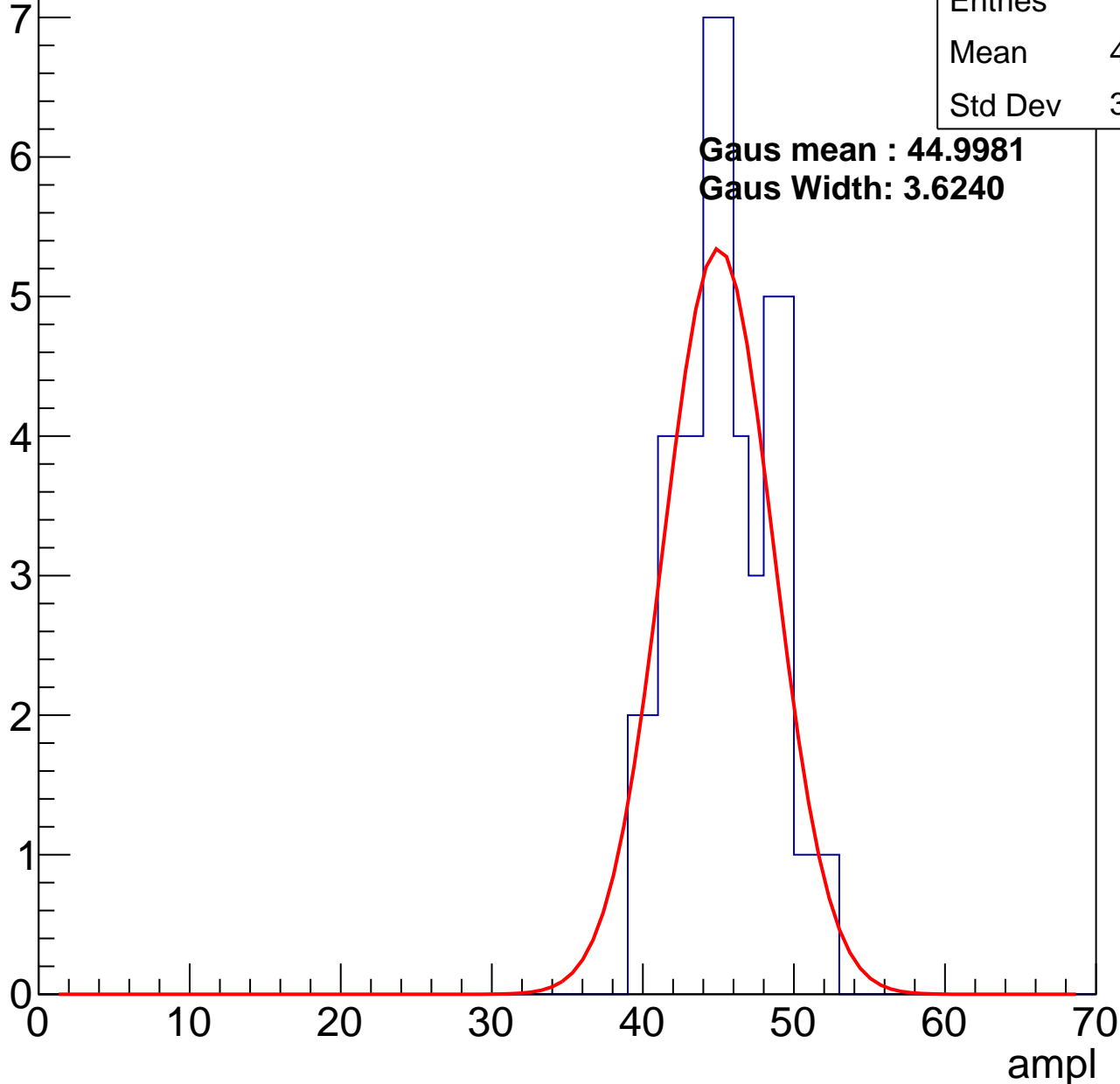
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	44.96
Std Dev	3.149

Gaus mean : 44.9981

Gaus Width: 3.6240

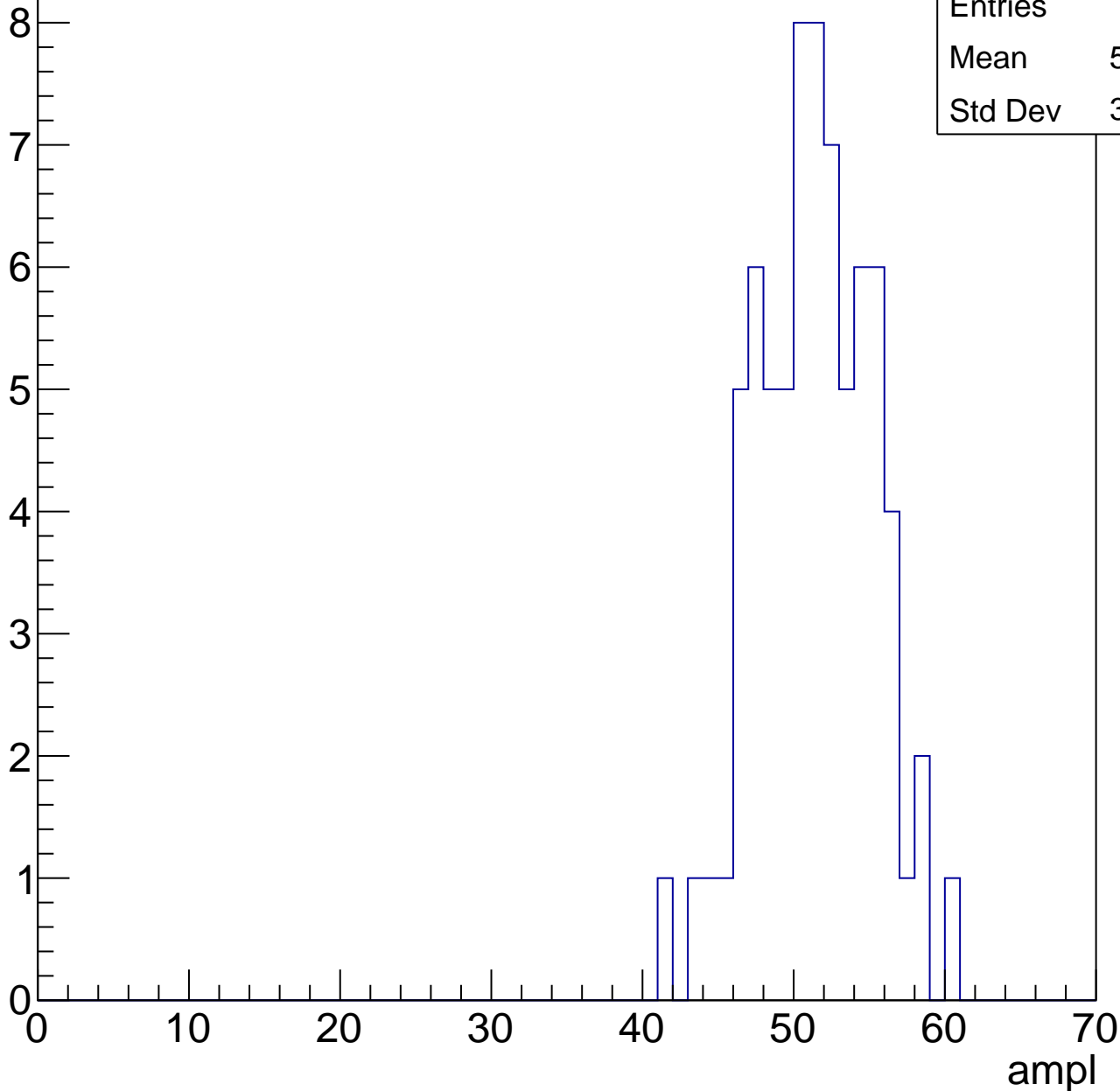


B0L001S, U23-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.93
Std Dev	3.765

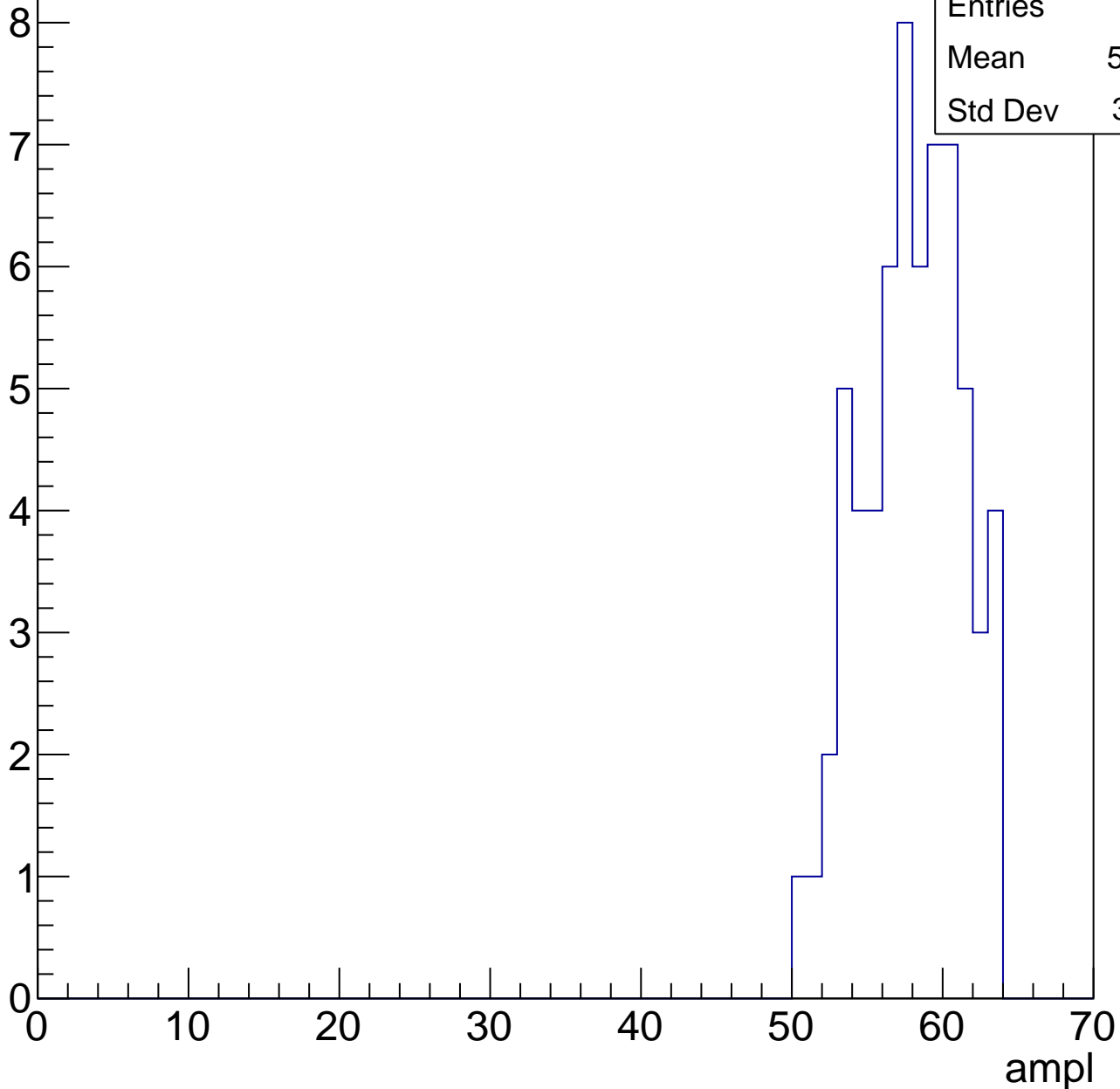


B0L001S, U23-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.49
Std Dev	3.221

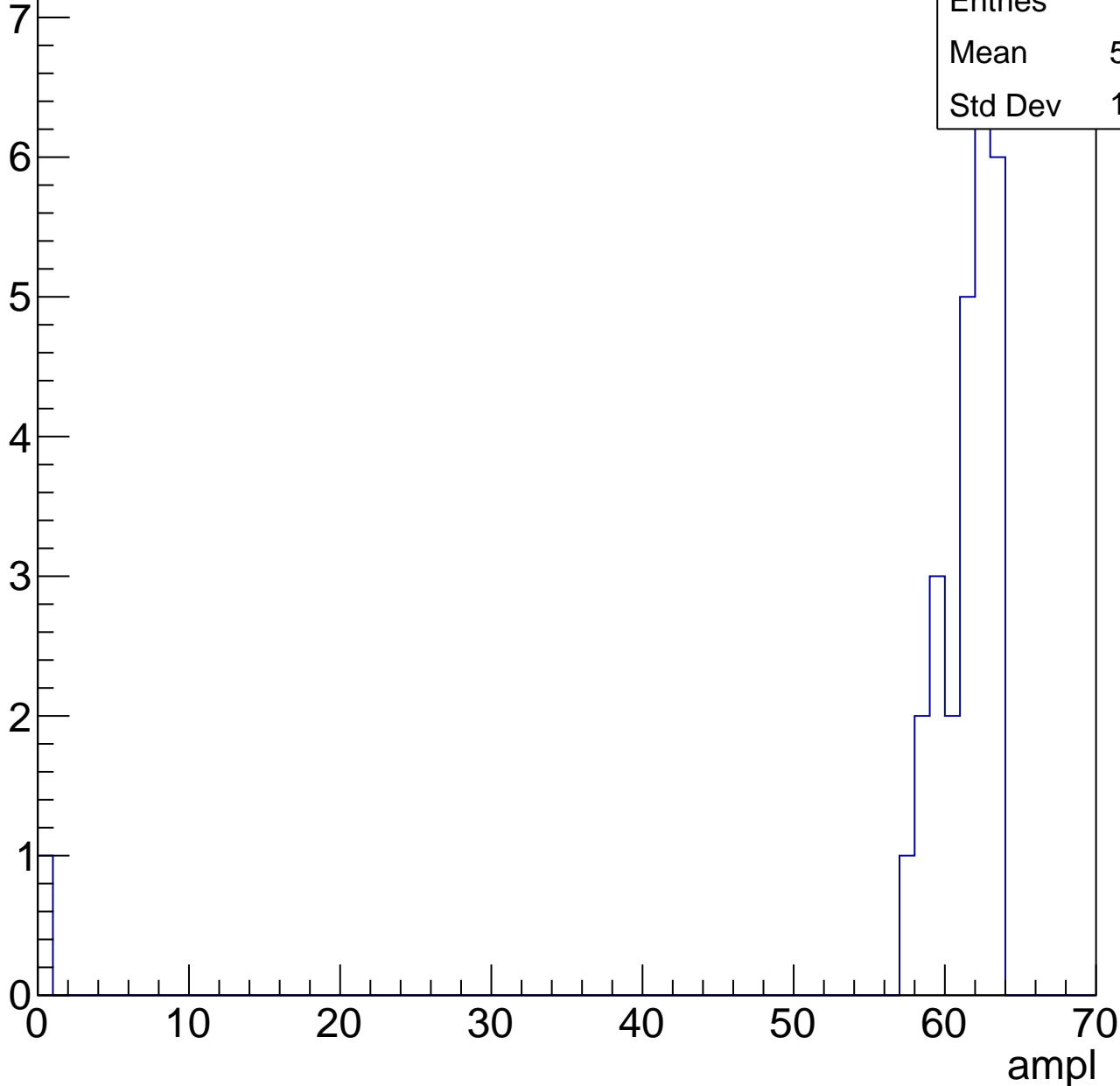


B0L001S, U23-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.78
Std Dev	11.65



B0L001S, U23-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U23-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch88, adc0

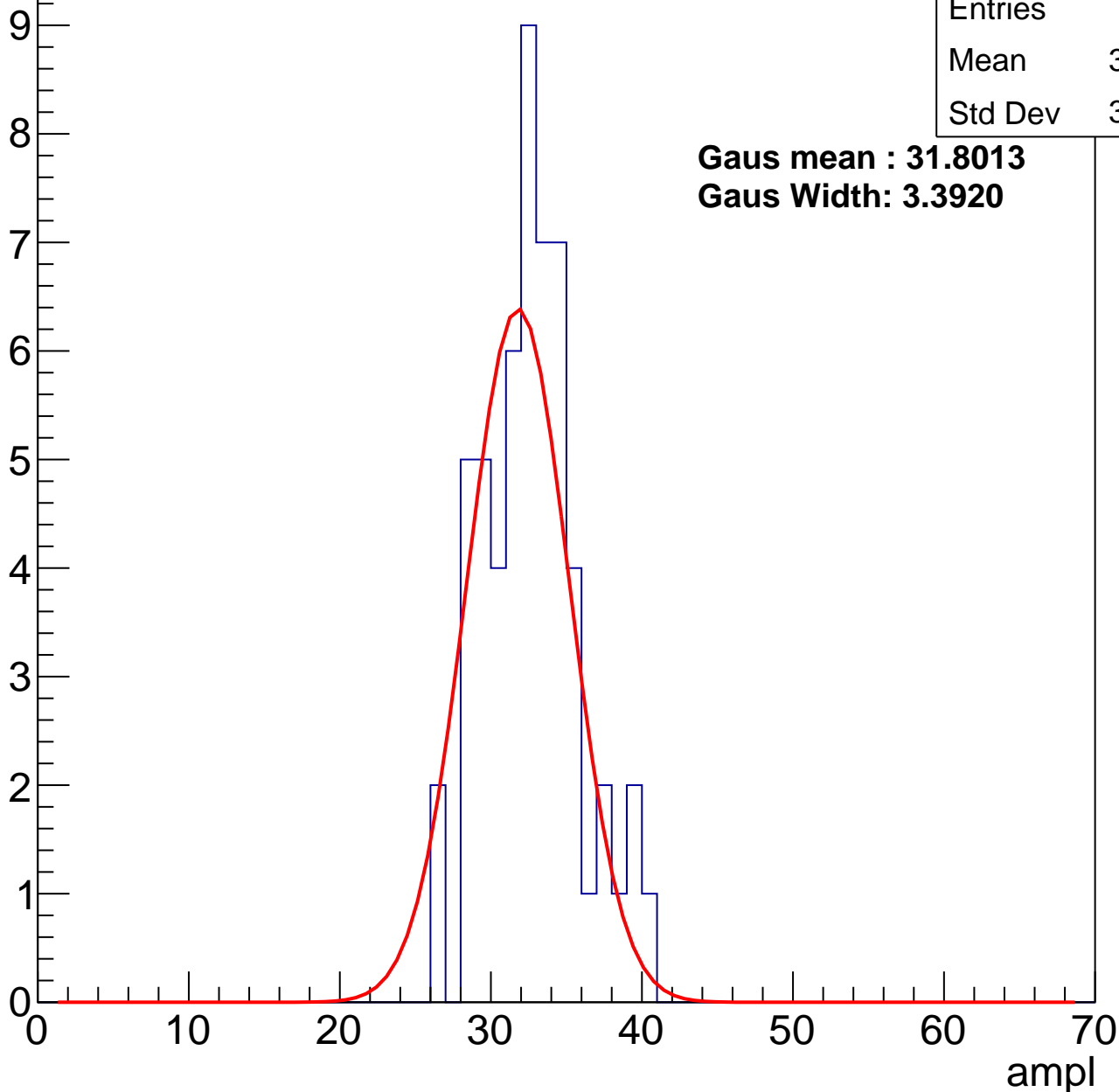
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	32.25
Std Dev	3.135

Gaus mean : 31.8013

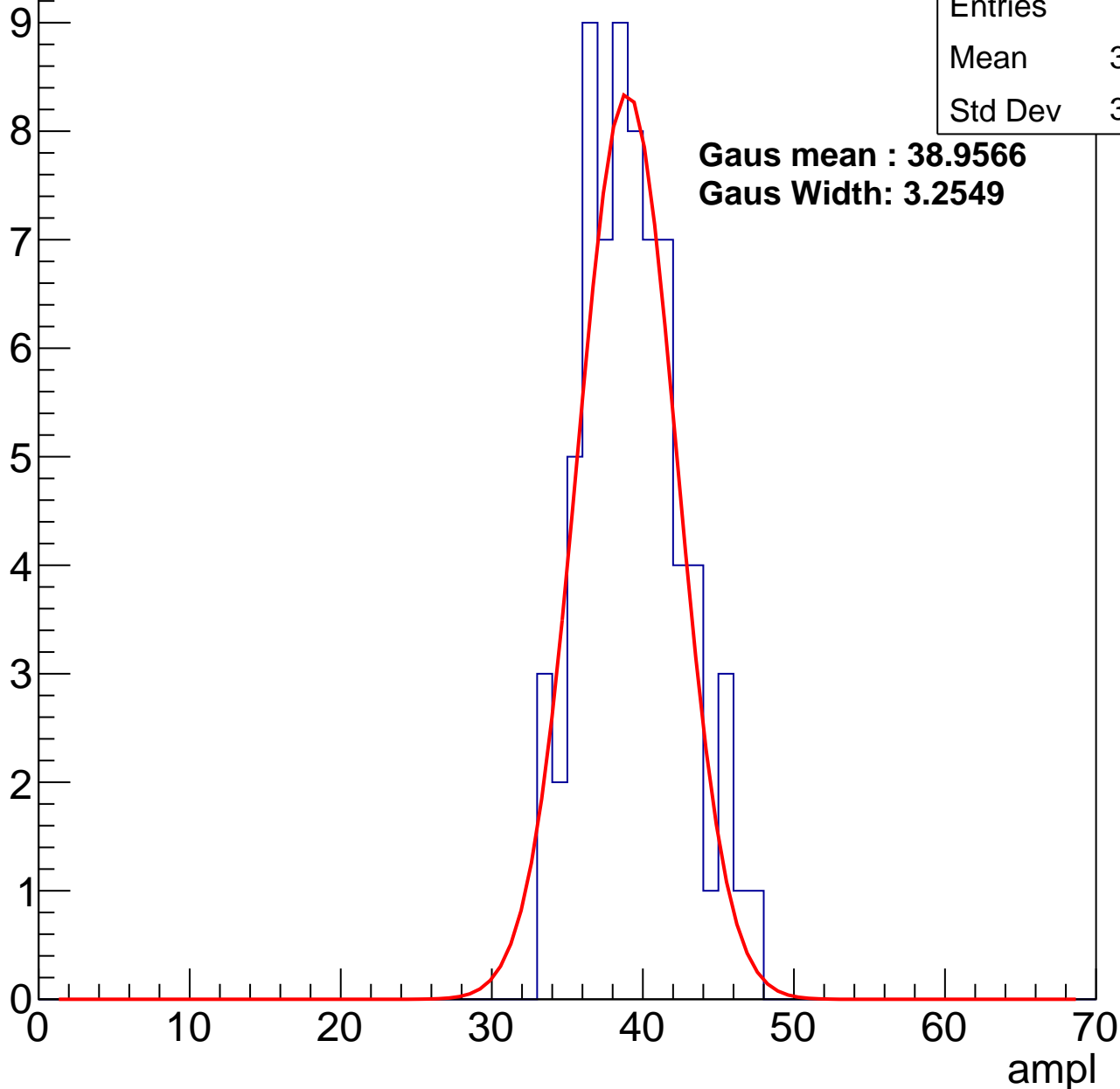
Gaus Width: 3.3920



B0L001S, U23-ch88, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch88, adc2

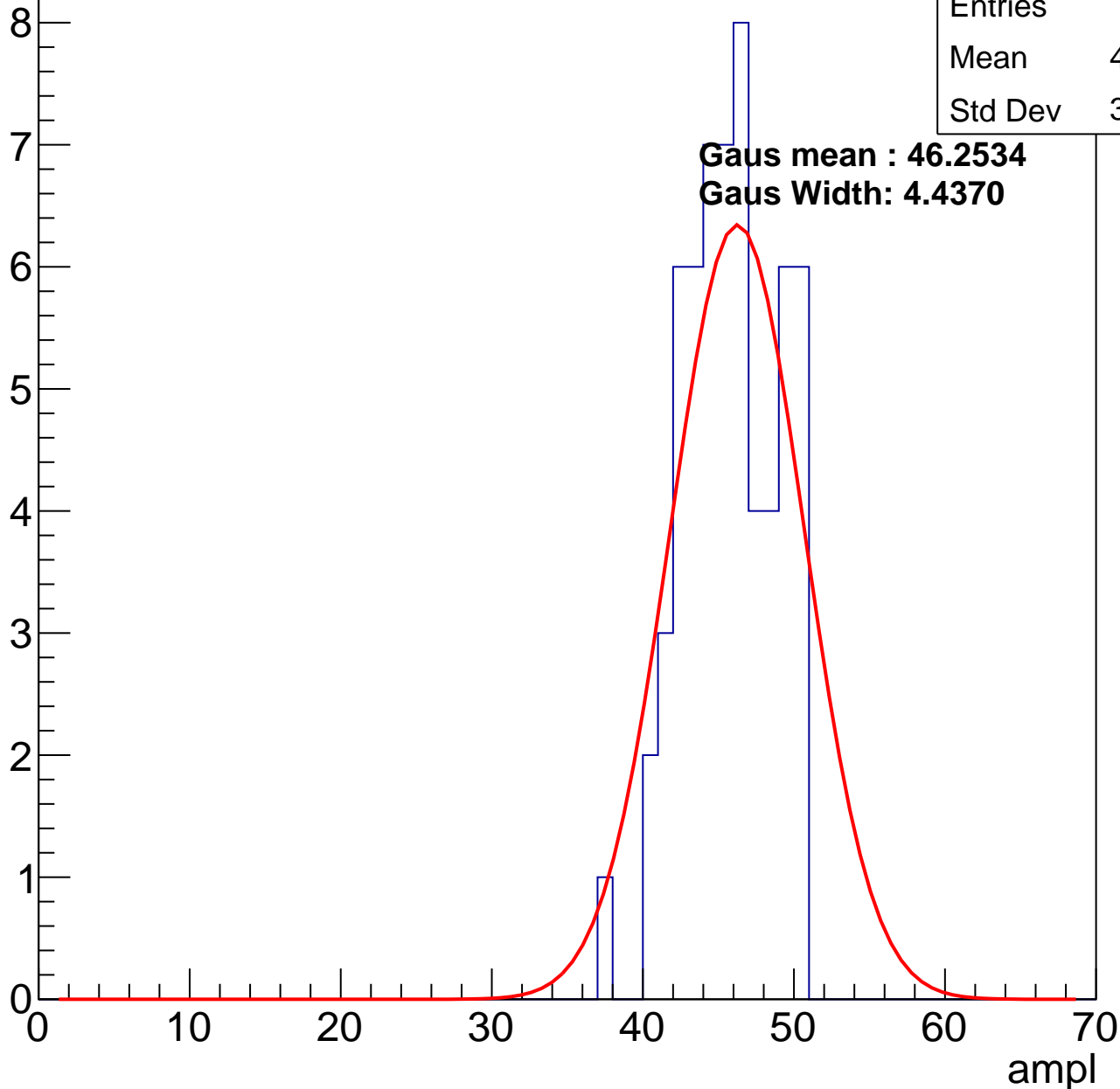
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45.25
Std Dev	3.026

Gaus mean : 46.2534

Gaus Width: 4.4370

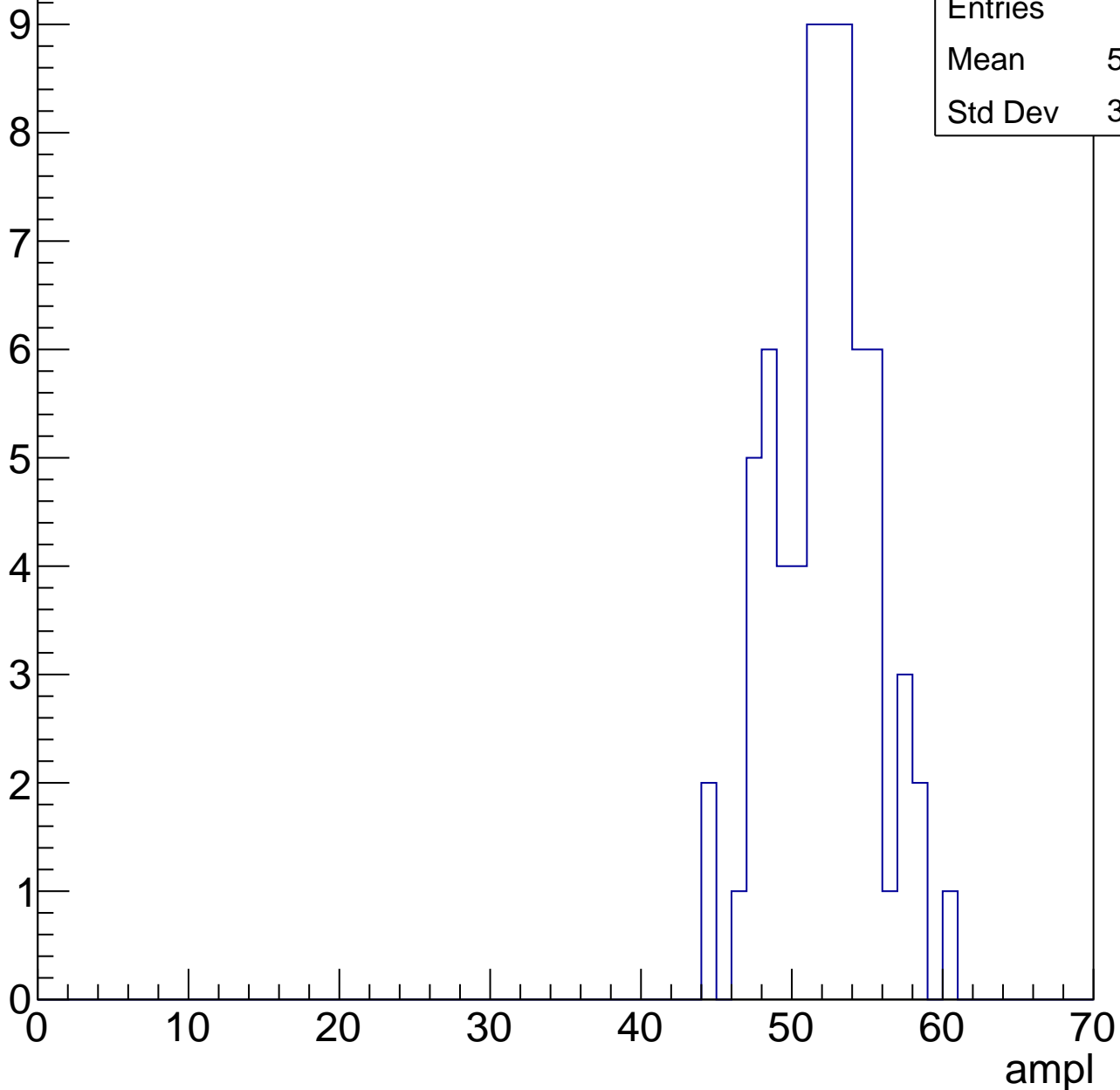


B0L001S, U23-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.68
Std Dev	3.327

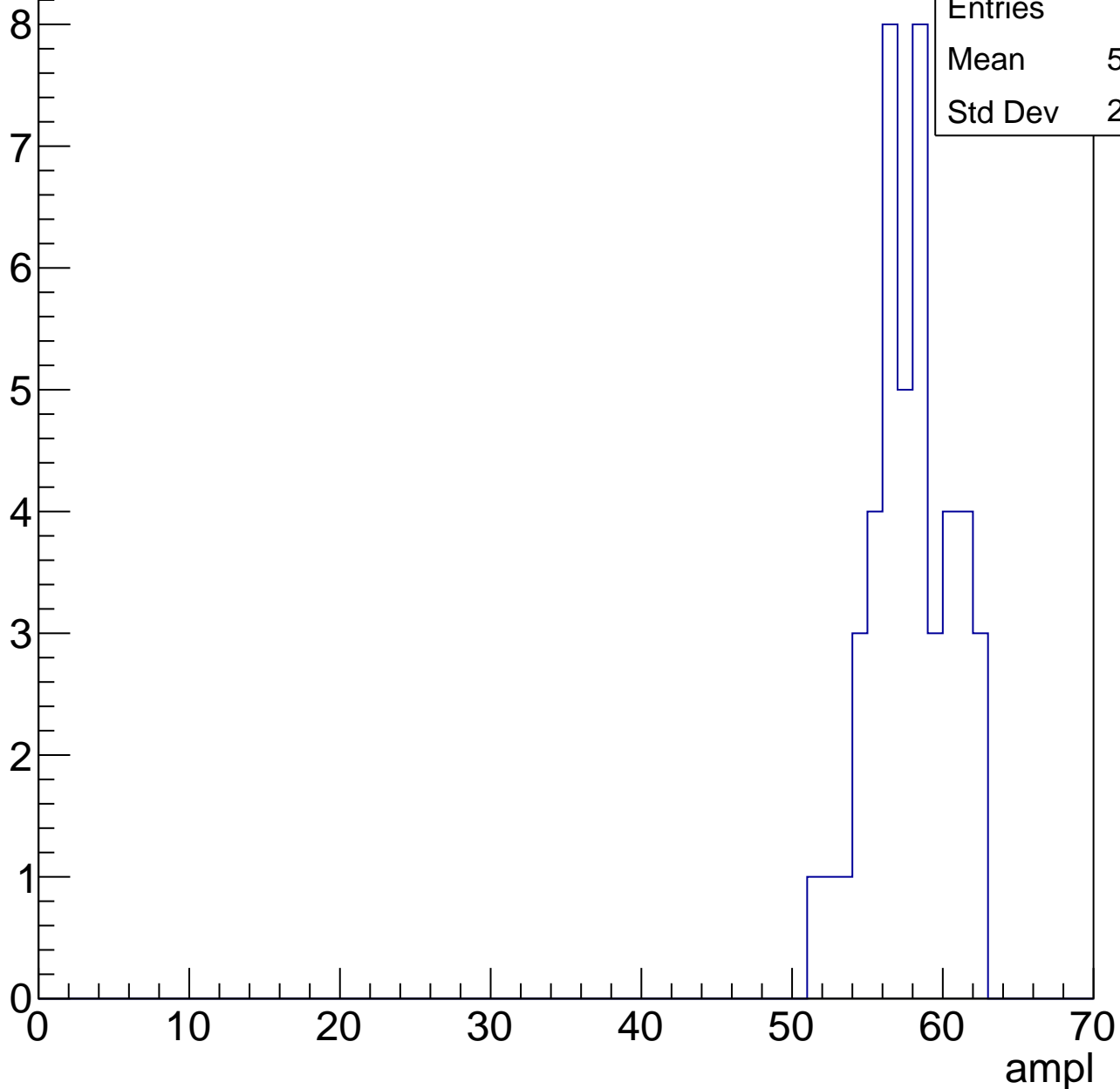


B0L001S, U23-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.38
Std Dev	2.652

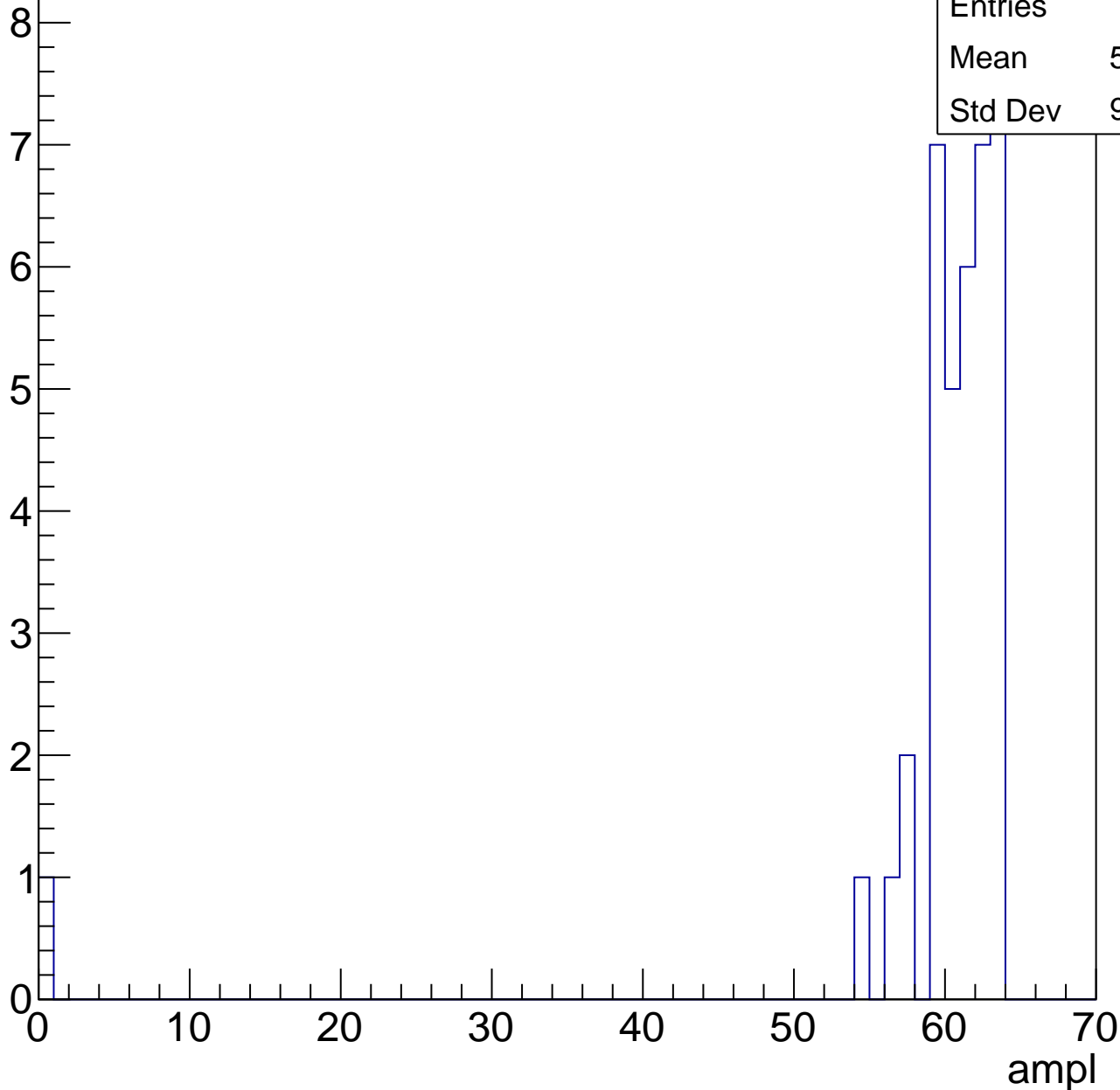


B0L001S, U23-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.97
Std Dev	9.925



B0L001S, U23-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L001S, U23-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch89, adc0

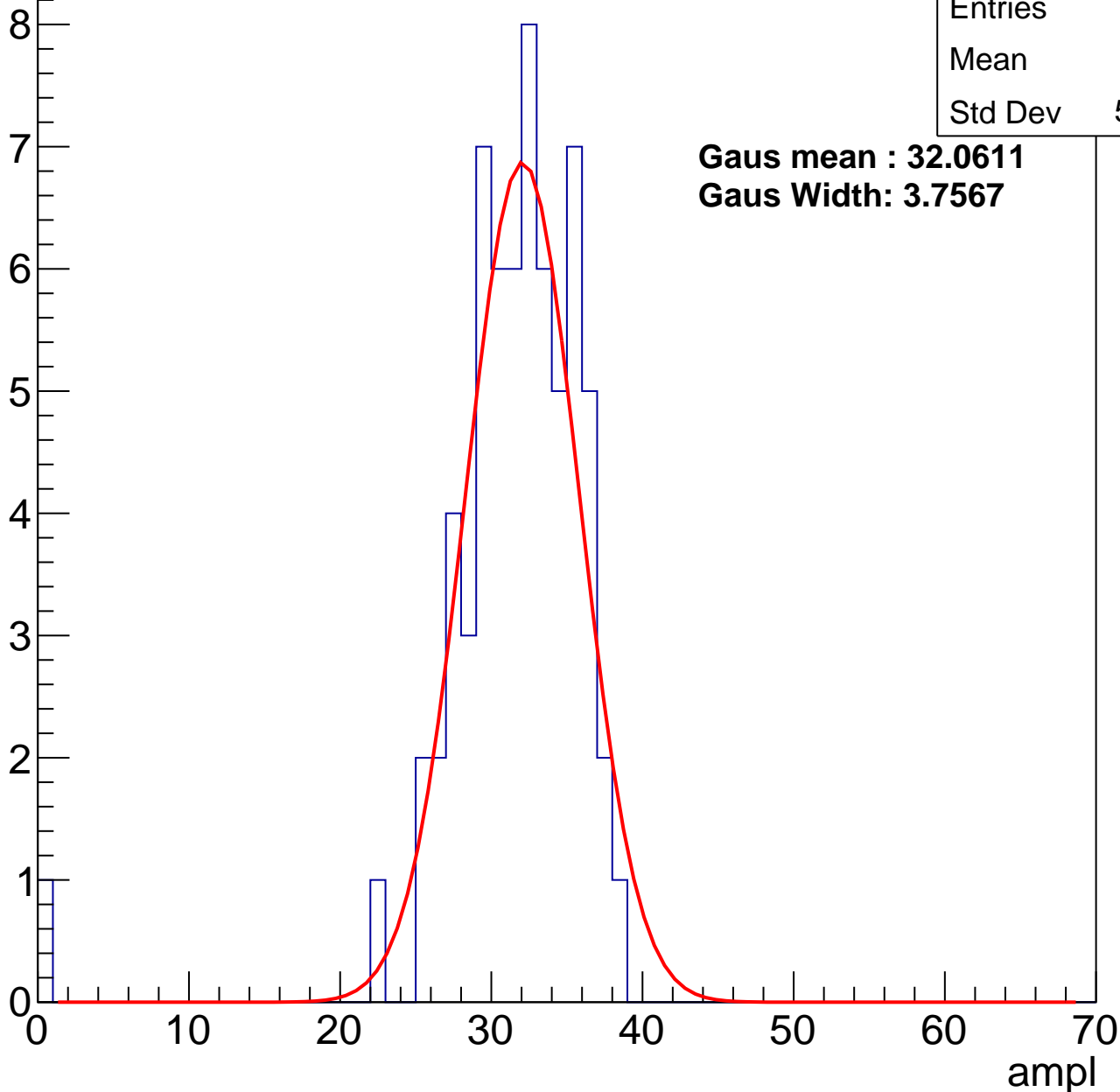
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	31
Std Dev	5.111

Gaus mean : 32.0611

Gaus Width: 3.7567



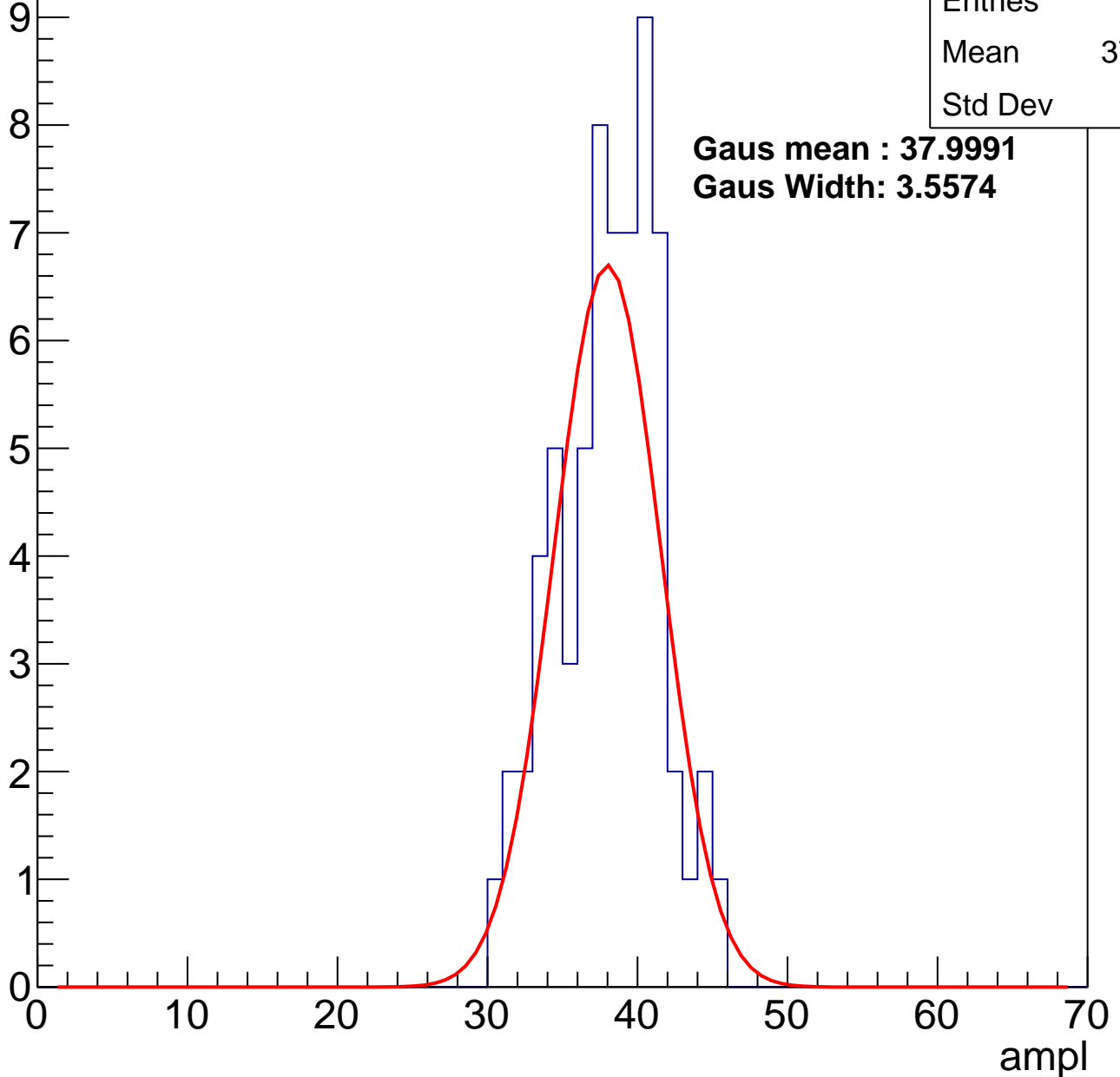
B0L001S, U23-ch89, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.65
Std Dev	3.36

Gaus mean : 37.9991
Gaus Width: 3.5574



B0L001S, U23-ch89, adc2

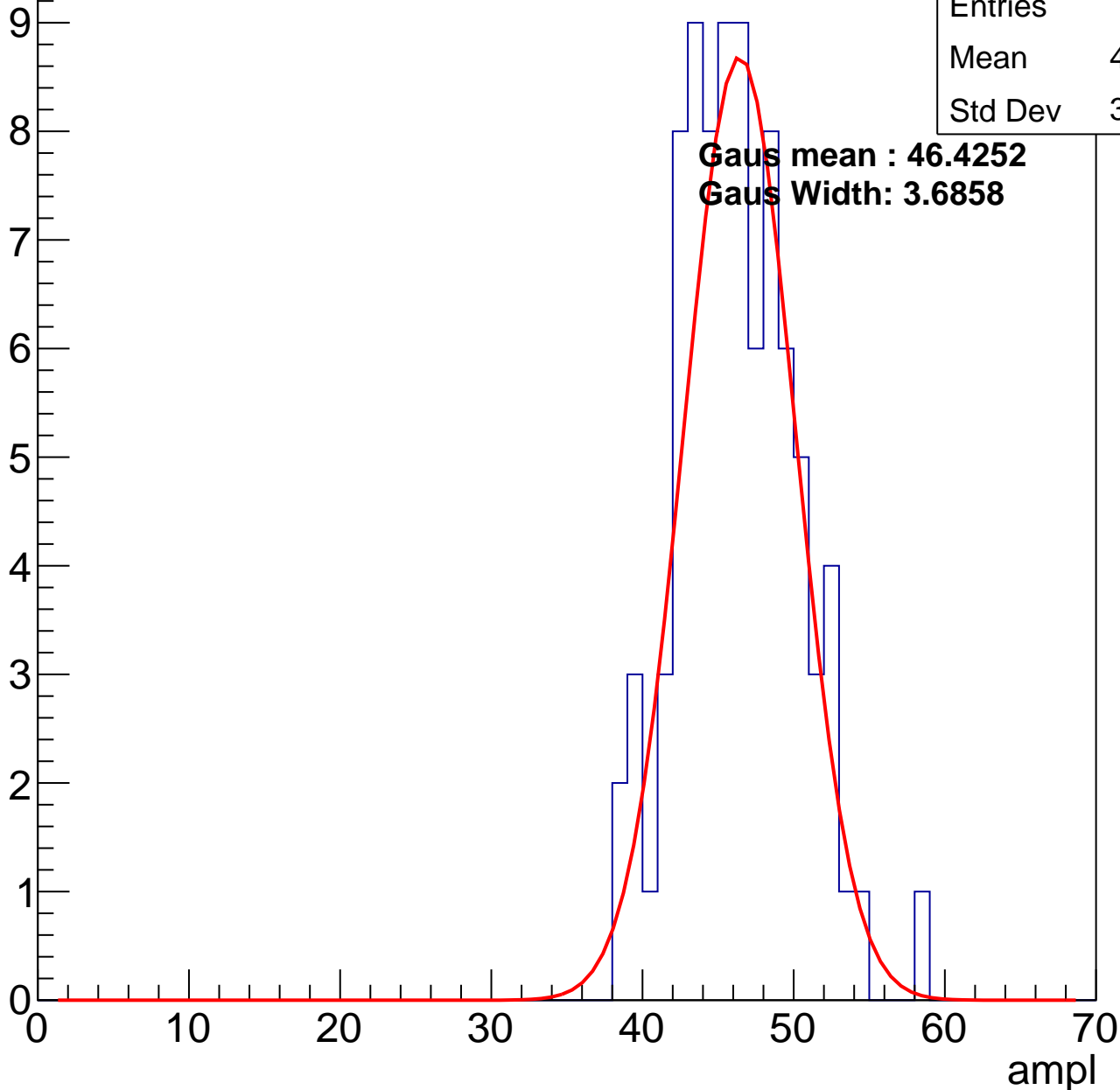
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	45.82
Std Dev	3.837

Gaus mean : 46.4252

Gaus Width: 3.6858

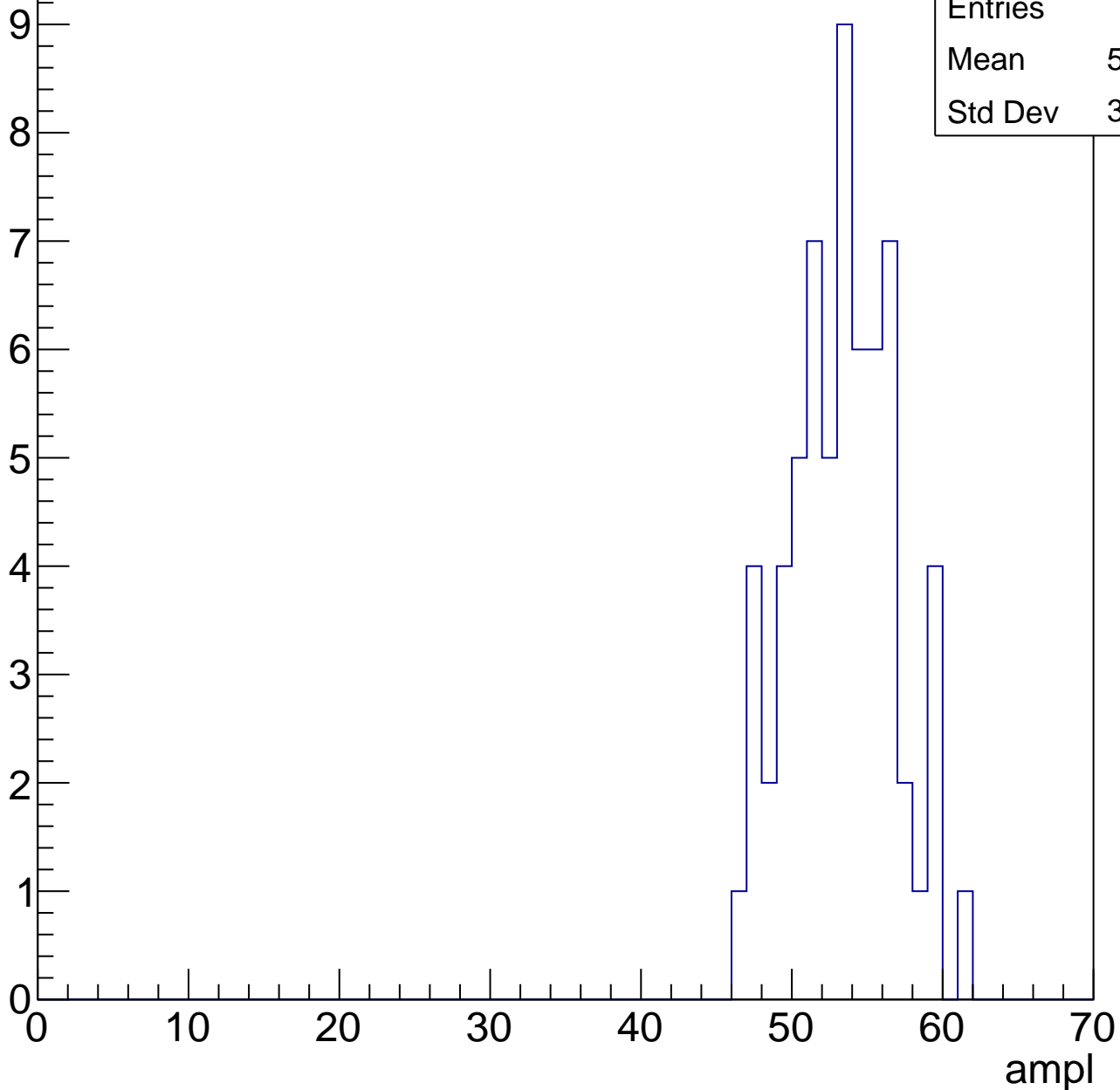


B0L001S, U23-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	52.89
Std Dev	3.405

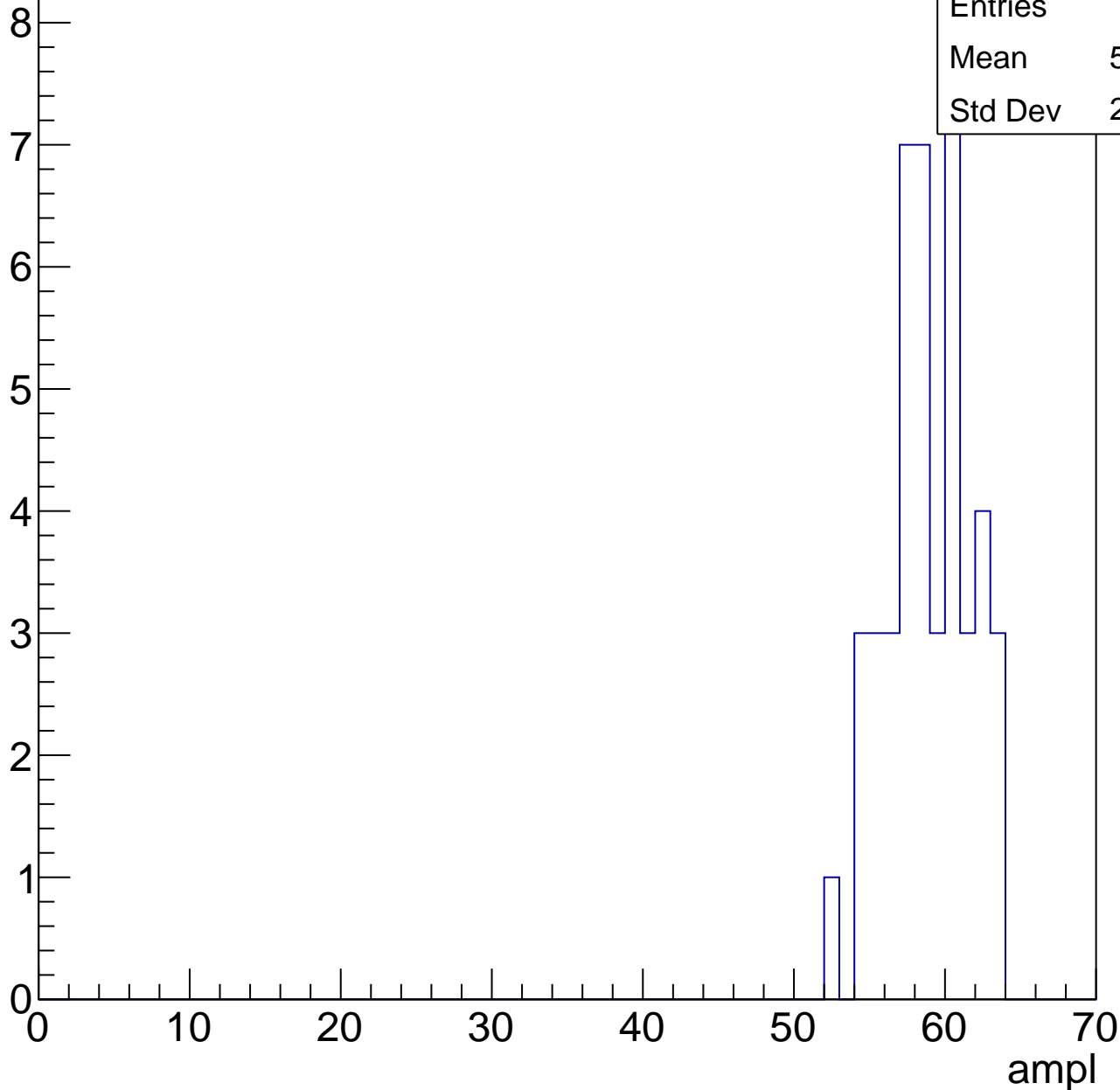


B0L001S, U23-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.42
Std Dev	2.679

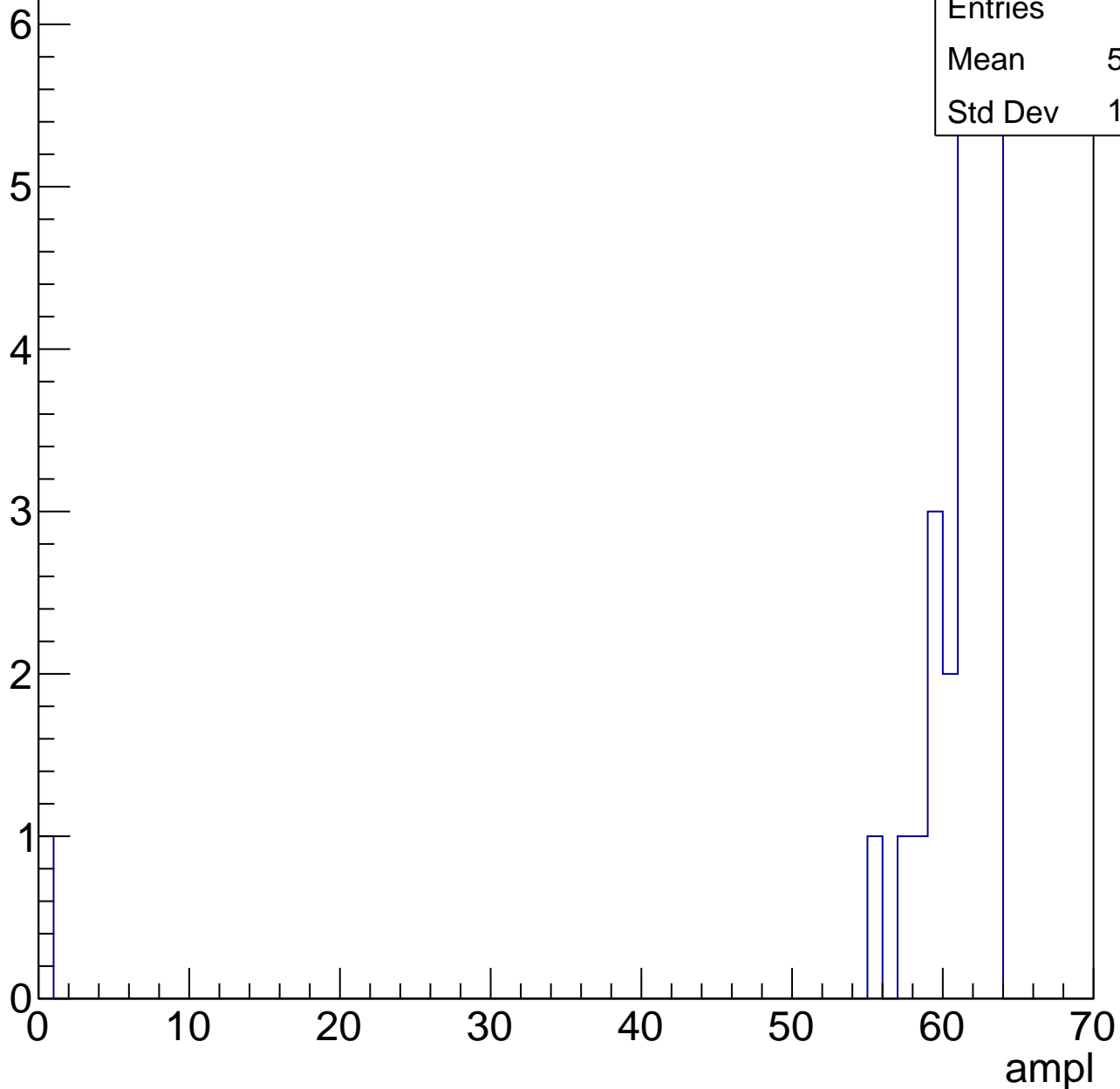


B0L001S, U23-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.63
Std Dev	11.67



B0L001S, U23-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch90, adc0

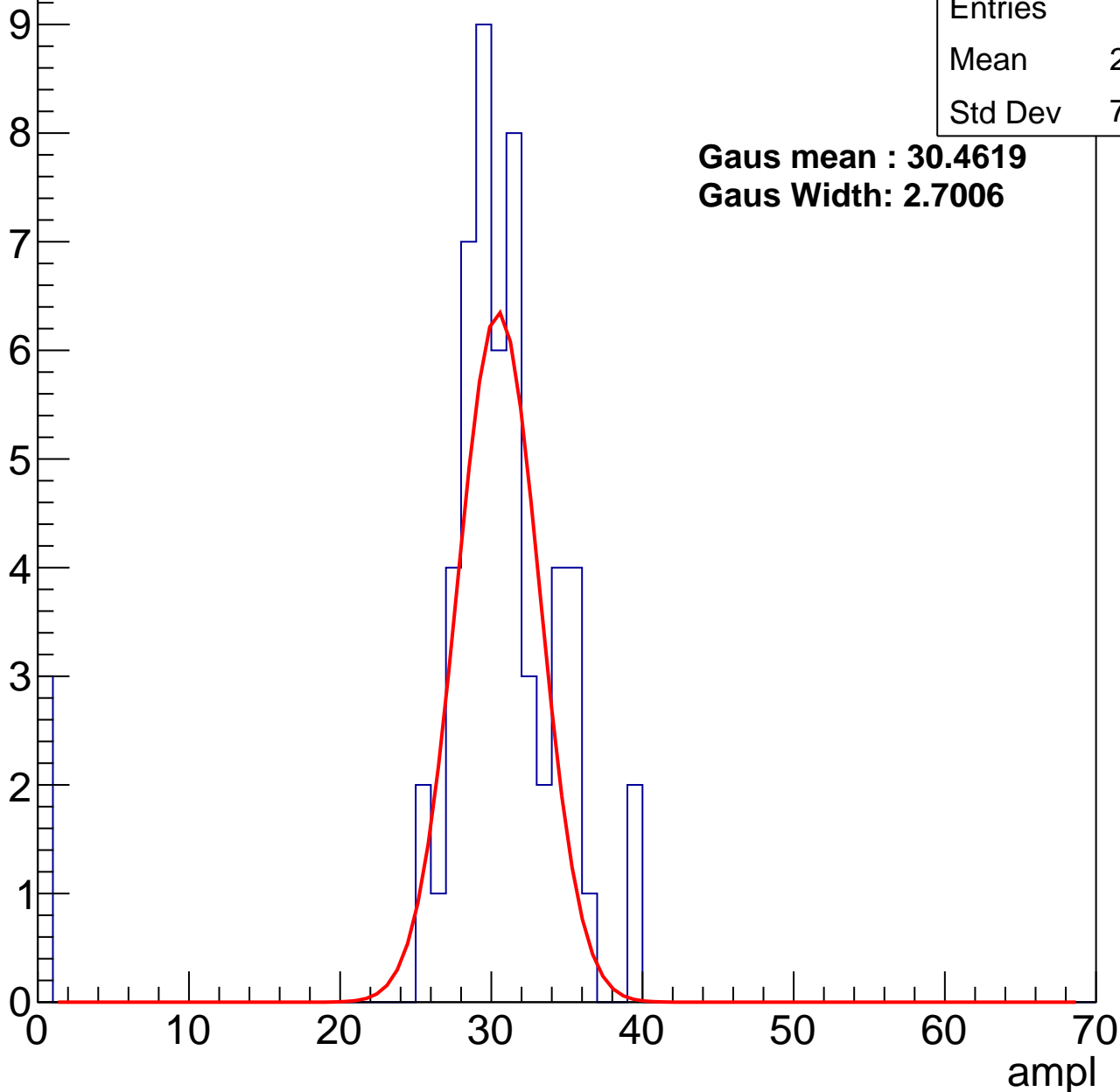
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	28.95
Std Dev	7.532

Gaus mean : 30.4619

Gaus Width: 2.7006



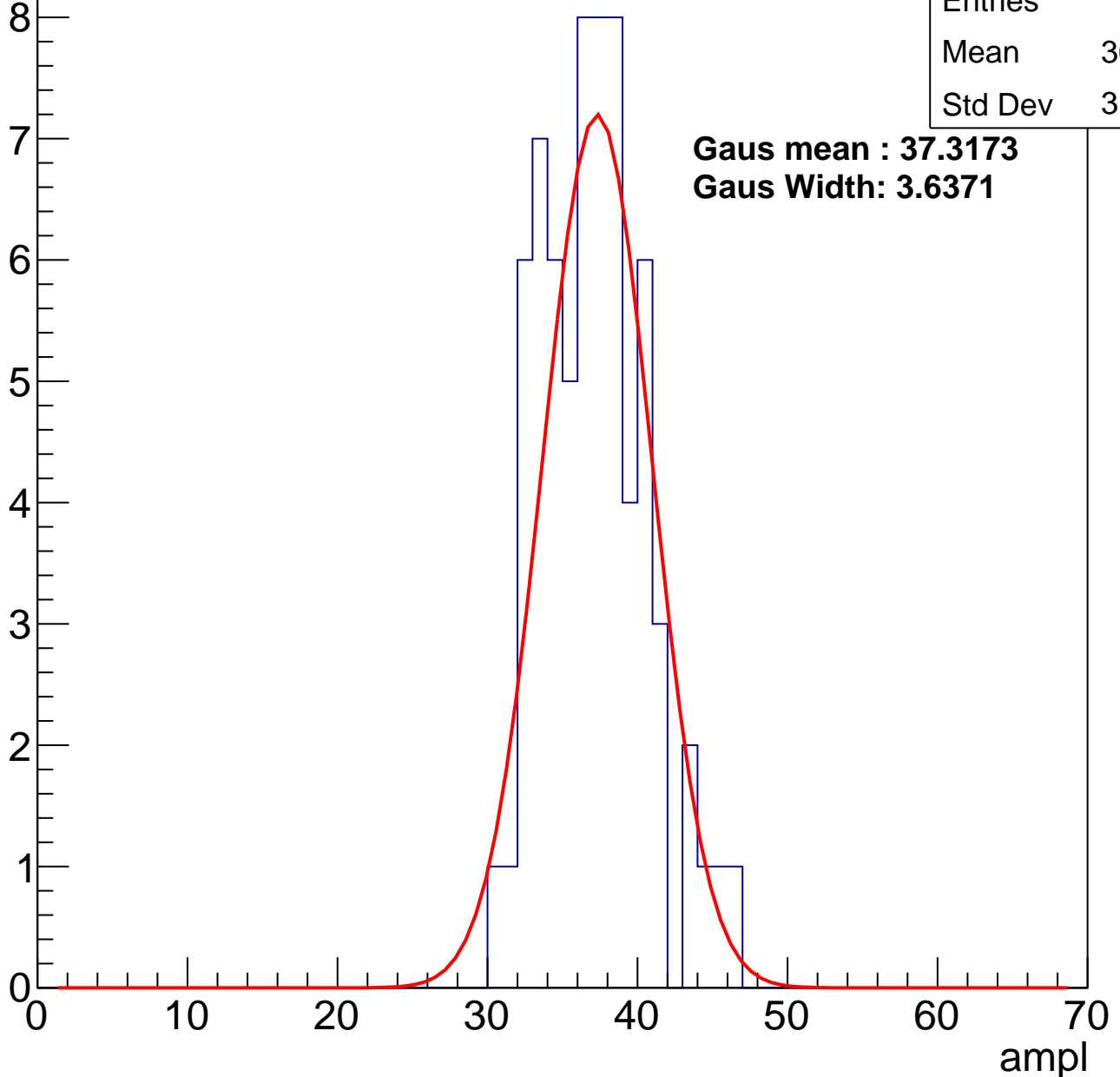
B0L001S, U23-ch90, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.63
Std Dev	3.455

Gaus mean : 37.3173
Gaus Width: 3.6371



B0L001S, U23-ch90, adc2

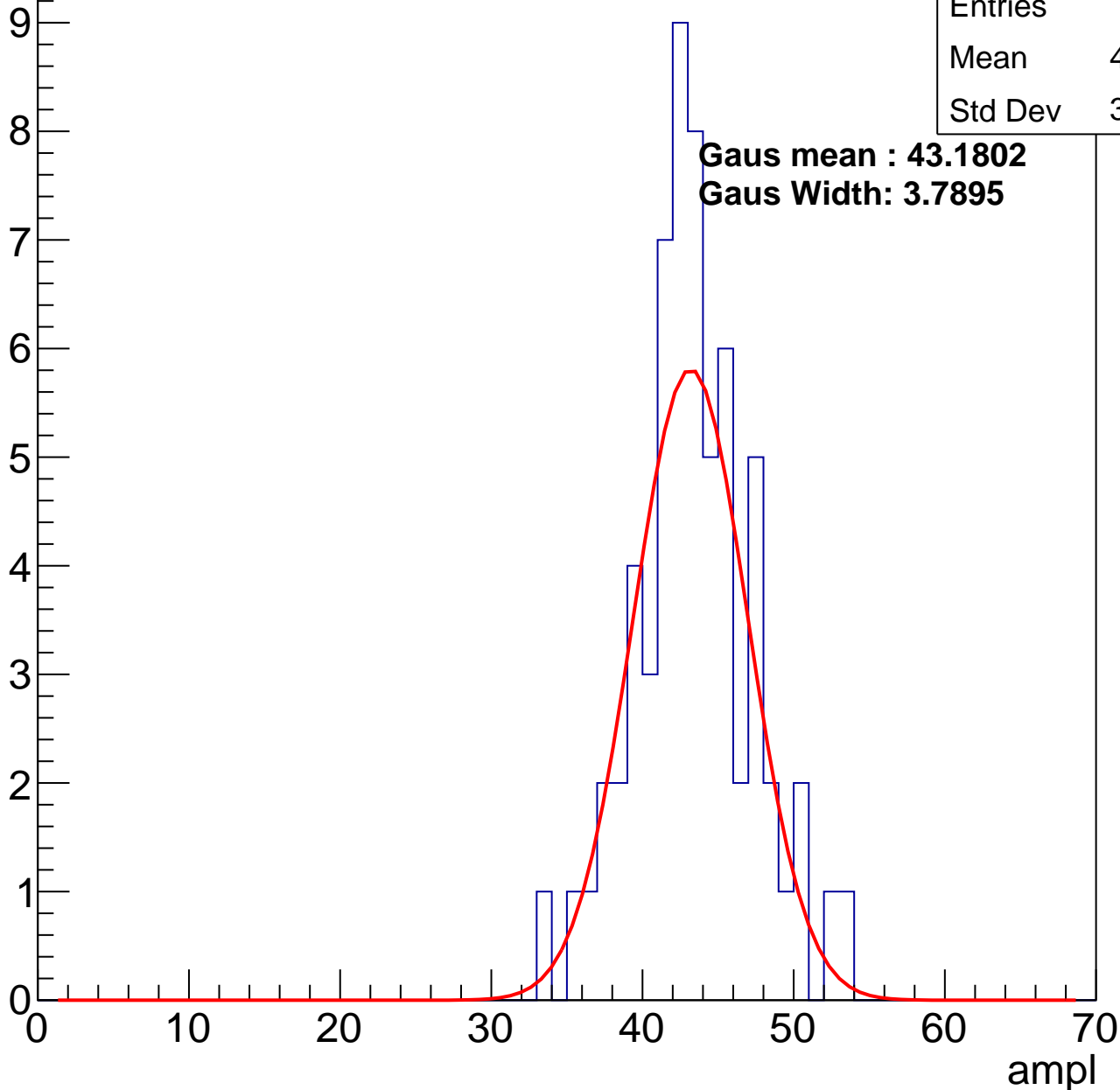
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.95
Std Dev	3.893

Gaus mean : 43.1802

Gaus Width: 3.7895

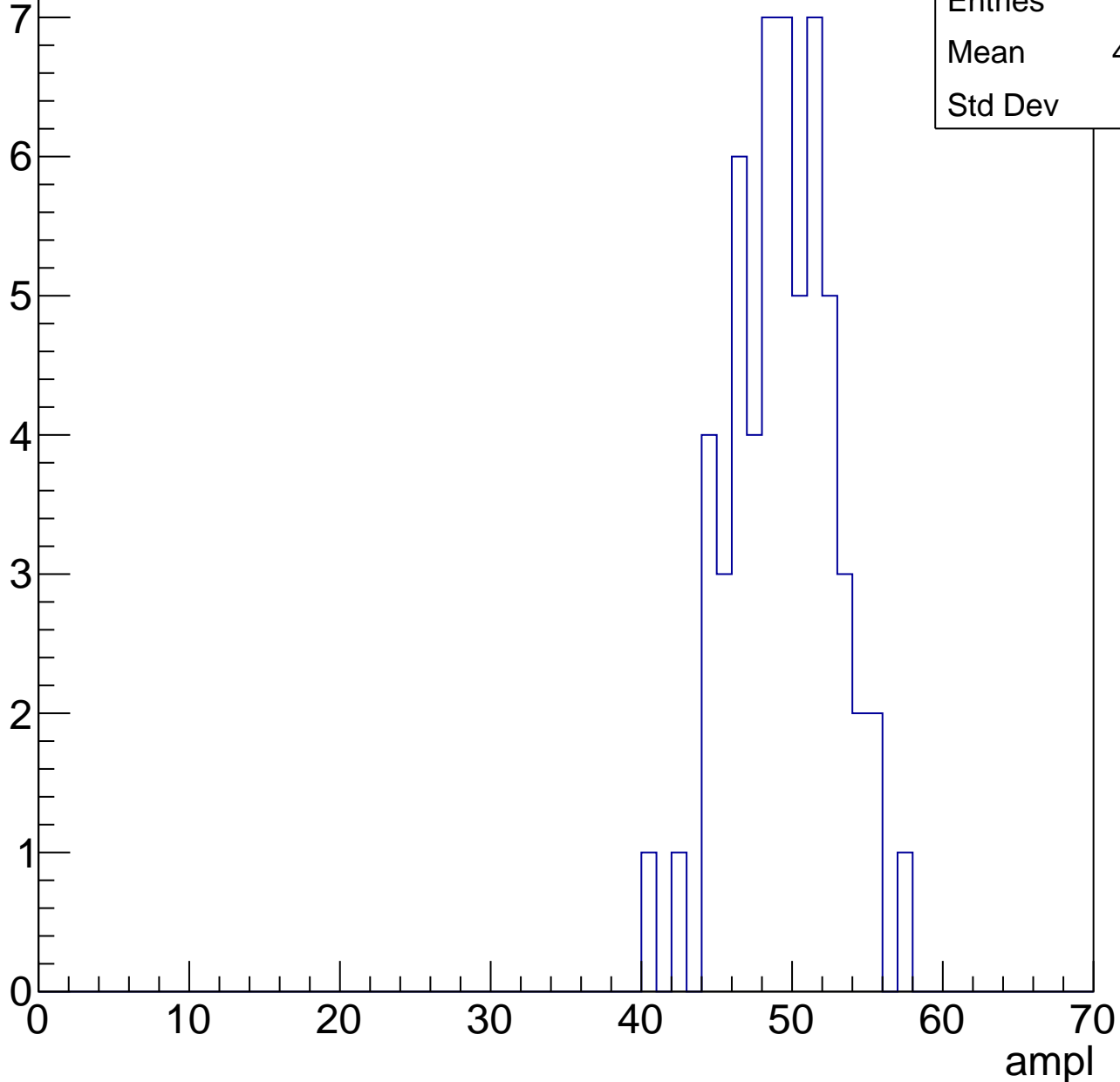


B0L001S, U23-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	48.91
Std Dev	3.39



B0L001S, U23-ch90, adc4

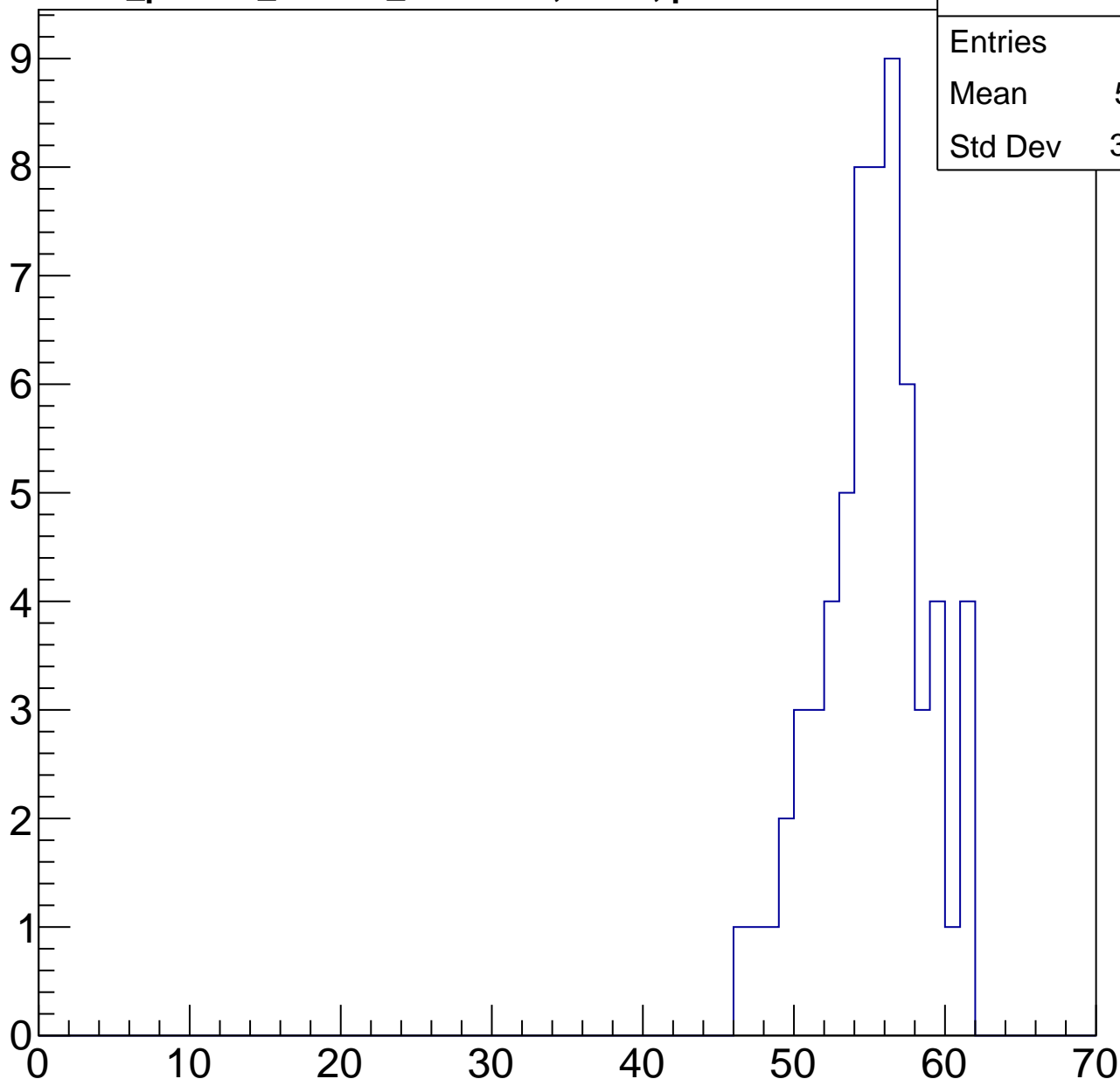
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	54.71
Std Dev	3.429

ampl



B0L001S, U23-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

Entries 52

Mean 59.1

Std Dev 8.56

8

6

4

2

0

0

10

20

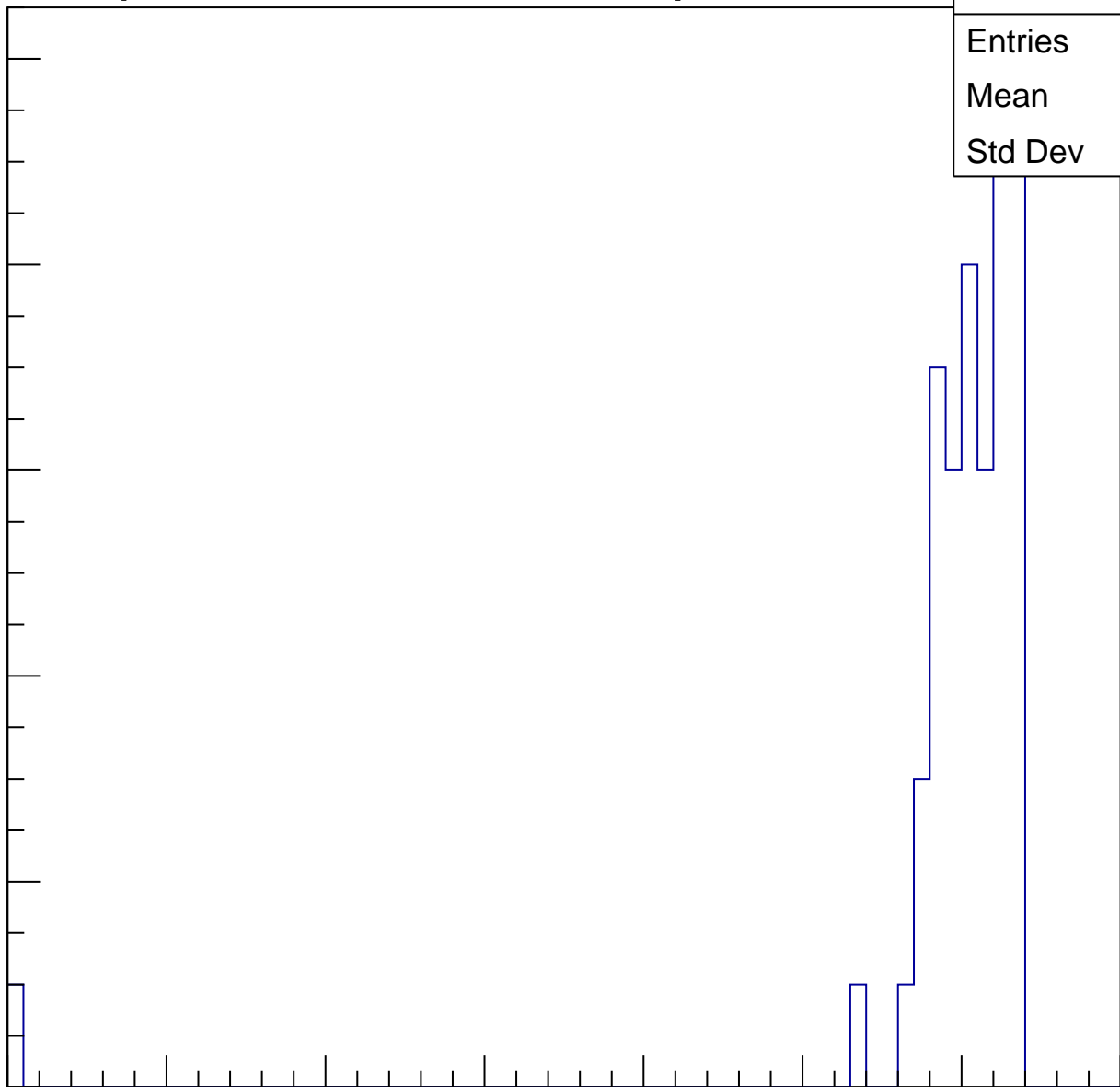
30

40

50

60

ampl



B0L001S, U23-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch91, adc0

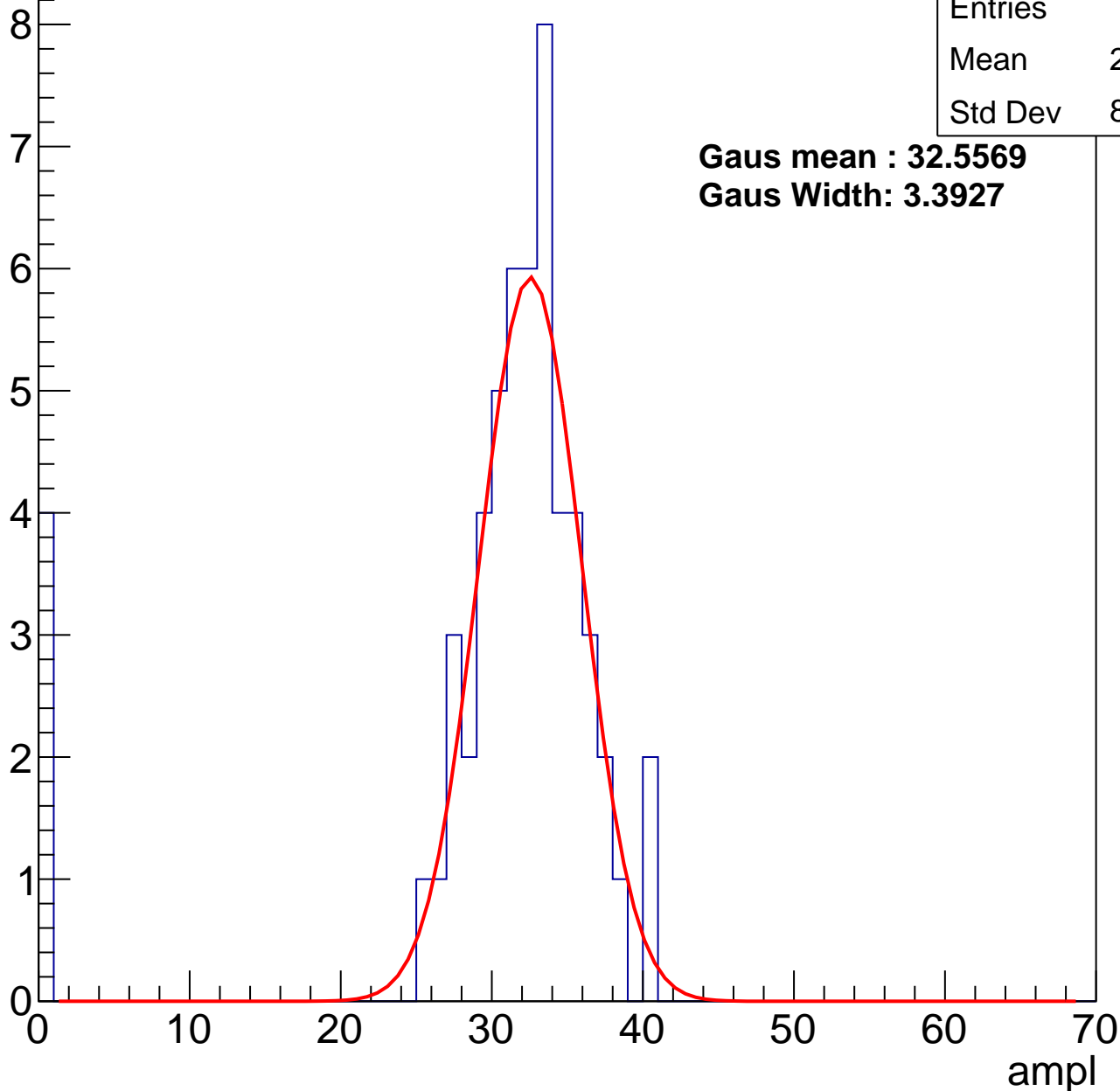
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.86
Std Dev	8.879

Gaus mean : 32.5569

Gaus Width: 3.3927



B0L001S, U23-ch91, adc1

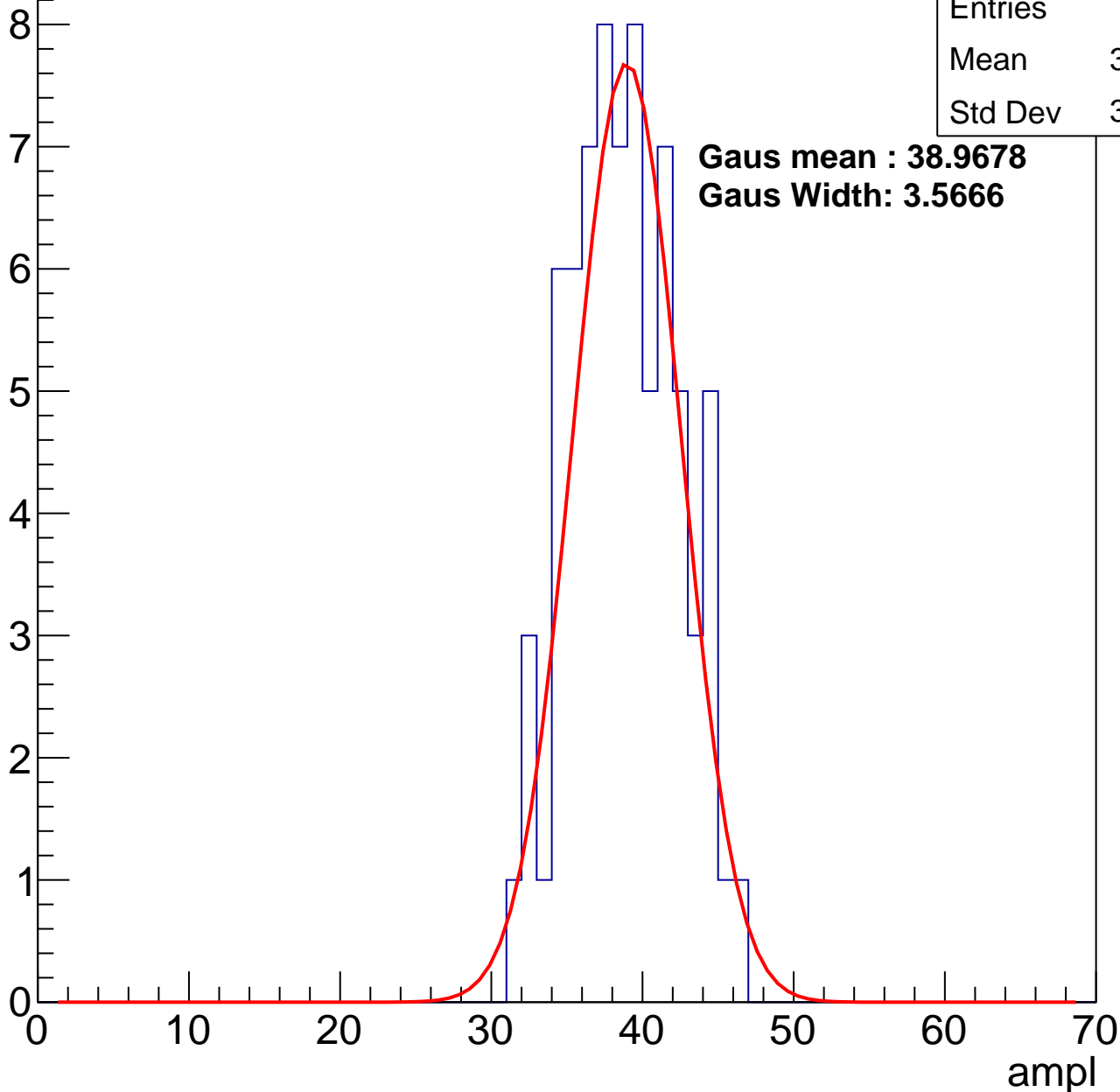
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.34
Std Dev	3.485

Gaus mean : 38.9678

Gaus Width: 3.5666



B0L001S, U23-ch91, adc2

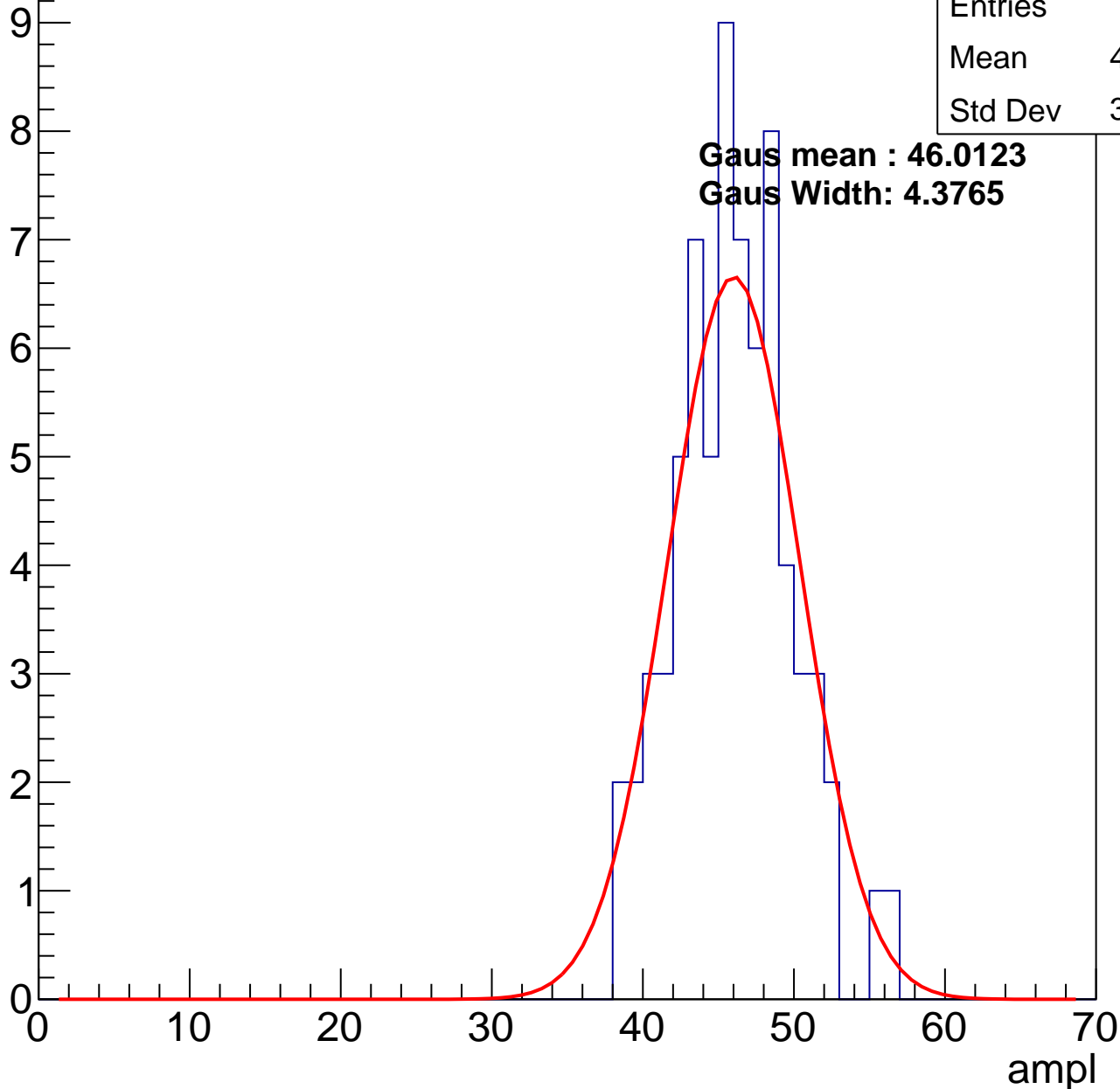
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	45.56
Std Dev	3.786

Gaus mean : 46.0123

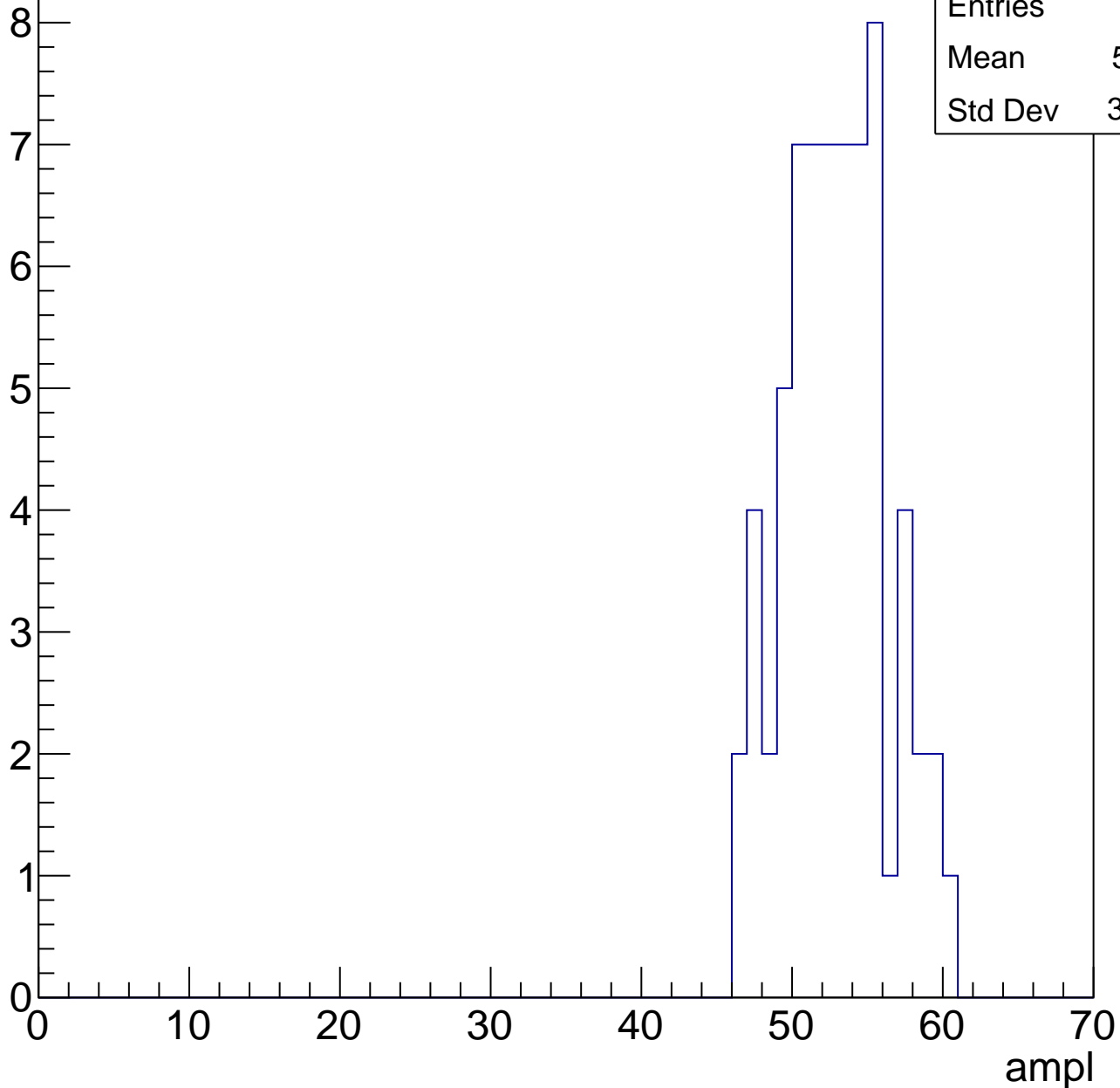
Gaus Width: 4.3765



B0L001S, U23-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



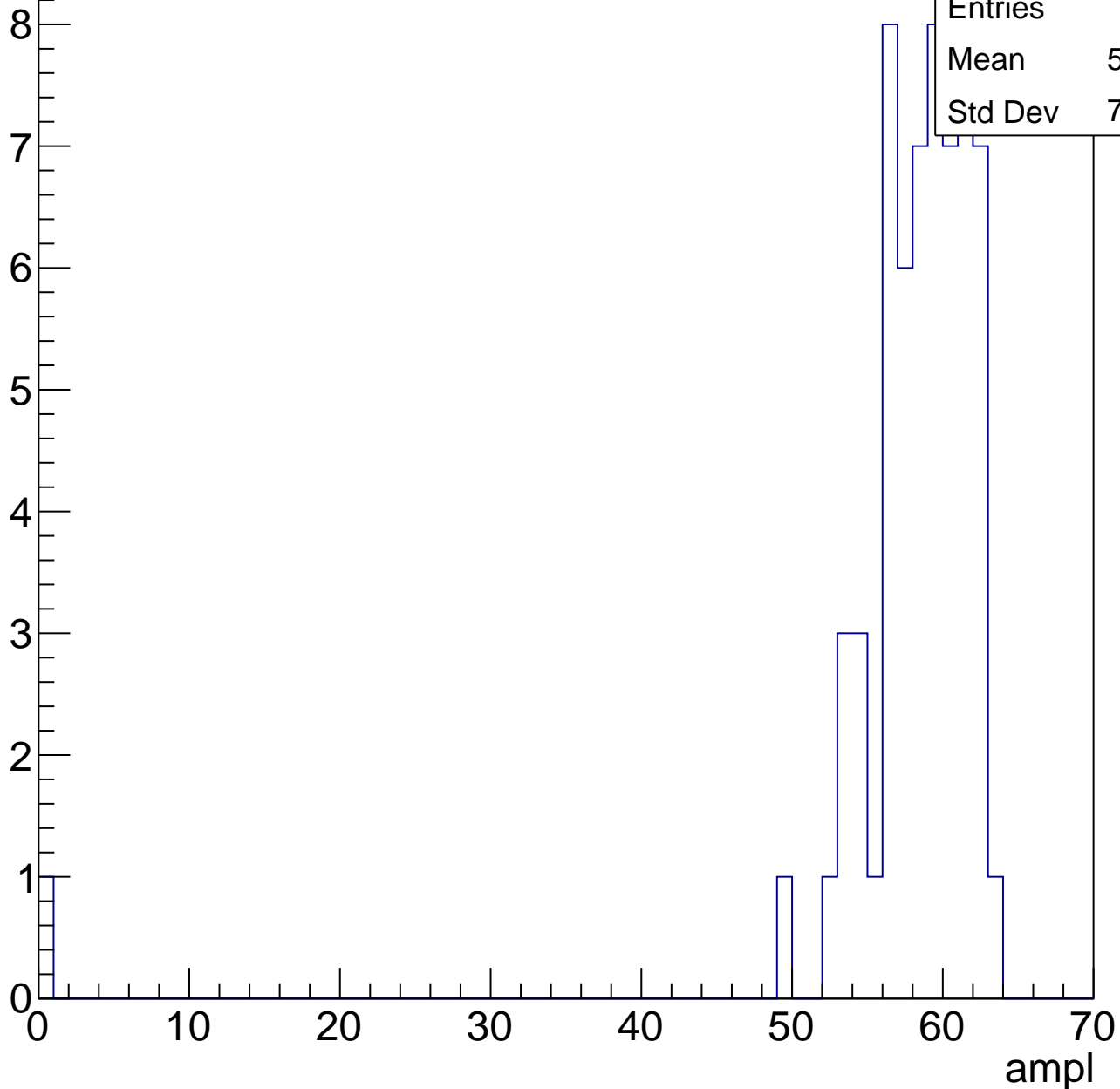
Entries	66
Mean	52.41
Std Dev	3.326

B0L001S, U23-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	57.26
Std Dev	7.892

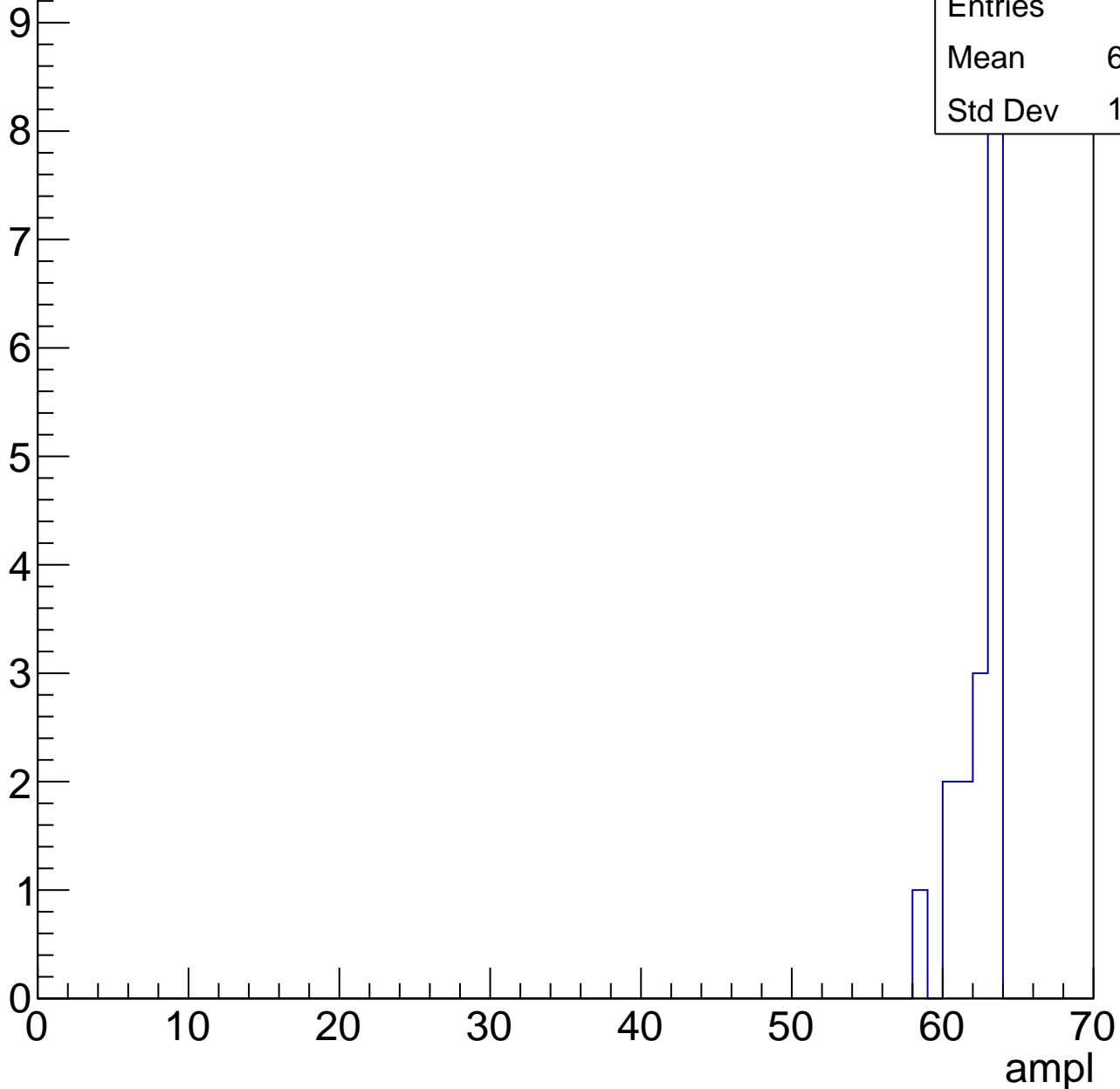


B0L001S, U23-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	17
Mean	61.94
Std Dev	1.434



B0L001S, U23-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch92, adc0

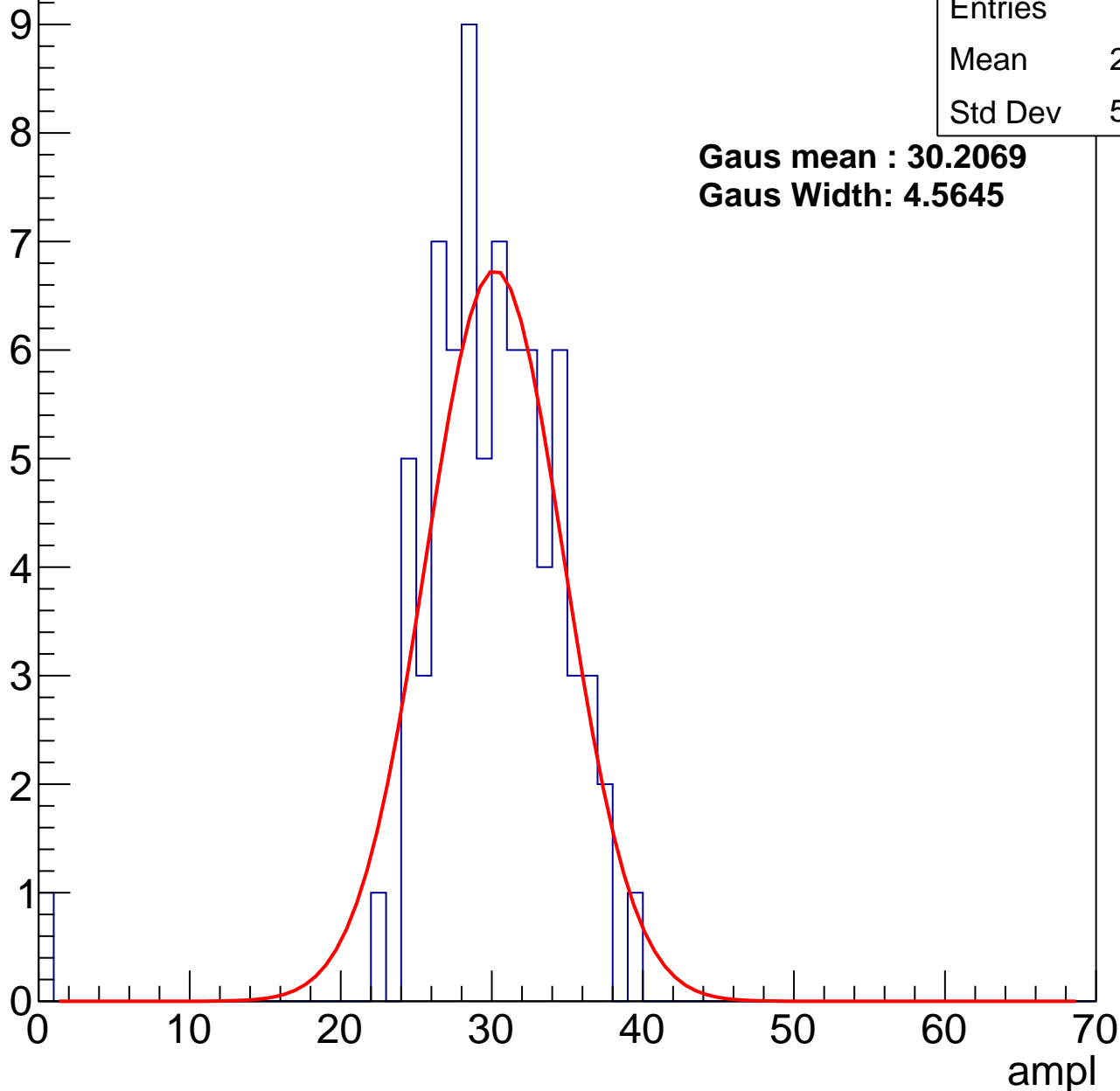
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.44
Std Dev	5.068

Gaus mean : 30.2069

Gaus Width: 4.5645



B0L001S, U23-ch92, adc1

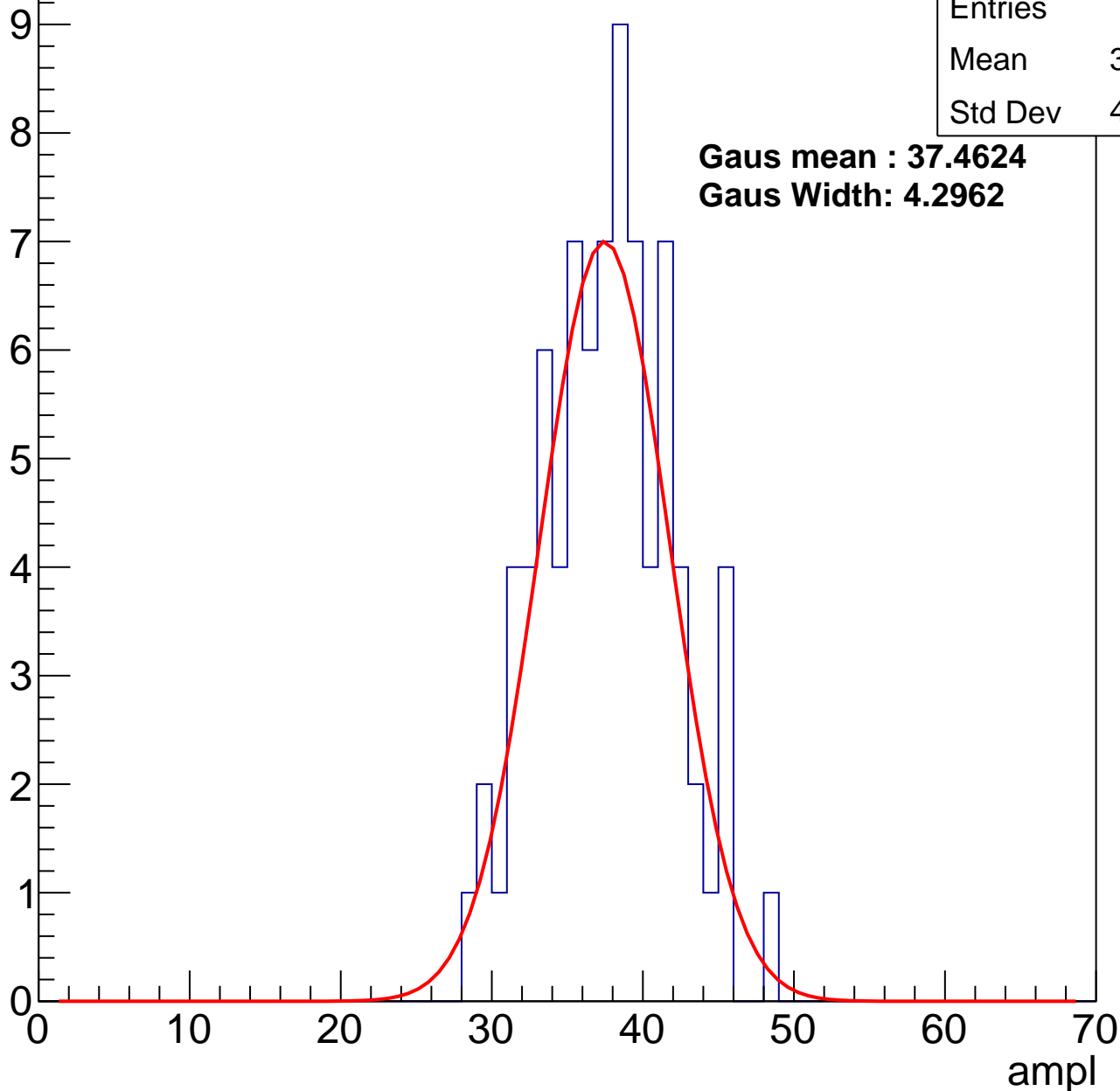
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.16
Std Dev	4.229

Gaus mean : 37.4624

Gaus Width: 4.2962



B0L001S, U23-ch92, adc2

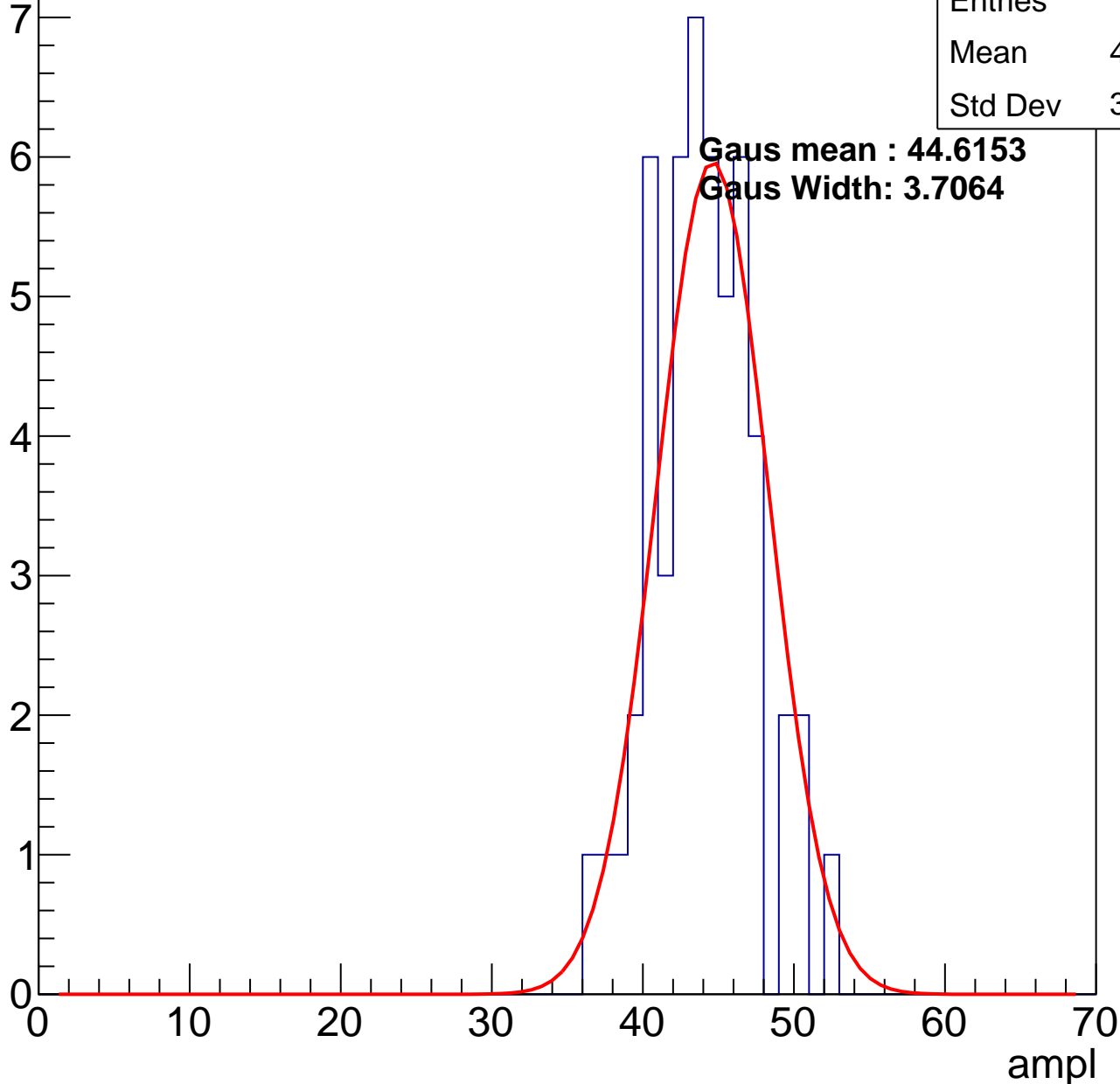
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	43.55
Std Dev	3.345

Gaus mean : 44.6153

Gaus Width: 3.7064

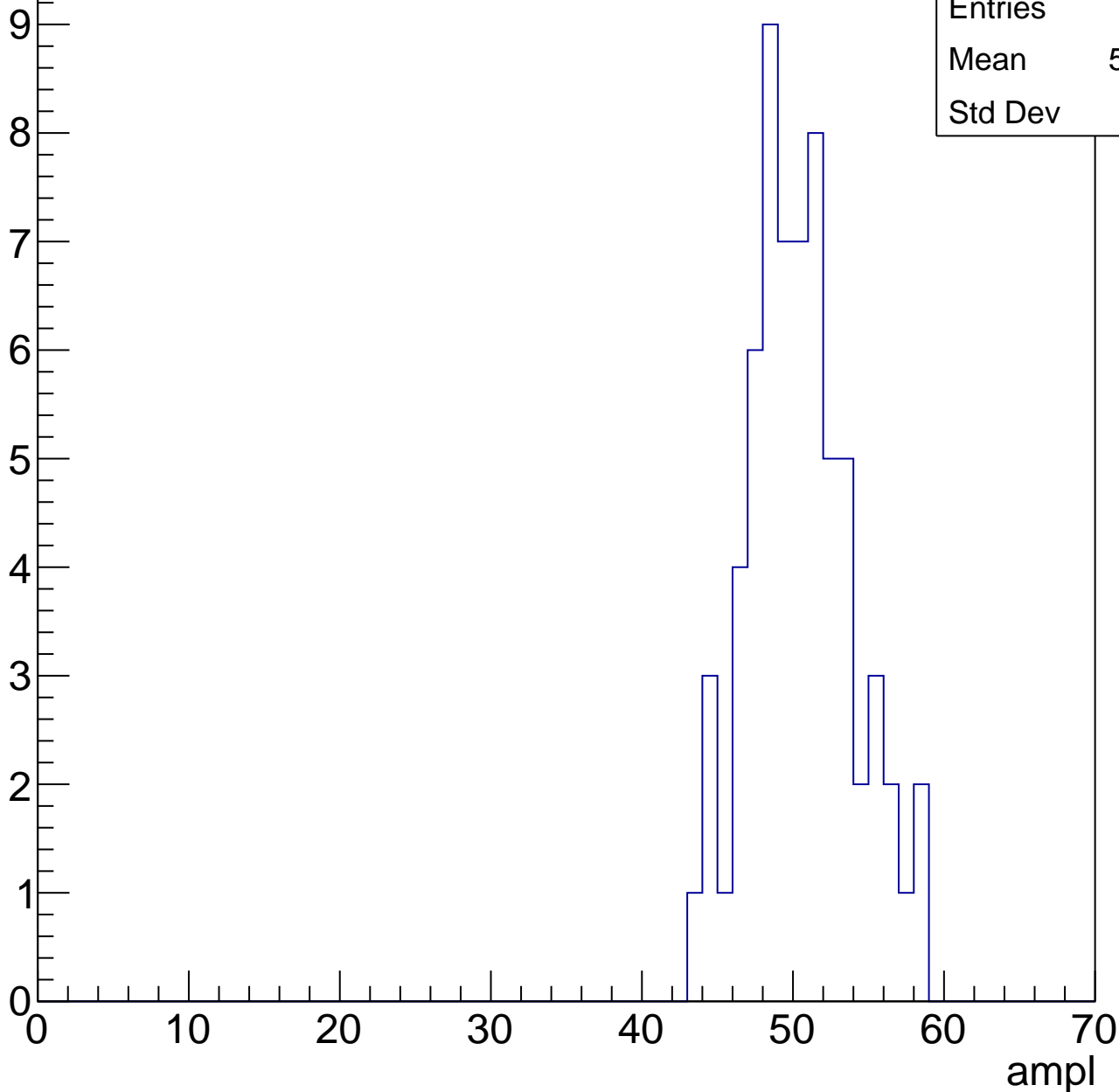


B0L001S, U23-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

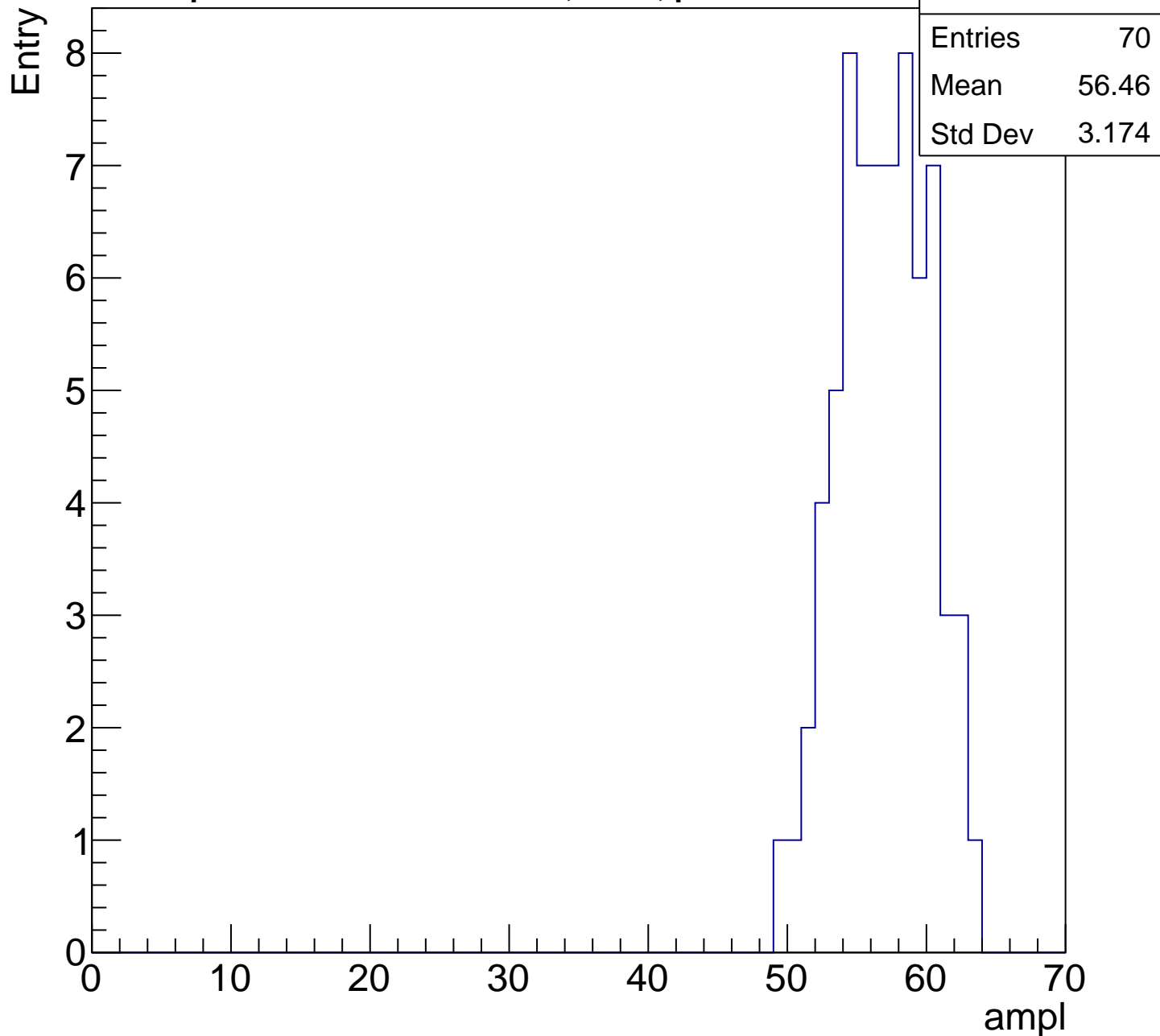
Entry

Entries	66
Mean	50.03
Std Dev	3.42



B0L001S, U23-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

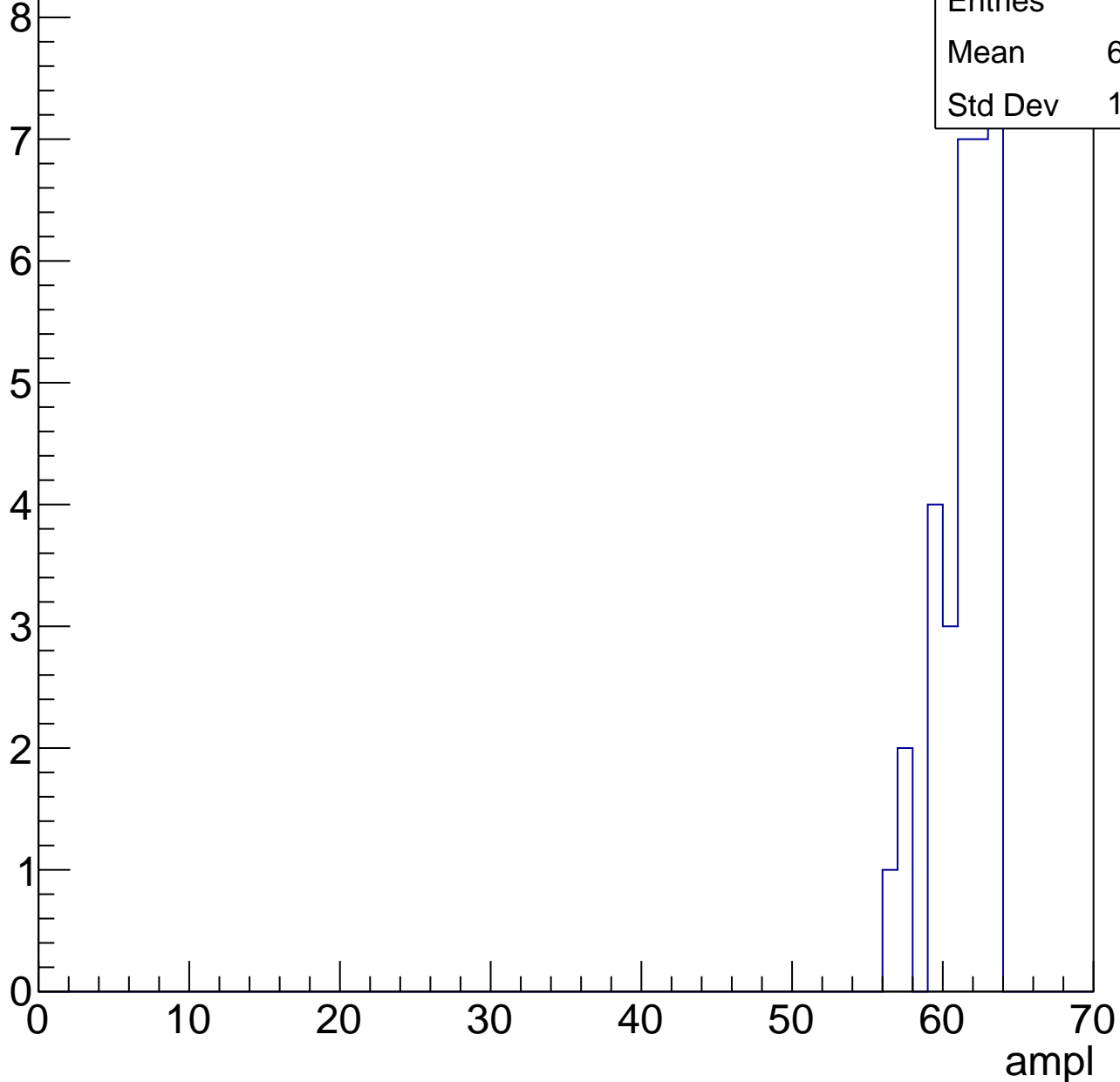


B0L001S, U23-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

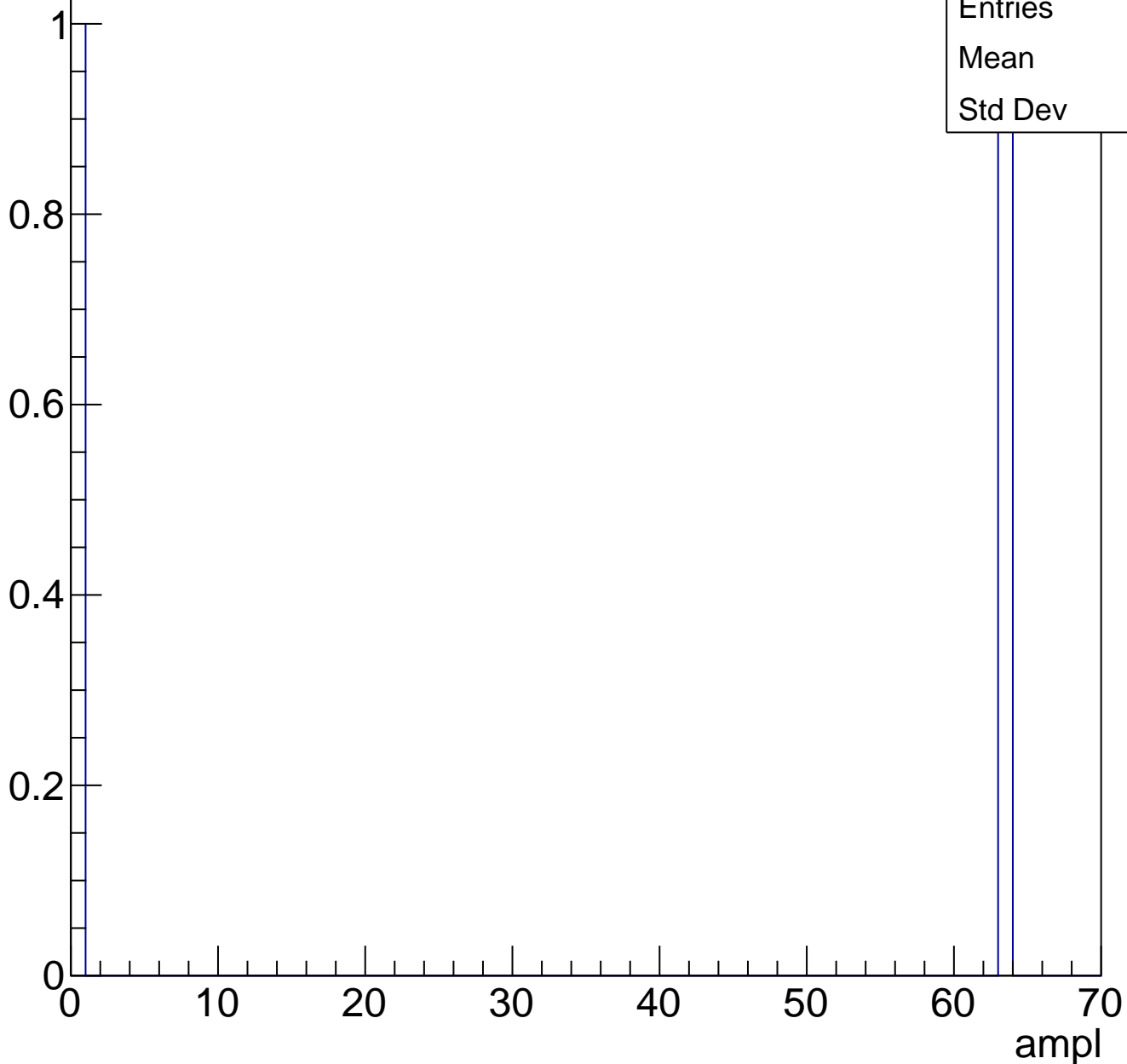
Entries	32
Mean	60.97
Std Dev	1.895



B0L001S, U23-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch93, adc0

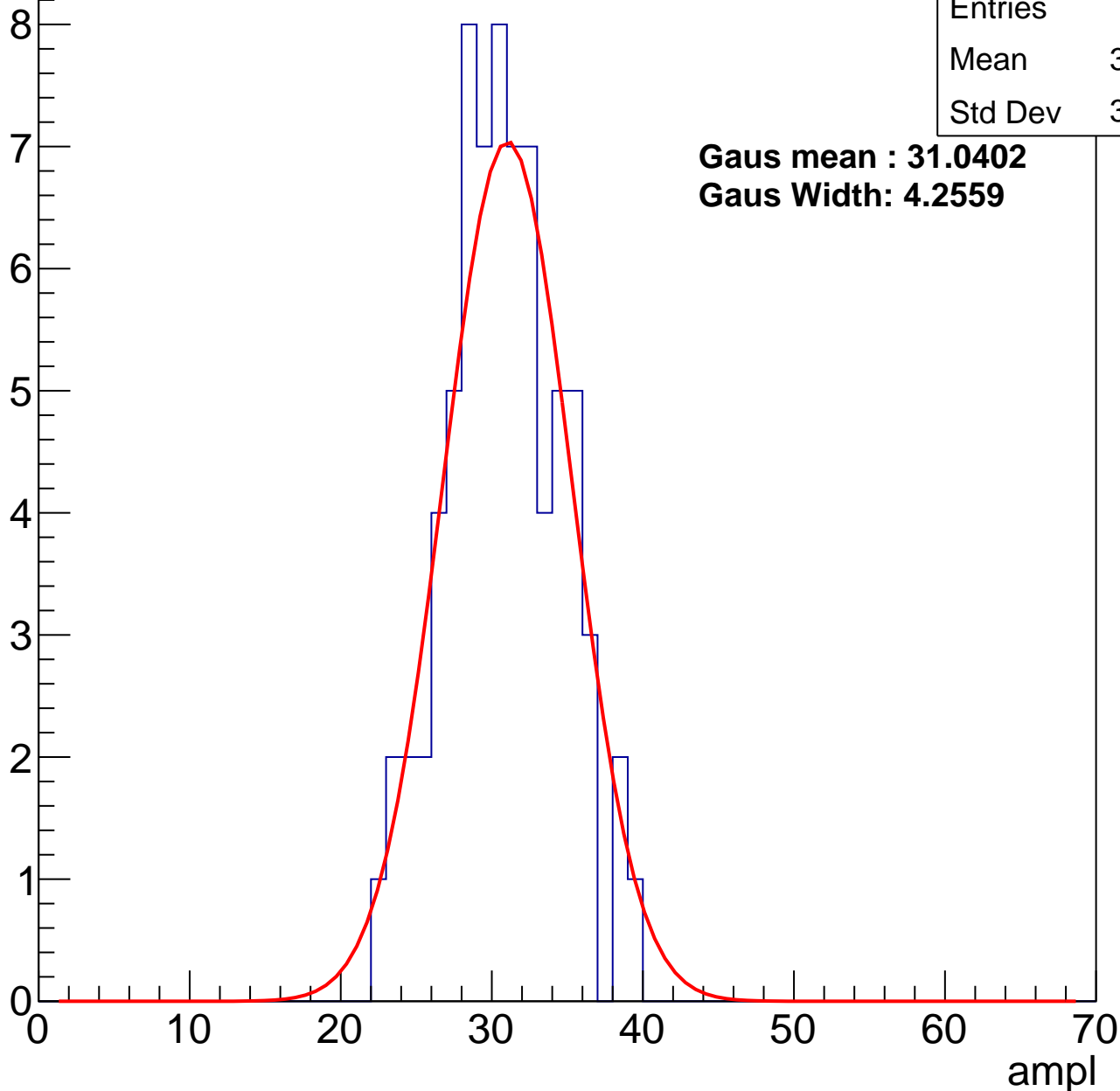
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.32
Std Dev	3.734

Gaus mean : 31.0402

Gaus Width: 4.2559



B0L001S, U23-ch93, adc1

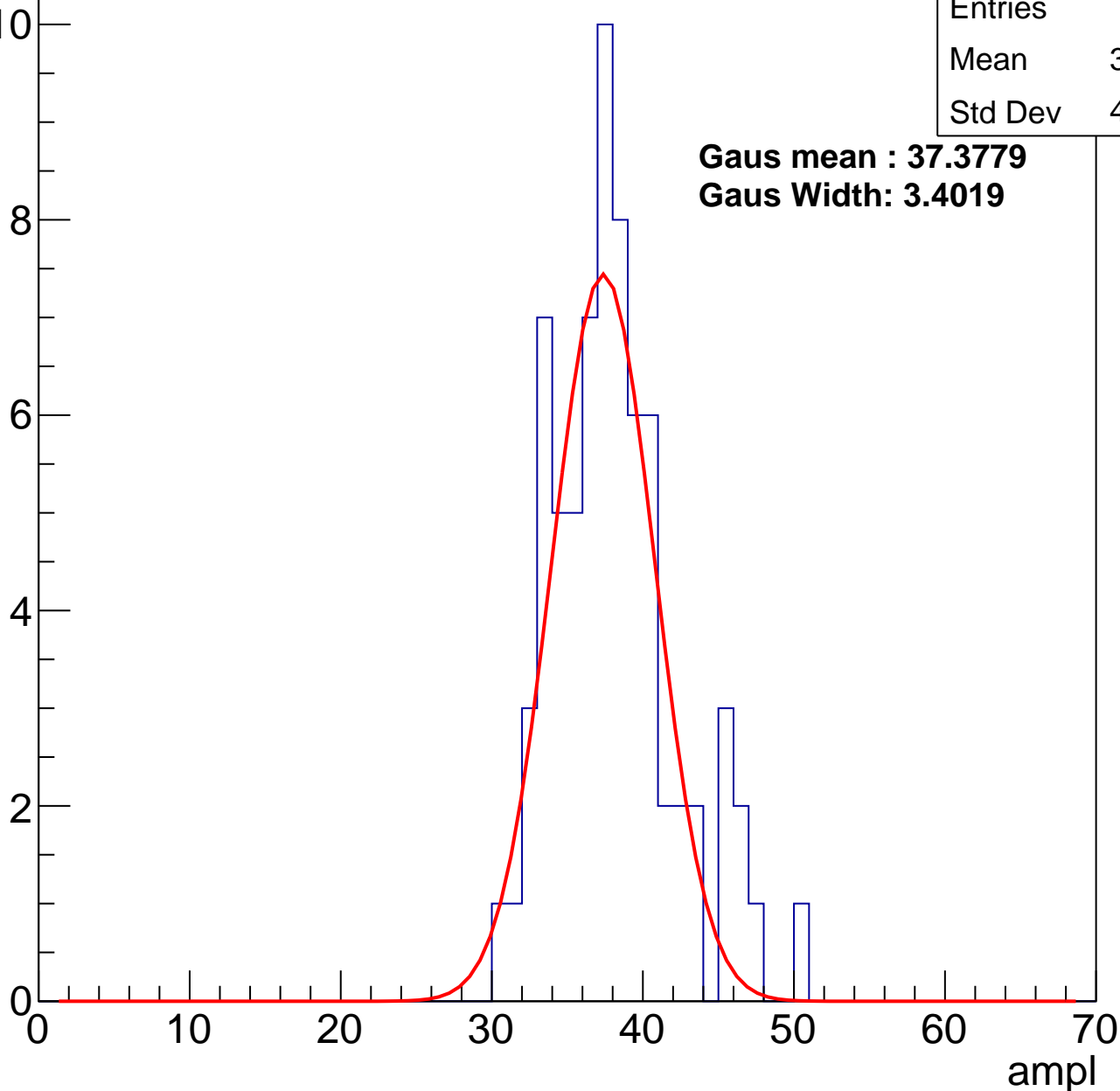
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.62
Std Dev	4.046

Gaus mean : 37.3779

Gaus Width: 3.4019



B0L001S, U23-ch93, adc2

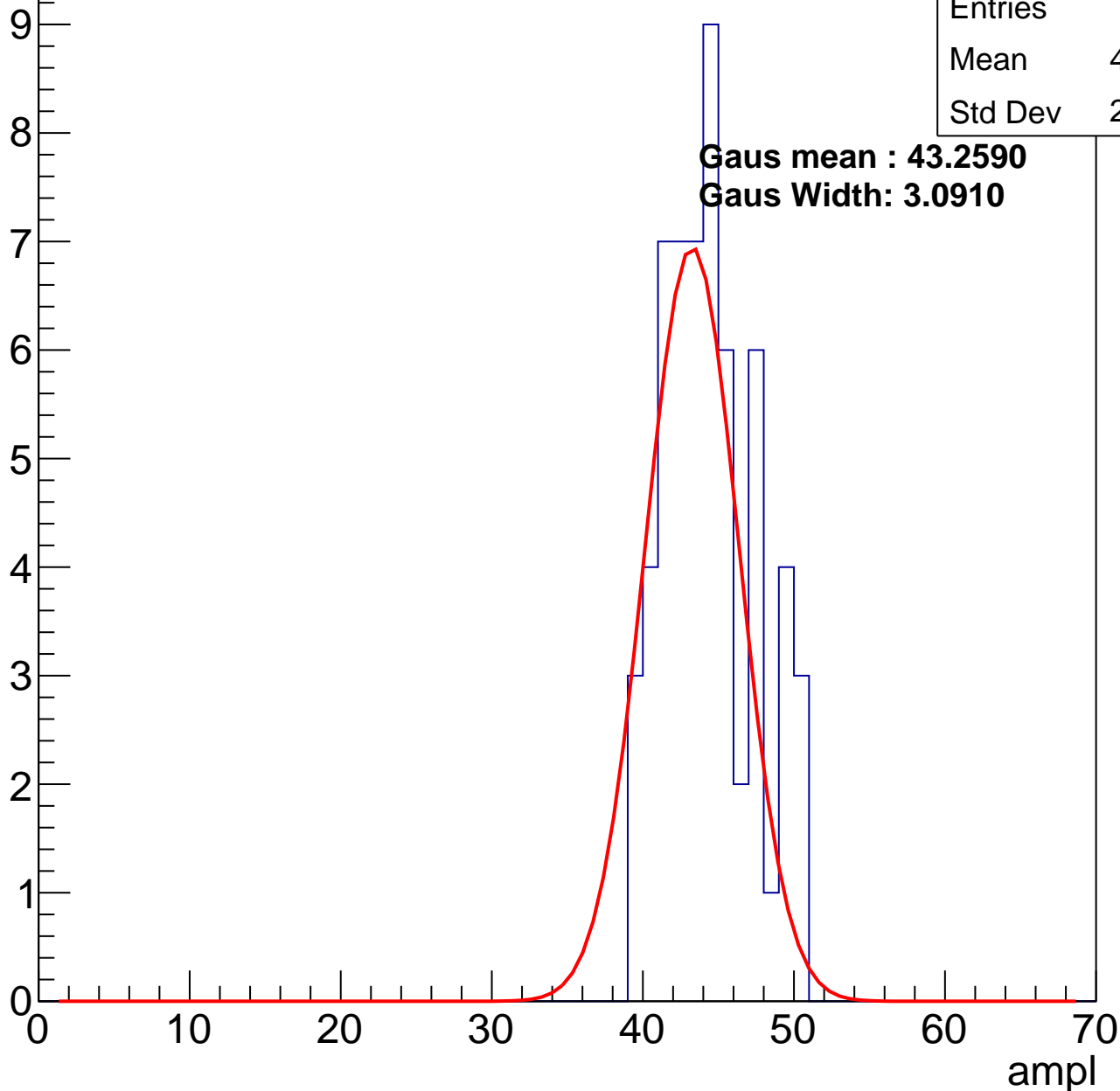
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.95
Std Dev	2.994

Gaus mean : 43.2590

Gaus Width: 3.0910

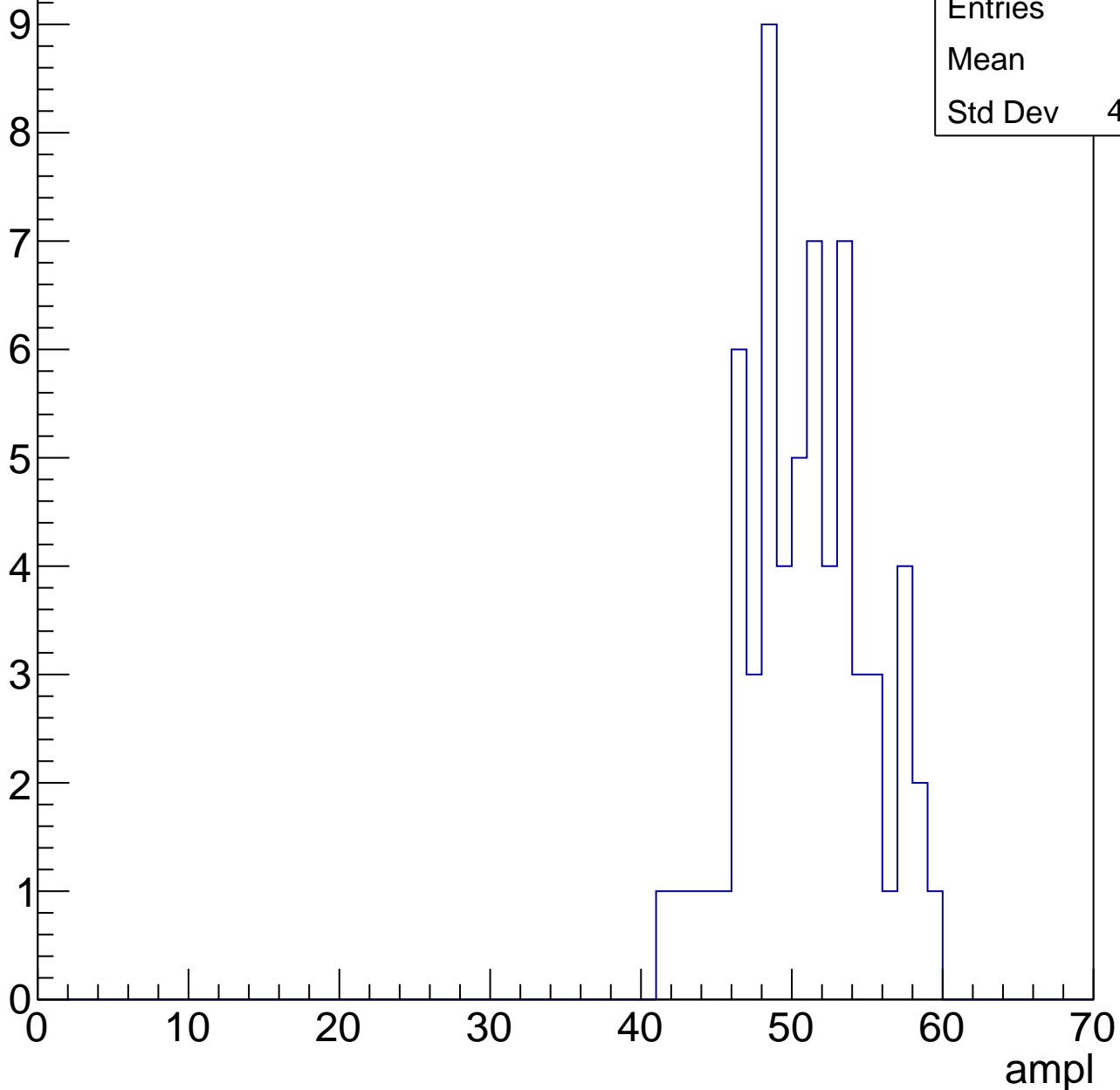


B0L001S, U23-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

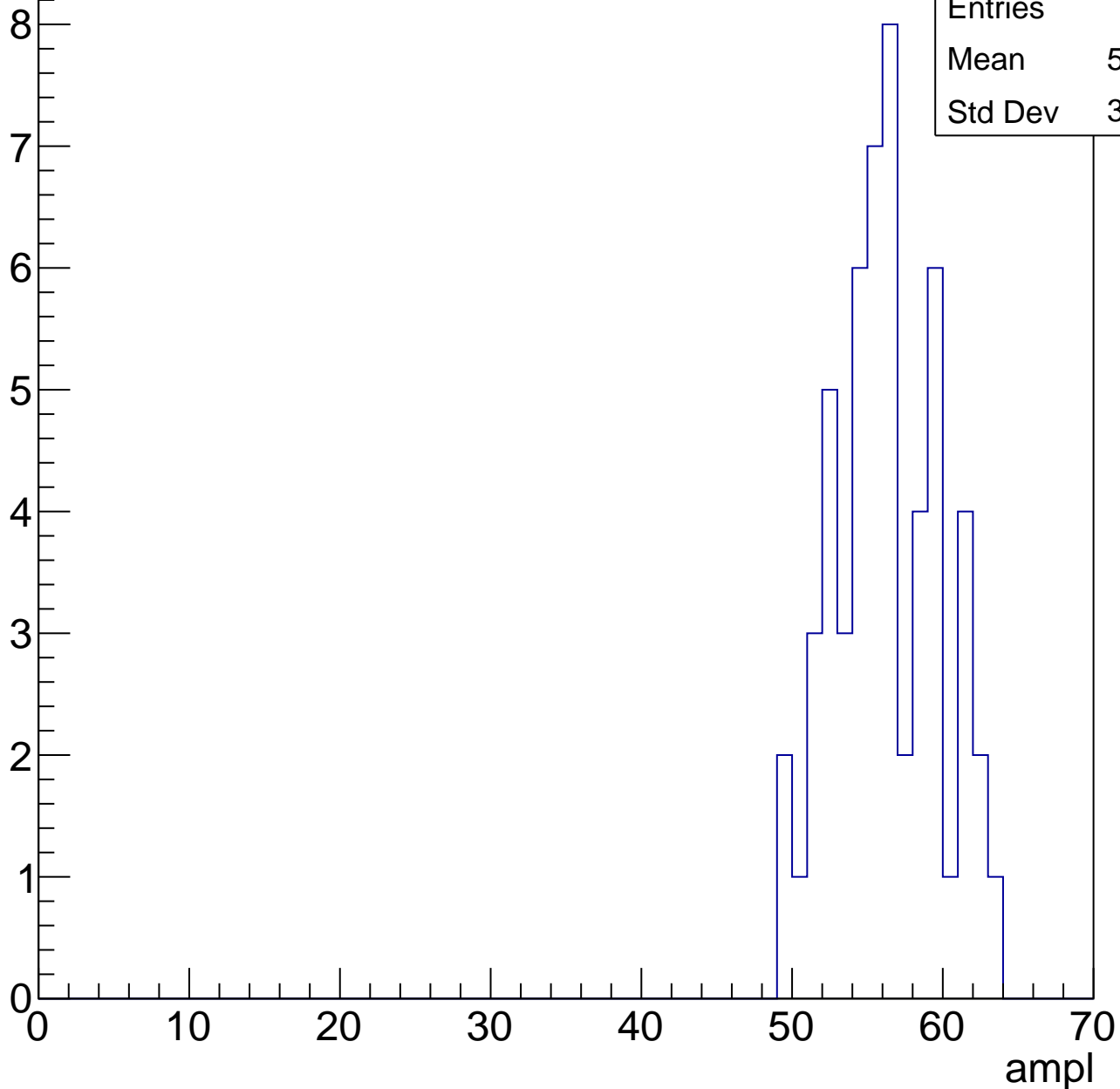
Entries	64
Mean	50.5
Std Dev	4.054



B0L001S, U23-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



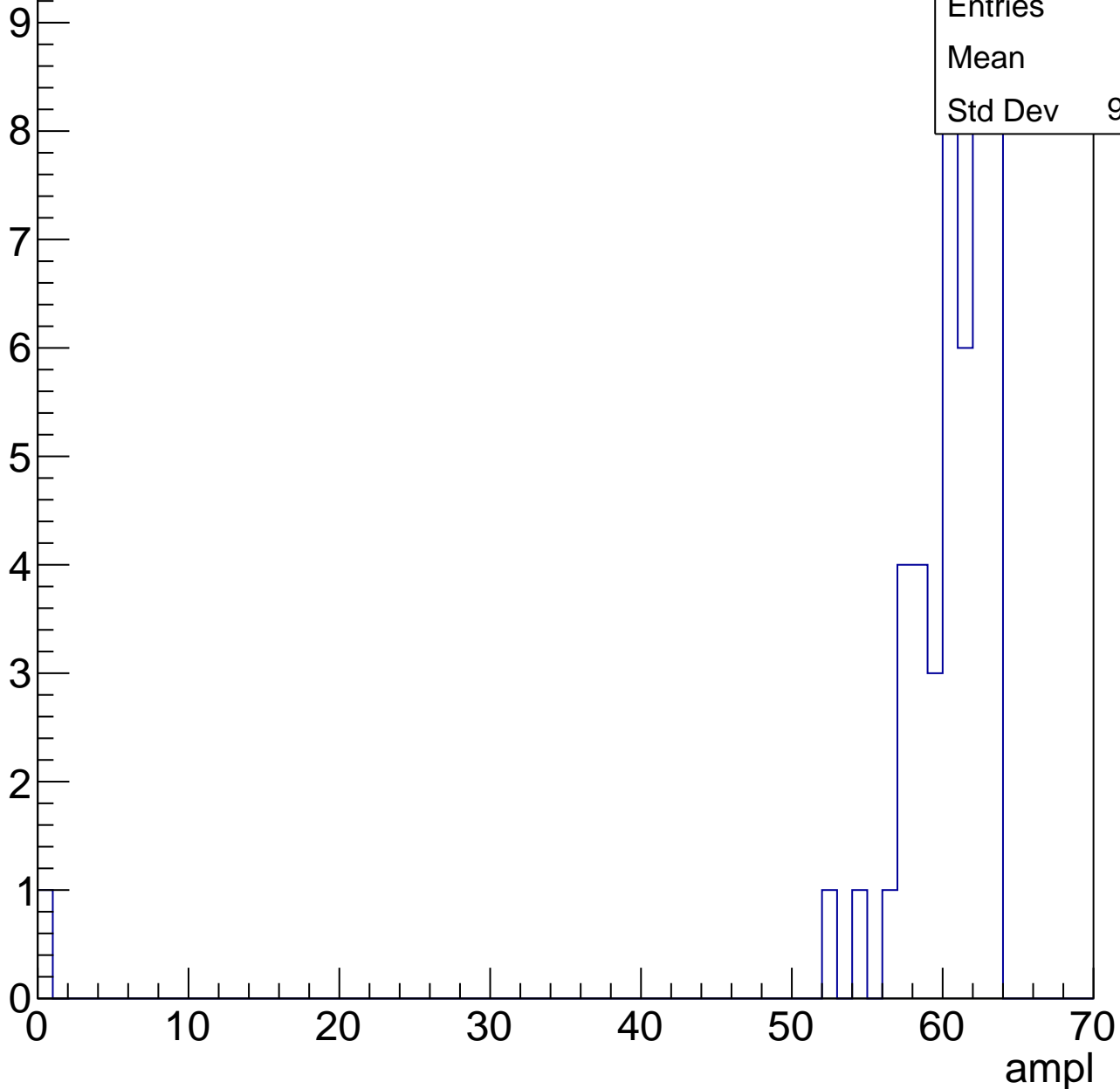
Entries	55
Mean	55.78
Std Dev	3.447

B0L001S, U23-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.8
Std Dev	9.107



B0L001S, U23-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch94, adc0

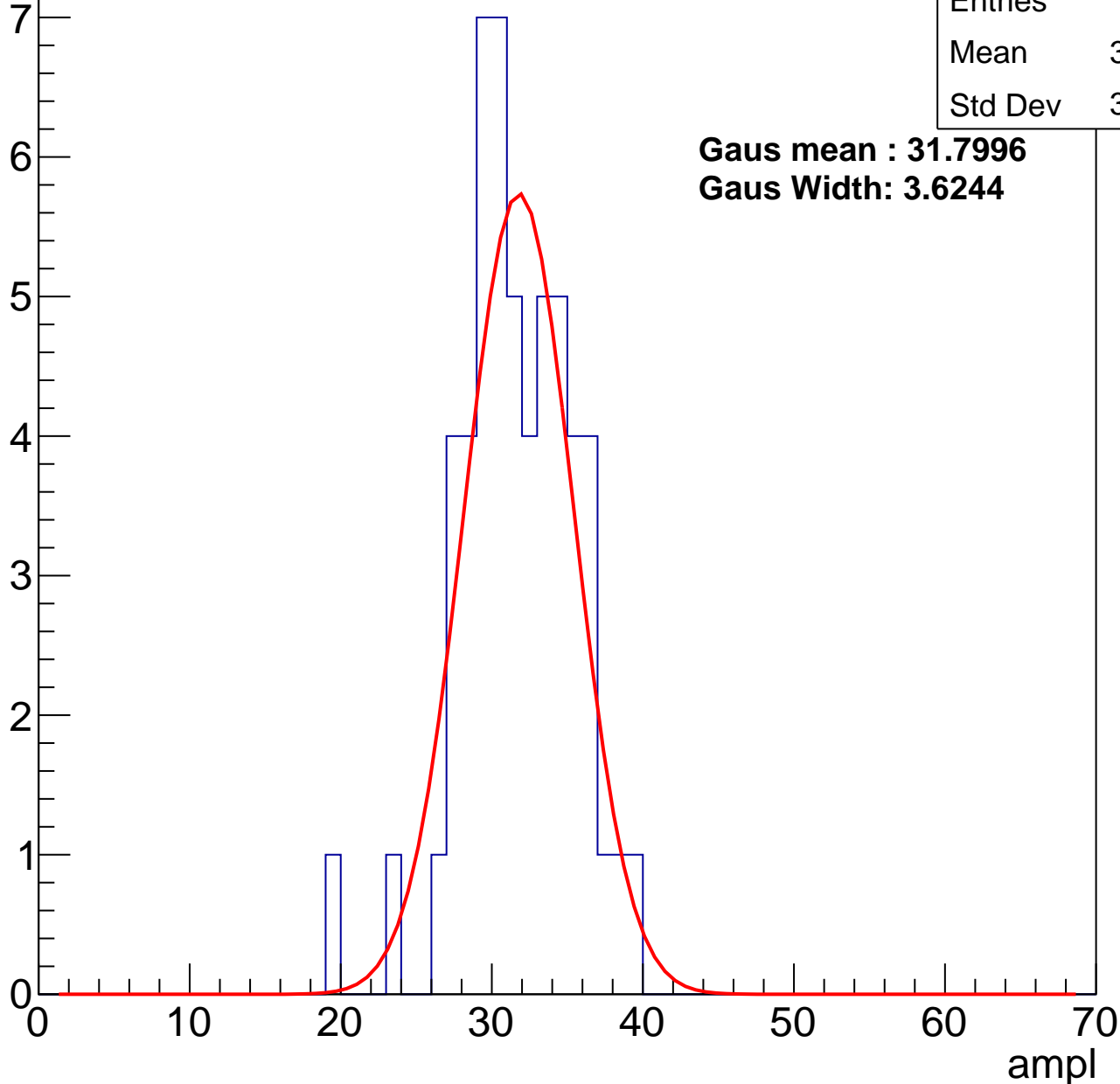
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.22
Std Dev	3.686

Gaus mean : 31.7996

Gaus Width: 3.6244



B0L001S, U23-ch94, adc1

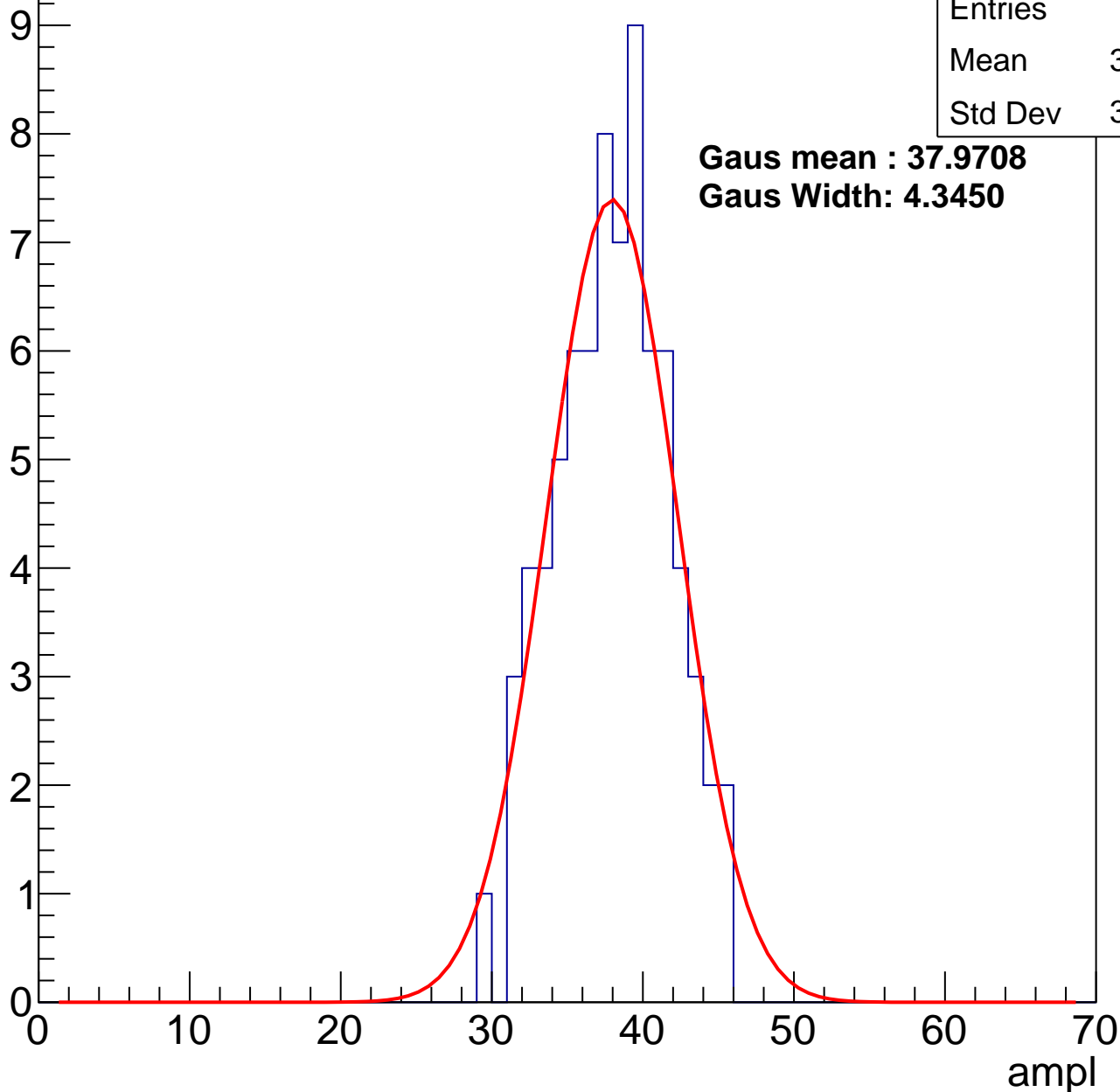
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	37.53
Std Dev	3.658

Gaus mean : 37.9708

Gaus Width: 4.3450

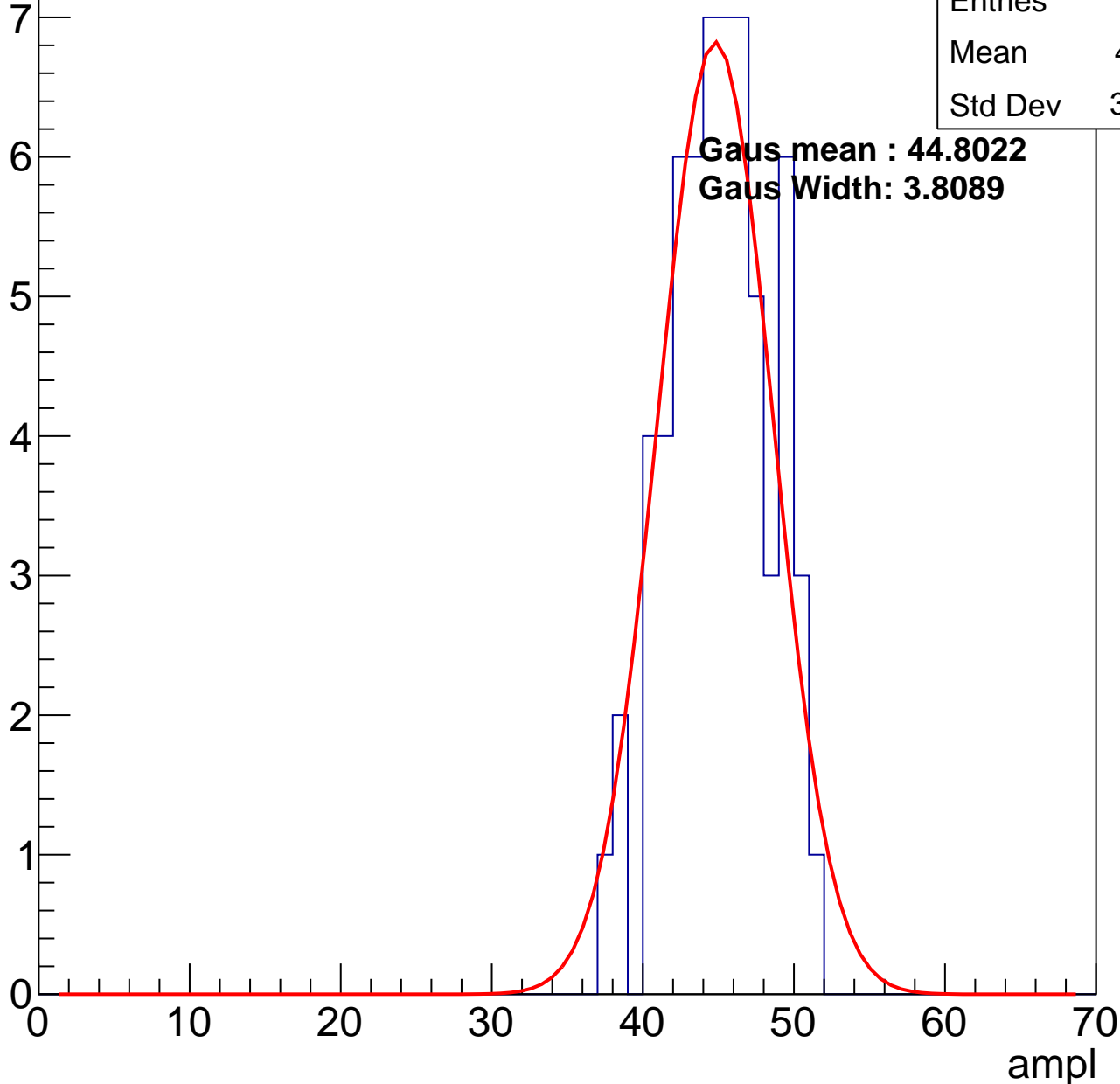


B0L001S, U23-ch94, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.61
Std Dev	3.269

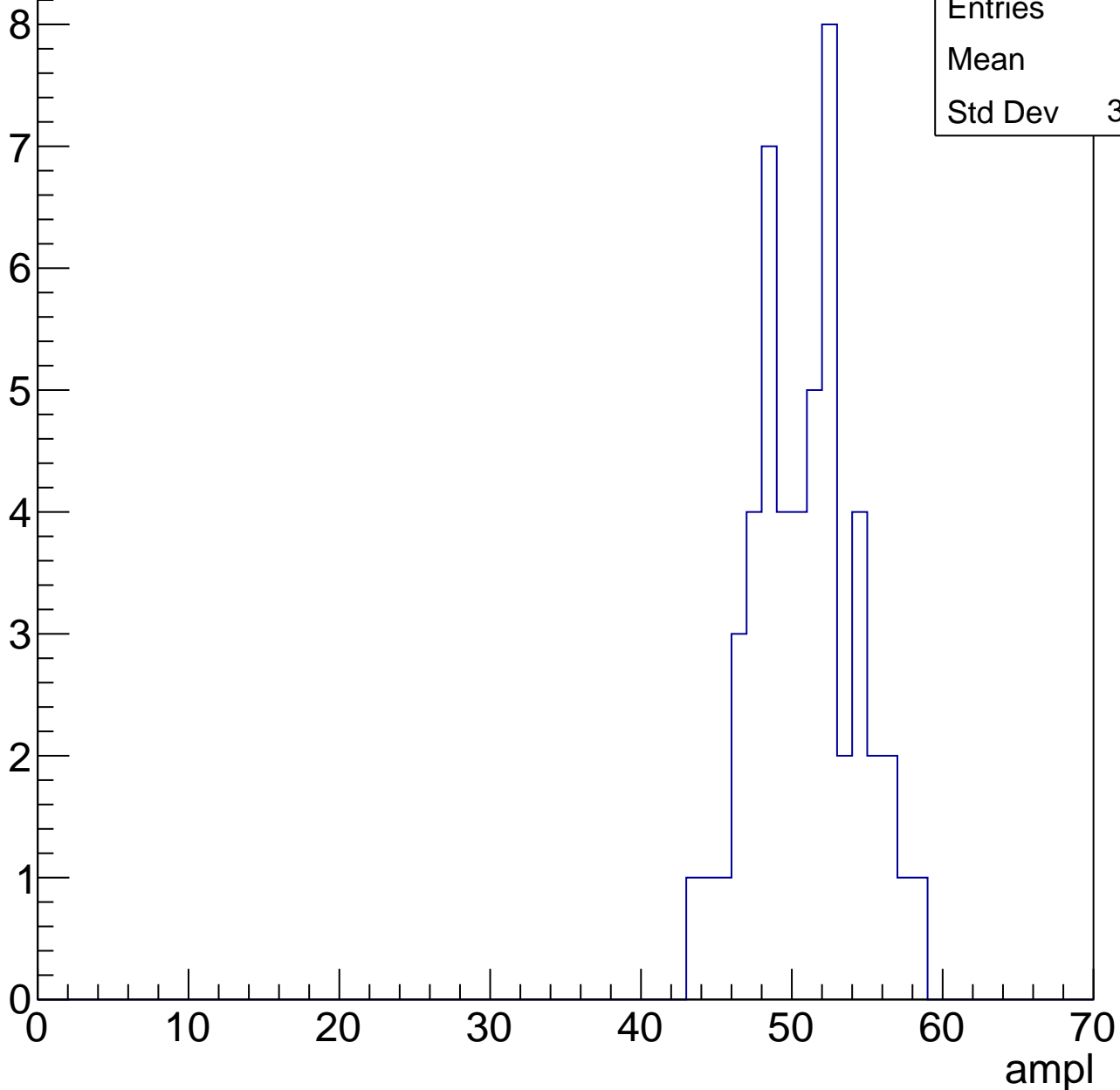


B0L001S, U23-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	50.4
Std Dev	3.382

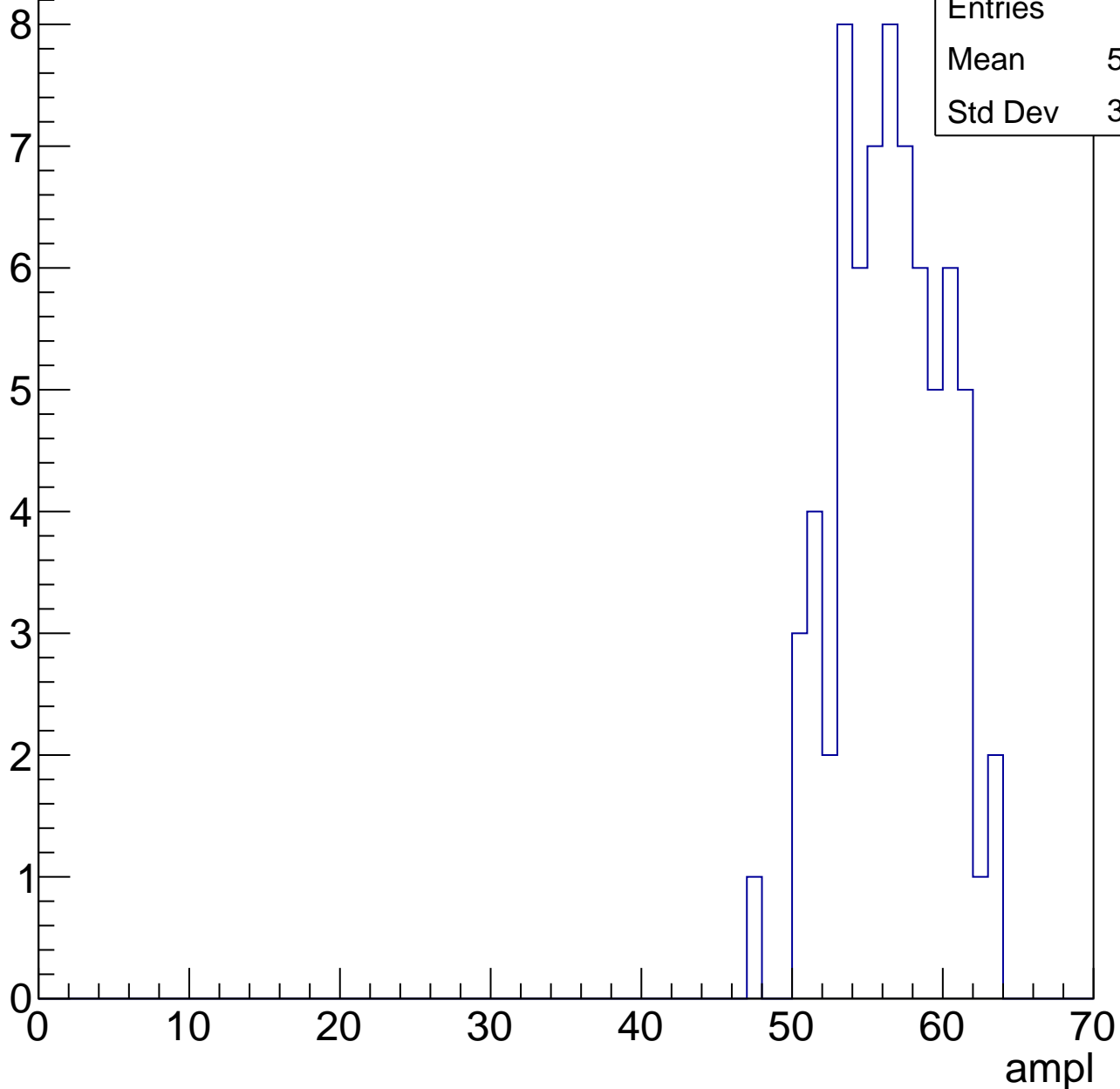


B0L001S, U23-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.07
Std Dev	3.469

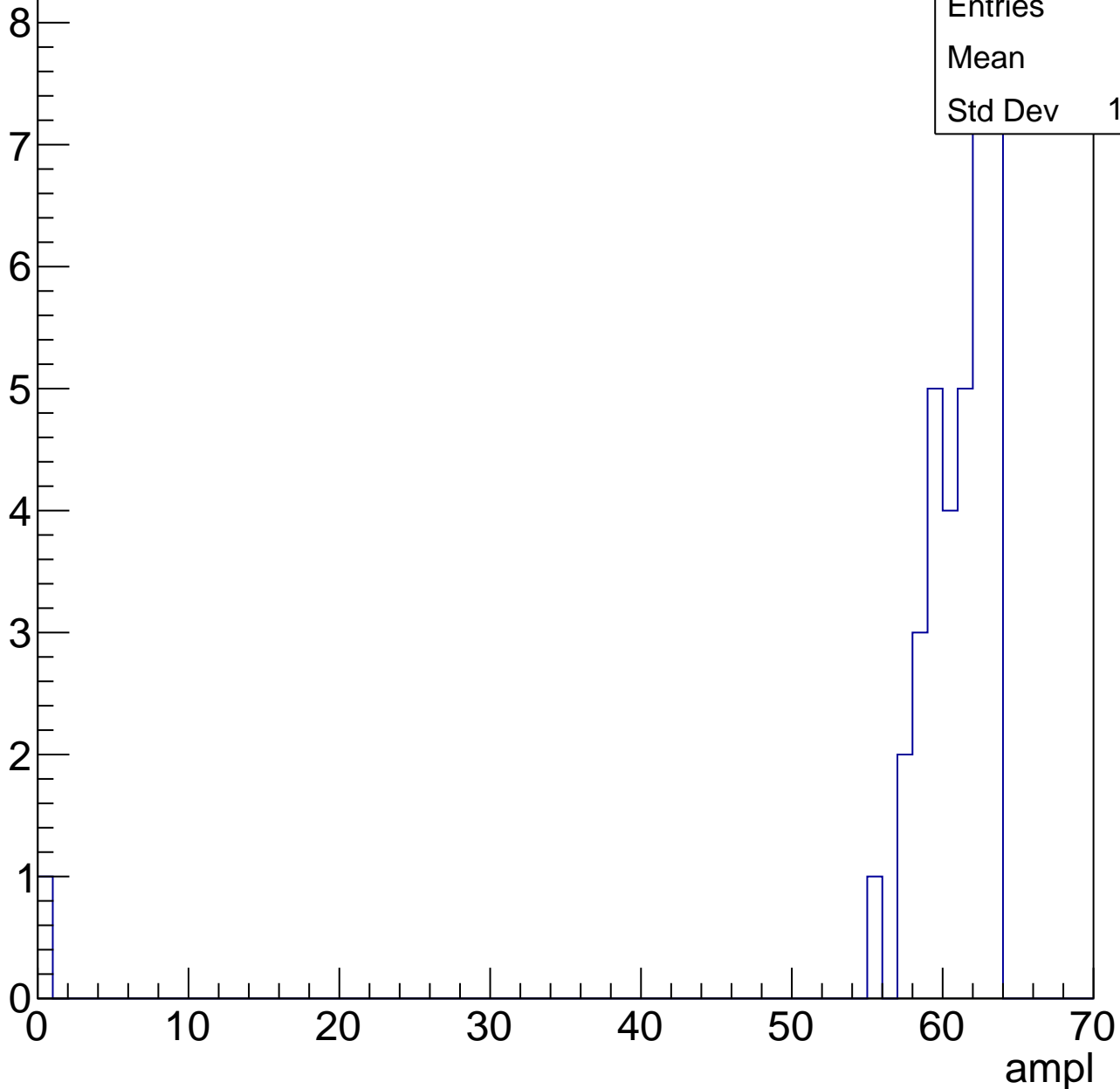


B0L001S, U23-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	59
Std Dev	10.04



B0L001S, U23-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch95, adc0

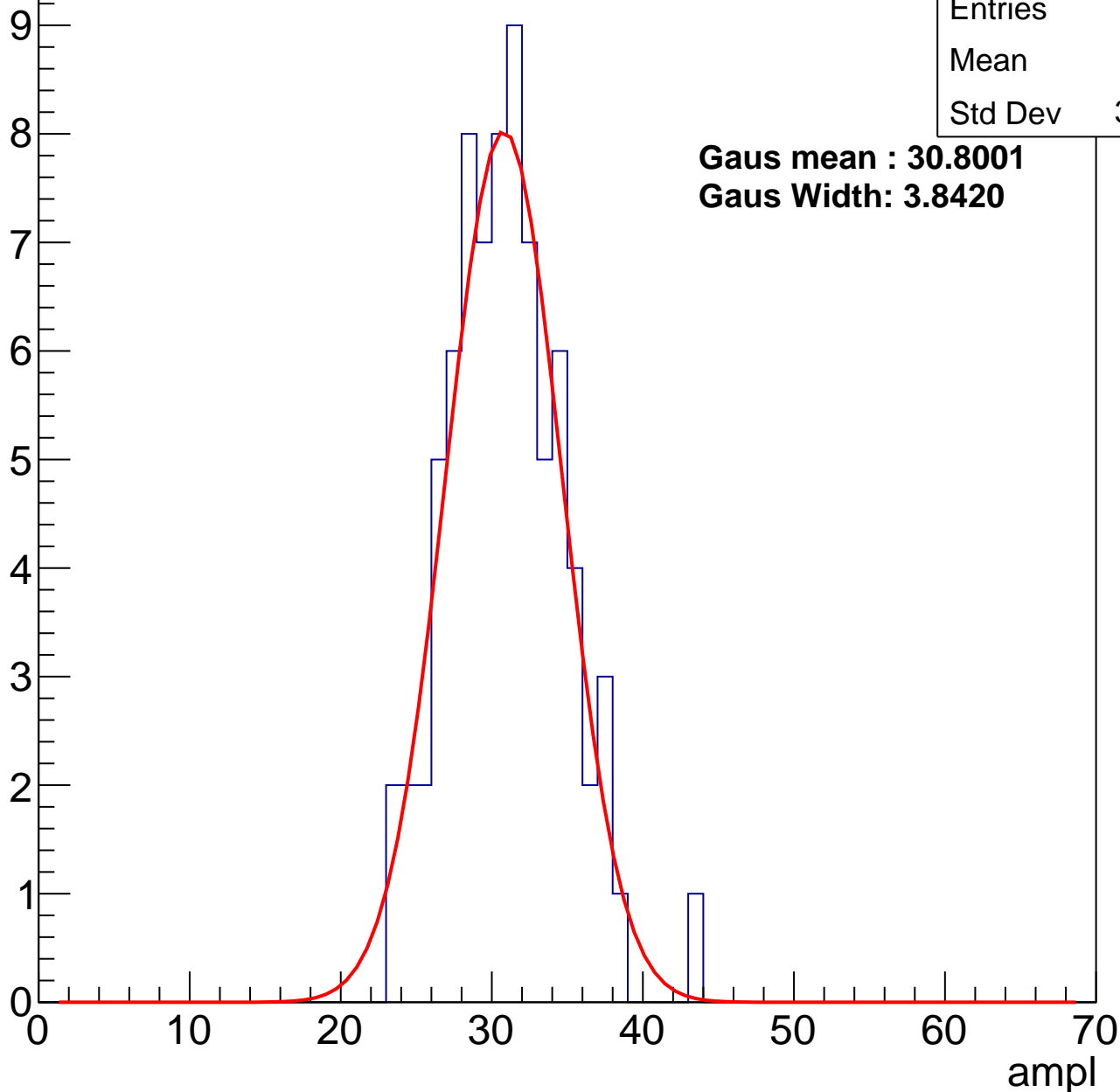
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.5
Std Dev	3.761

Gaus mean : 30.8001

Gaus Width: 3.8420



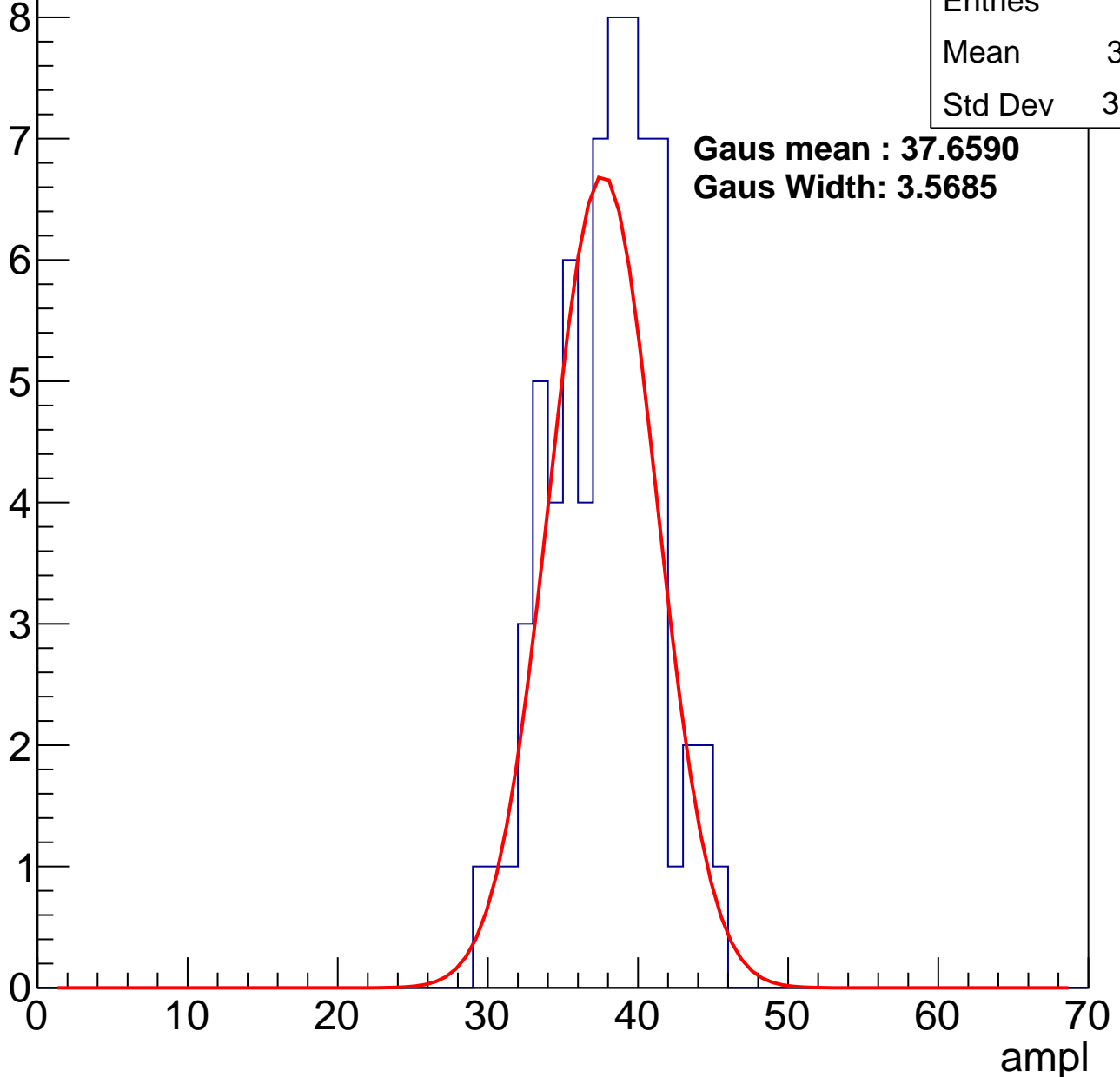
B0L001S, U23-ch95, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.41
Std Dev	3.495

Gaus mean : 37.6590
Gaus Width: 3.5685



B0L001S, U23-ch95, adc2

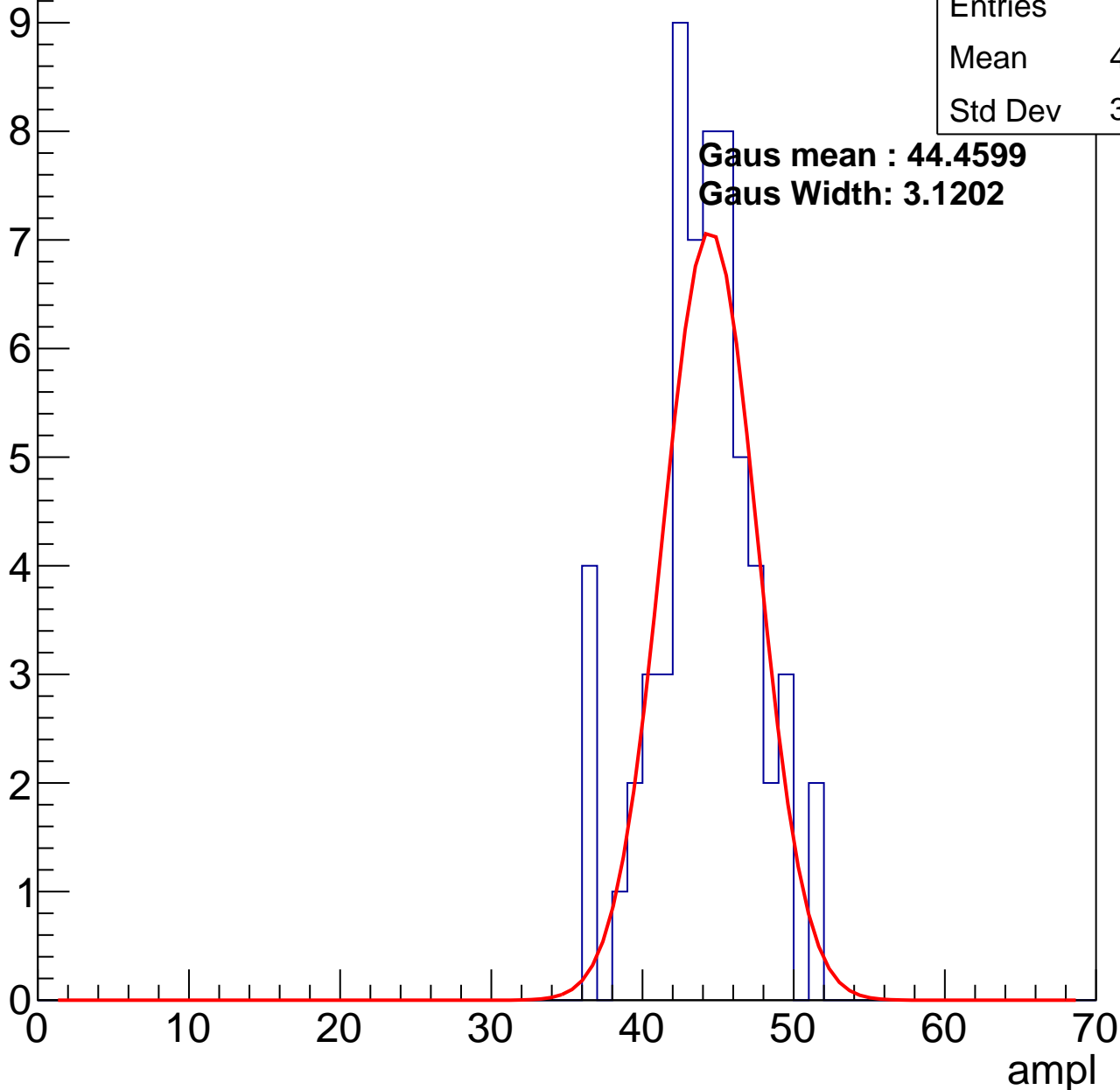
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.56
Std Dev	3.429

Gaus mean : 44.4599

Gaus Width: 3.1202

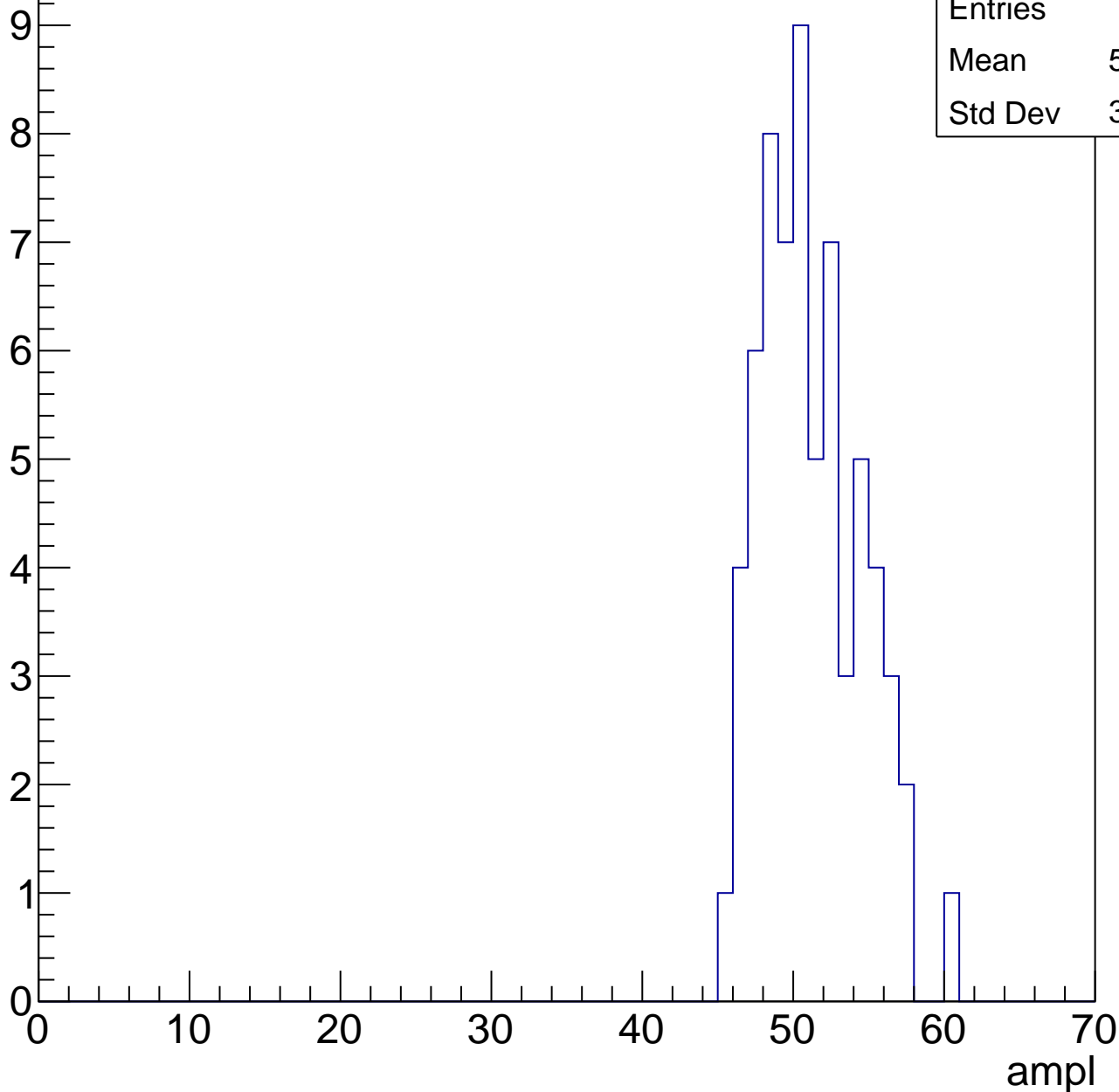


B0L001S, U23-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

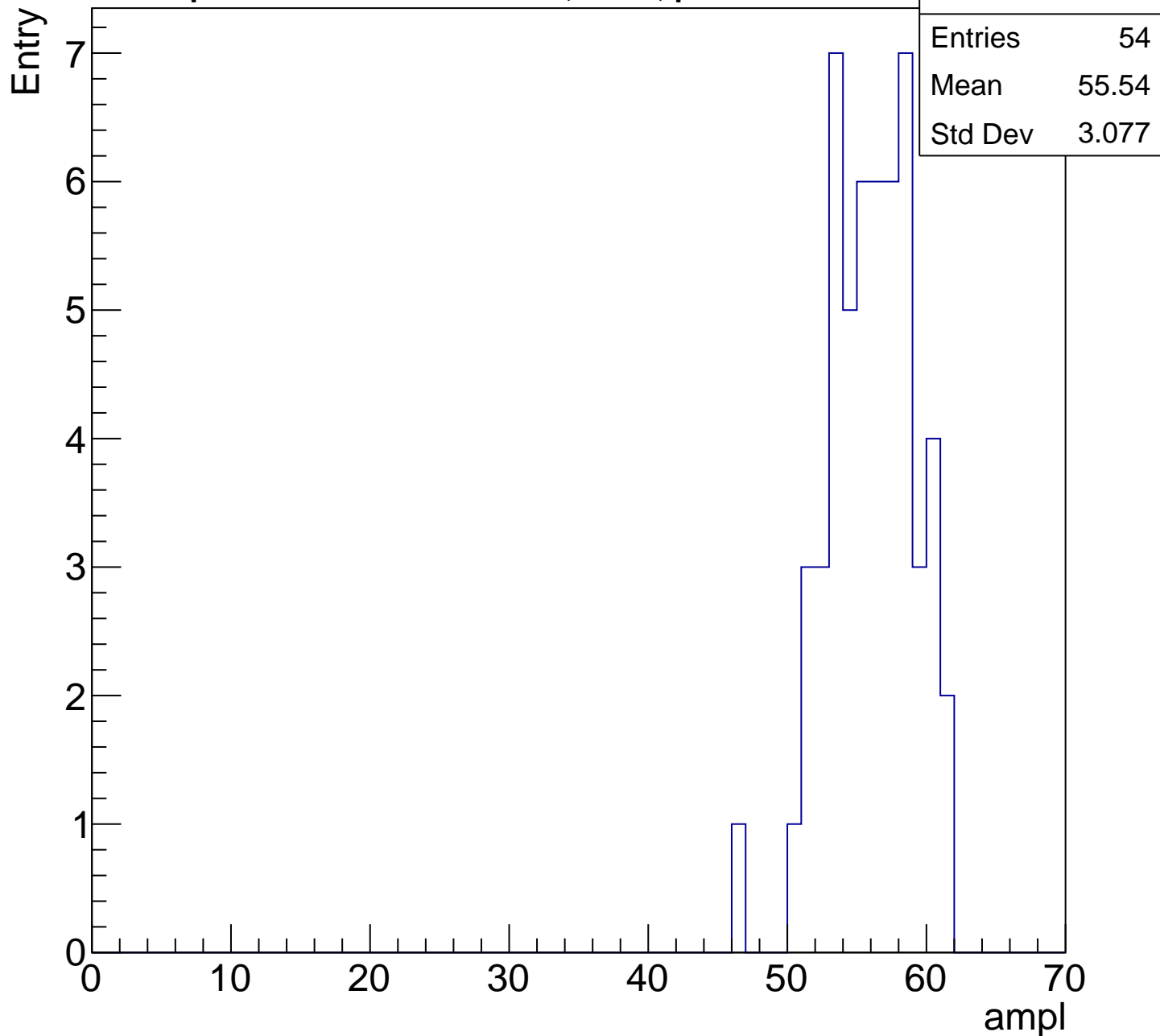
Entry

Entries	65
Mean	50.74
Std Dev	3.264



B0L001S, U23-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U23-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	42
Mean	59.14
Std Dev	9.425

Entry

10

8

6

4

2

0

0

10

20

30

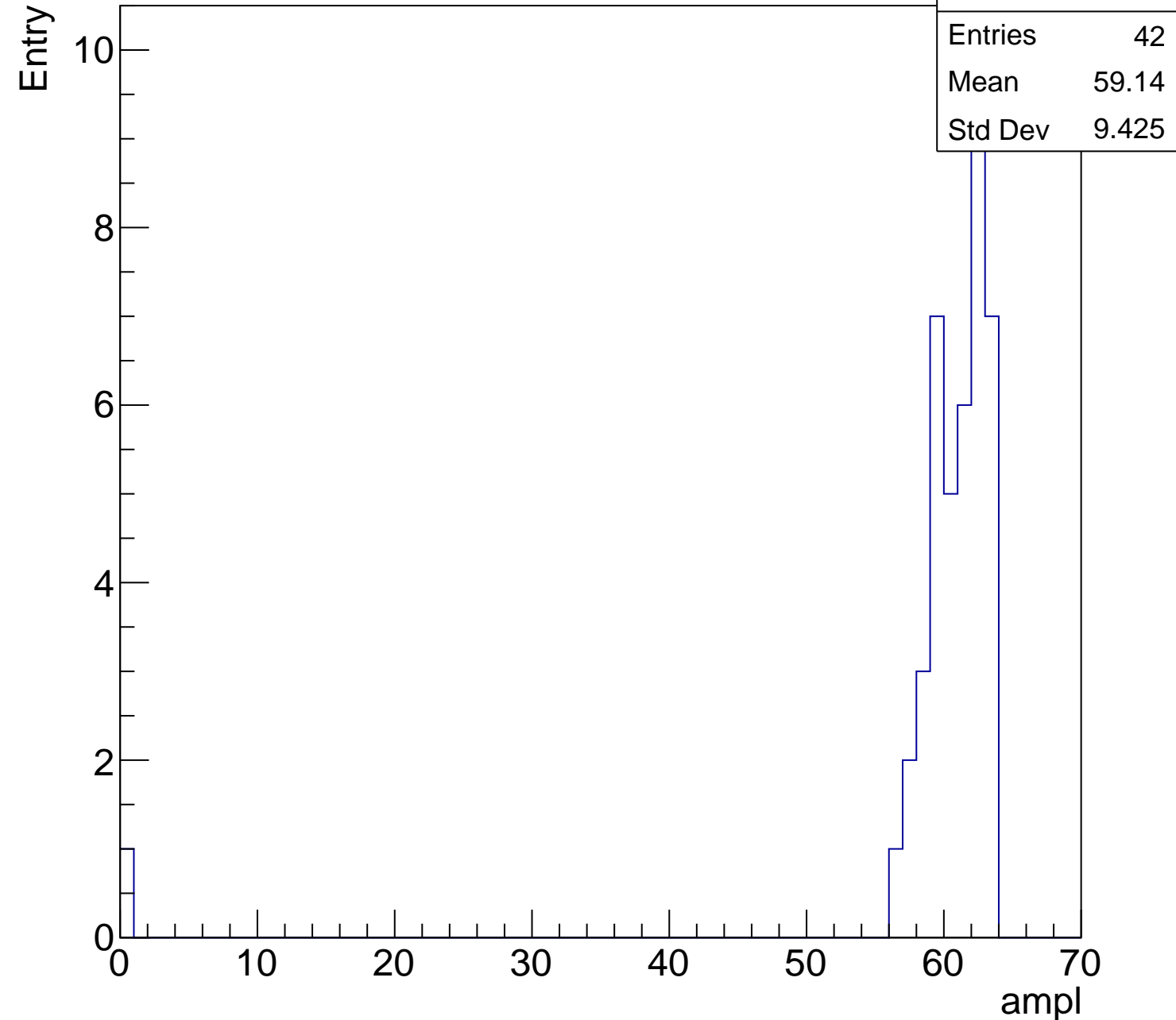
40

50

60

ampl

70



B0L001S, U23-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.2

Std Dev

0.9798

Entries	5
Mean	62.2
Std Dev	0.9798

B0L001S, U23-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch96, adc0

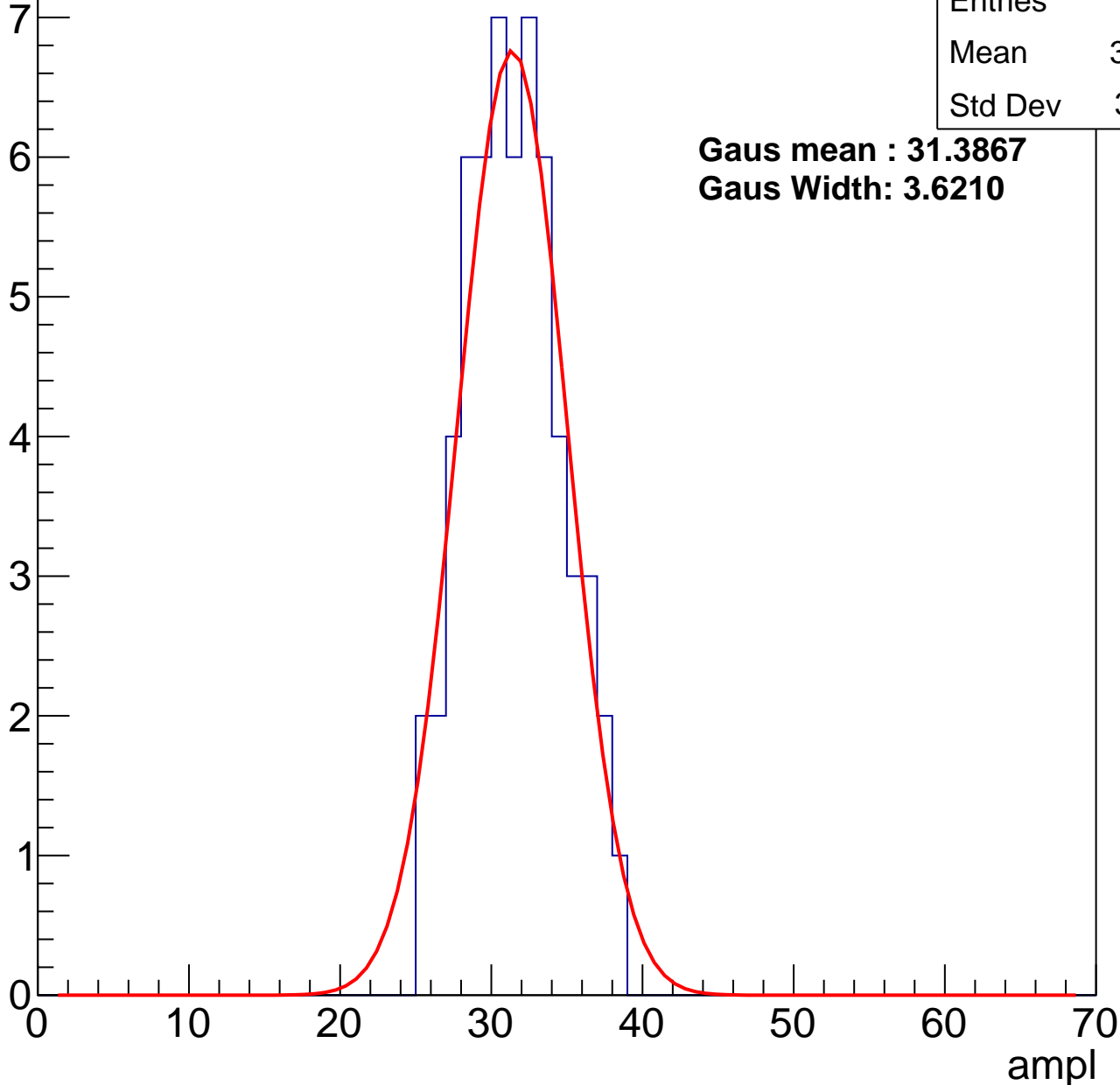
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.03
Std Dev	3.141

Gaus mean : 31.3867

Gaus Width: 3.6210



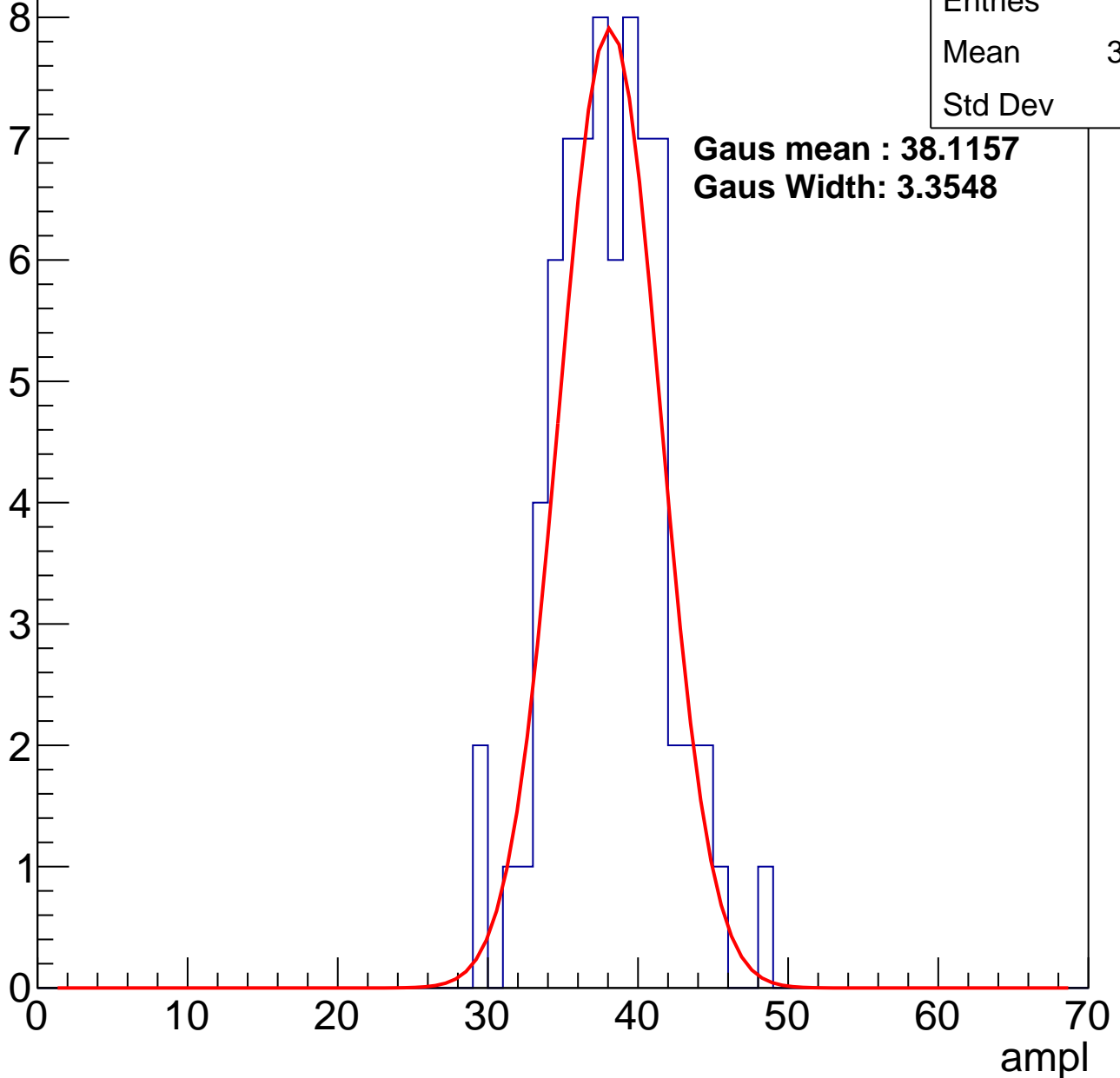
B0L001S, U23-ch96, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.61
Std Dev	3.6

Gaus mean : 38.1157
Gaus Width: 3.3548



B0L001S, U23-ch96, adc2

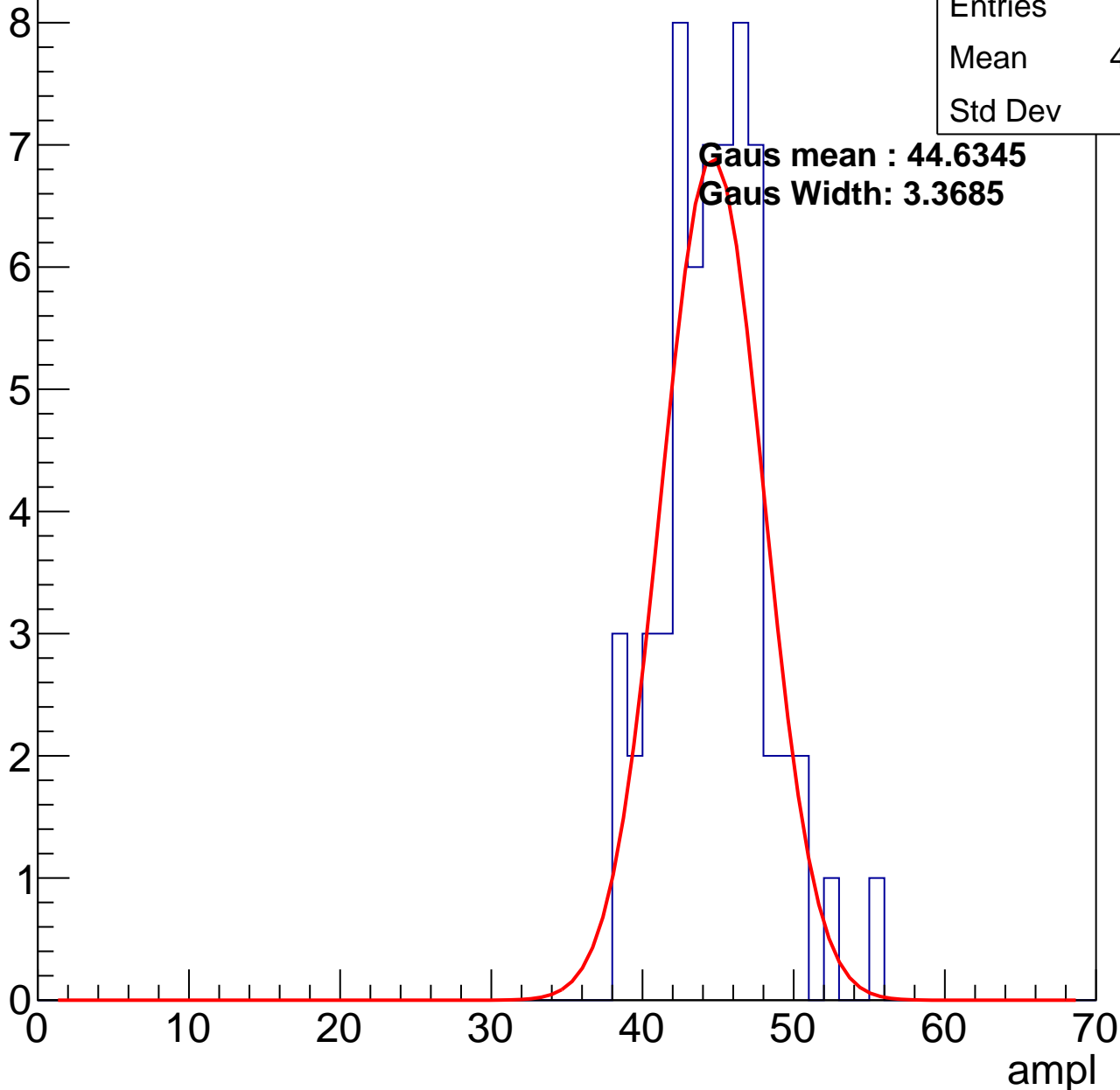
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.35
Std Dev	3.37

Gaus mean : 44.6345

Gaus Width: 3.3685

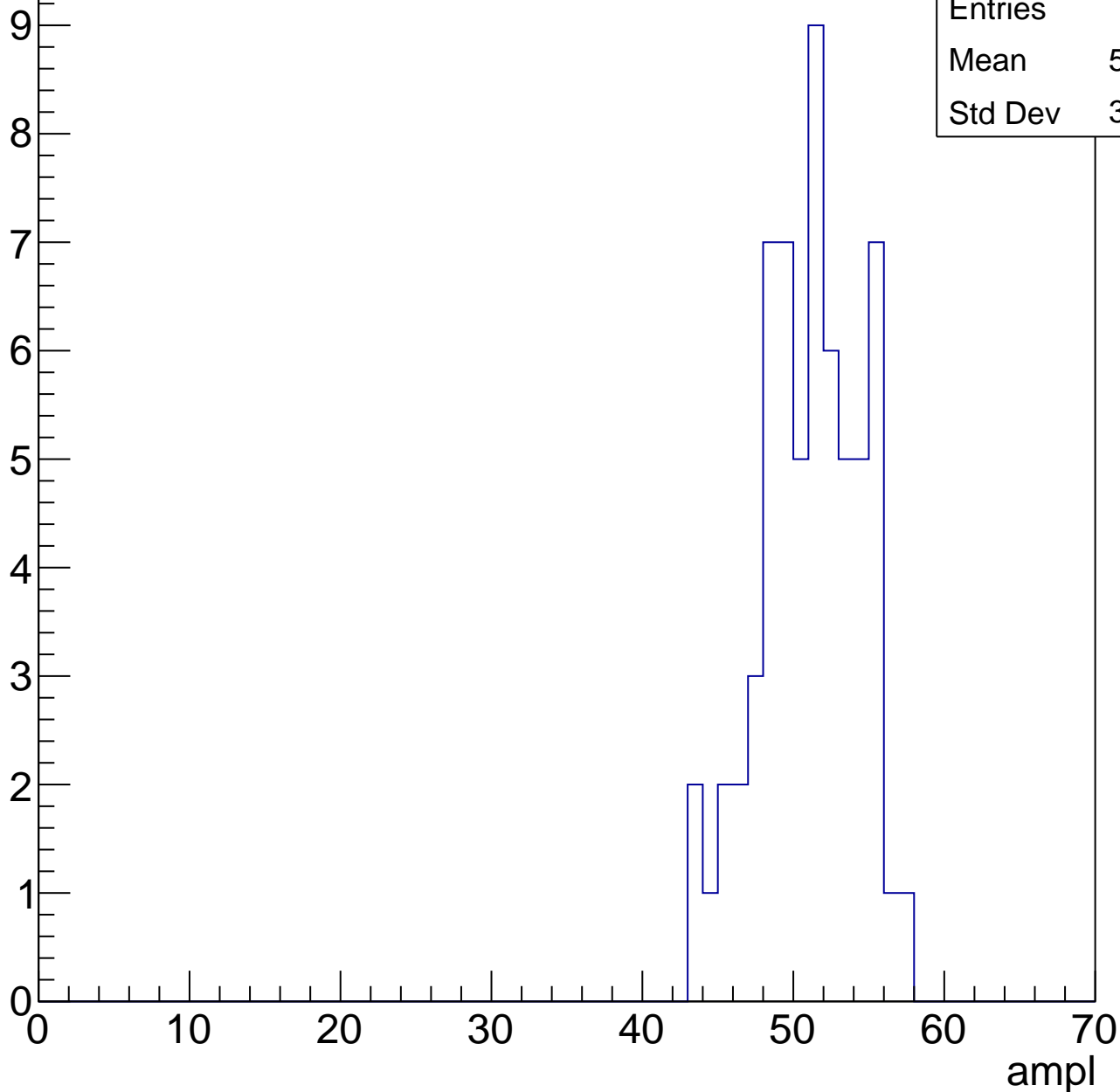


B0L001S, U23-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

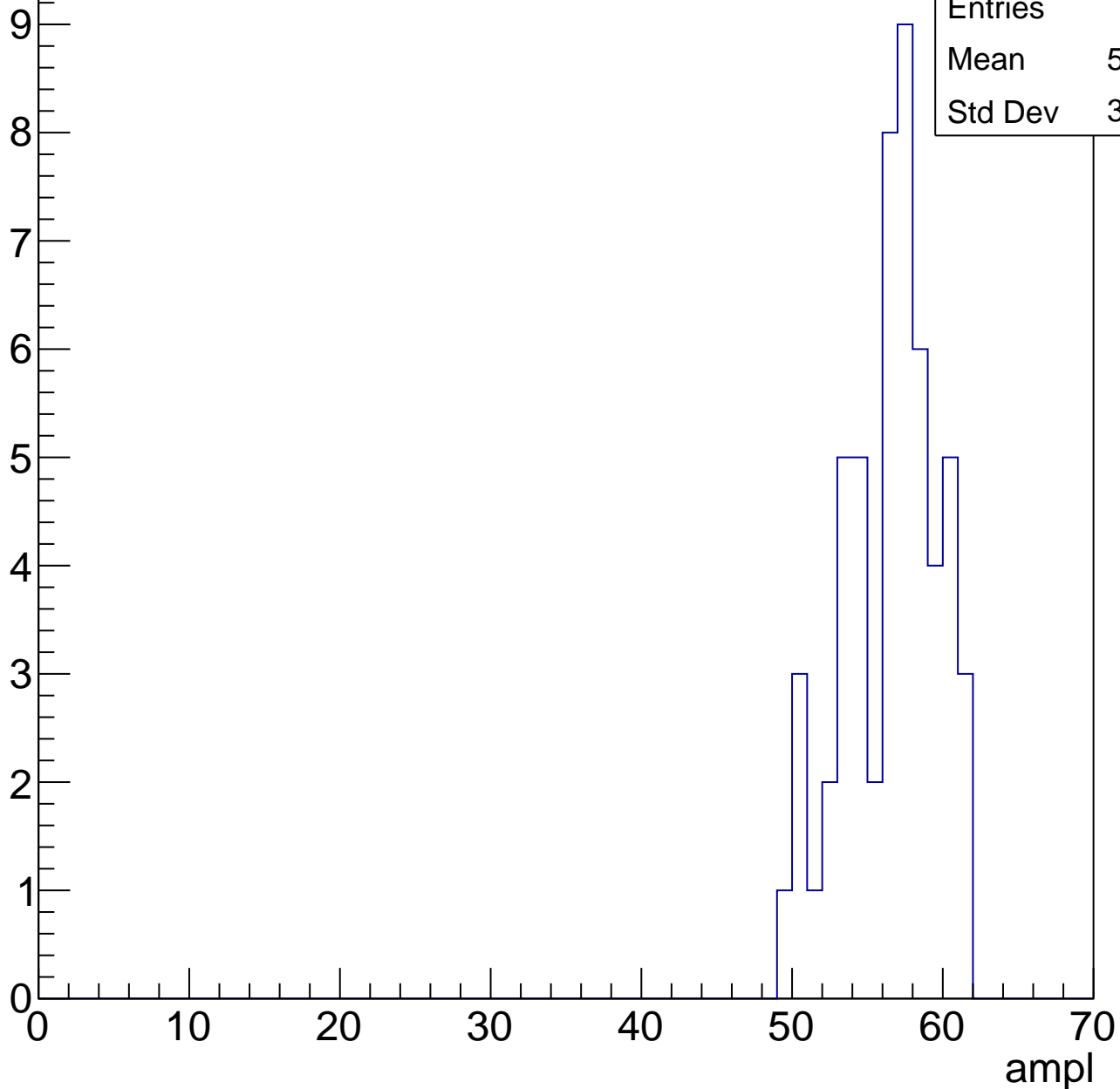
Entries	63
Mean	50.57
Std Dev	3.274



B0L001S, U23-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	45
Mean	59.36
Std Dev	9.214

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

B0L001S, U23-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch97, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	28.79
Std Dev	3.496

Gaus mean : 29.6774

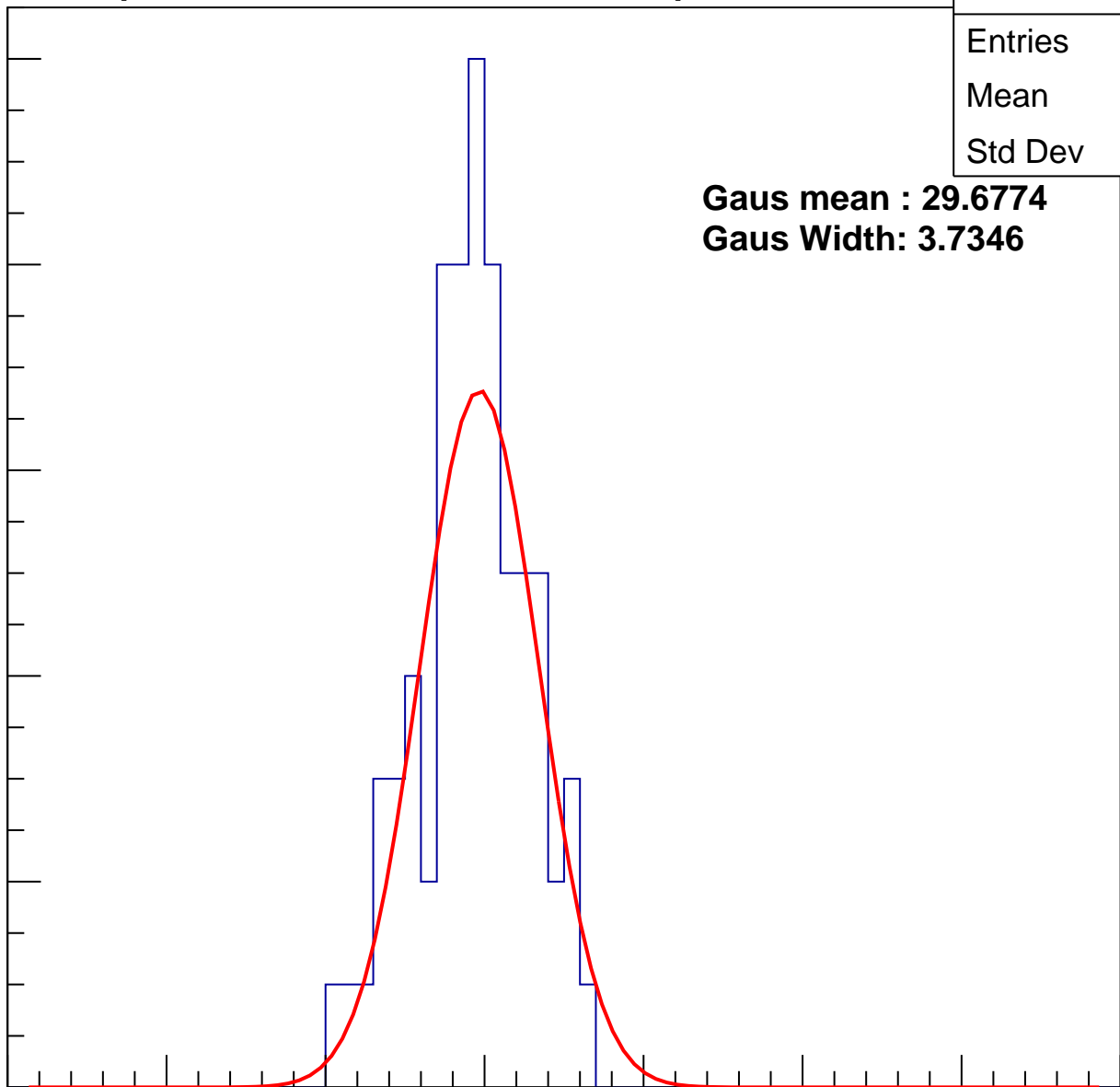
Gaus Width: 3.7346

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch97, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	36.11
Std Dev	3.892

Gaus mean : 36.4597

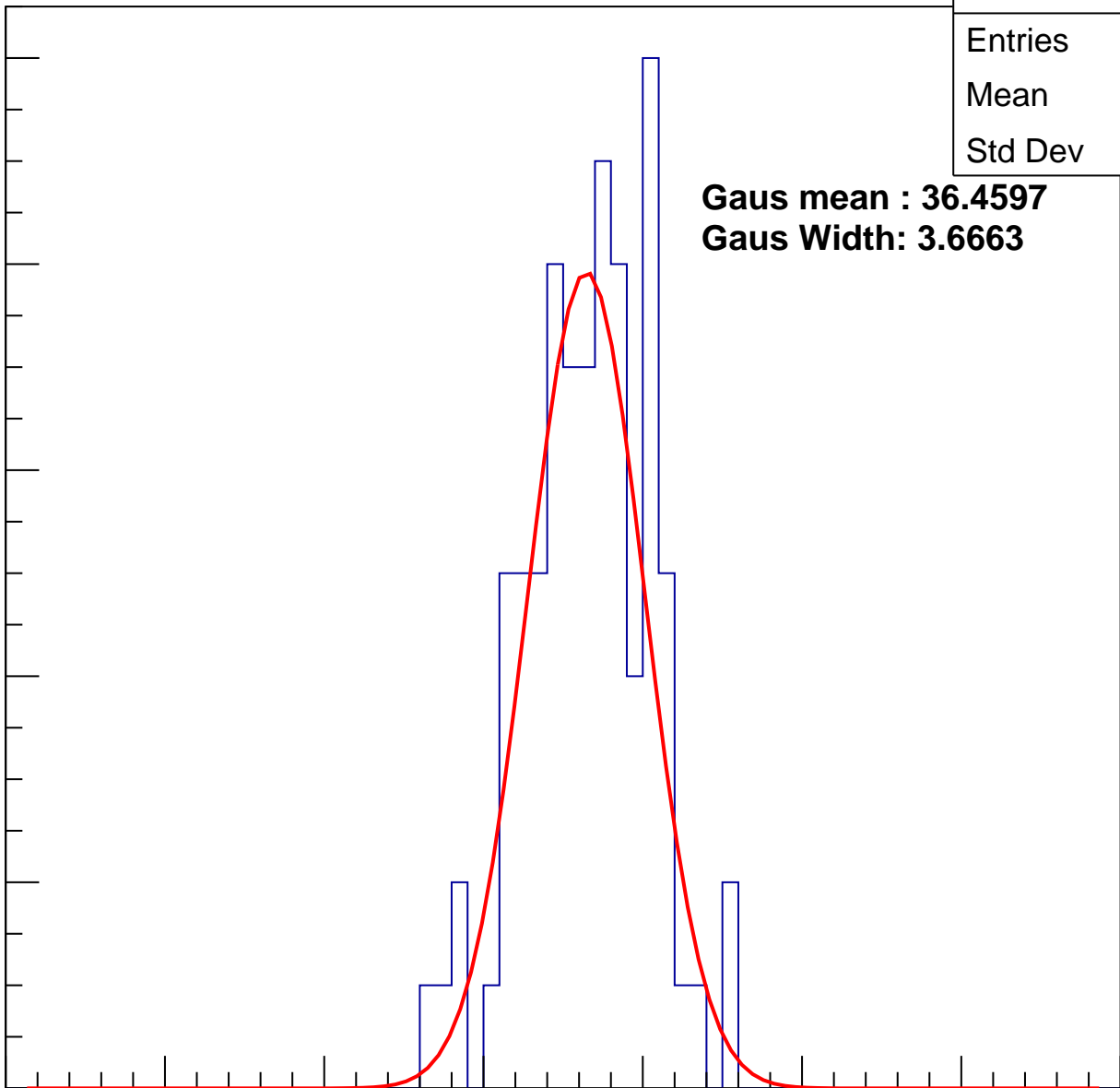
Gaus Width: 3.6663

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch97, adc2

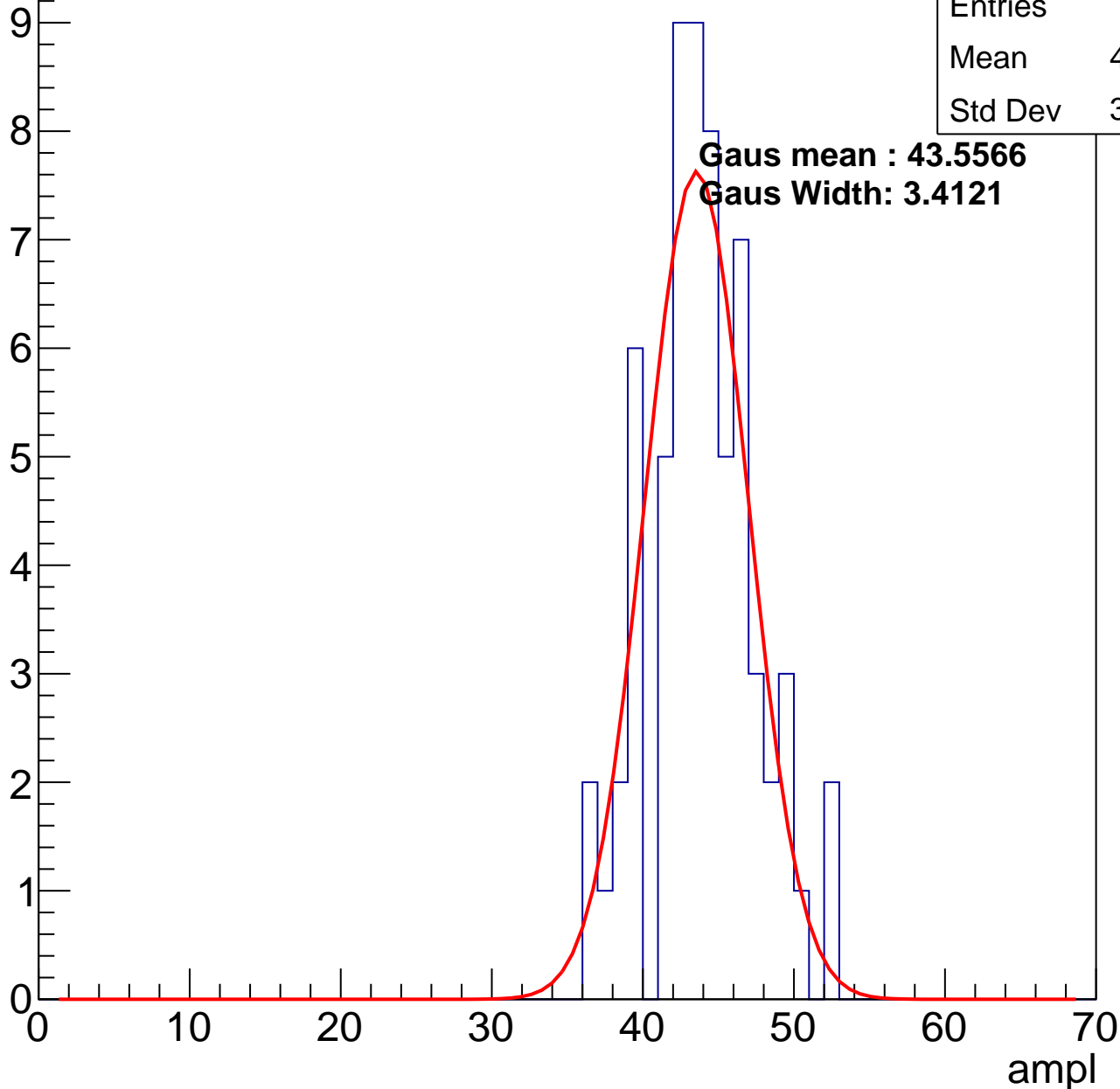
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.48
Std Dev	3.513

Gaus mean : 43.5566

Gaus Width: 3.4121

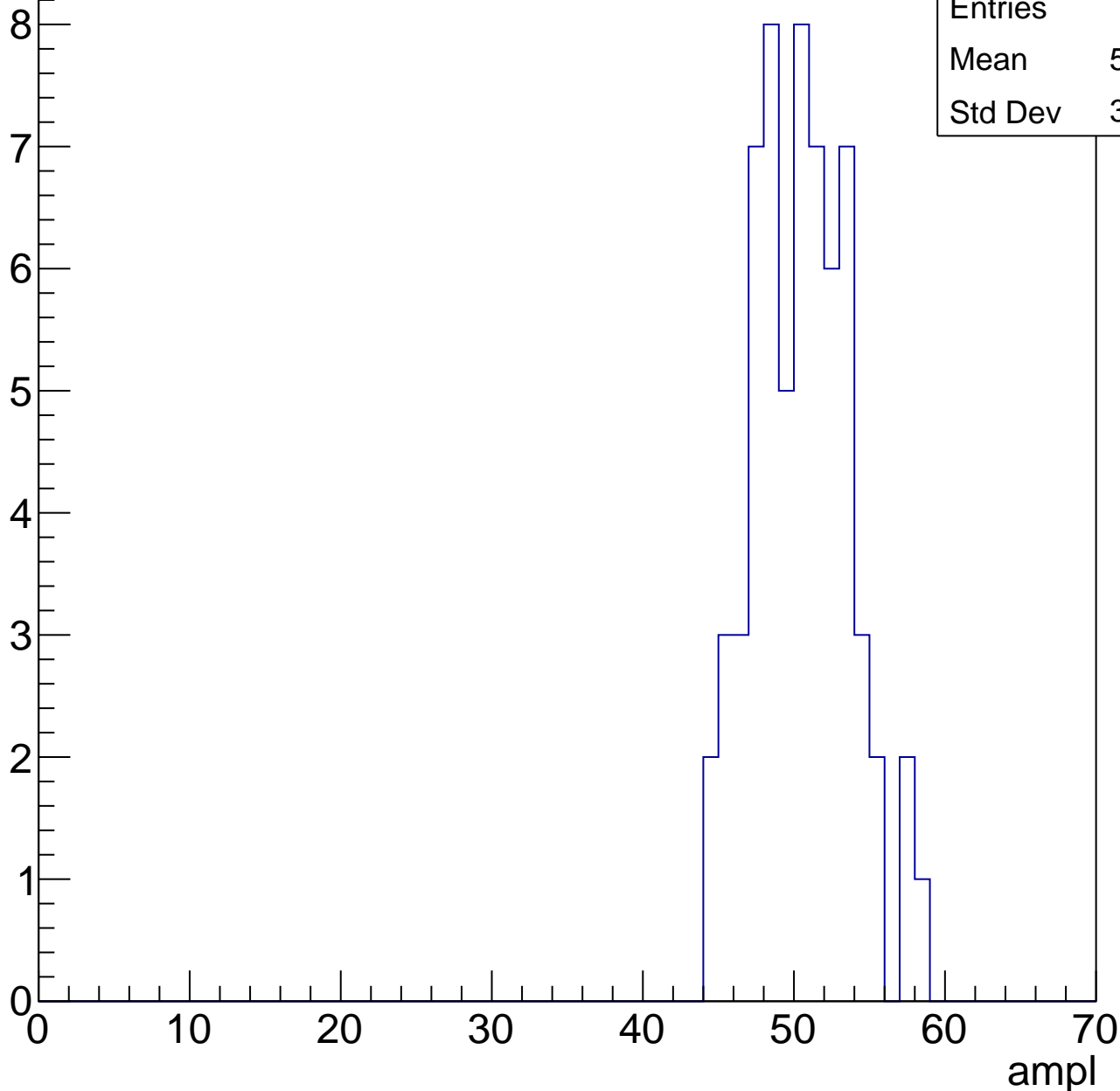


B0L001S, U23-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.05
Std Dev	3.184

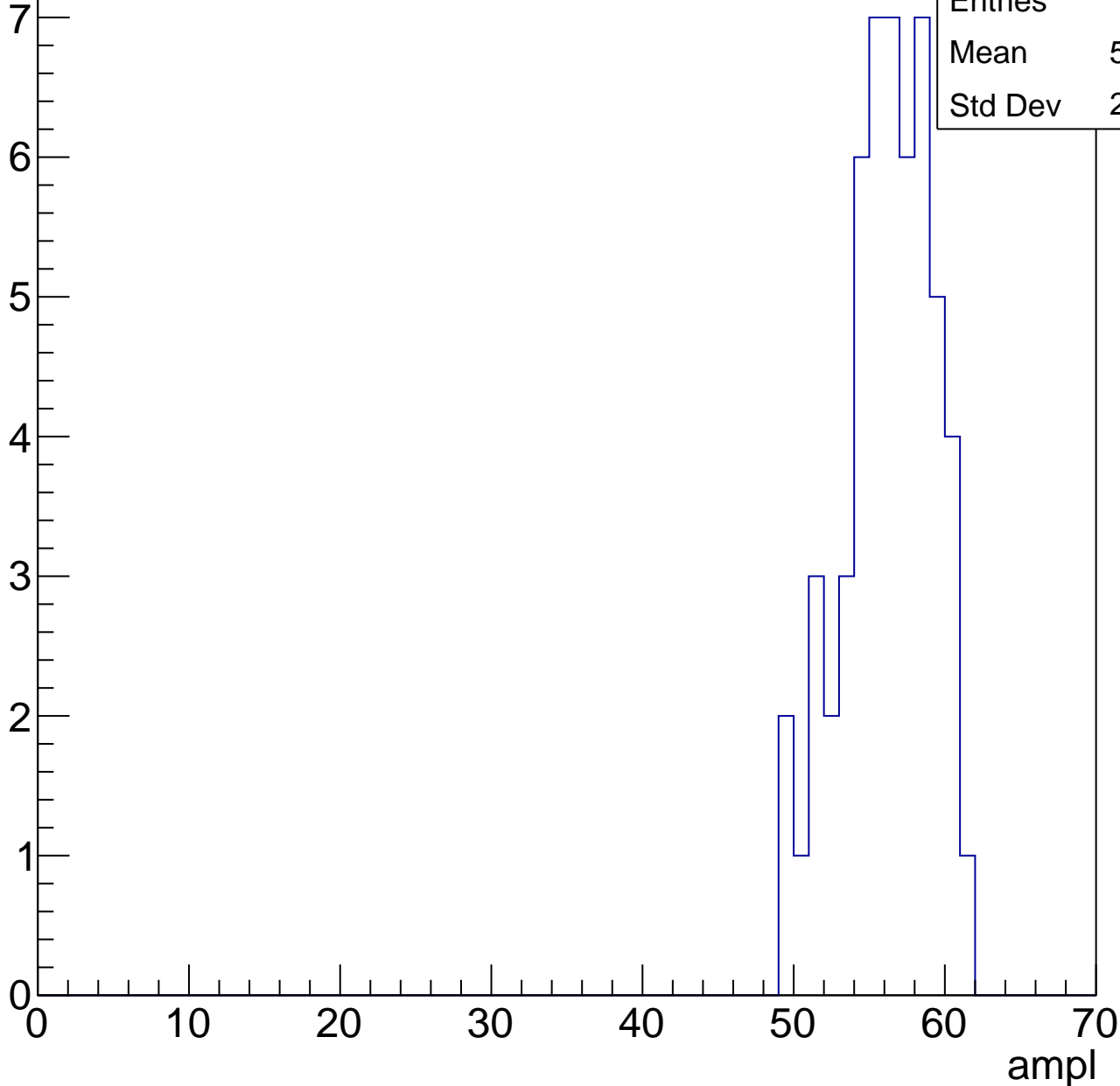


B0L001S, U23-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.72
Std Dev	2.928

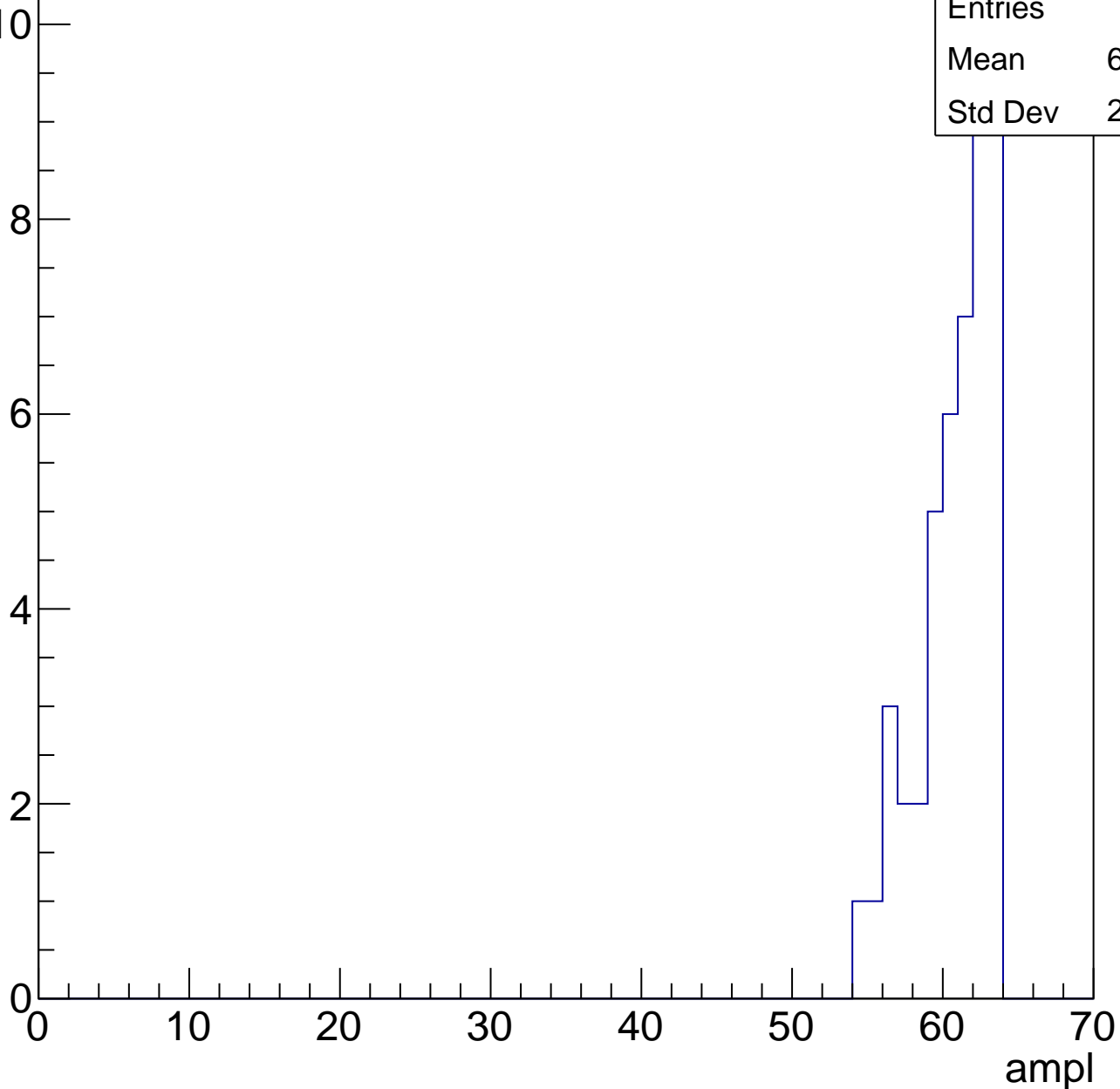


B0L001S, U23-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

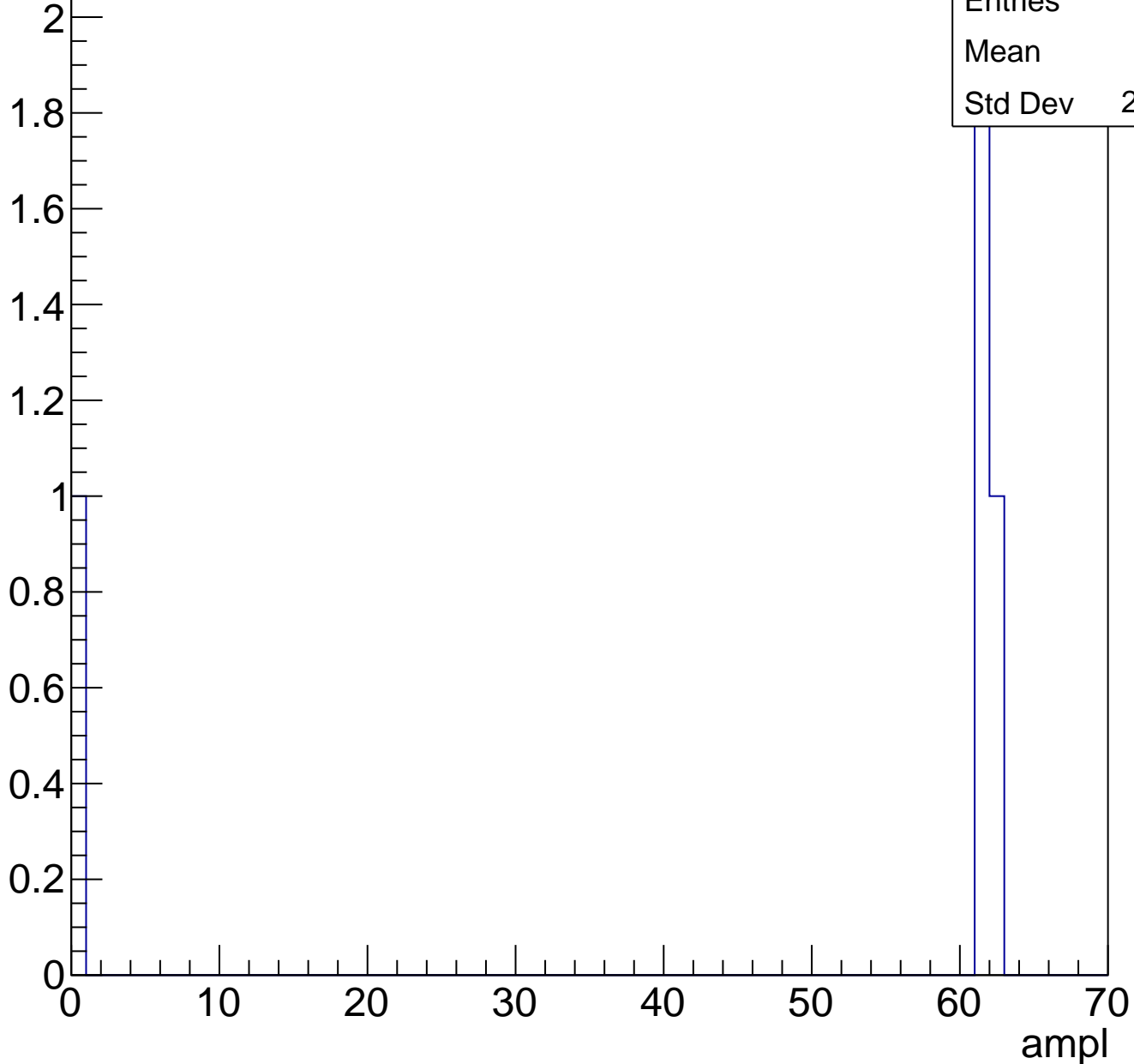
Entries	46
Mean	60.37
Std Dev	2.408



B0L001S, U23-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch98, adc0

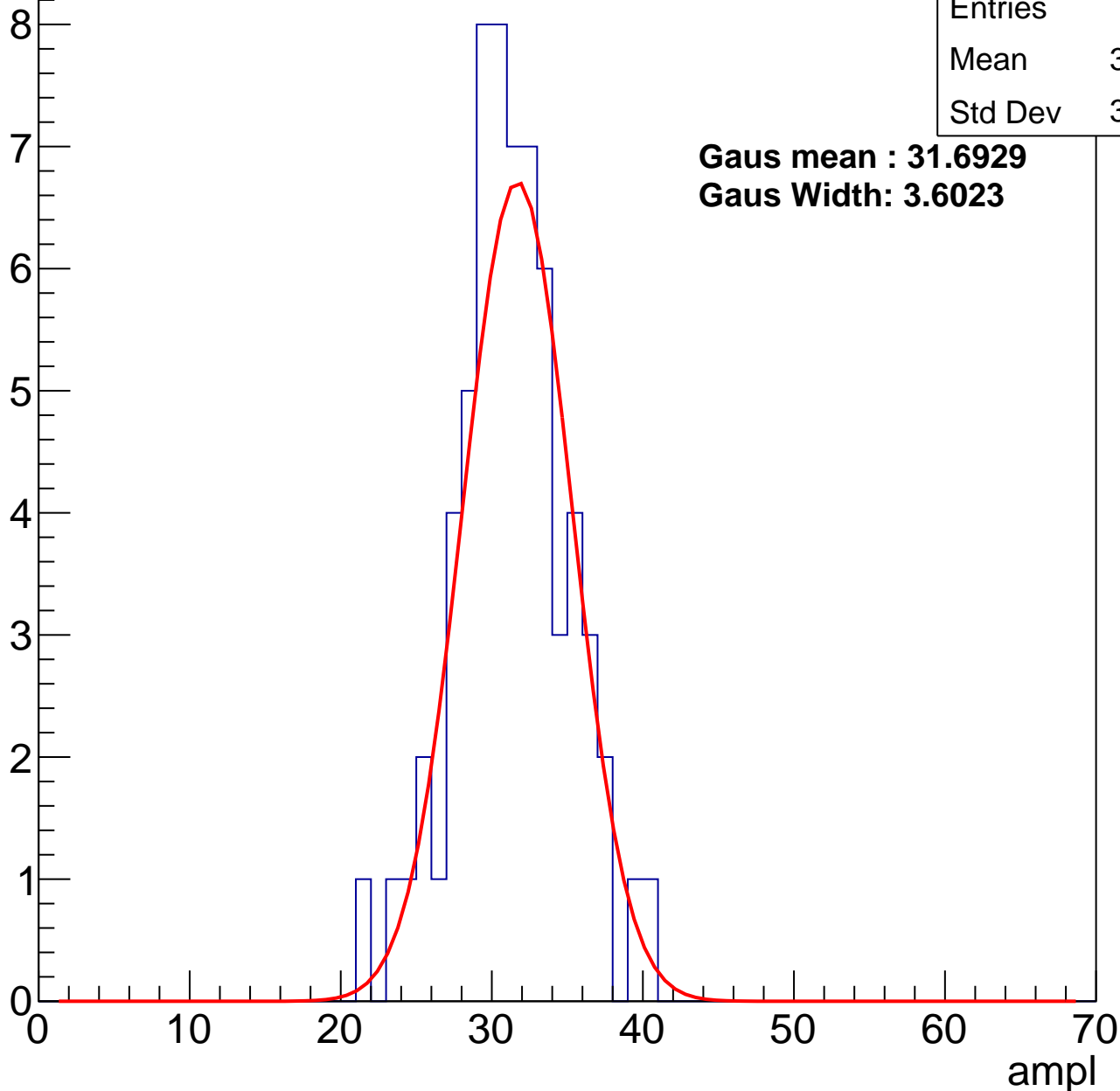
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.86
Std Dev	3.662

Gaus mean : 31.6929

Gaus Width: 3.6023



B0L001S, U23-ch98, adc1

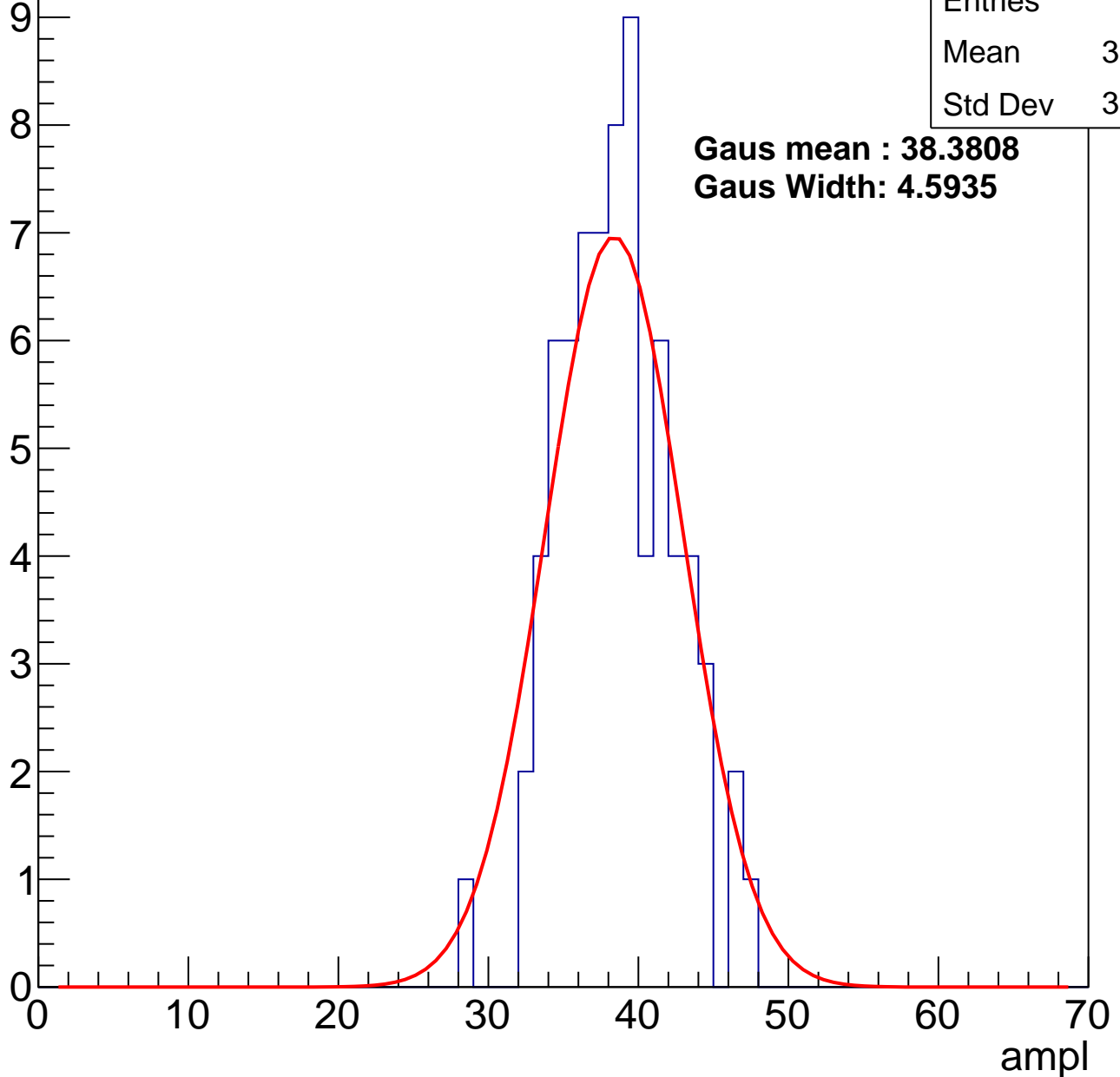
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.12
Std Dev	3.705

Gaus mean : 38.3808

Gaus Width: 4.5935



B0L001S, U23-ch98, adc2

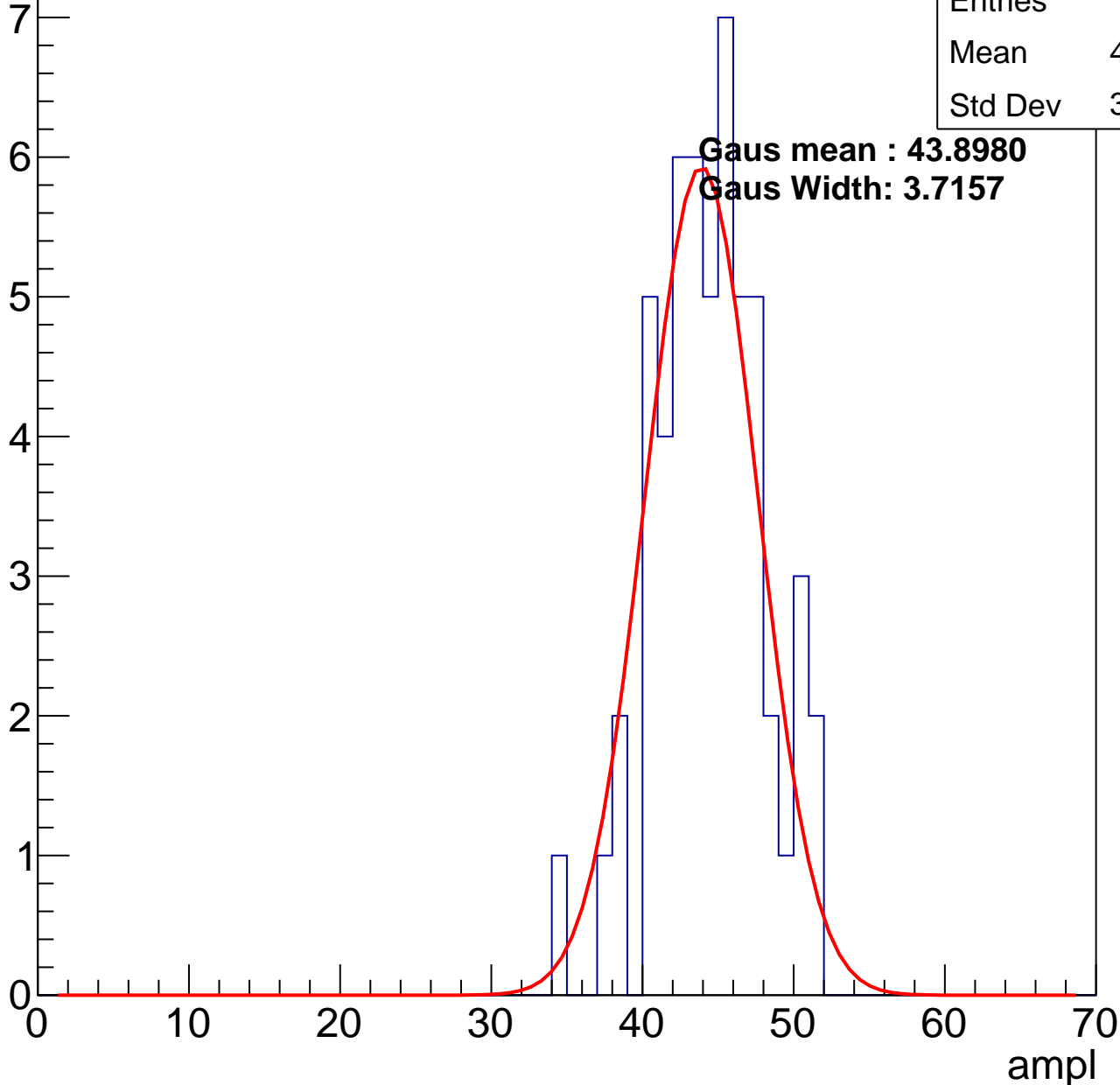
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.96
Std Dev	3.572

Gaus mean : 43.8980

Gaus Width: 3.7157

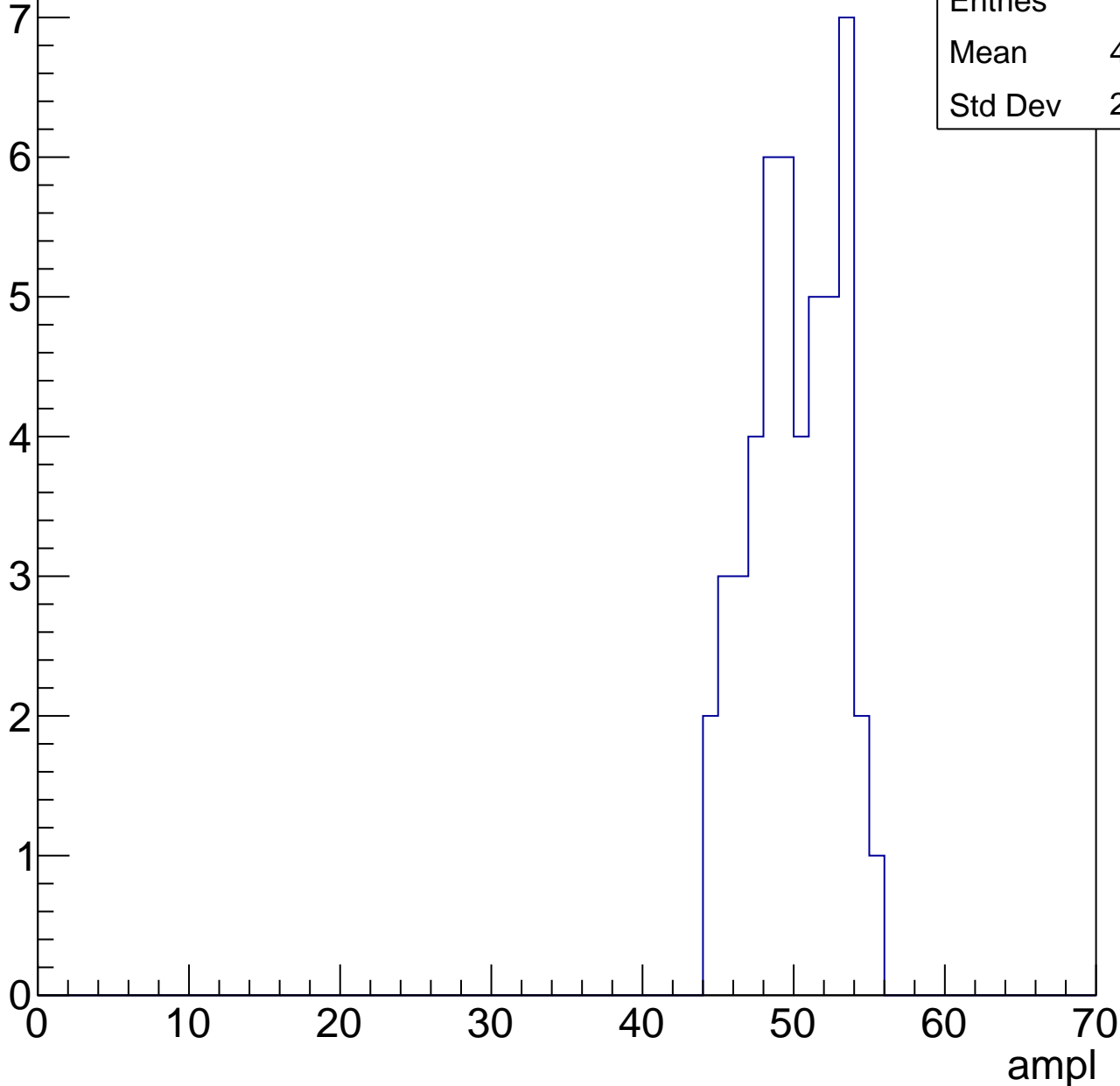


B0L001S, U23-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	49.58
Std Dev	2.878

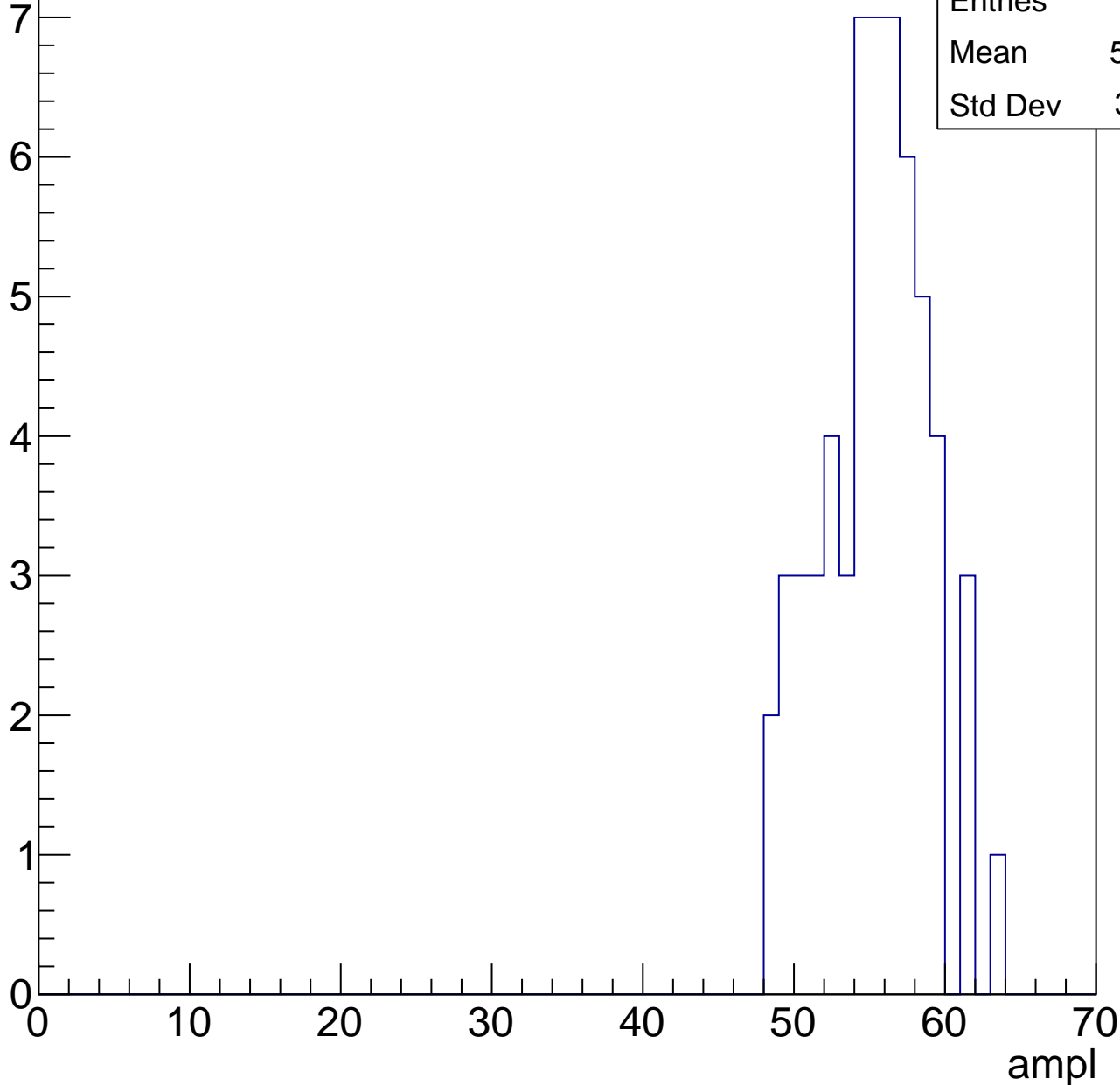


B0L001S, U23-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	54.86
Std Dev	3.461

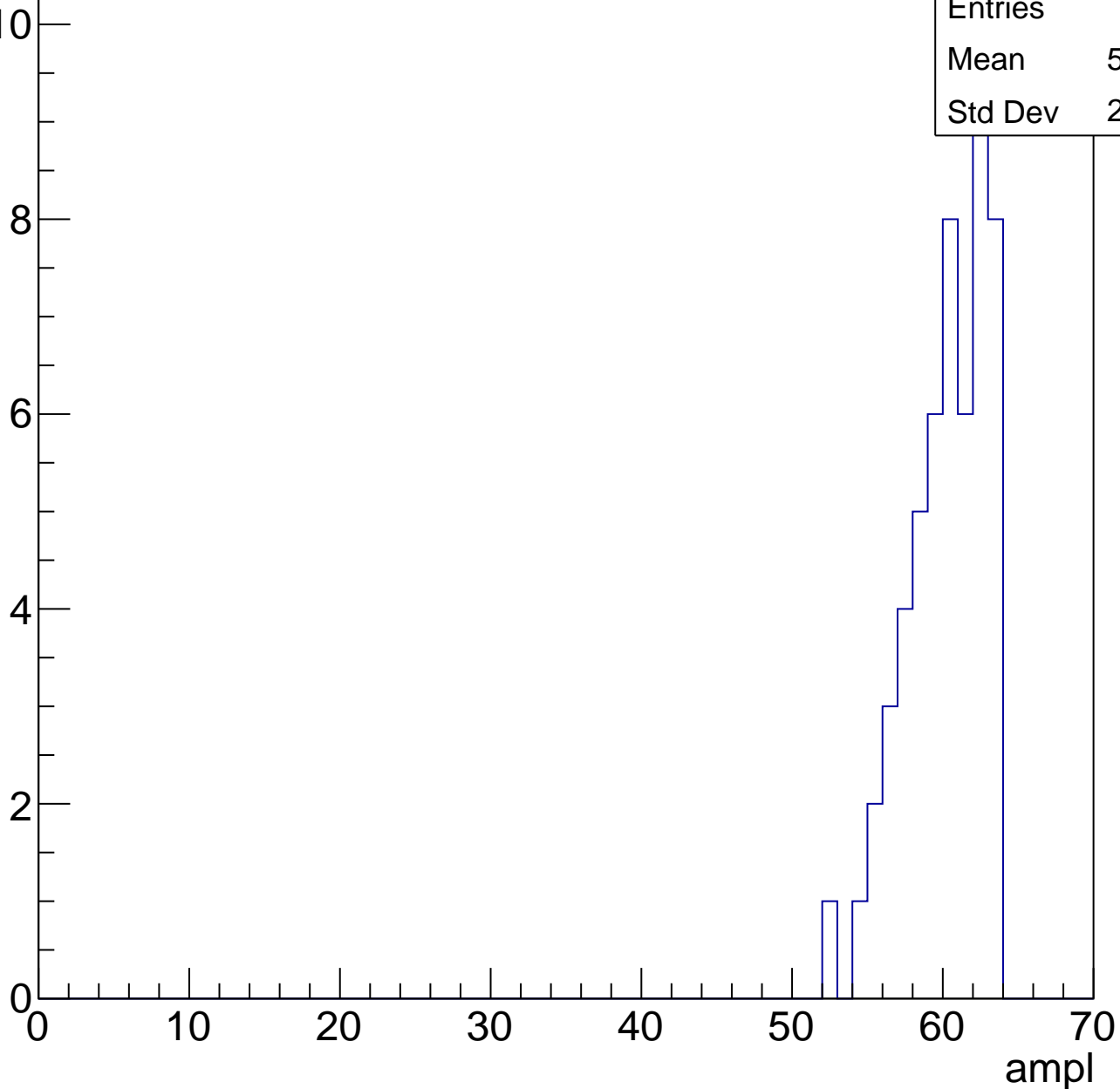


B0L001S, U23-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	59.74
Std Dev	2.633



B0L001S, U23-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

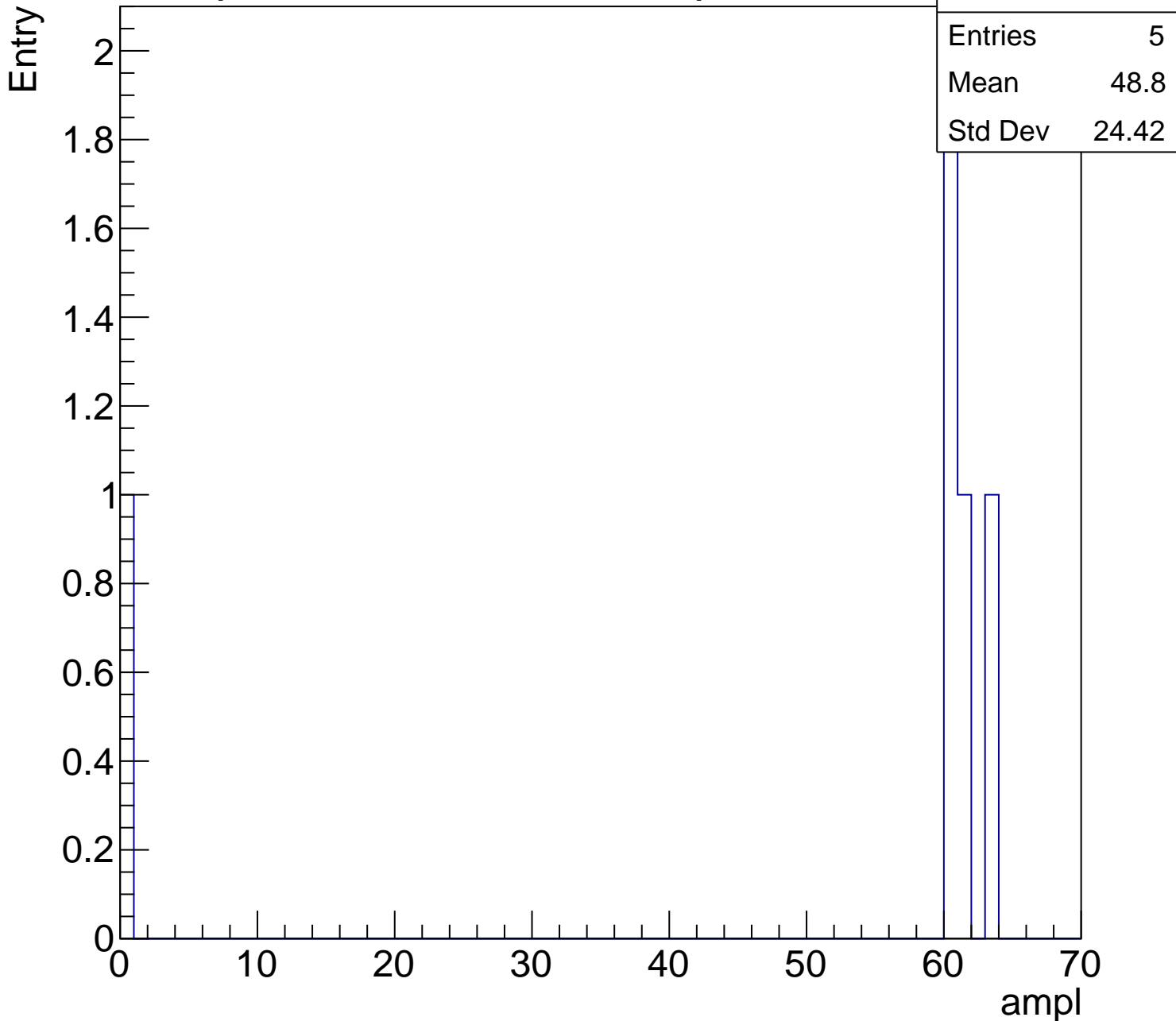
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	48.8
Std Dev	24.42

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U23-ch99, adc0

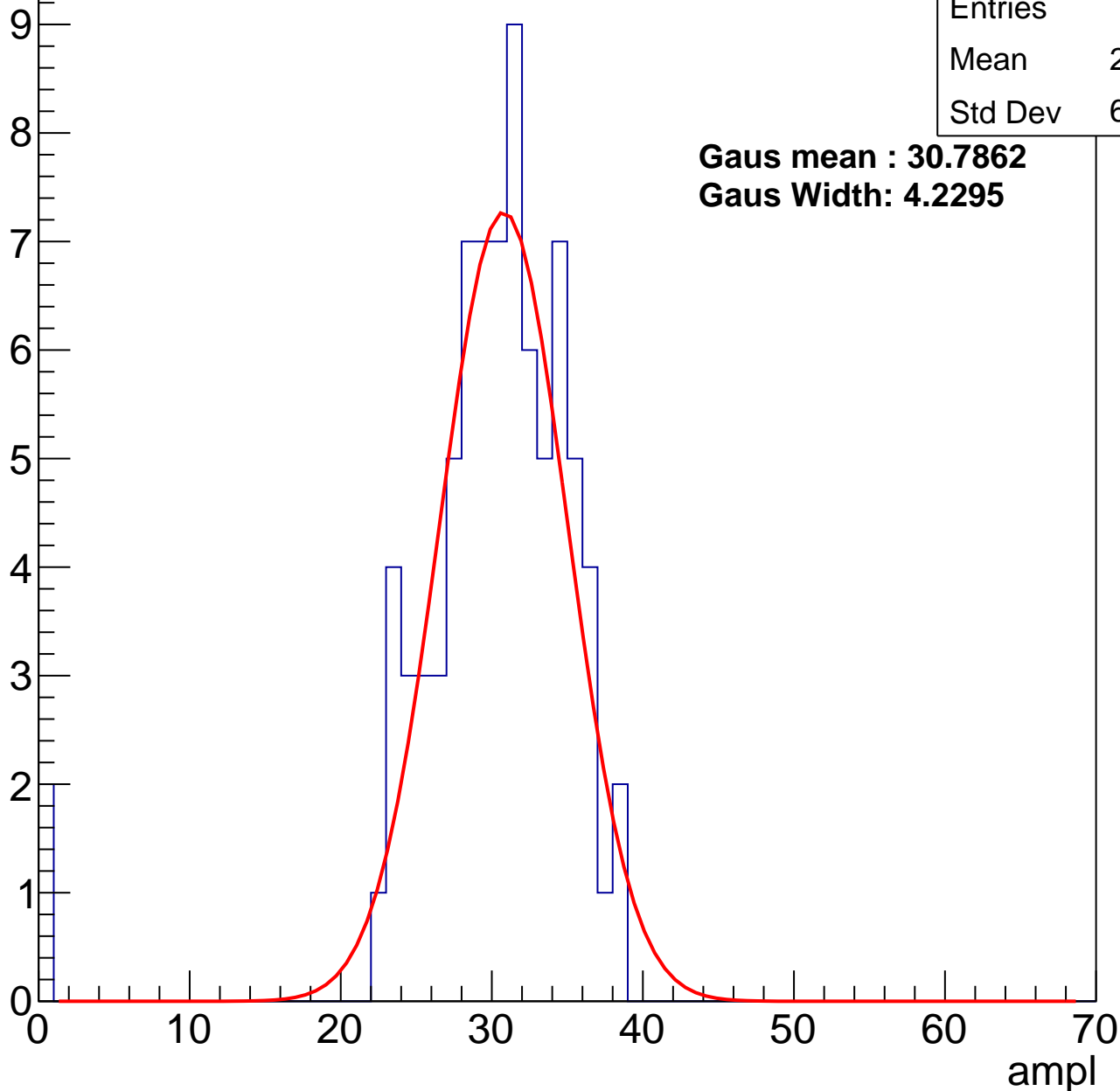
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	29.49
Std Dev	6.072

Gaus mean : 30.7862

Gaus Width: 4.2295



B0L001S, U23-ch99, adc1

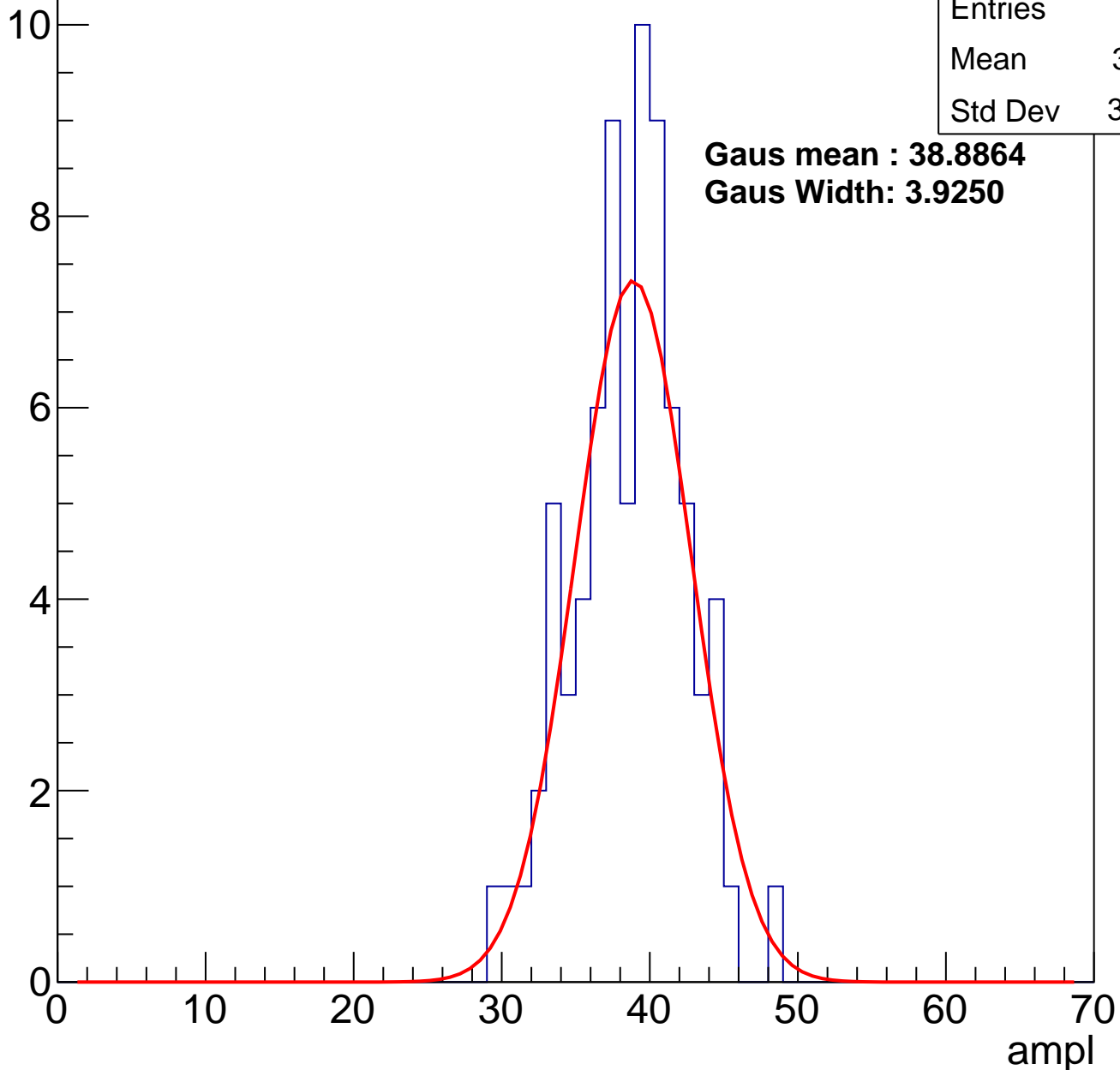
calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	38.21
Std Dev	3.722

Gaus mean : 38.8864

Gaus Width: 3.9250

Entry



B0L001S, U23-ch99, adc2

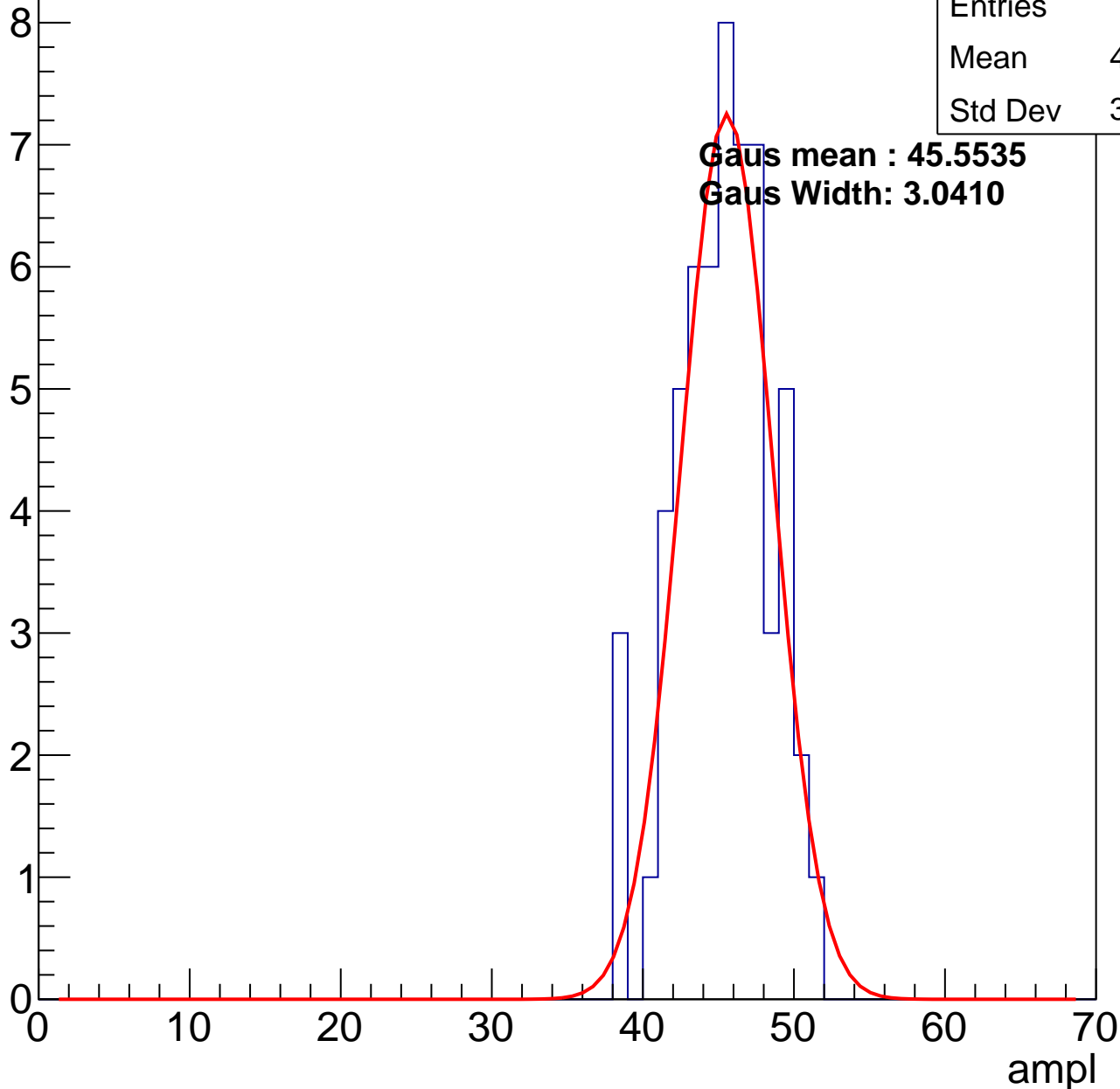
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.84
Std Dev	3.045

Gaus mean : 45.5535

Gaus Width: 3.0410

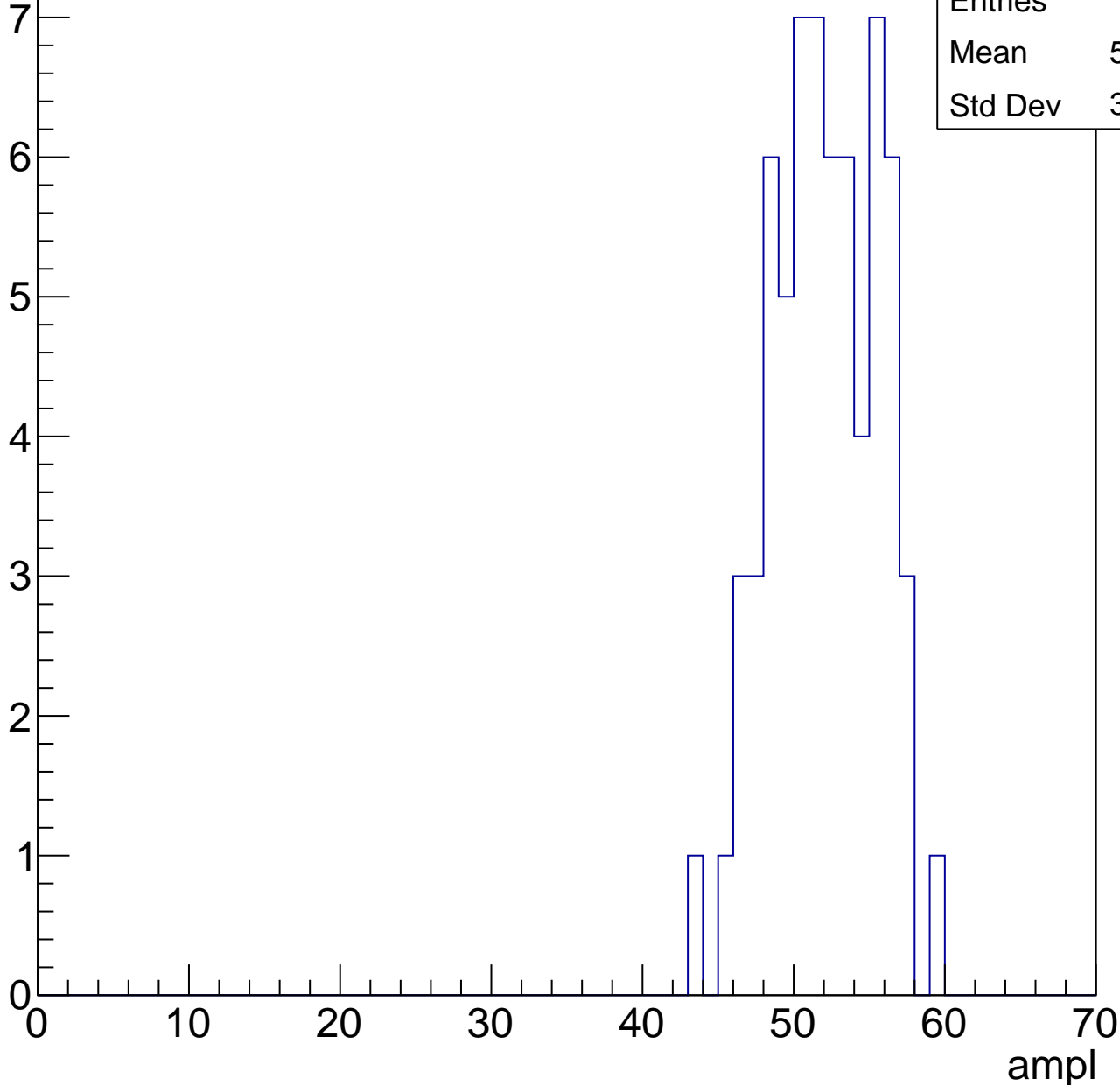


B0L001S, U23-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.58
Std Dev	3.442

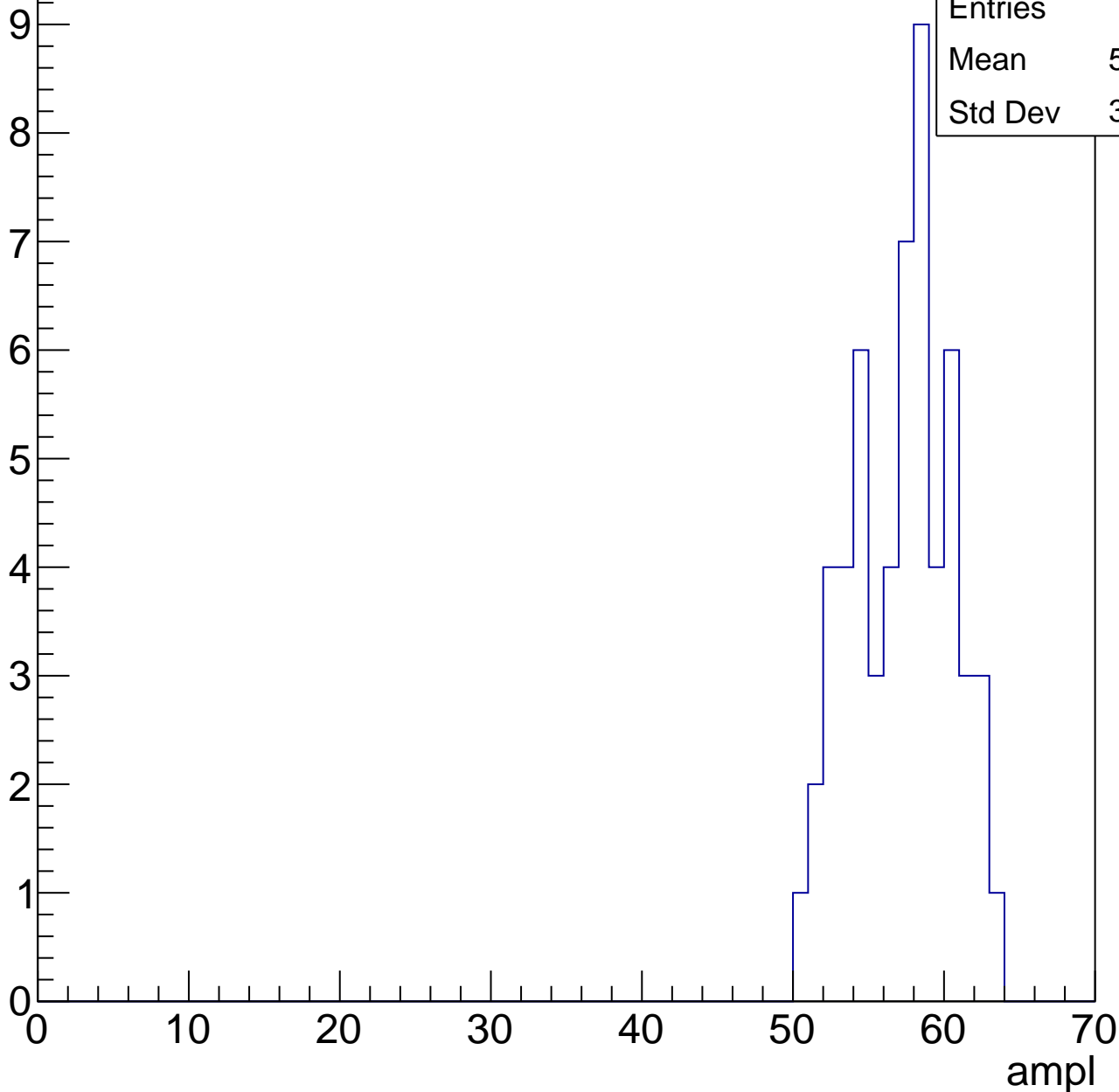


B0L001S, U23-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.74
Std Dev	3.215

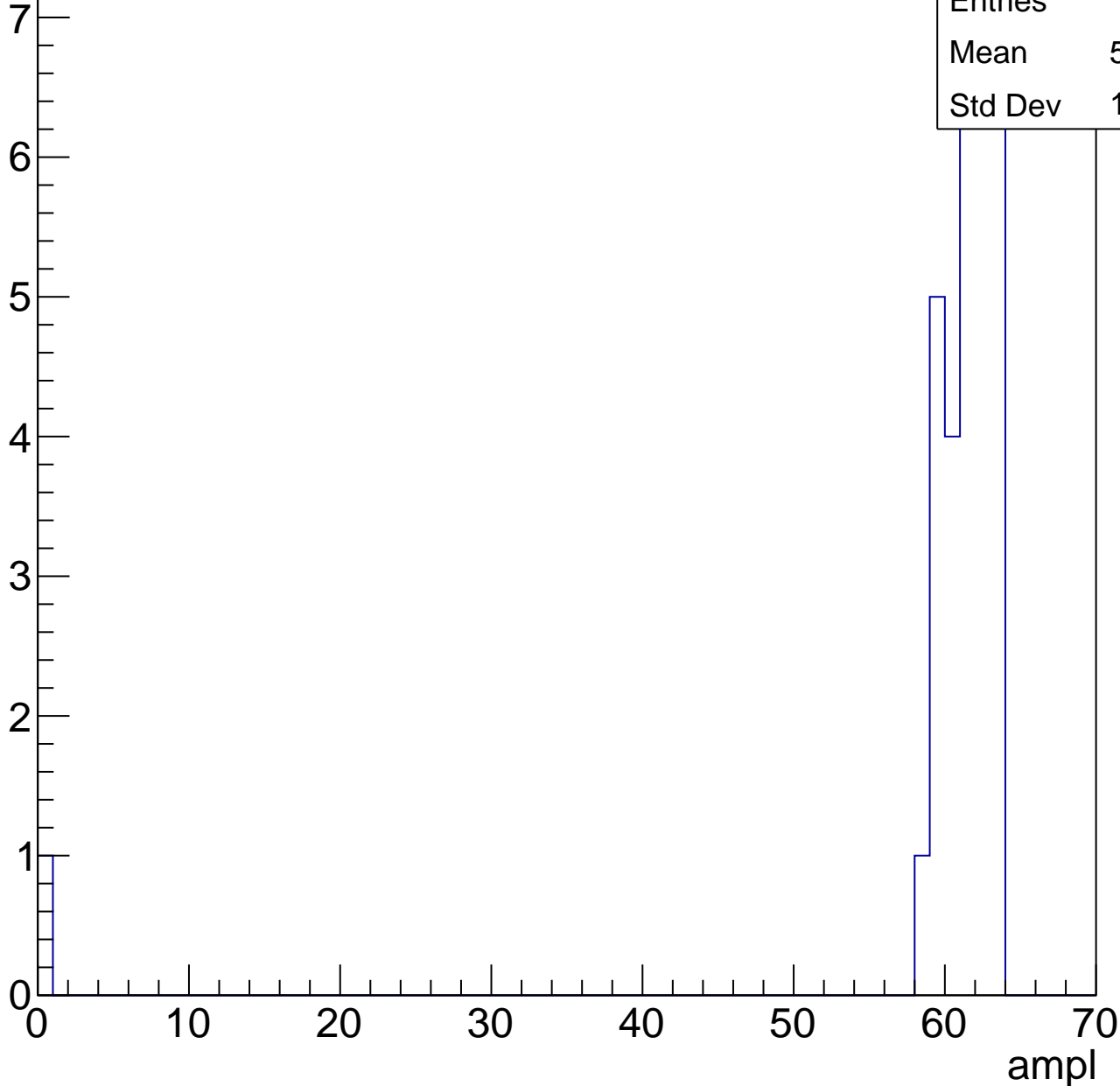


B0L001S, U23-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59.22
Std Dev	10.73



B0L001S, U23-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch100, adc0

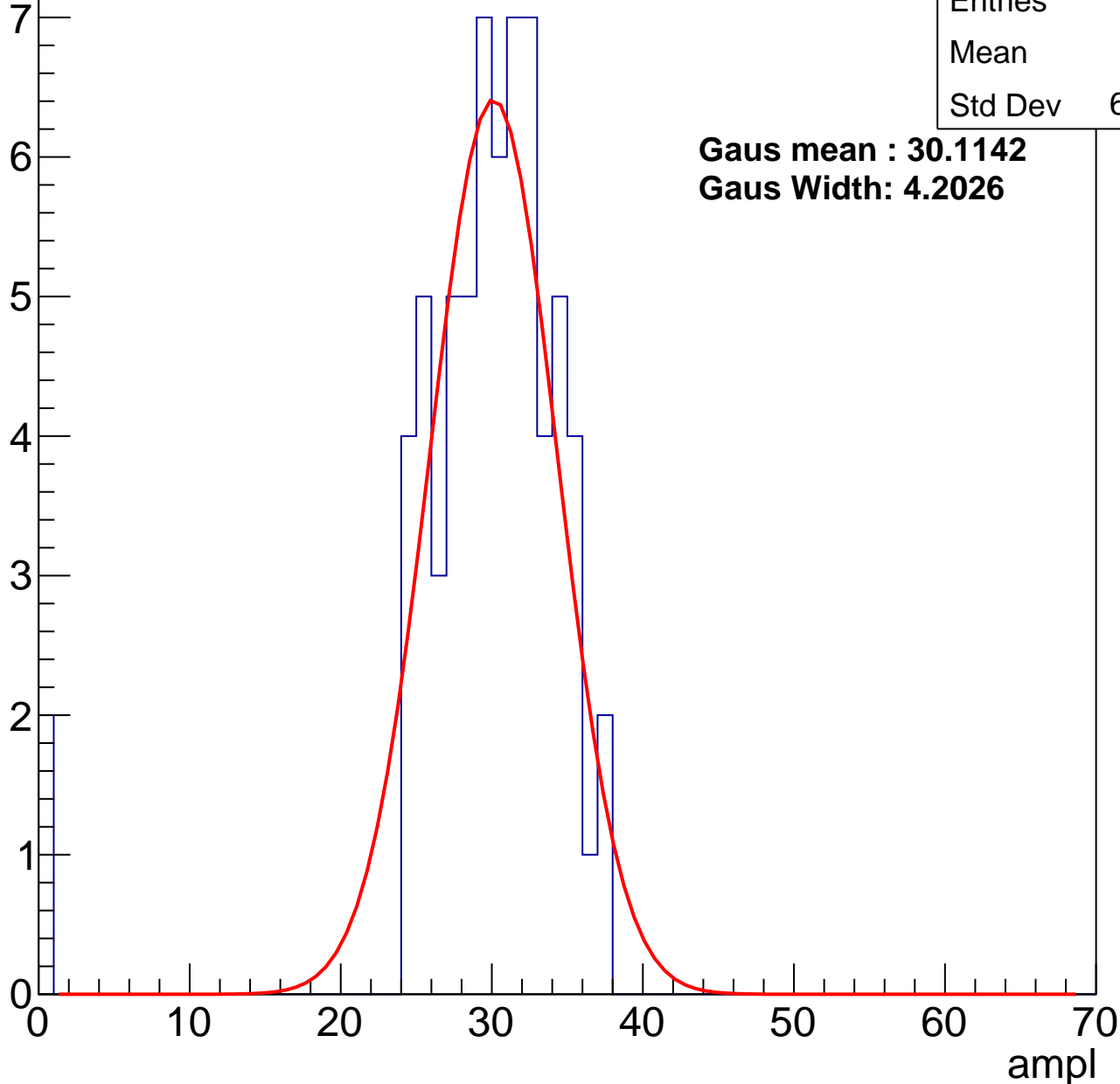
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.1
Std Dev	6.133

Gaus mean : 30.1142

Gaus Width: 4.2026



B0L001S, U23-ch100, adc1

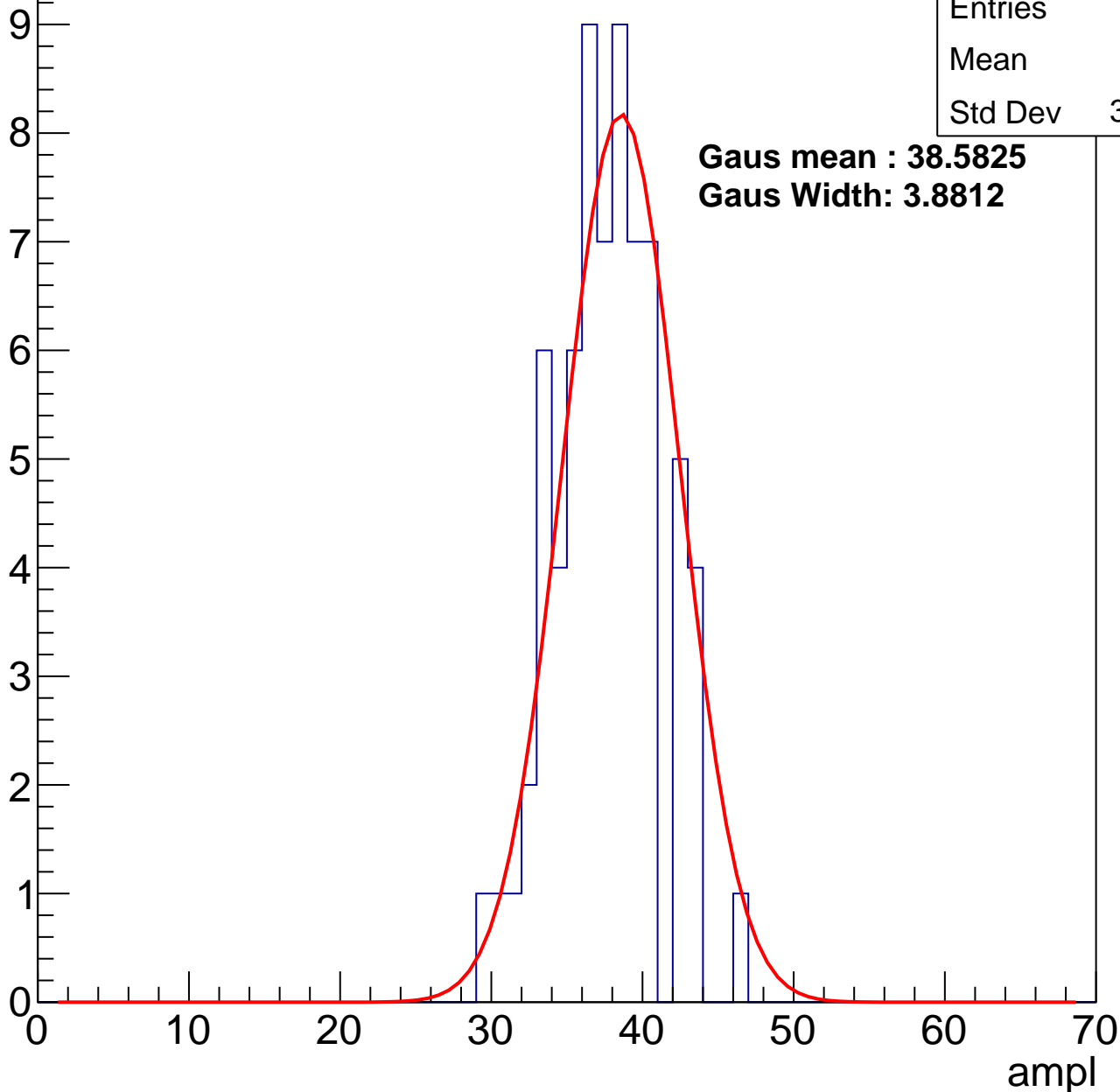
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.2
Std Dev	3.404

Gaus mean : 38.5825

Gaus Width: 3.8812



B0L001S, U23-ch100, adc2

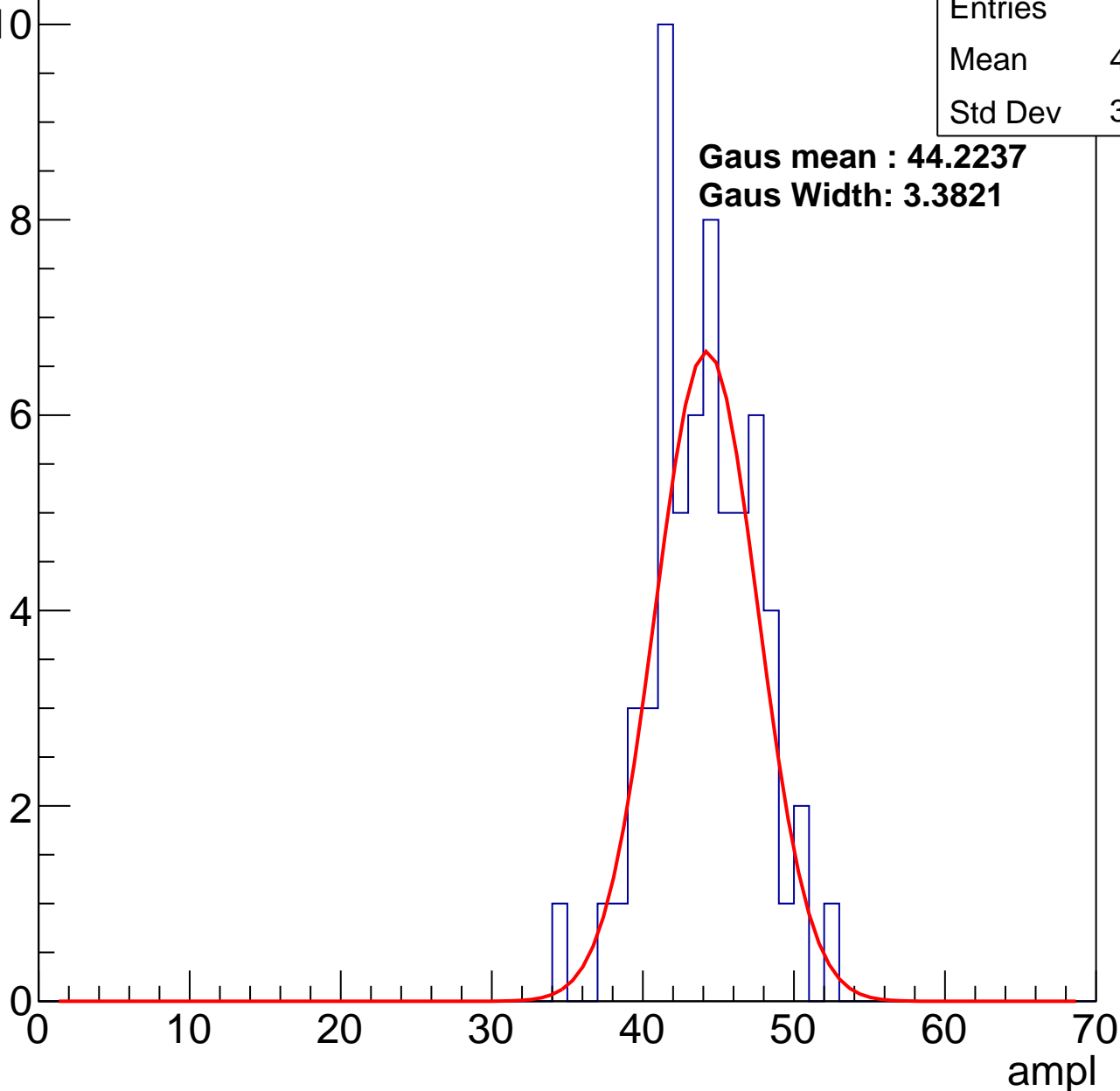
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.65
Std Dev	3.408

Gaus mean : 44.2237

Gaus Width: 3.3821

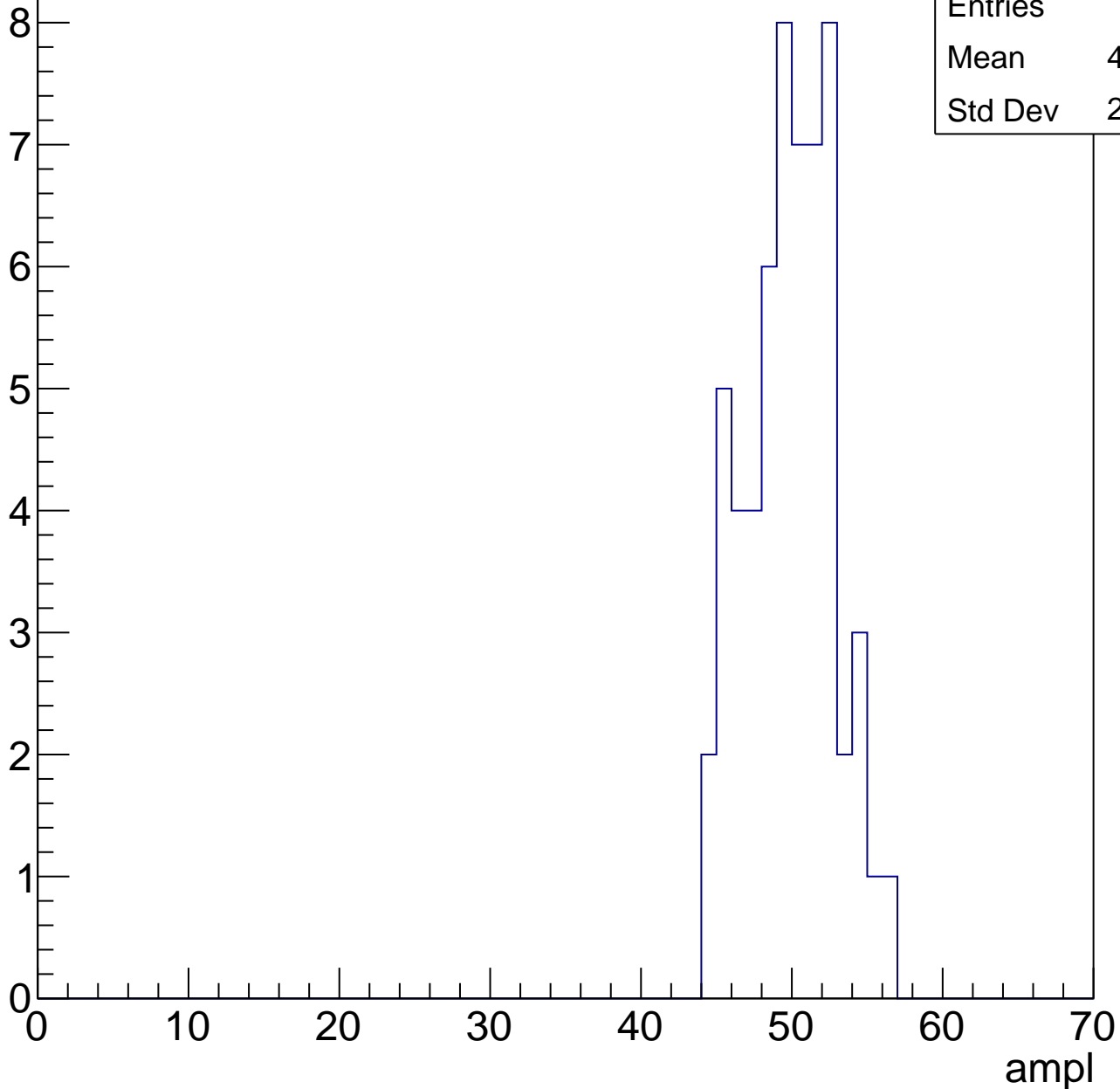


B0L001S, U23-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	49.43
Std Dev	2.865

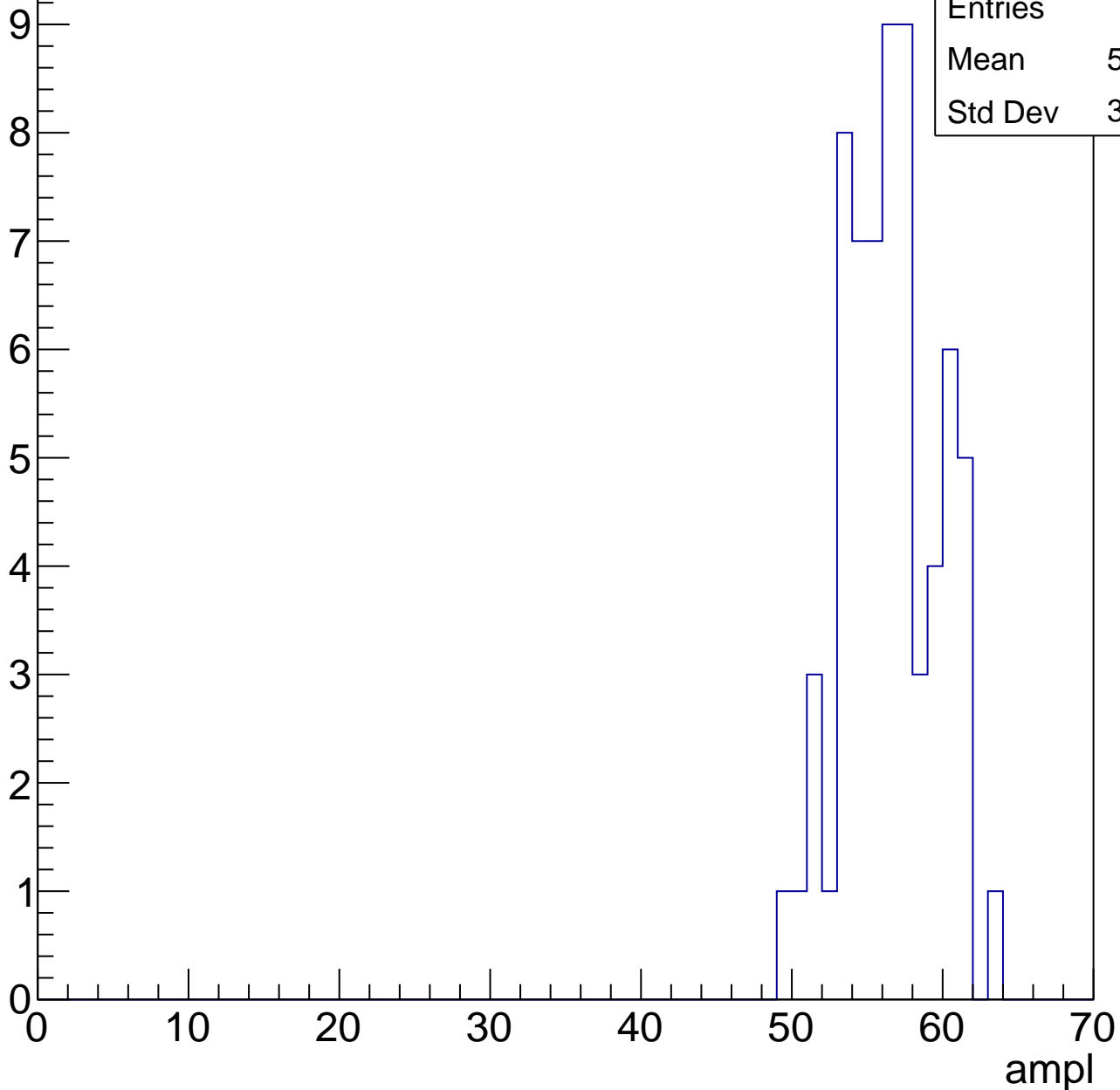


B0L001S, U23-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.09
Std Dev	3.062

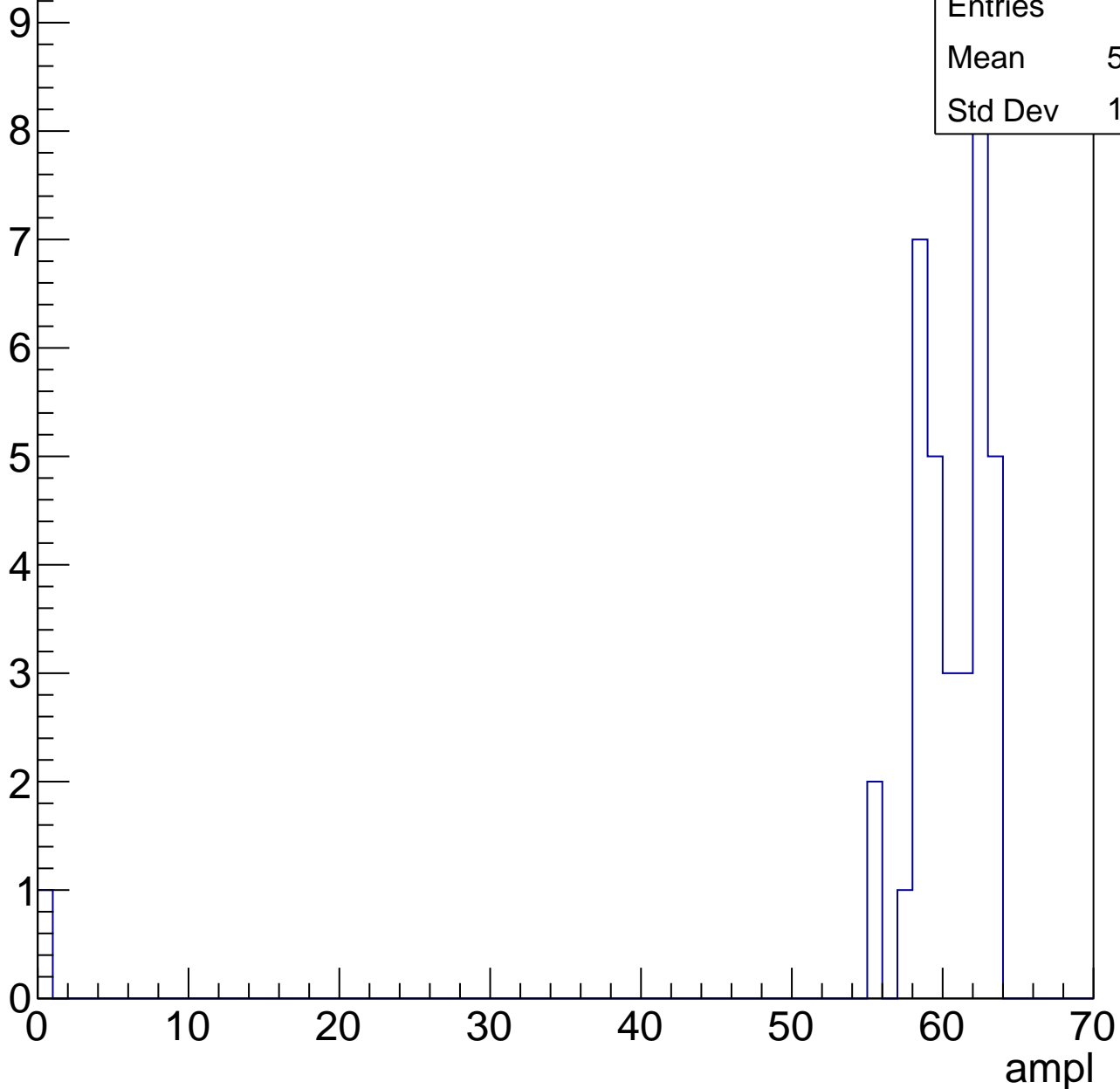


B0L001S, U23-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.44
Std Dev	10.12



B0L001S, U23-ch100, adc6

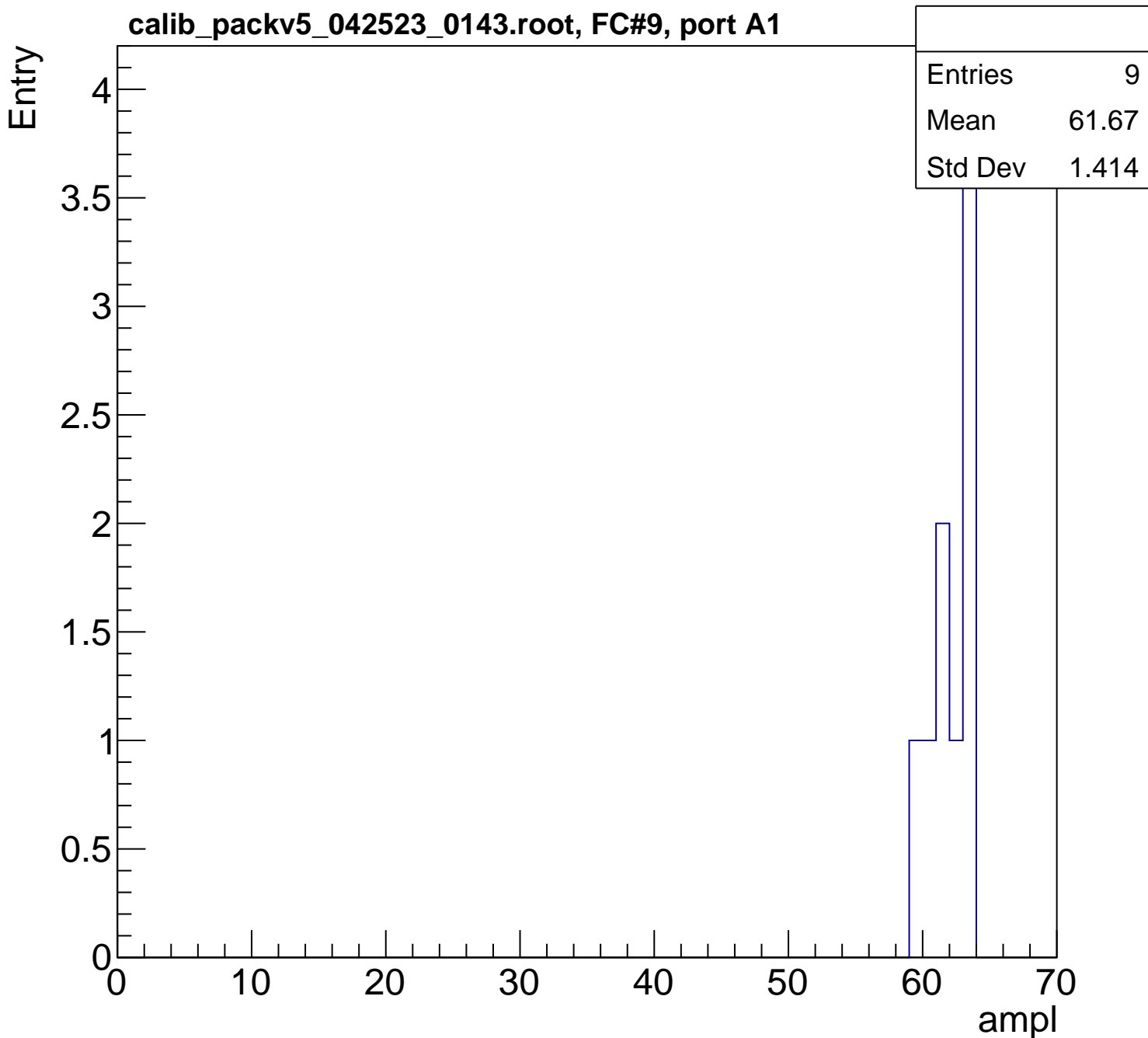
calib_packv5_042523_0143.root, FC#9, port A1

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.67
Std Dev	1.414

ampl



B0L001S, U23-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U23-ch101, adc0

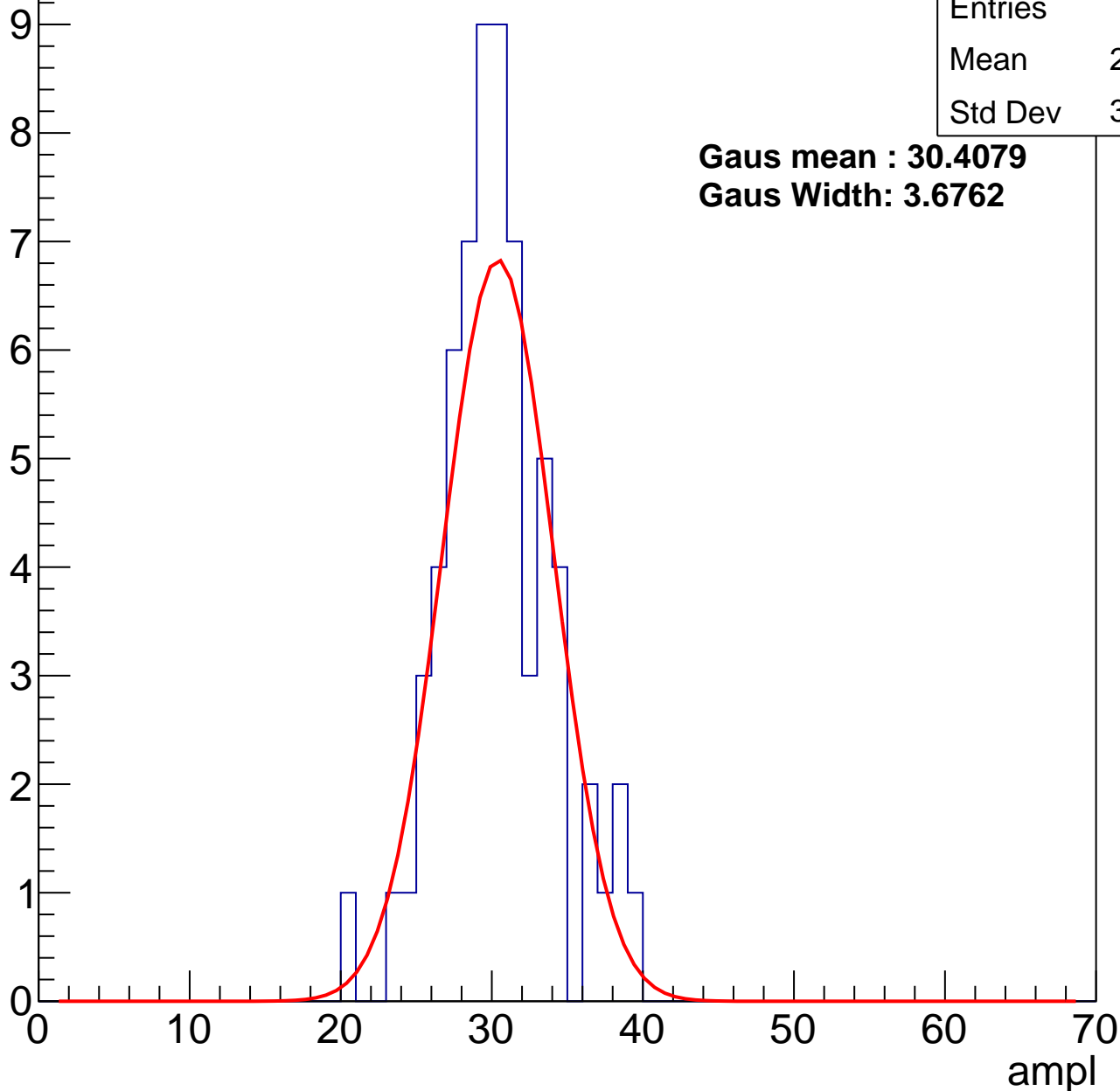
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.89
Std Dev	3.635

Gaus mean : 30.4079

Gaus Width: 3.6762



B0L001S, U23-ch101, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	36.07
Std Dev	3.539

Gaus mean : 35.3960

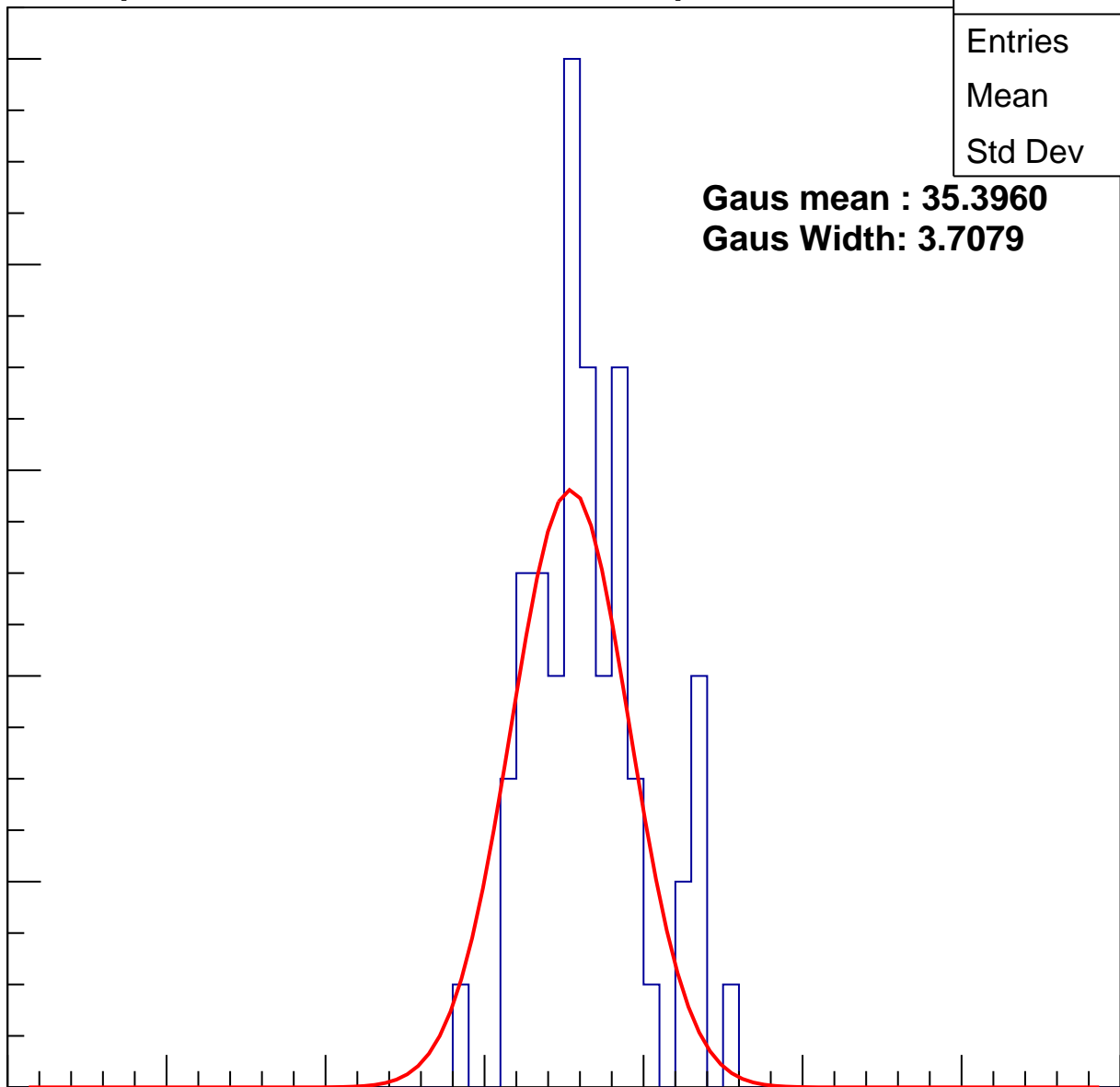
Gaus Width: 3.7079

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch101, adc2

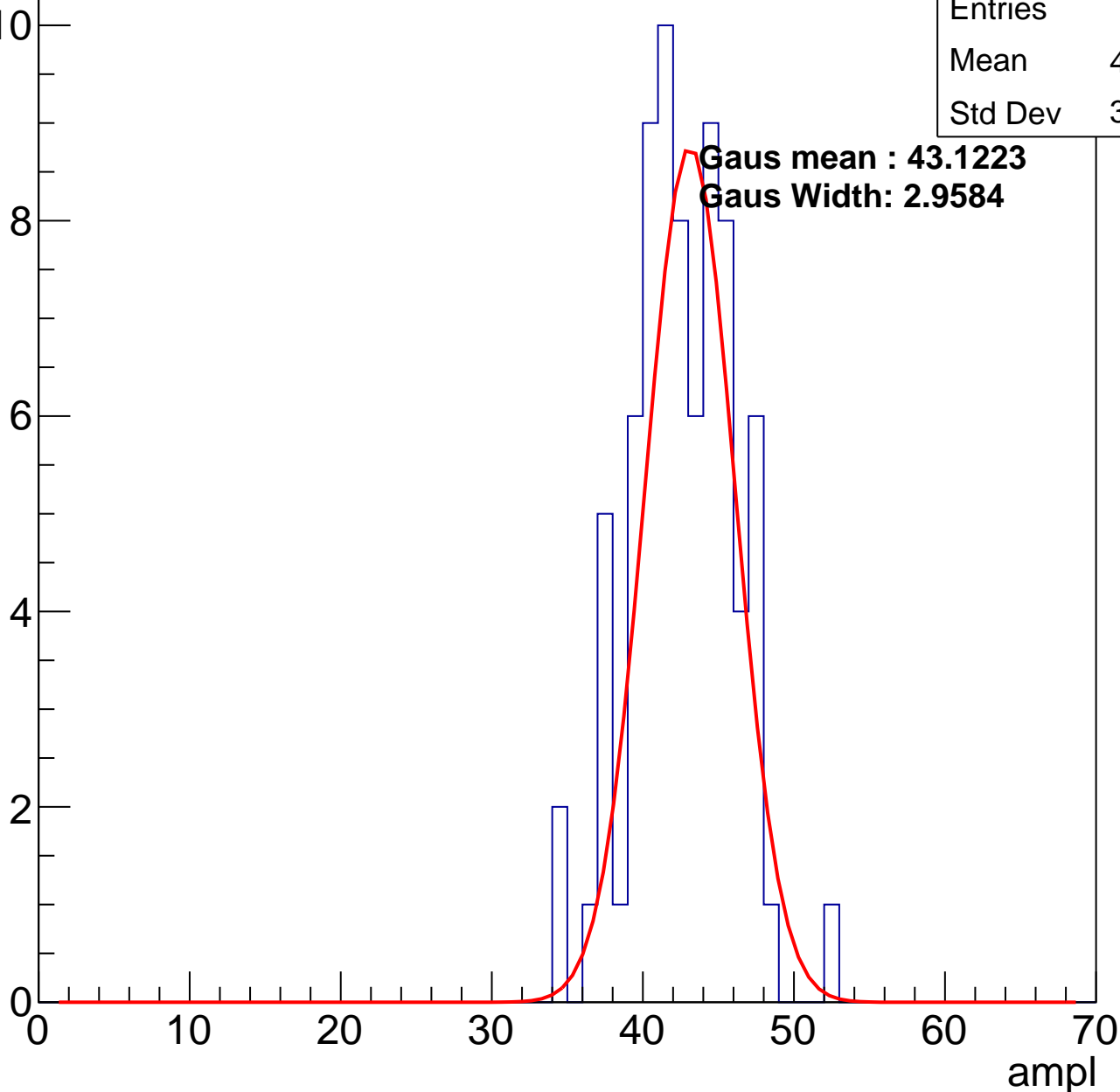
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	42.17
Std Dev	3.355

Gaus mean : 43.1223

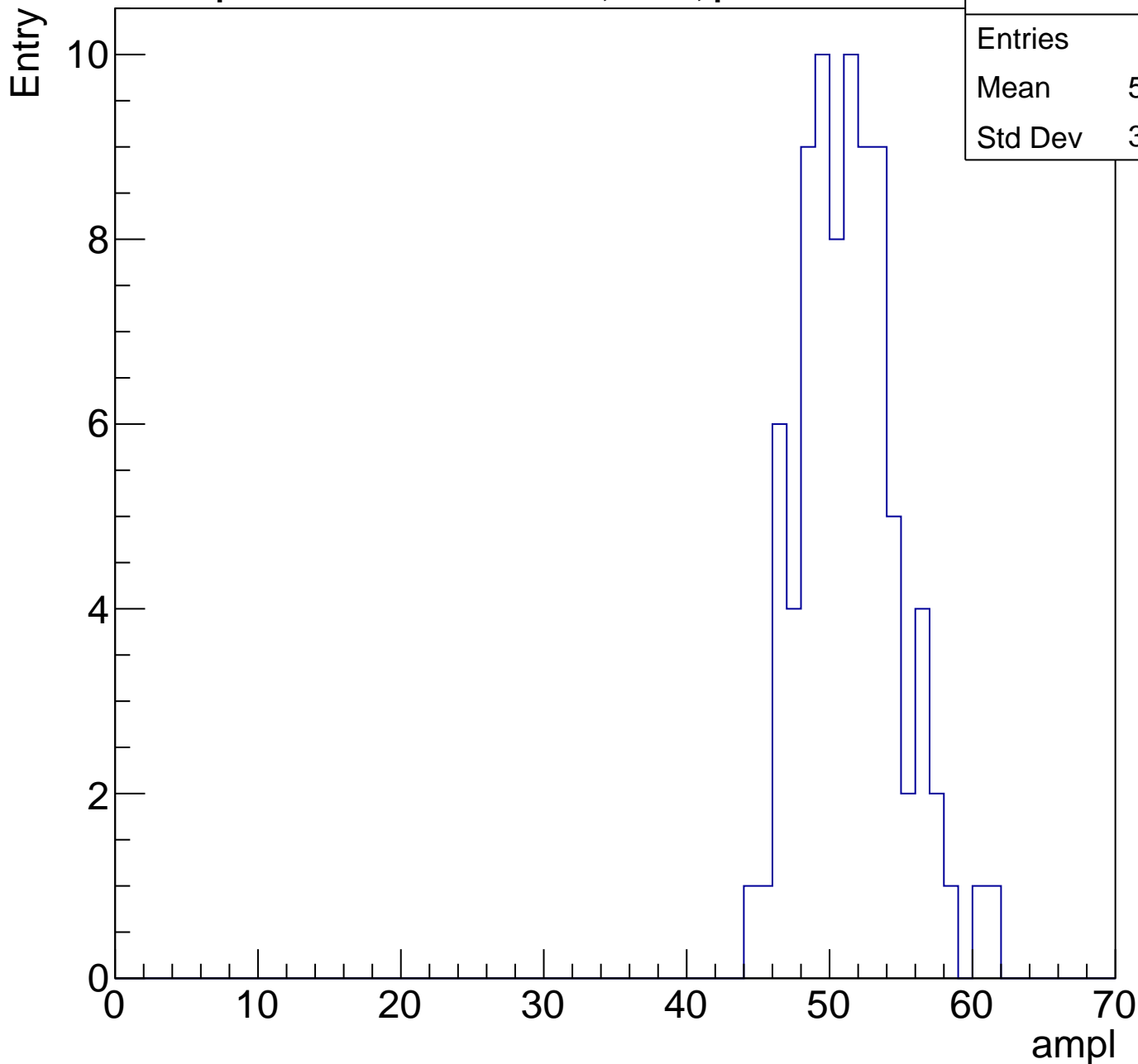
Gaus Width: 2.9584



B0L001S, U23-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	50.93
Std Dev	3.386

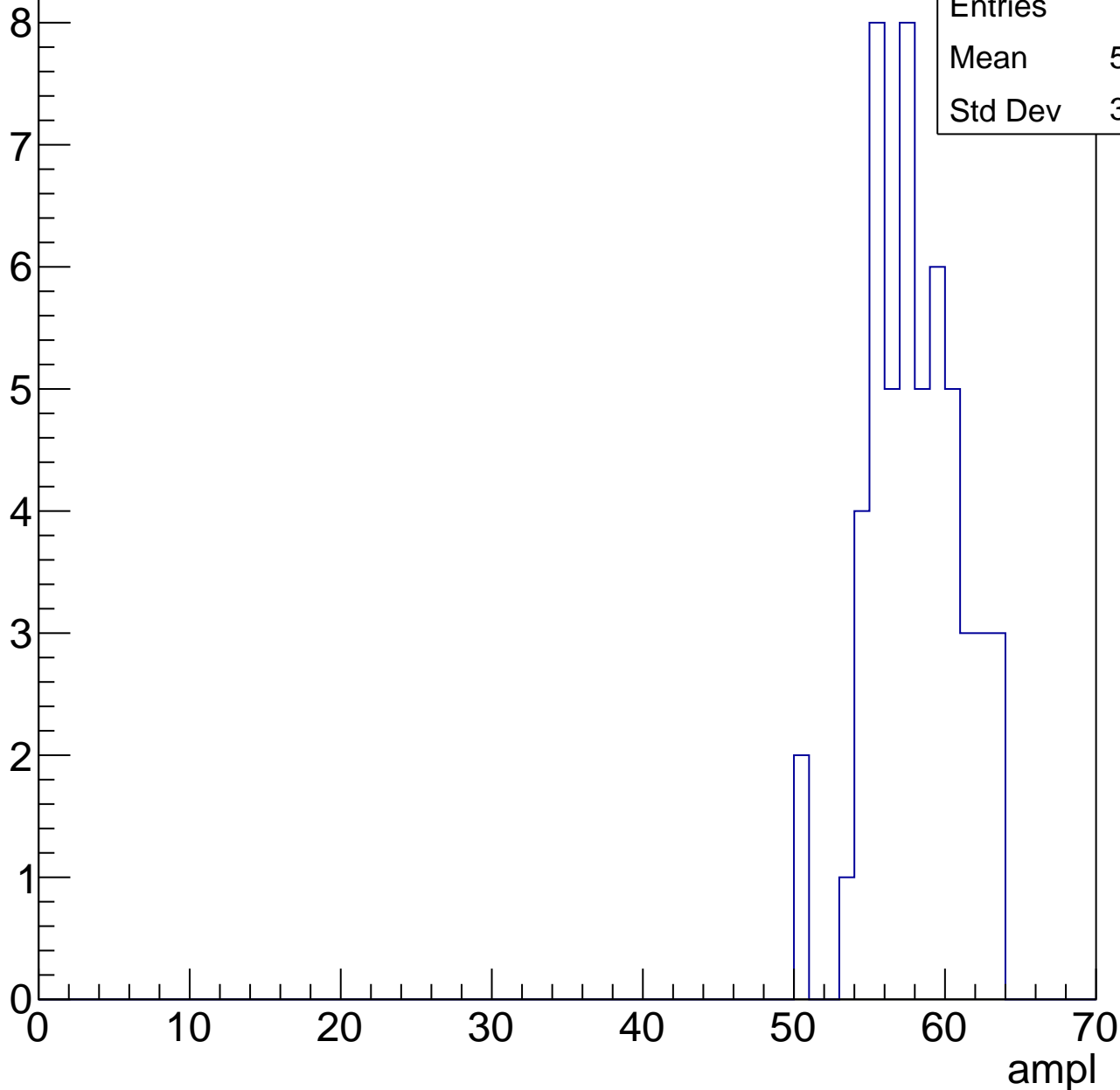


B0L001S, U23-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.49
Std Dev	3.007

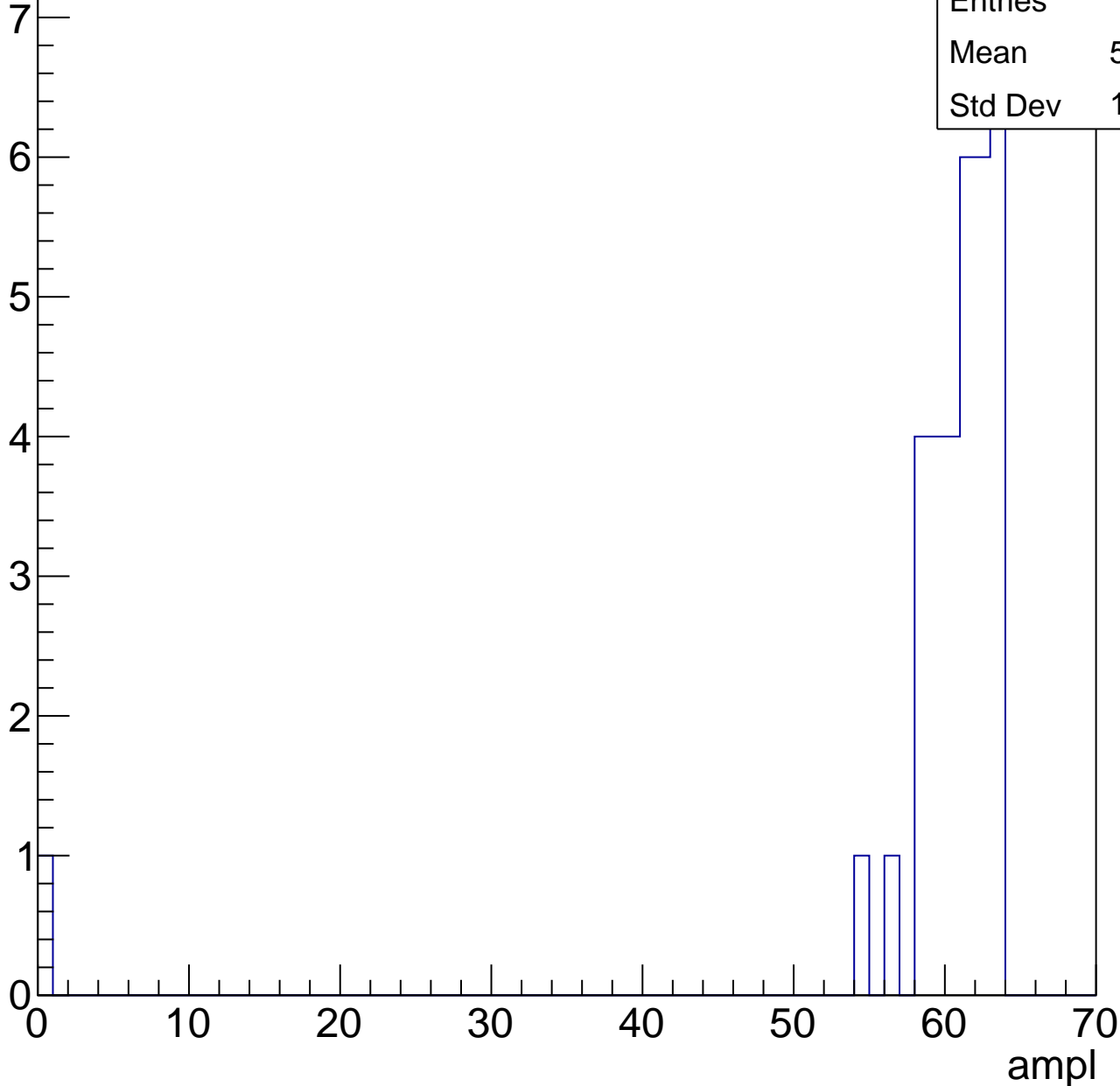


B0L001S, U23-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.74
Std Dev	10.45



B0L001S, U23-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch102, adc0

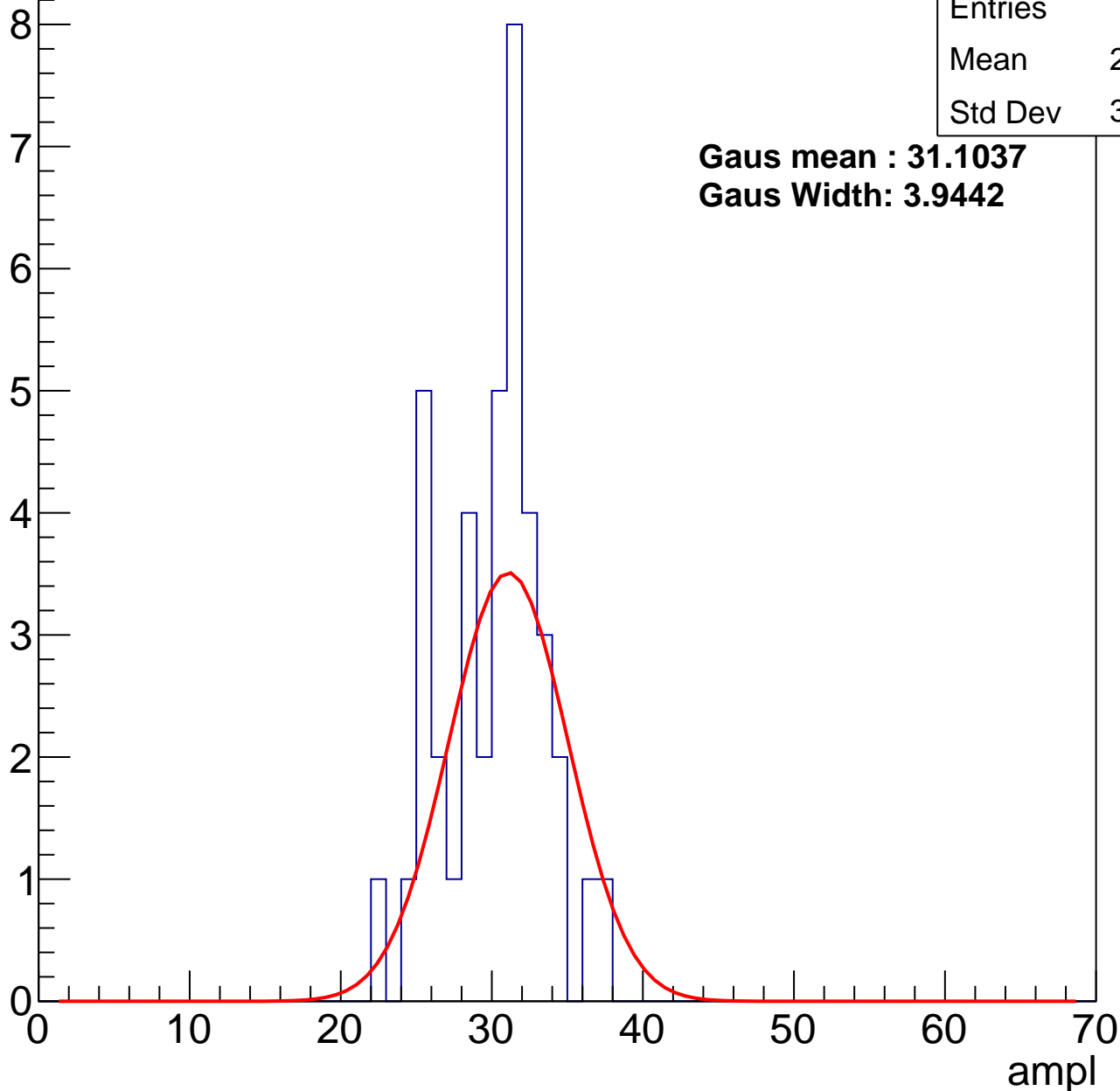
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	29.65
Std Dev	3.343

Gaus mean : 31.1037

Gaus Width: 3.9442



B0L001S, U23-ch102, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	91
Mean	36.23
Std Dev	4.146

Gaus mean : 36.9539

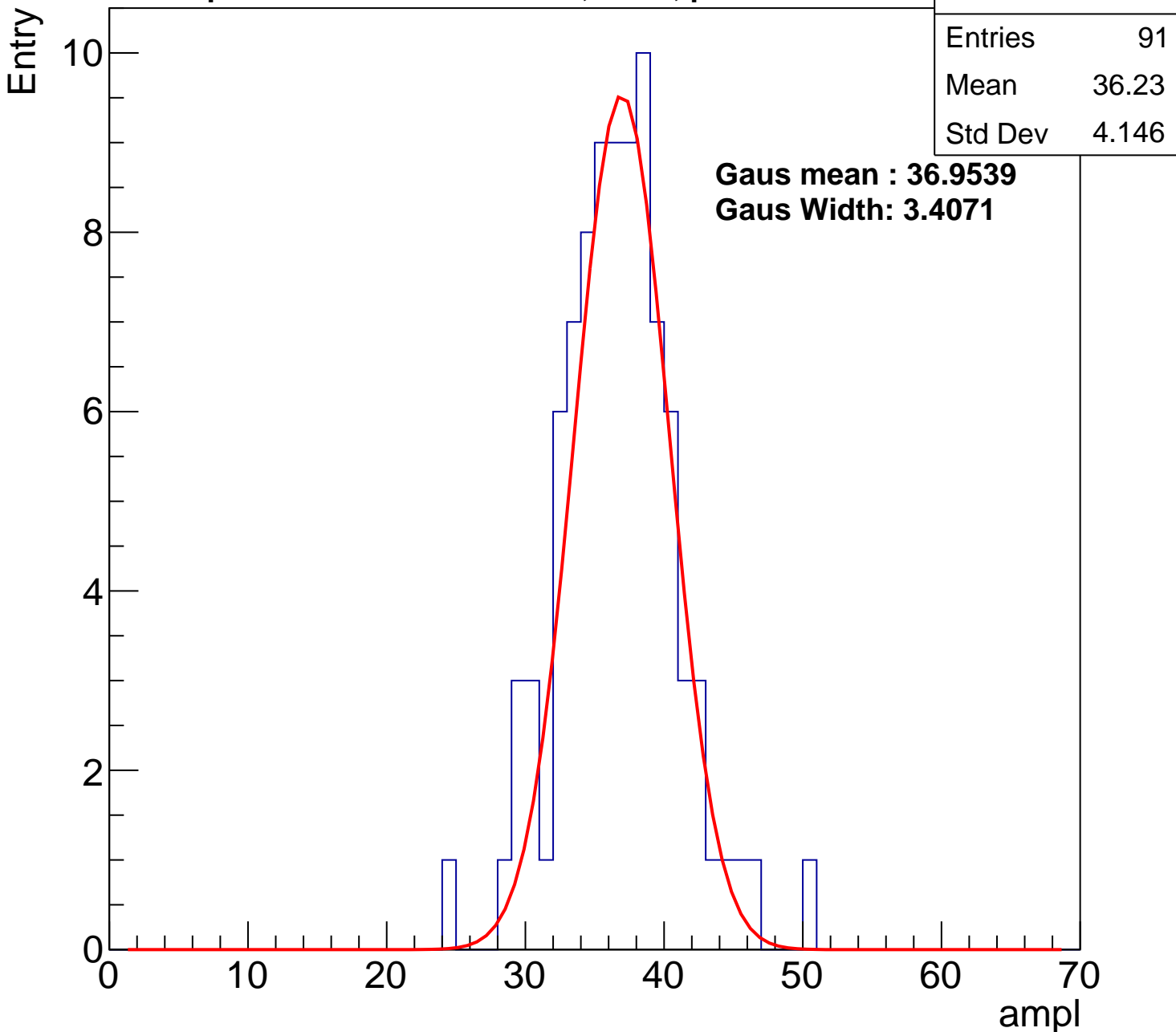
Gaus Width: 3.4071

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch102, adc2

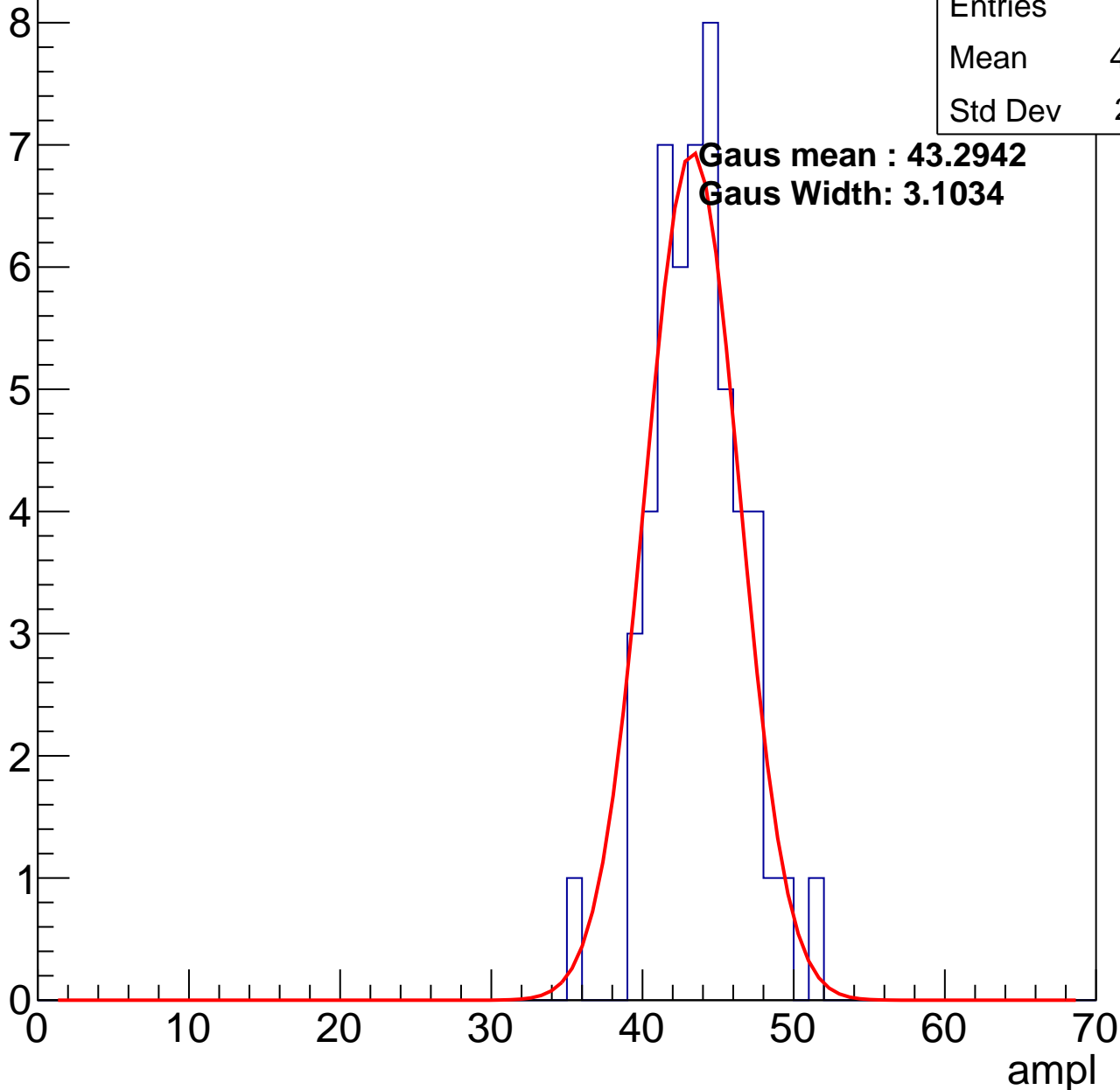
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.25
Std Dev	2.881

Gaus mean : 43.2942

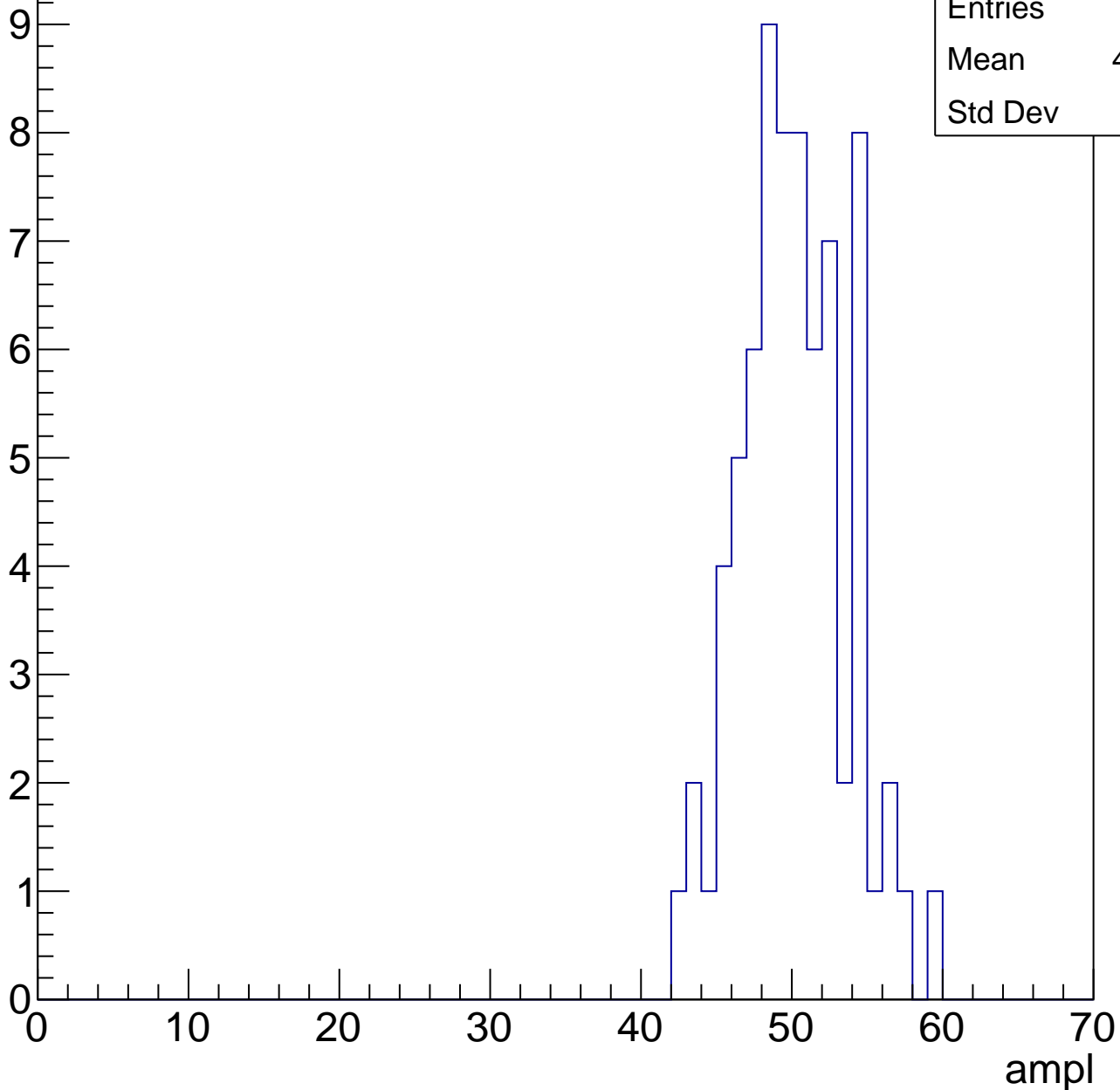
Gaus Width: 3.1034



B0L001S, U23-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

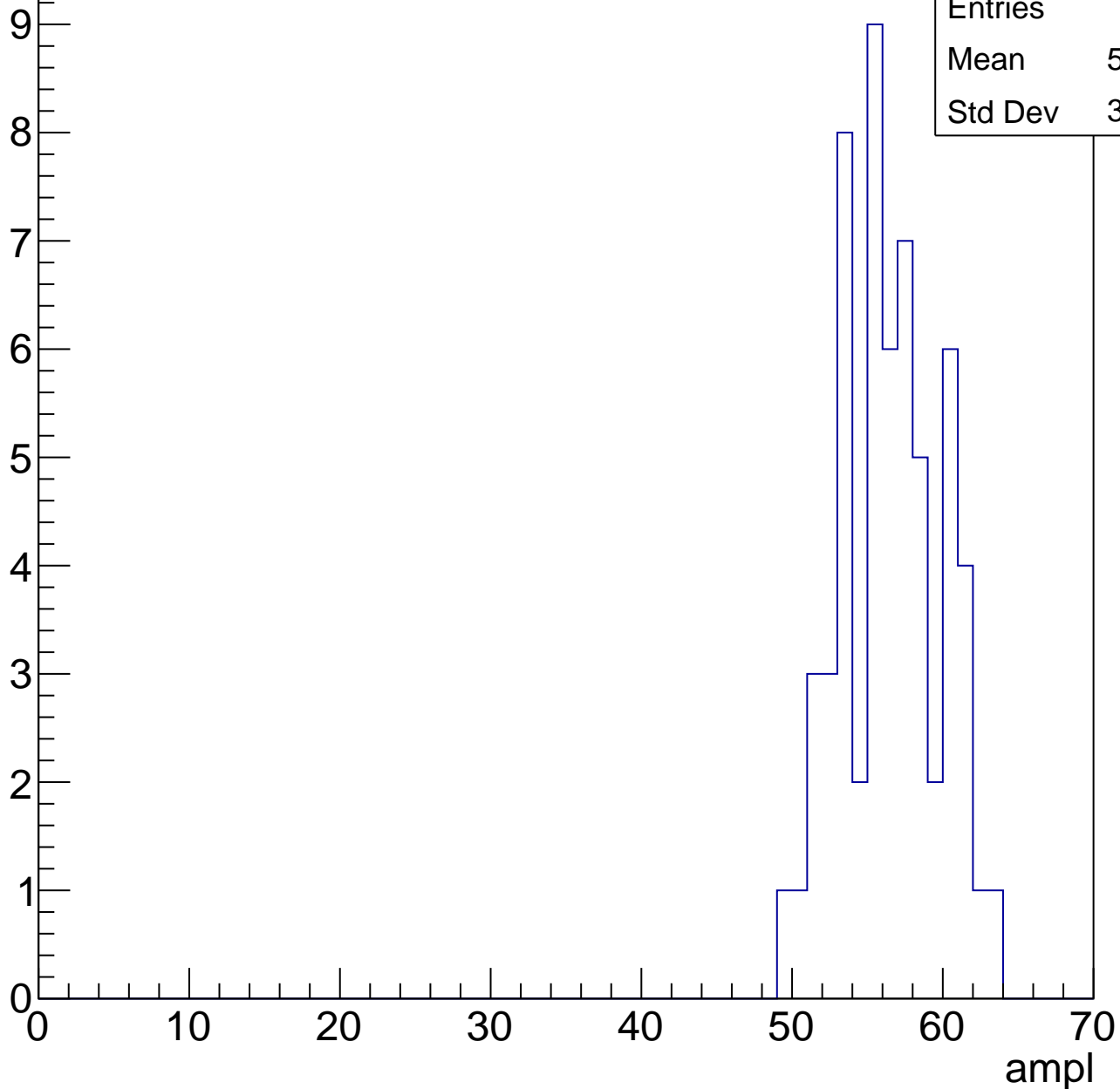


Entries	72
Mean	49.71
Std Dev	3.49

B0L001S, U23-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

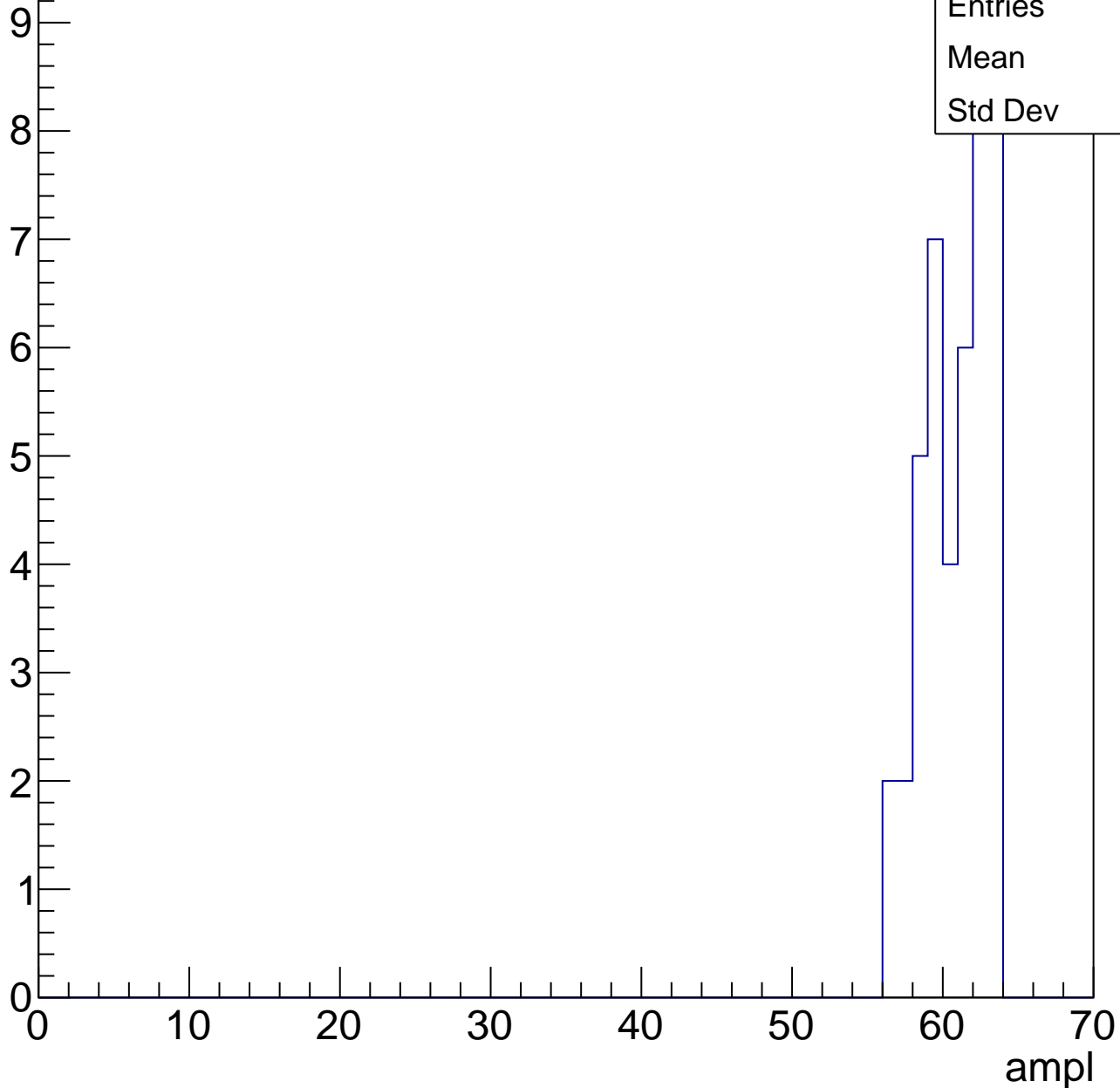


Entries	59
Mean	56.05
Std Dev	3.249

B0L001S, U23-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch103, adc0

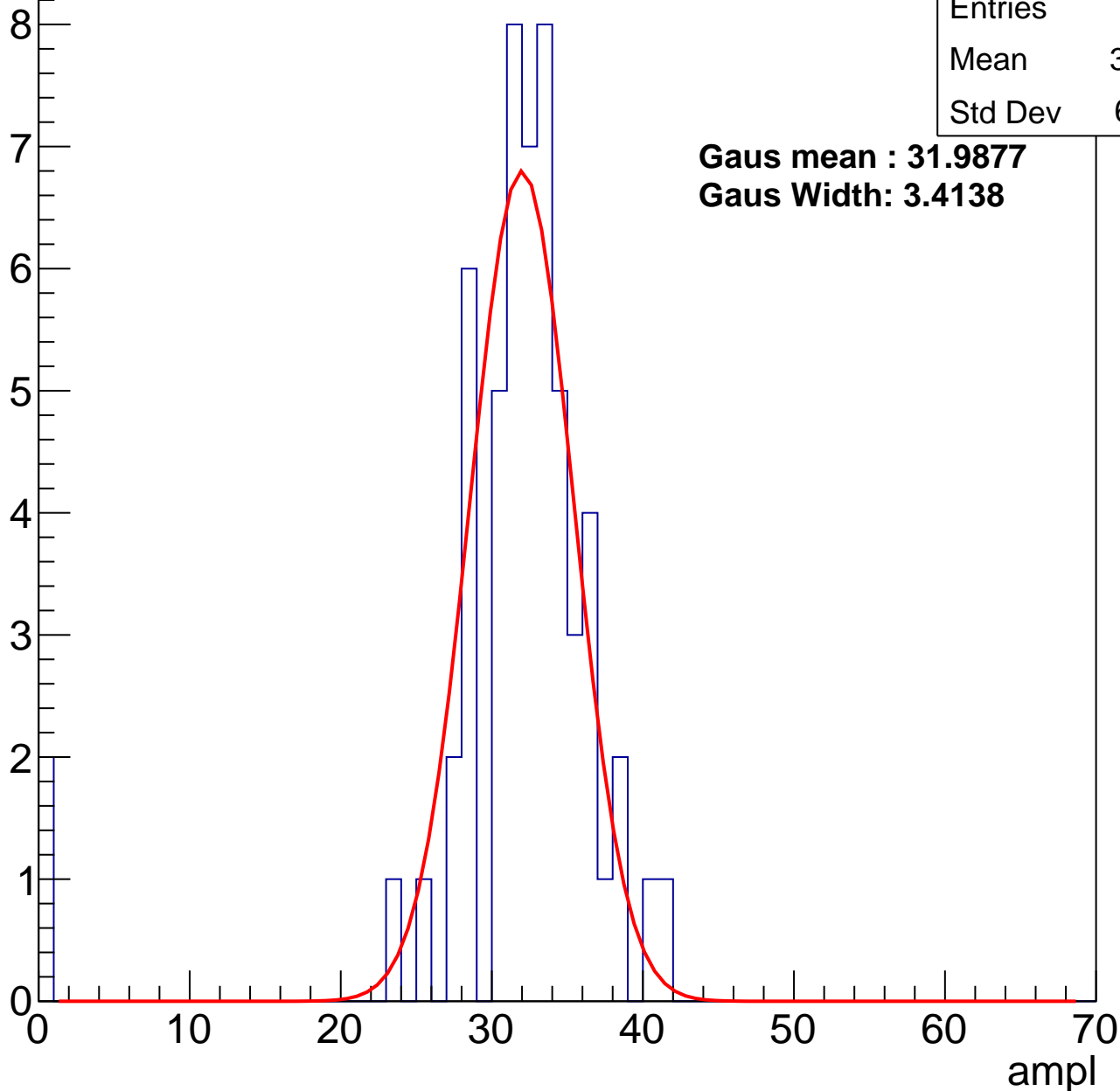
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	31.04
Std Dev	6.831

Gaus mean : 31.9877

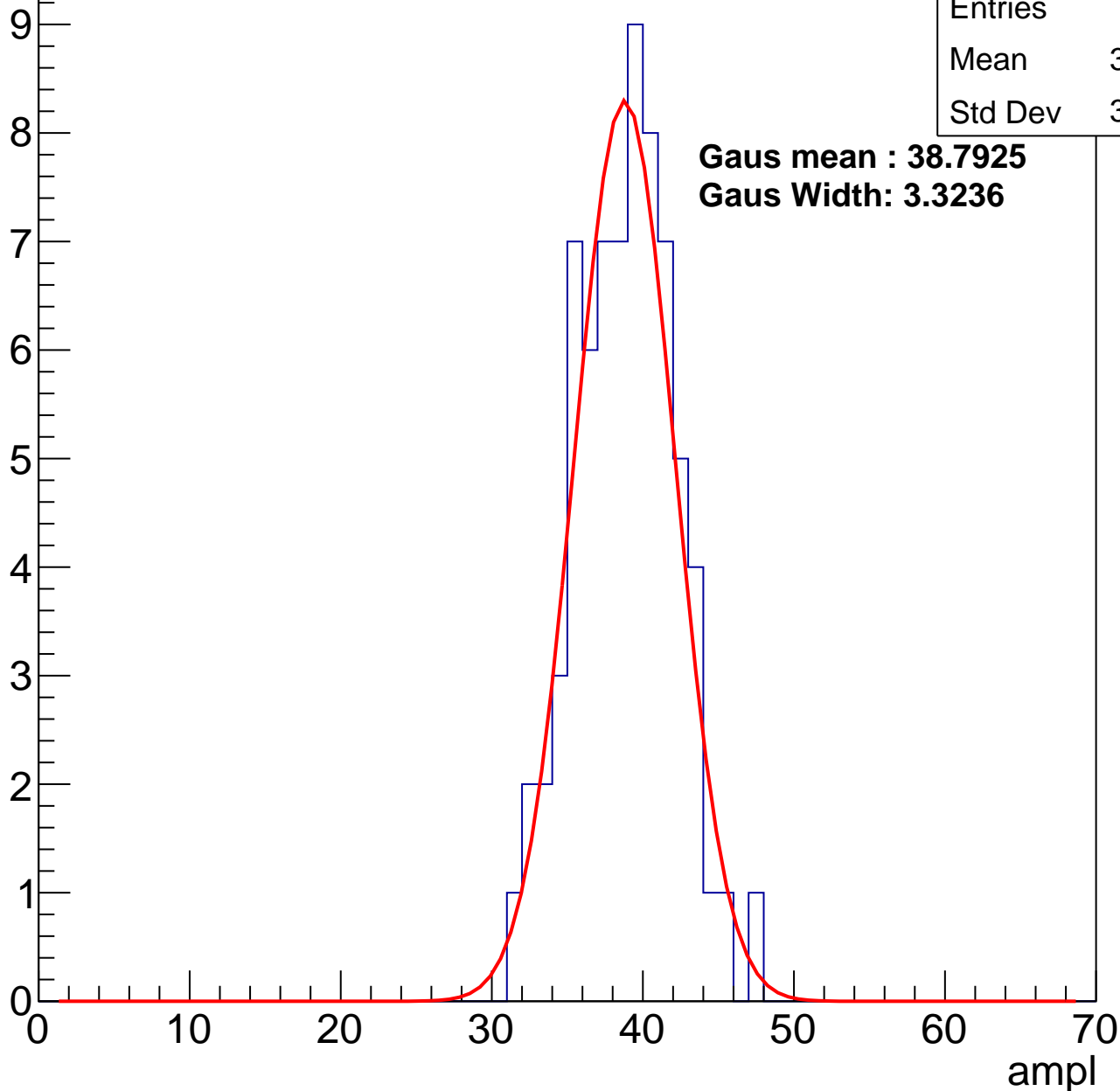
Gaus Width: 3.4138



B0L001S, U23-ch103, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch103, adc2

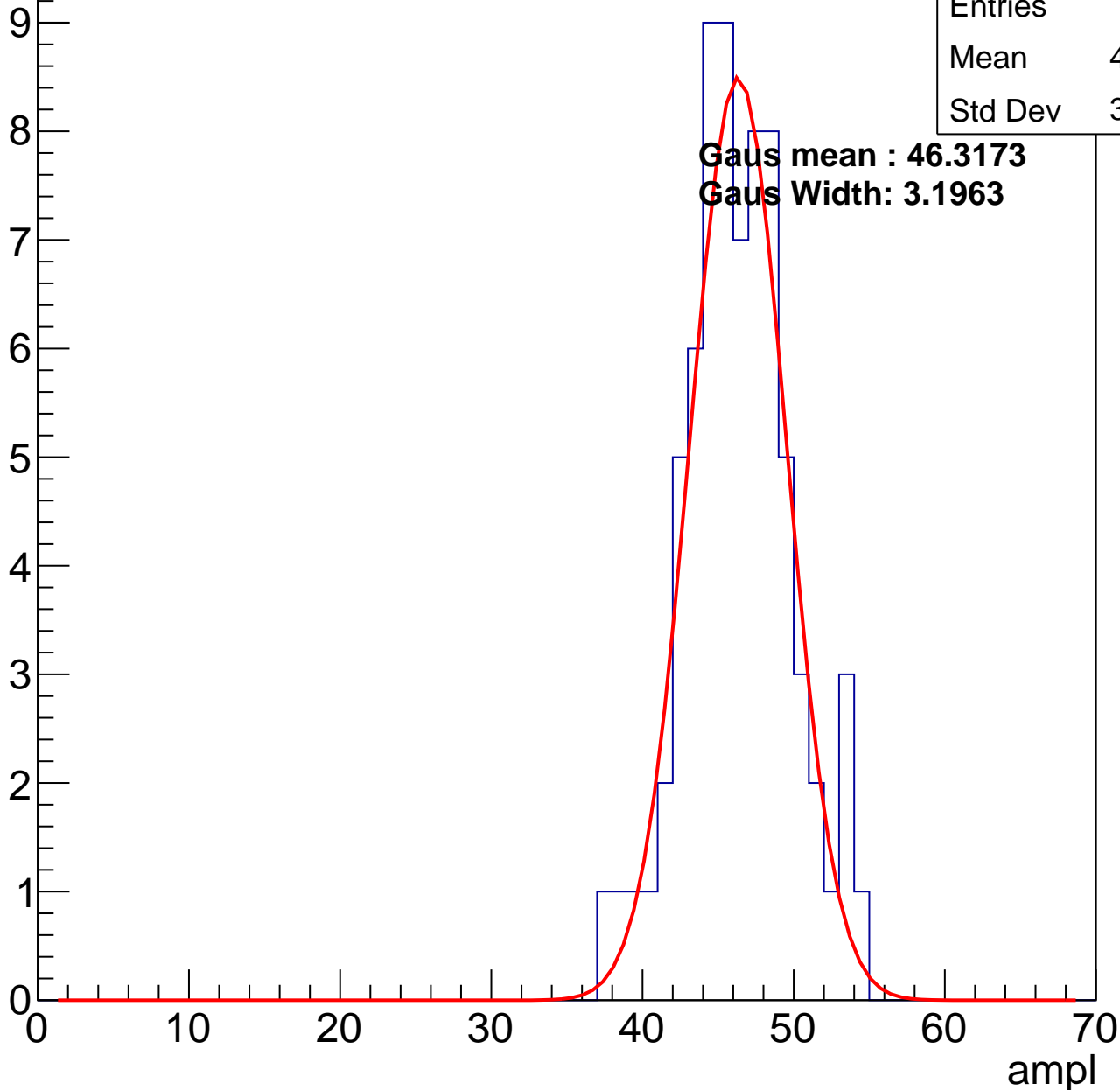
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.88
Std Dev	3.488

Gaus mean : 46.3173

Gaus Width: 3.1963

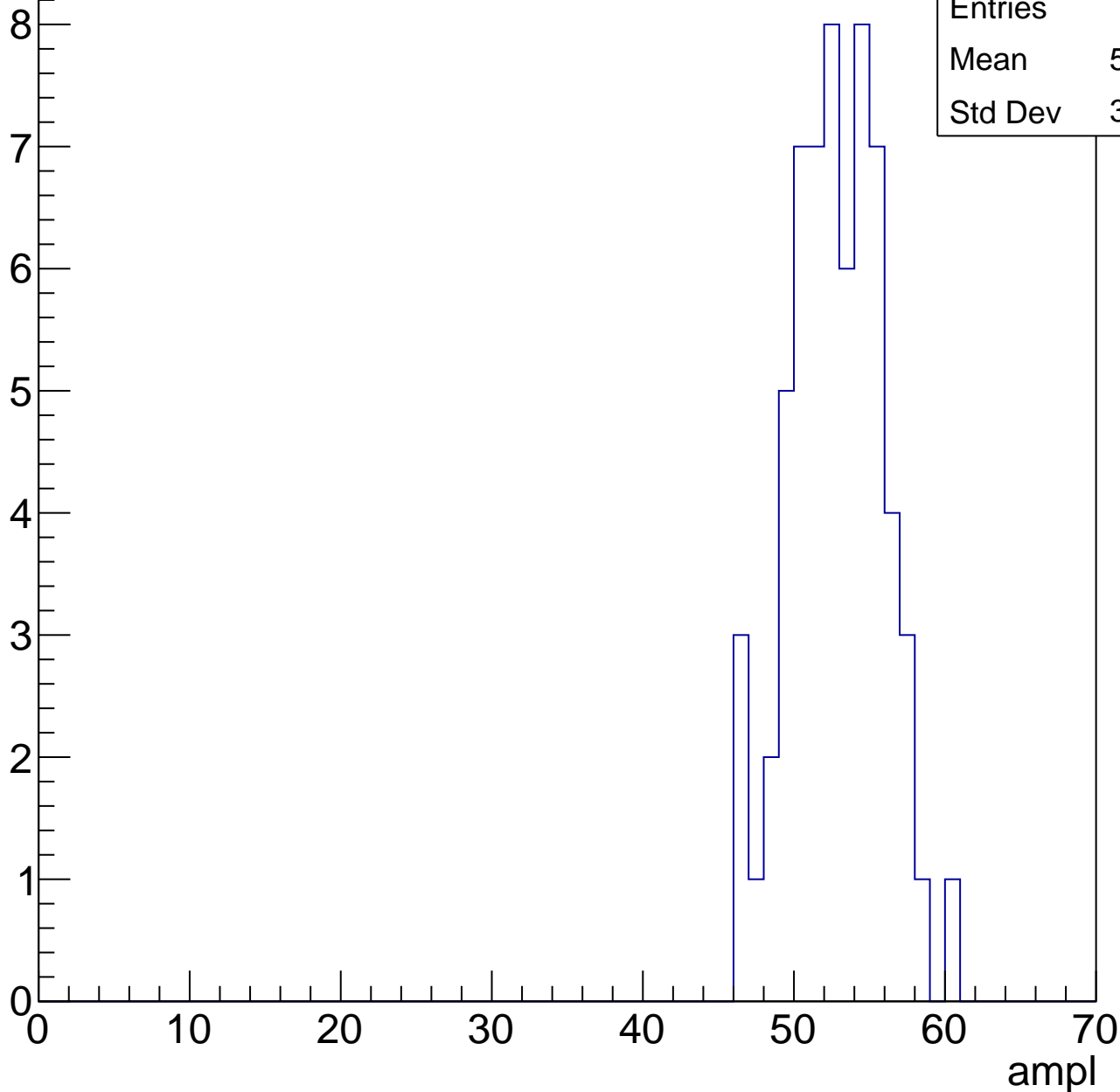


B0L001S, U23-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.33
Std Dev	3.029

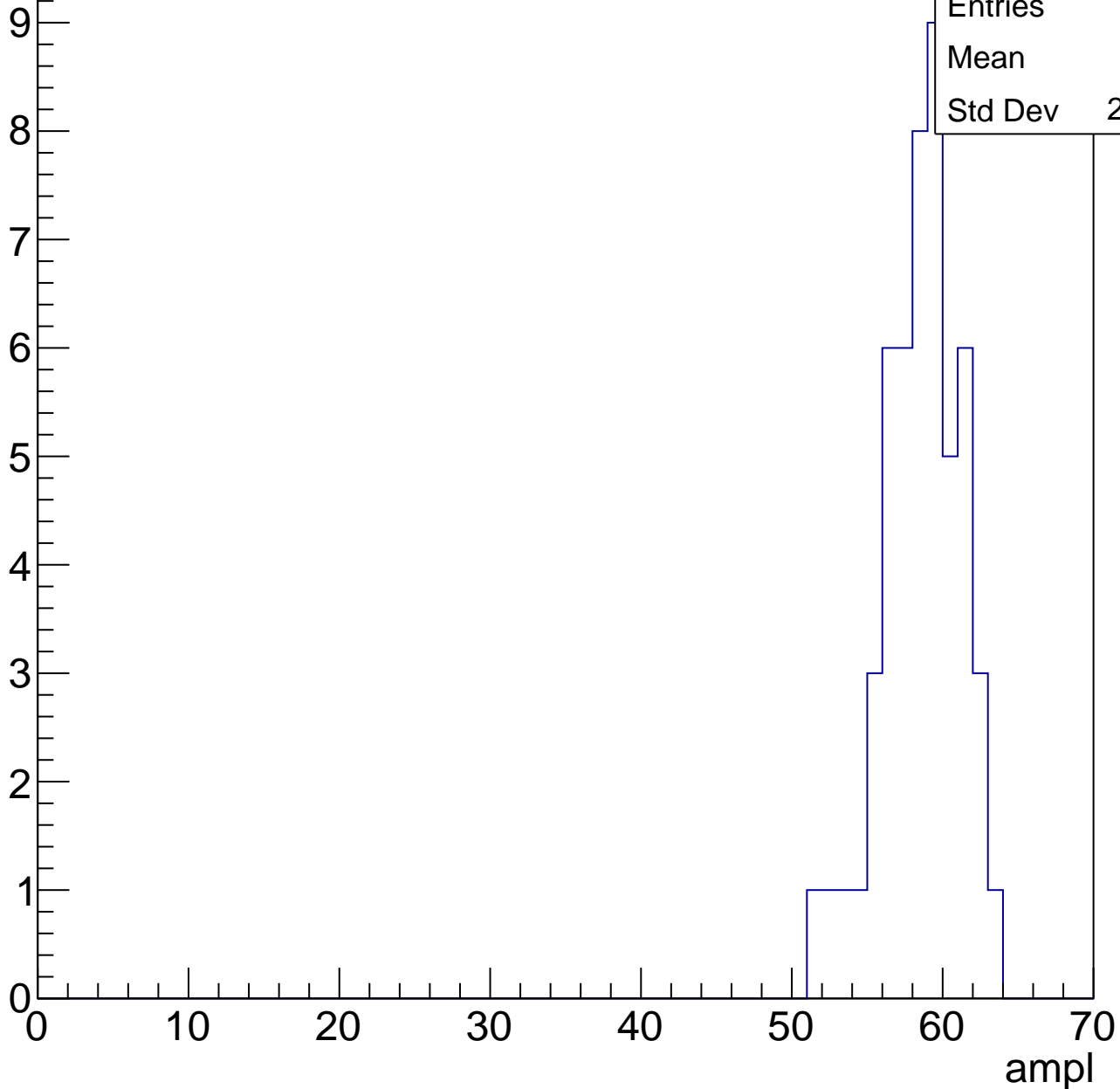


B0L001S, U23-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	58.1
Std Dev	2.576

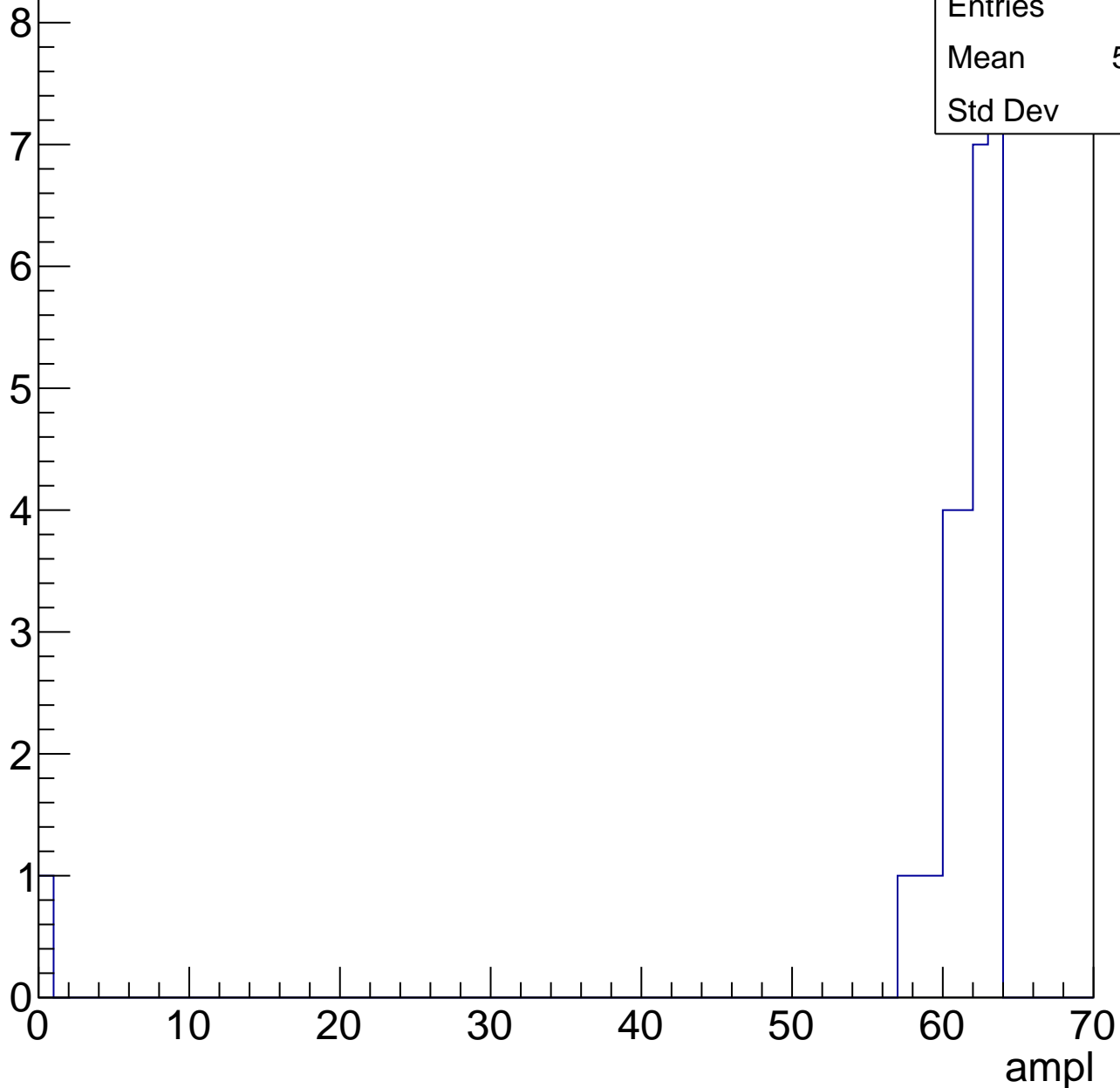


B0L001S, U23-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	59.11
Std Dev	11.7



B0L001S, U23-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch104, adc0

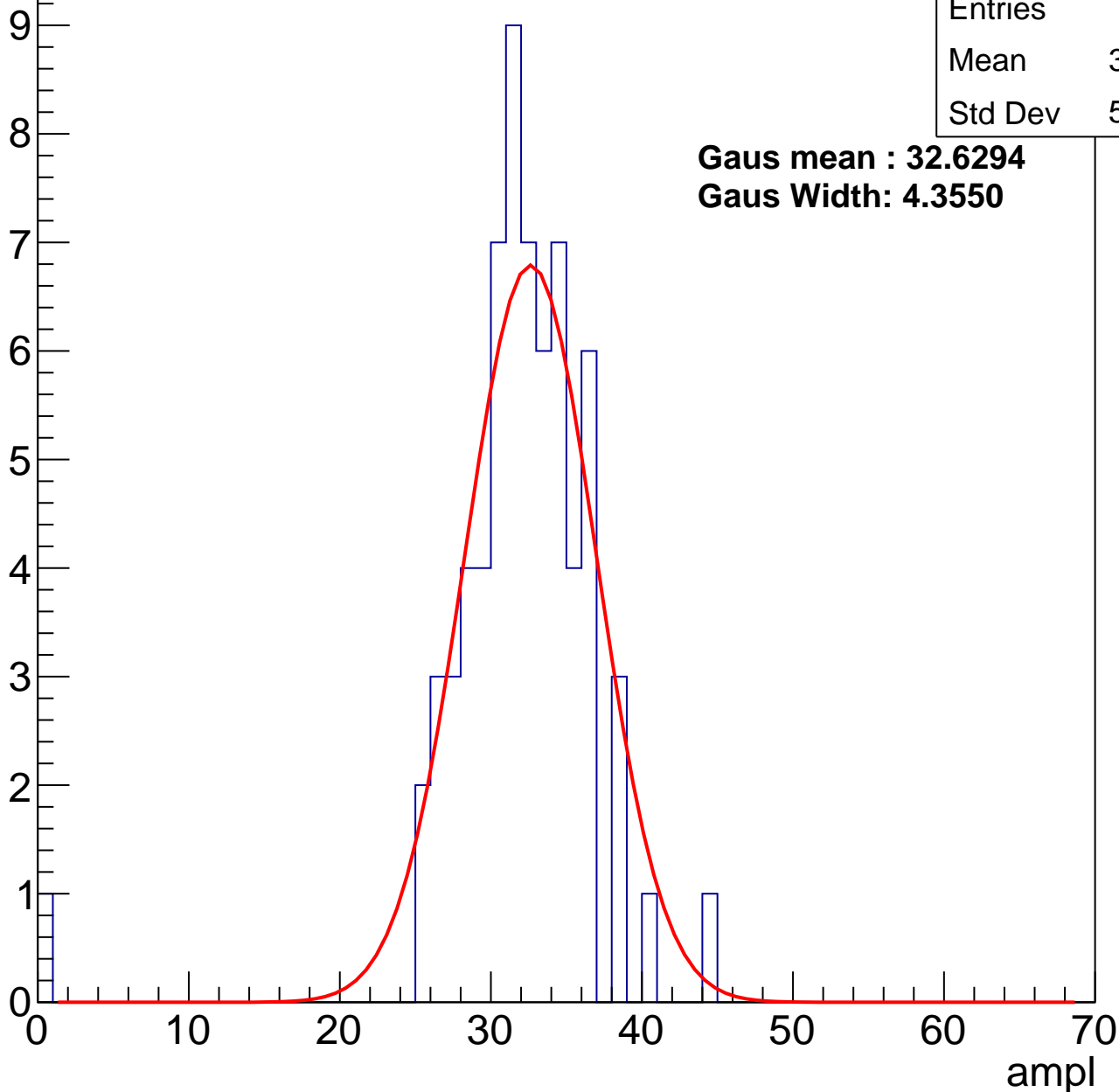
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.47
Std Dev	5.293

Gaus mean : 32.6294

Gaus Width: 4.3550



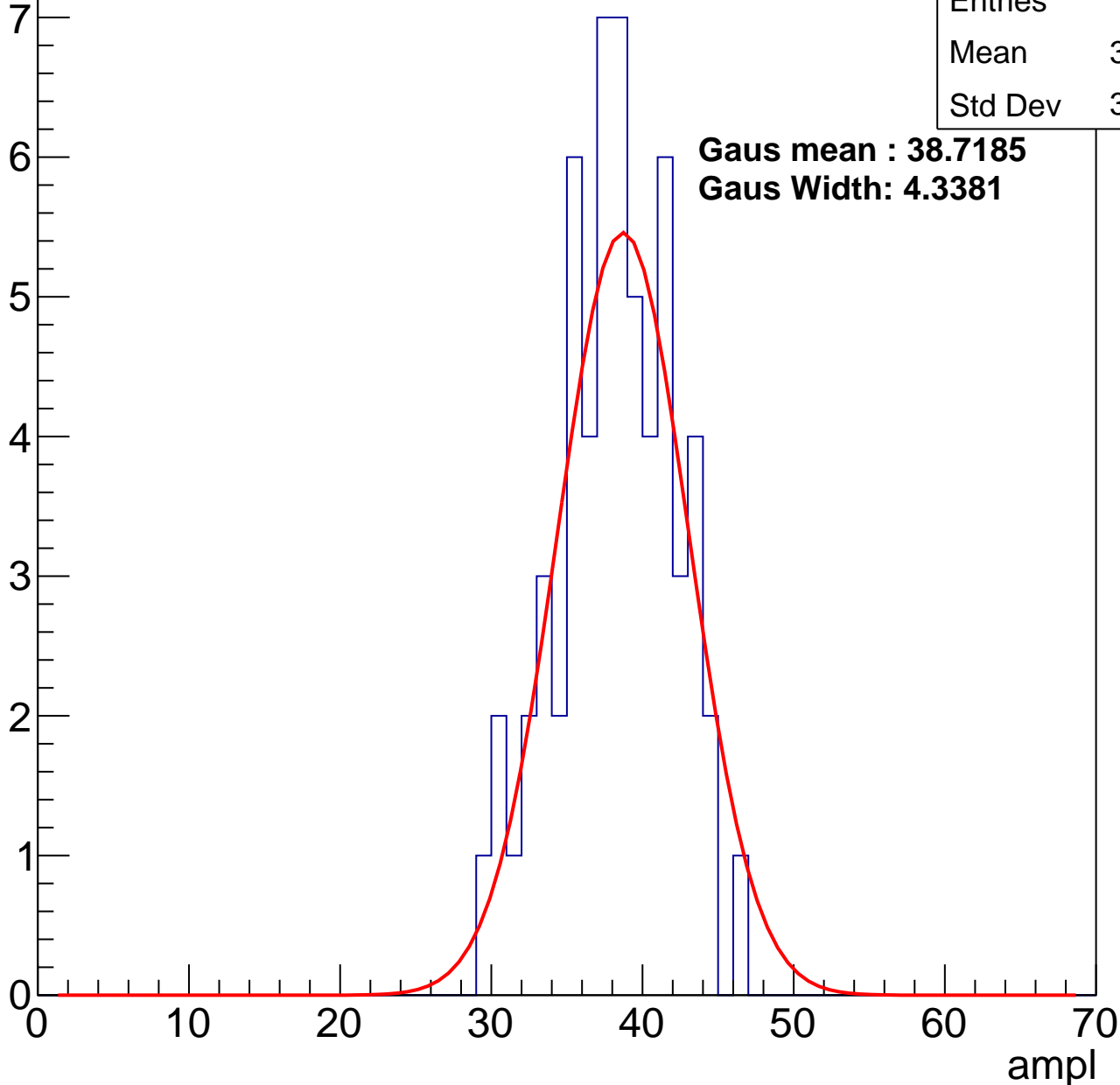
B0L001S, U23-ch104, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.72
Std Dev	3.804

Gaus mean : 38.7185
Gaus Width: 4.3381



B0L001S, U23-ch104, adc2

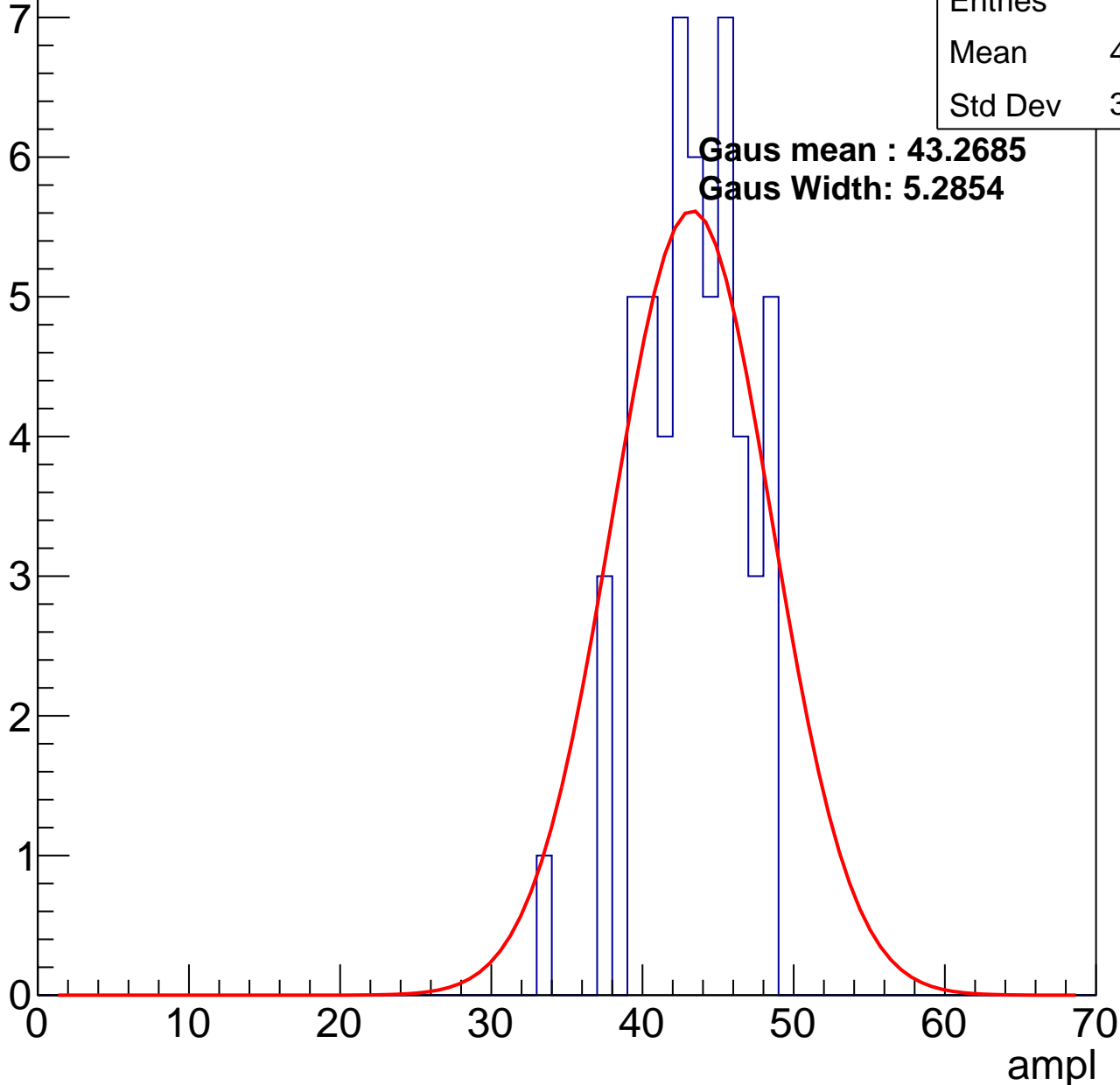
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.82
Std Dev	3.292

Gaus mean : 43.2685

Gaus Width: 5.2854

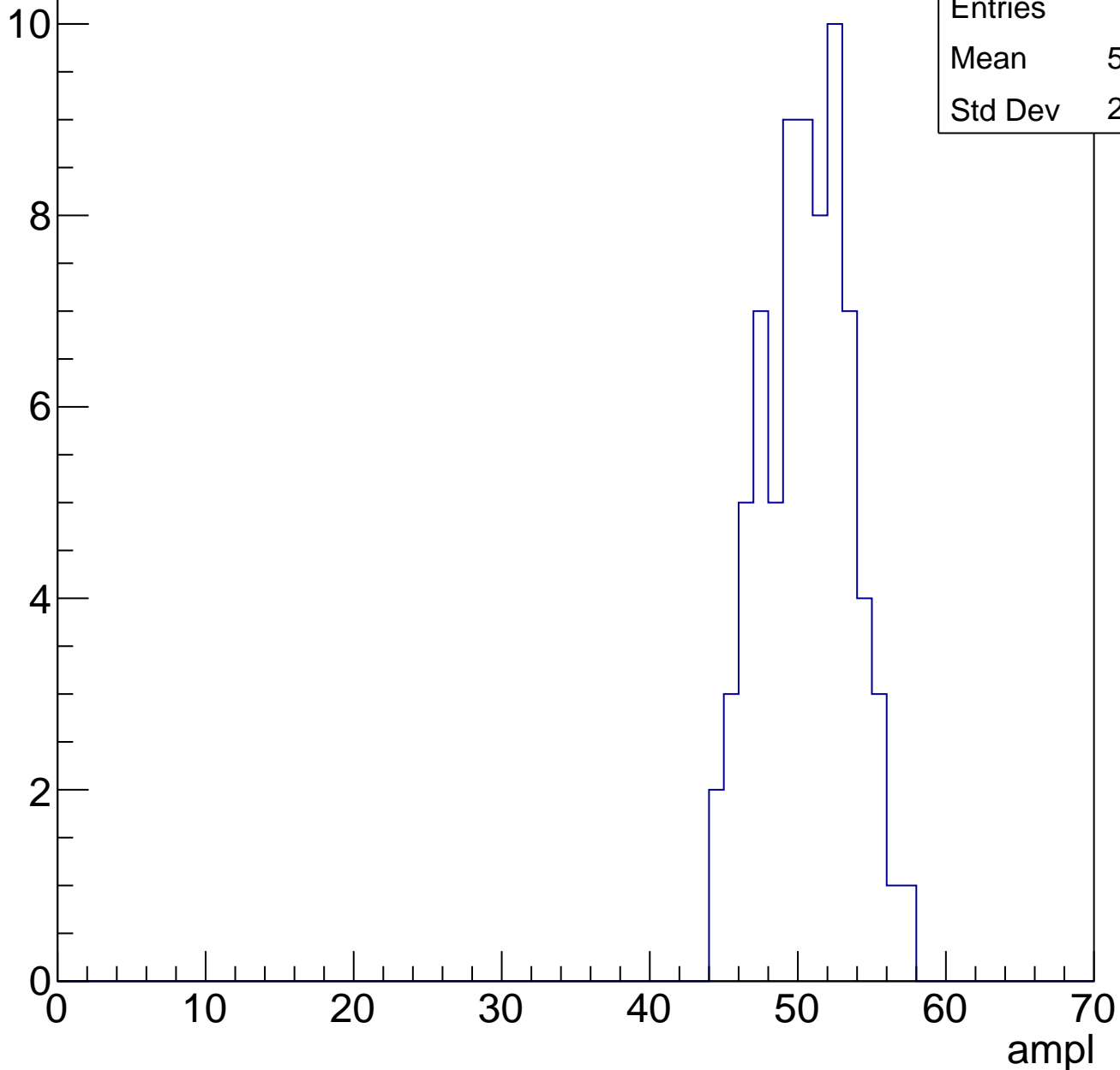


B0L001S, U23-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	50.08
Std Dev	2.972

Entry

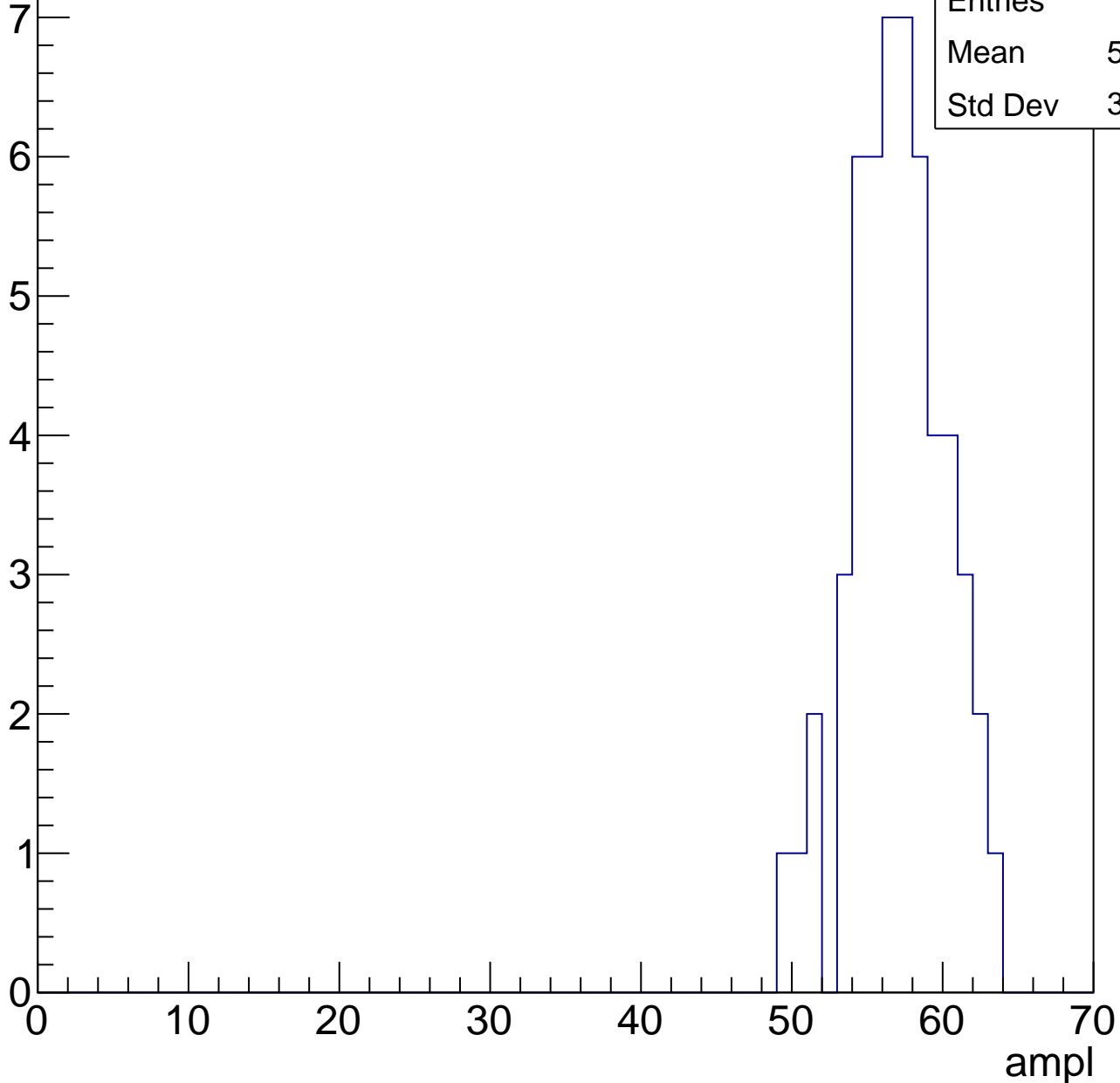


B0L001S, U23-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.58
Std Dev	3.074

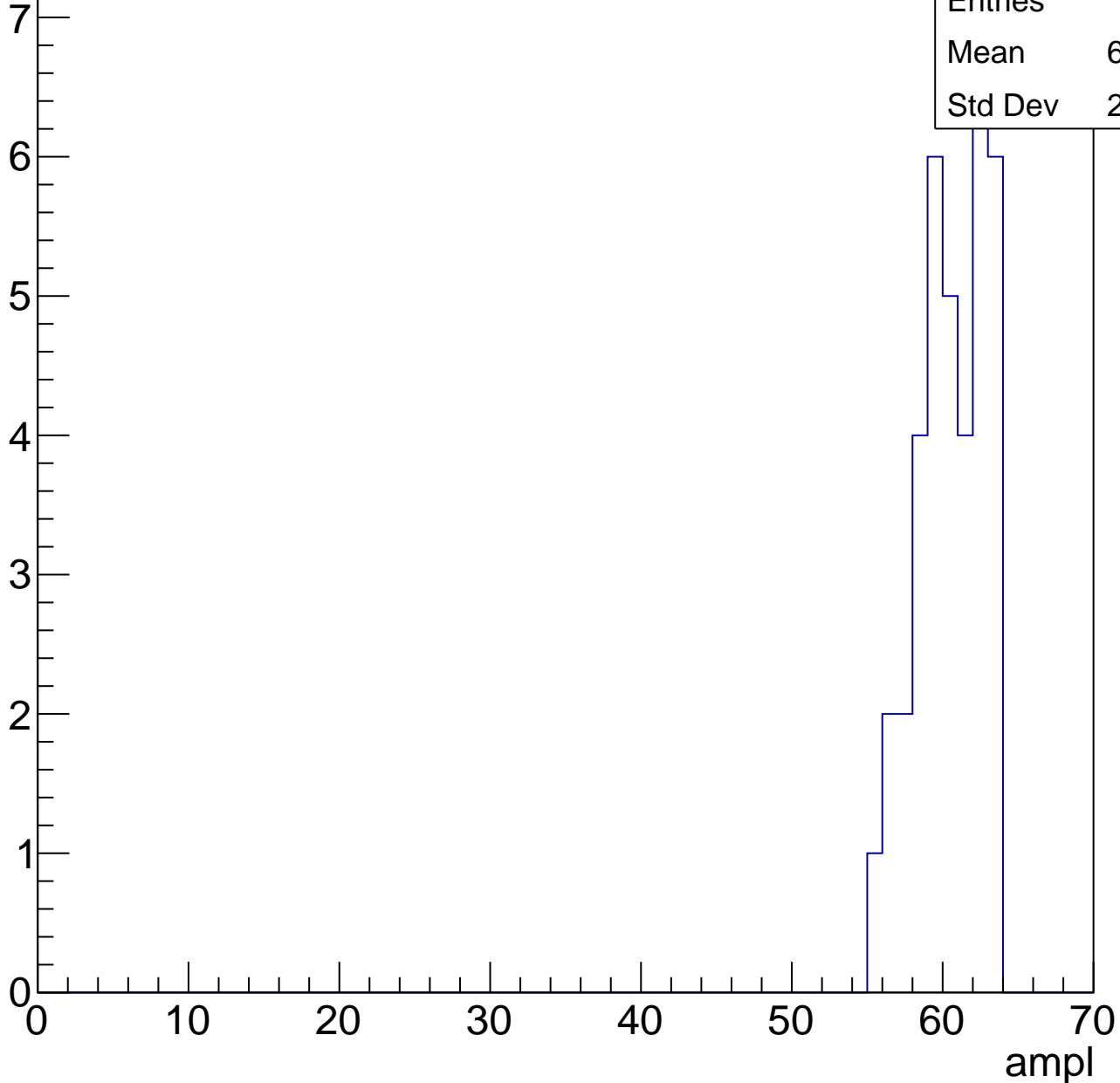


B0L001S, U23-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

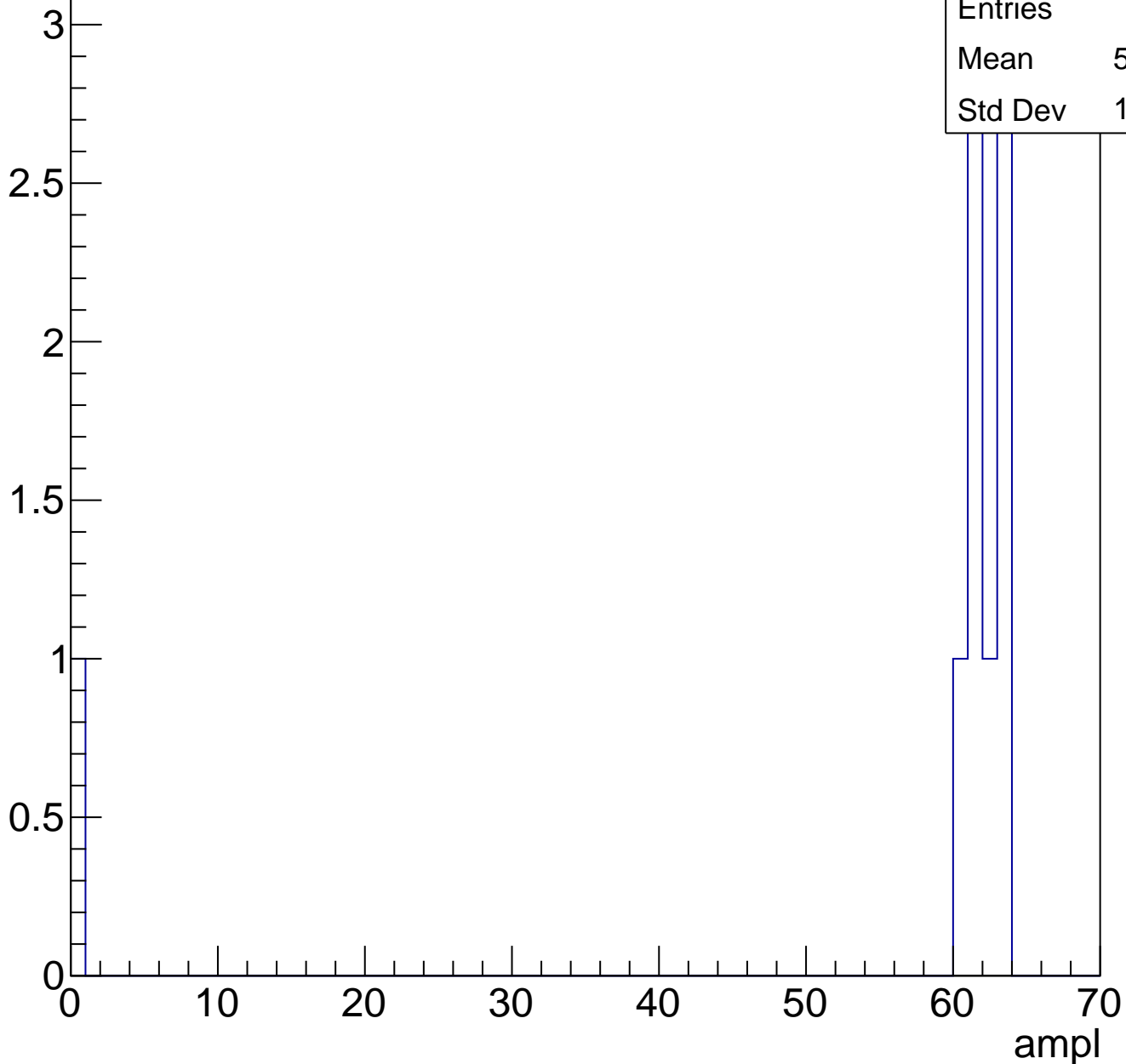
Entries	37
Mean	60.08
Std Dev	2.222



B0L001S, U23-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch105, adc0

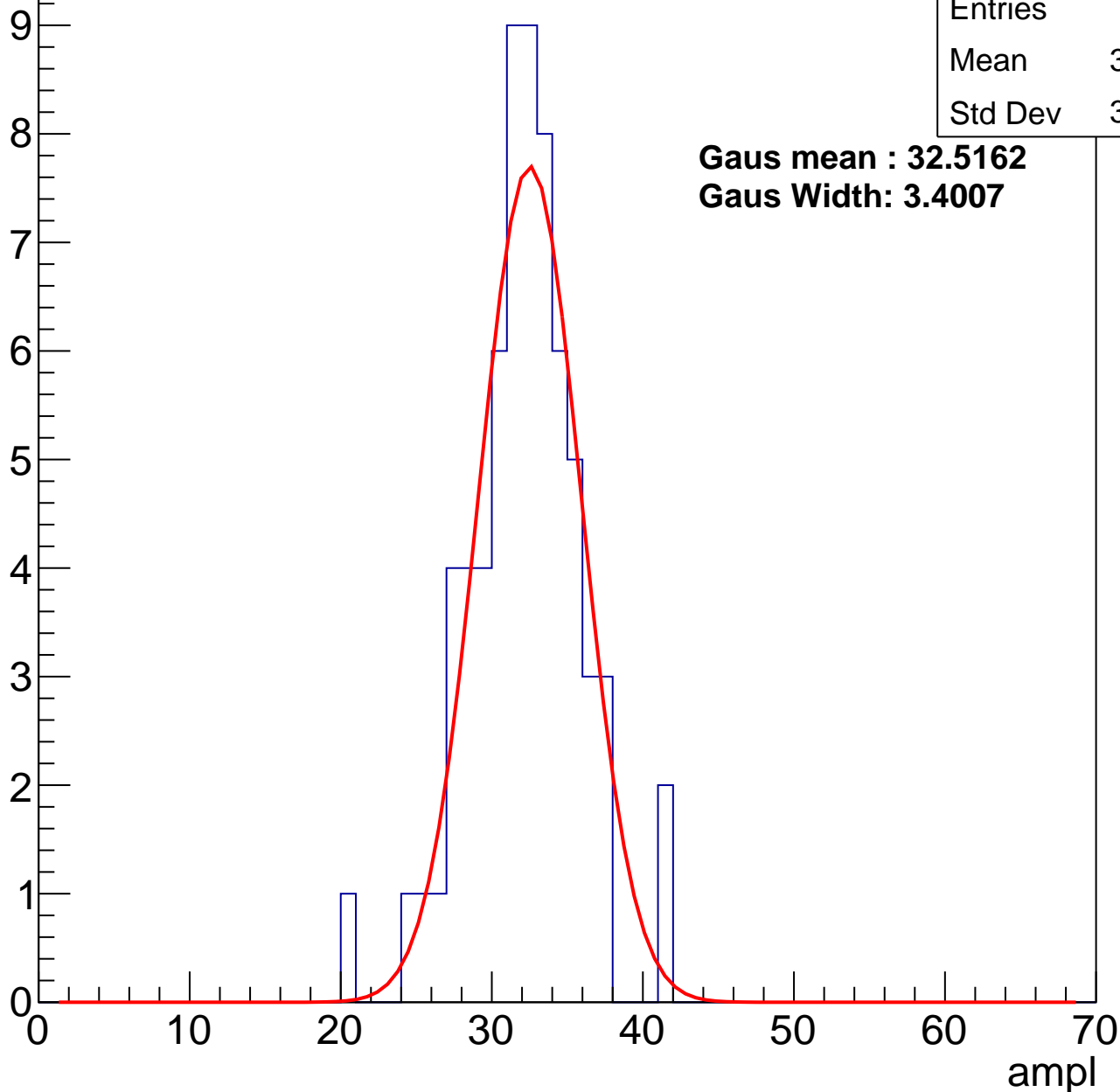
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	31.67
Std Dev	3.626

Gaus mean : 32.5162

Gaus Width: 3.4007



B0L001S, U23-ch105, adc1

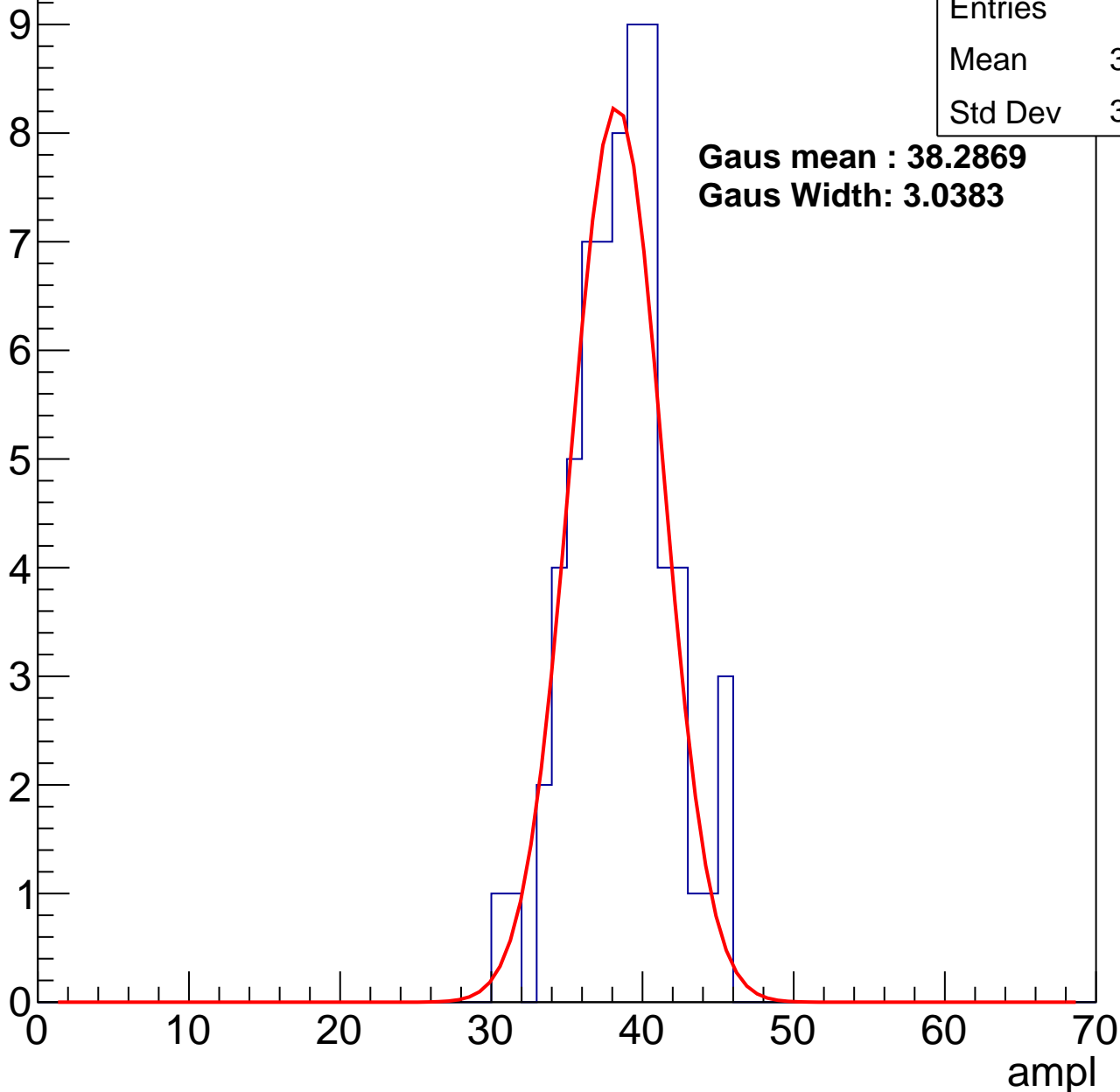
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.15
Std Dev	3.159

Gaus mean : 38.2869

Gaus Width: 3.0383



B0L001S, U23-ch105, adc2

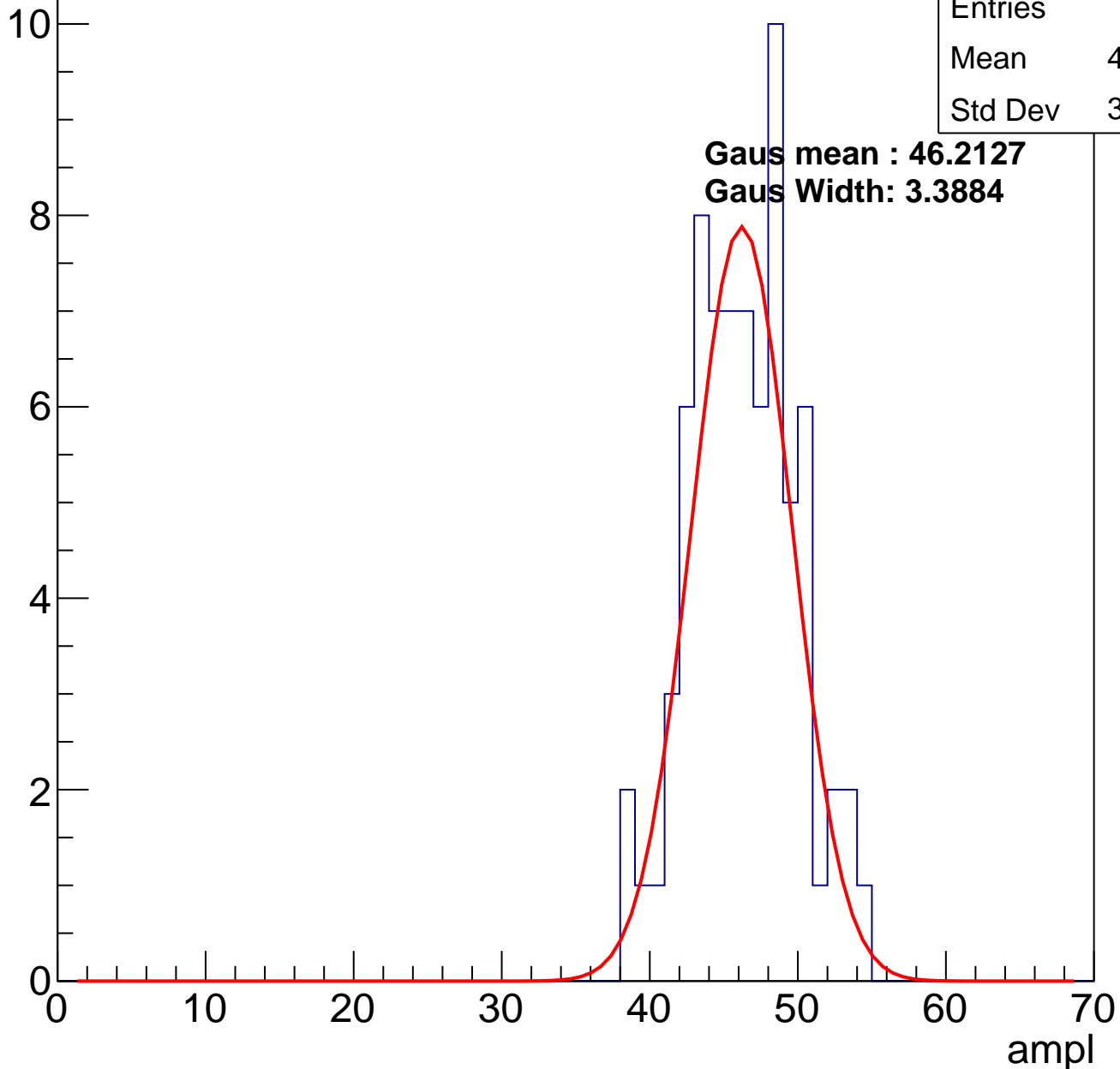
calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	45.88
Std Dev	3.533

Gaus mean : 46.2127

Gaus Width: 3.3884

Entry

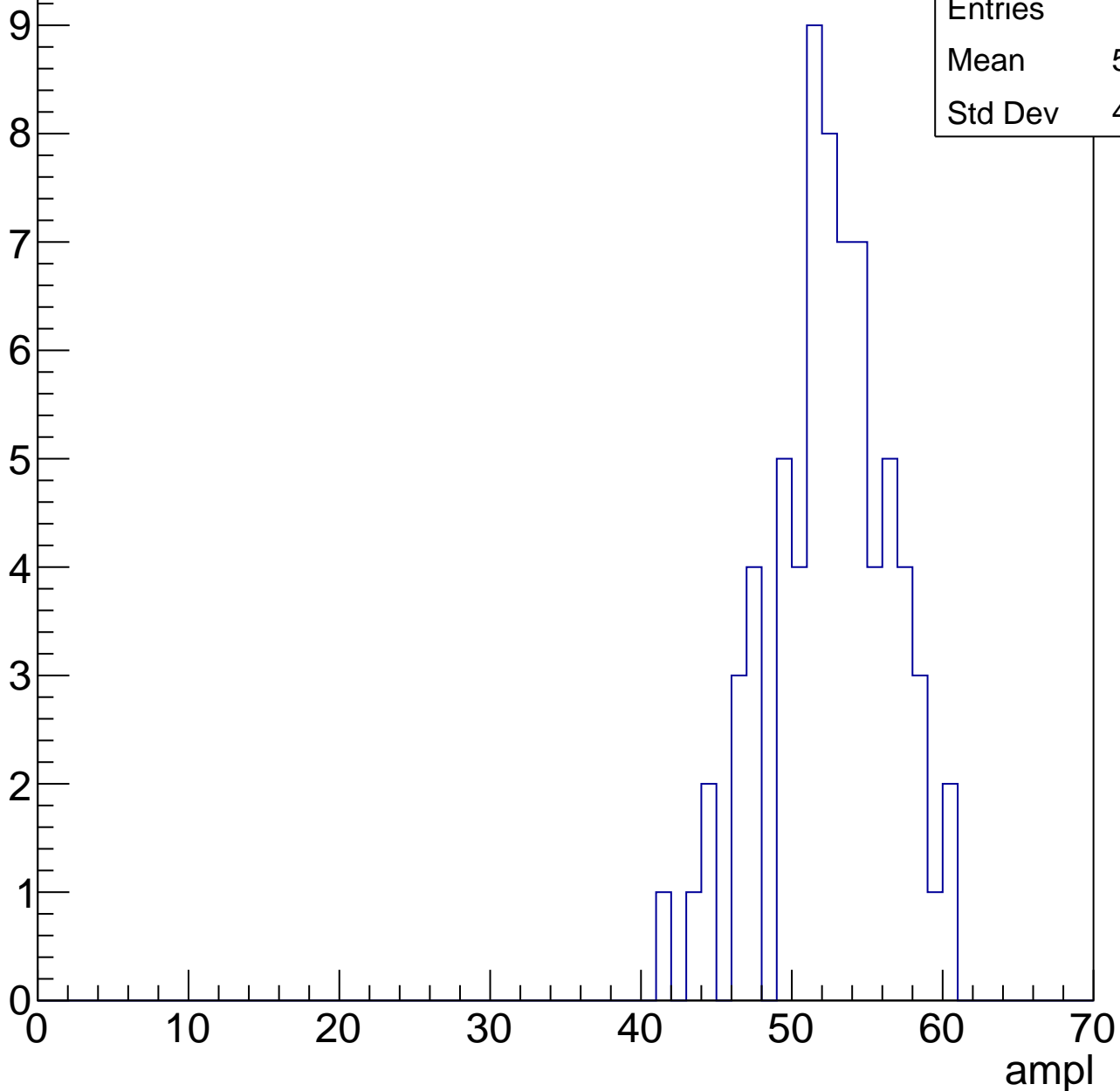


B0L001S, U23-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.11
Std Dev	4.041



B0L001S, U23-ch105, adc4

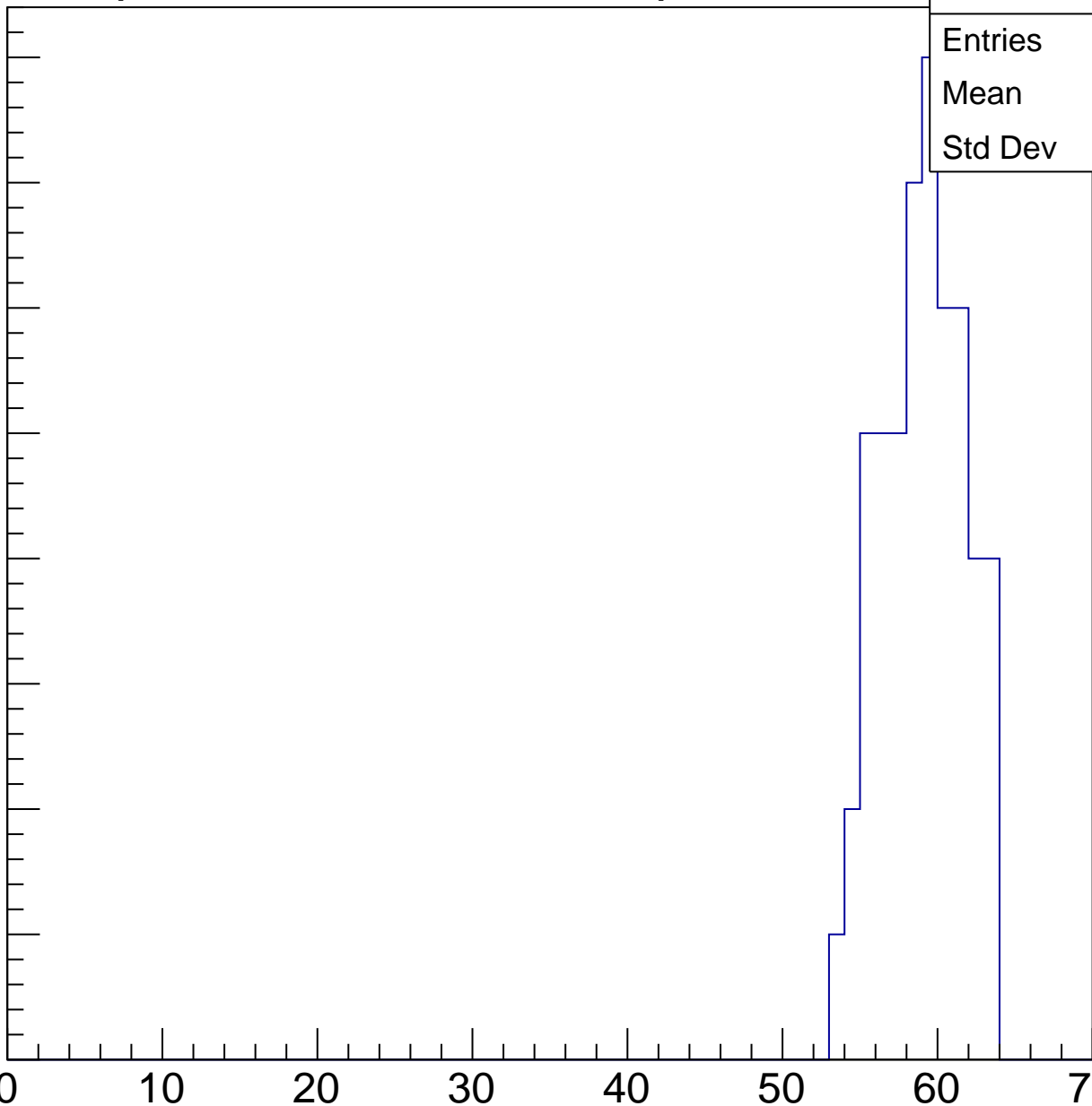
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.58
Std Dev	2.602

ampl

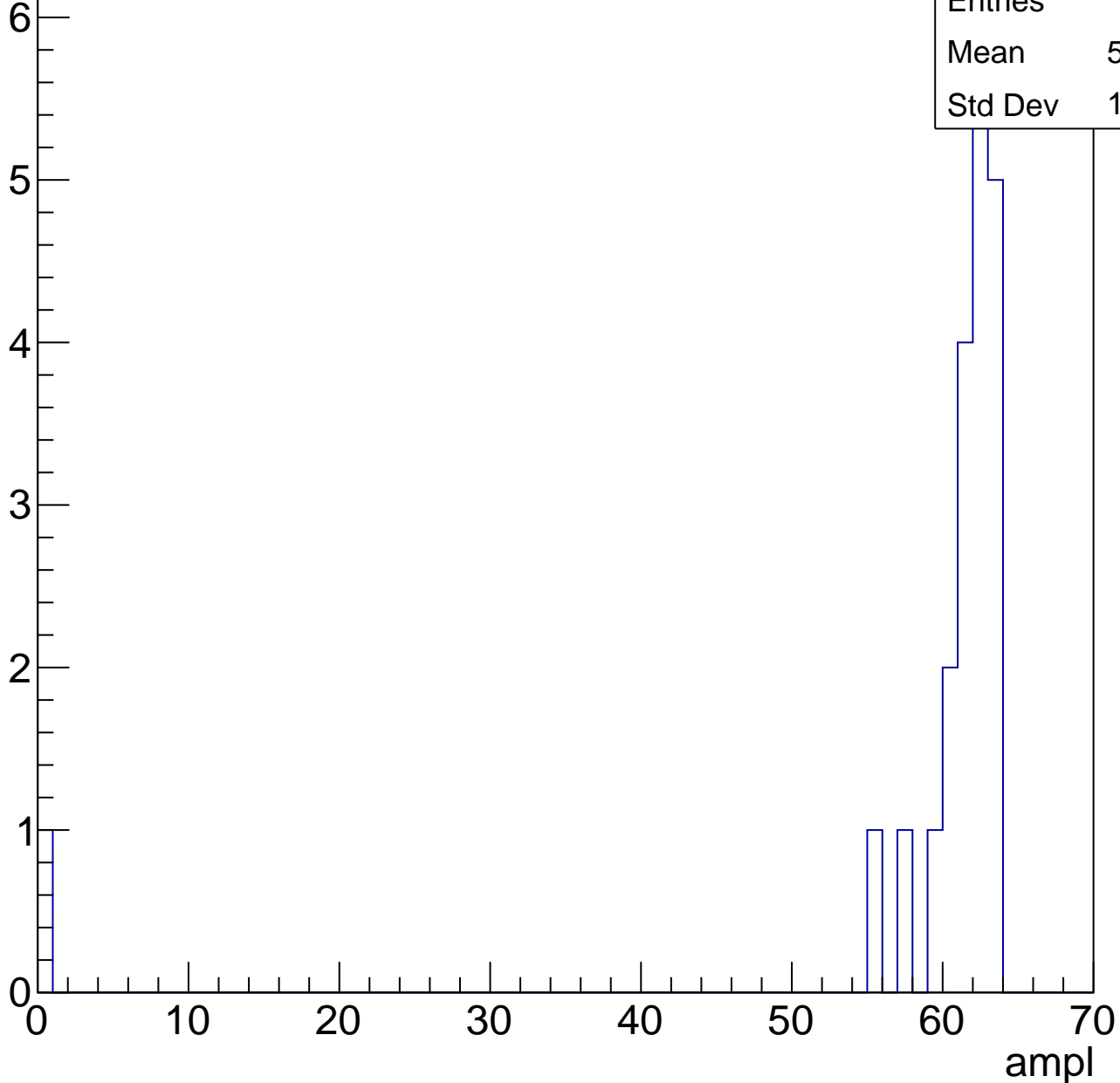


B0L001S, U23-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.19
Std Dev	13.16



B0L001S, U23-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch106, adc0

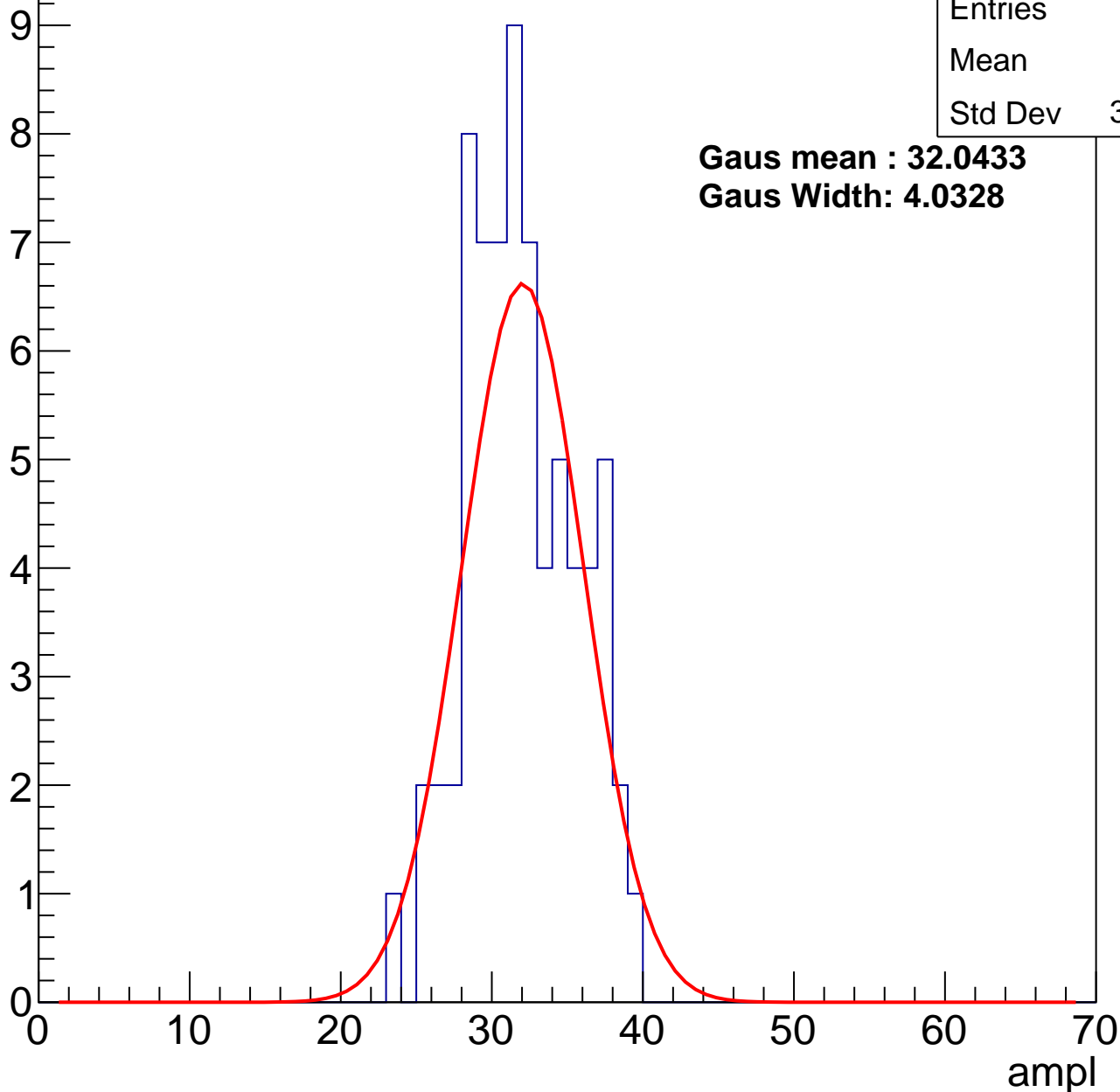
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	31.5
Std Dev	3.549

Gaus mean : 32.0433

Gaus Width: 4.0328



B0L001S, U23-ch106, adc1

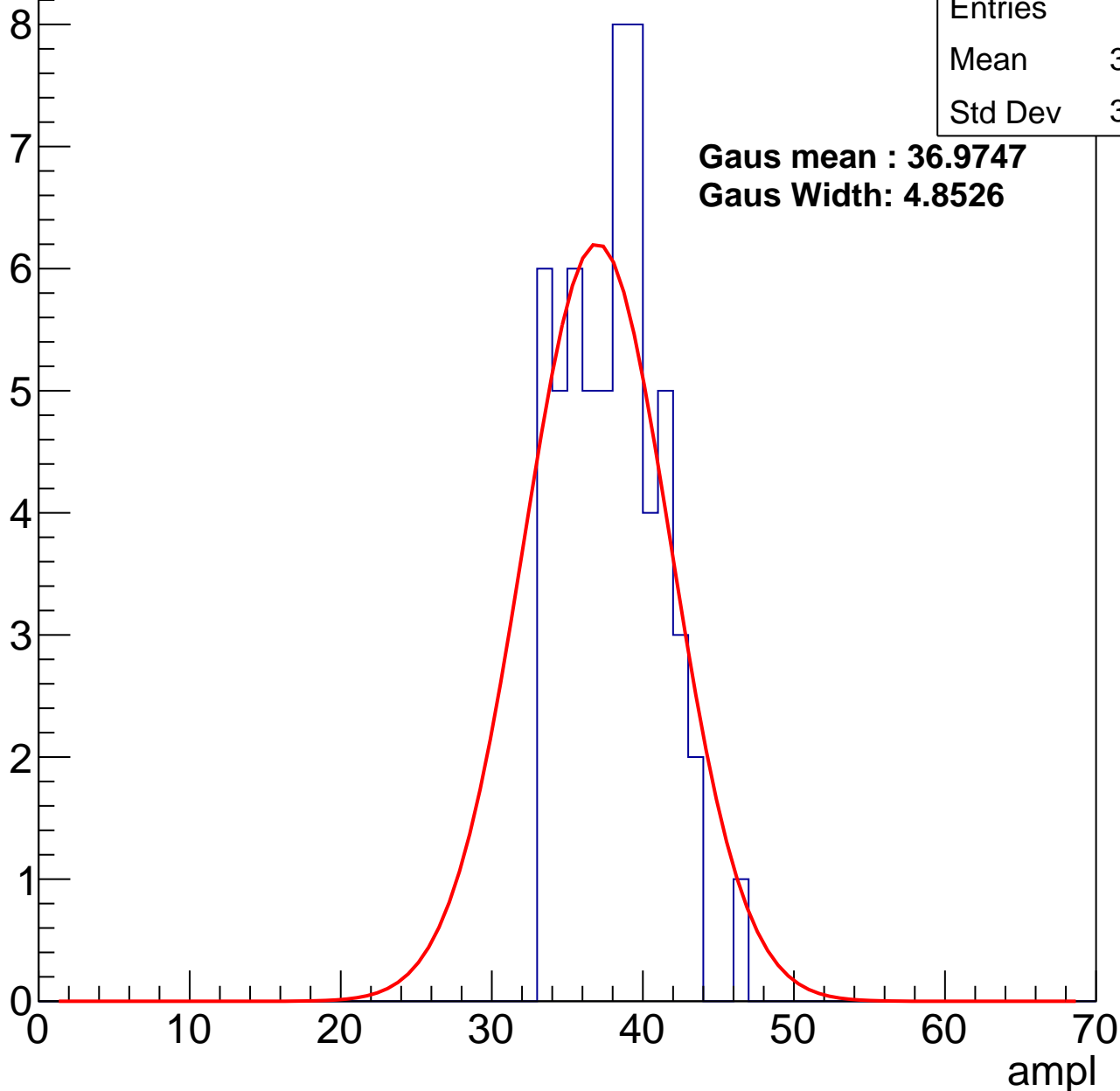
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.62
Std Dev	3.028

Gaus mean : 36.9747

Gaus Width: 4.8526



B0L001S, U23-ch106, adc2

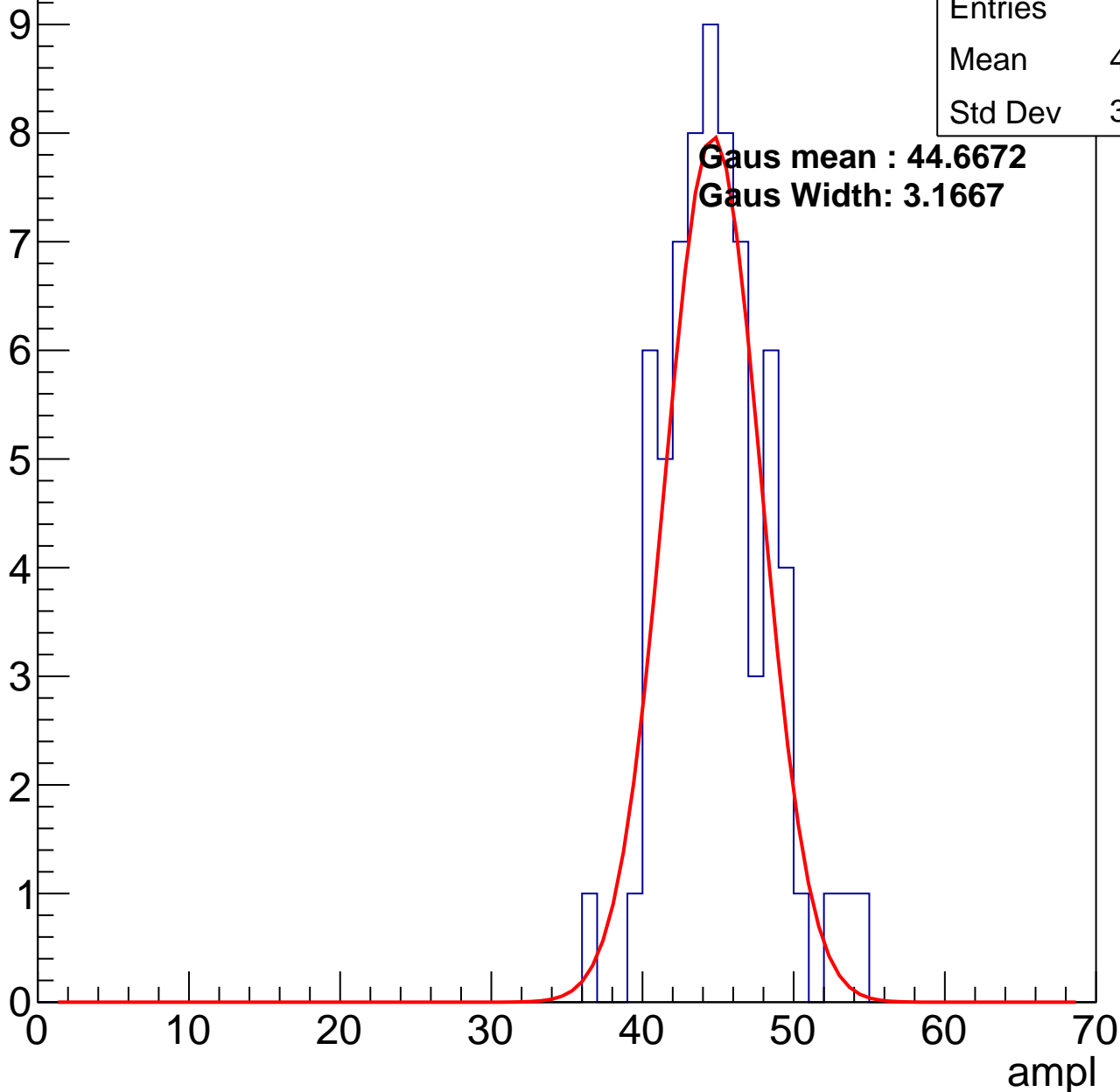
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.49
Std Dev	3.382

Gaus mean : 44.6672

Gaus Width: 3.1667

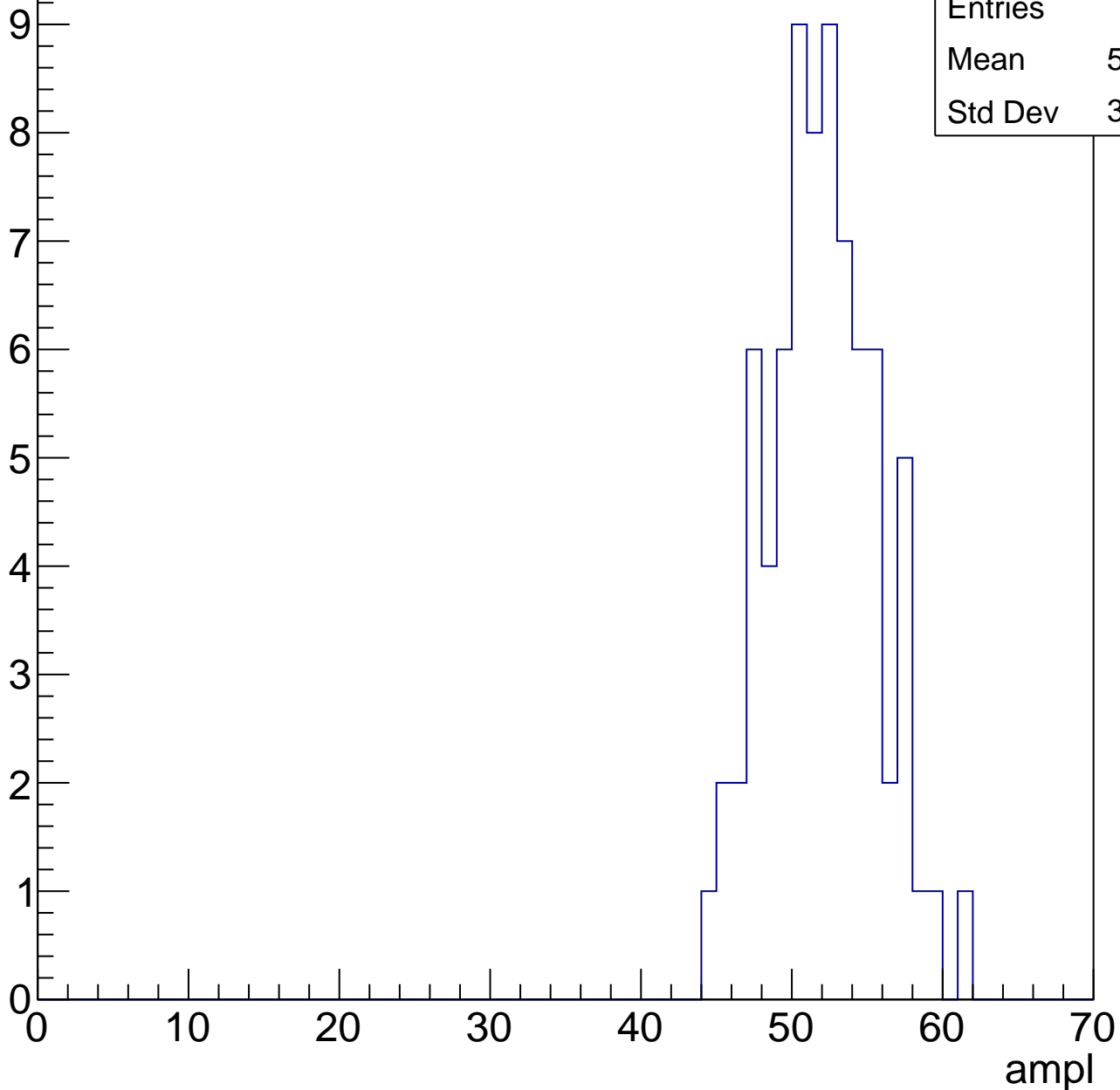


B0L001S, U23-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.58
Std Dev	3.518

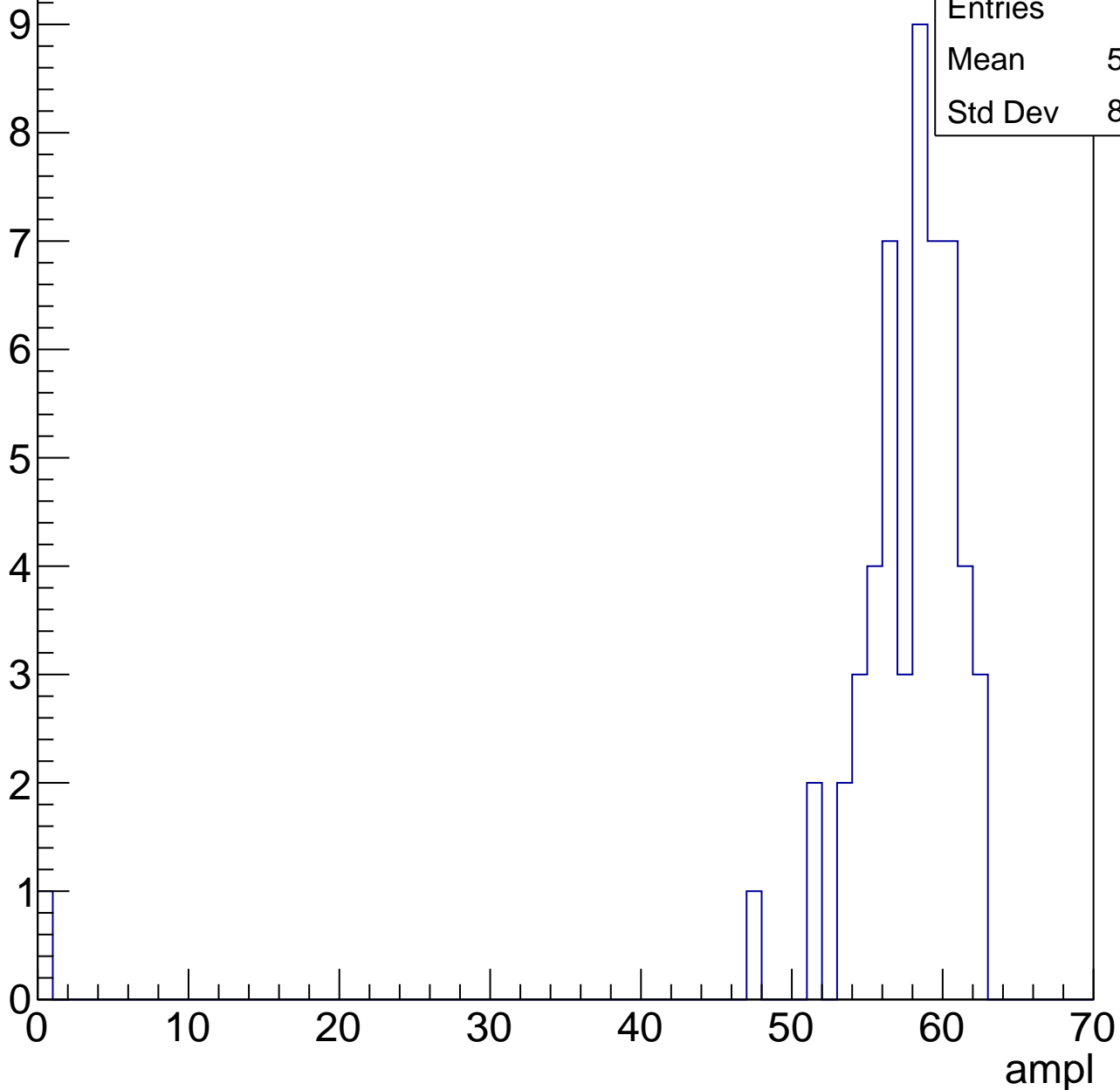


B0L001S, U23-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.32
Std Dev	8.375



B0L001S, U23-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

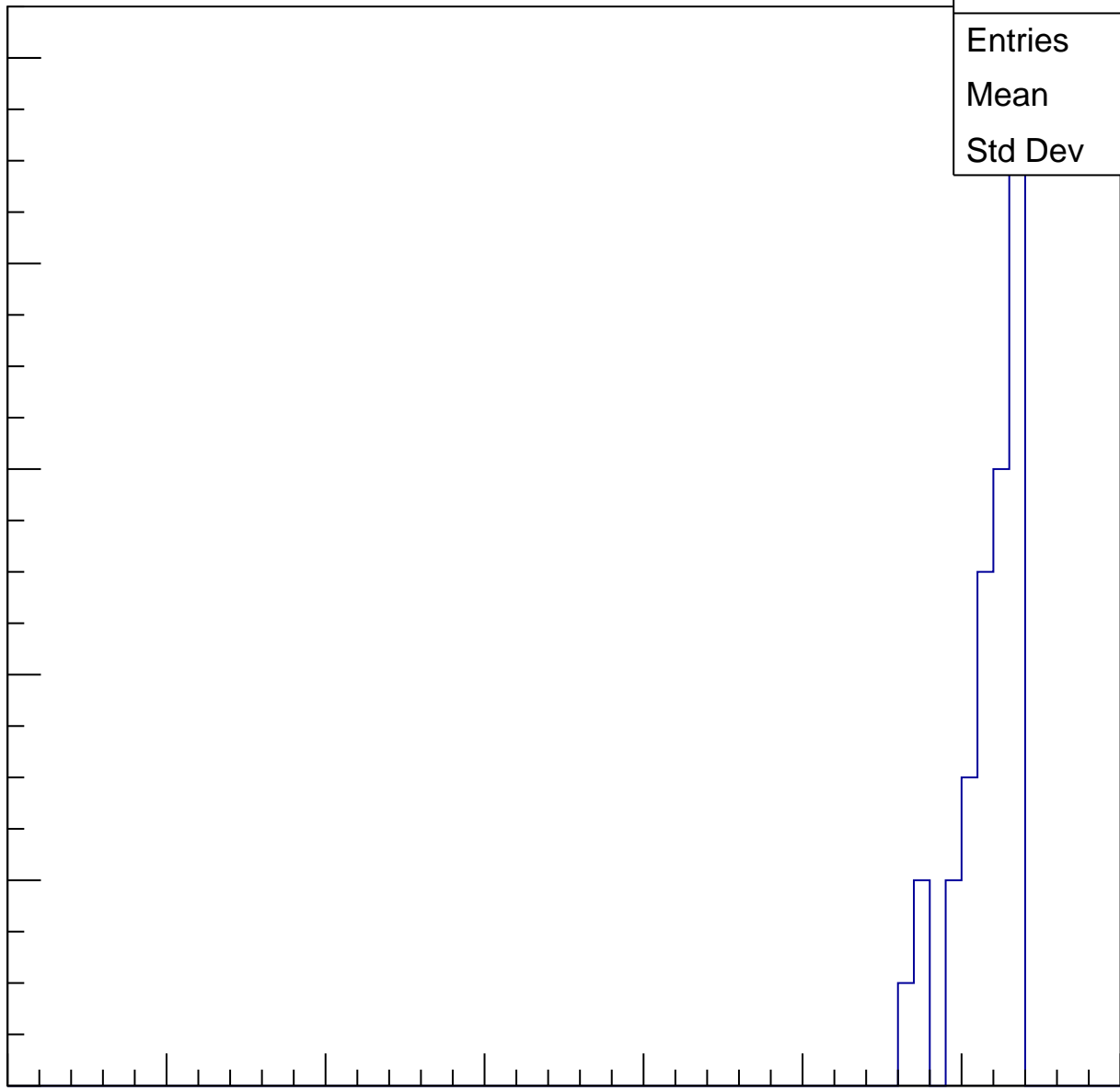
Entries	29
Mean	61.21
Std Dev	1.972

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch107, adc0

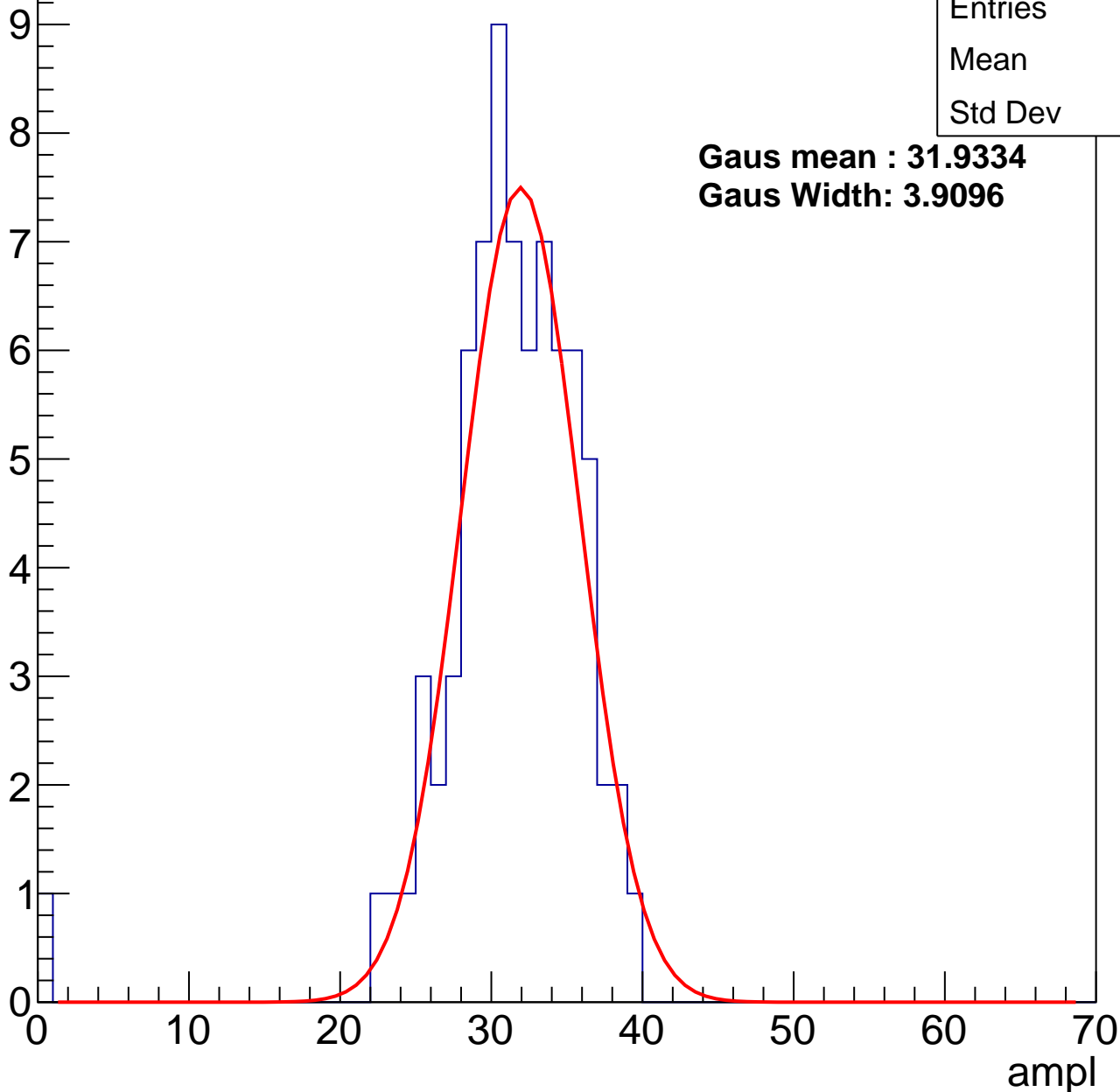
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.8
Std Dev	5.12

Gaus mean : 31.9334

Gaus Width: 3.9096



B0L001S, U23-ch107, adc1

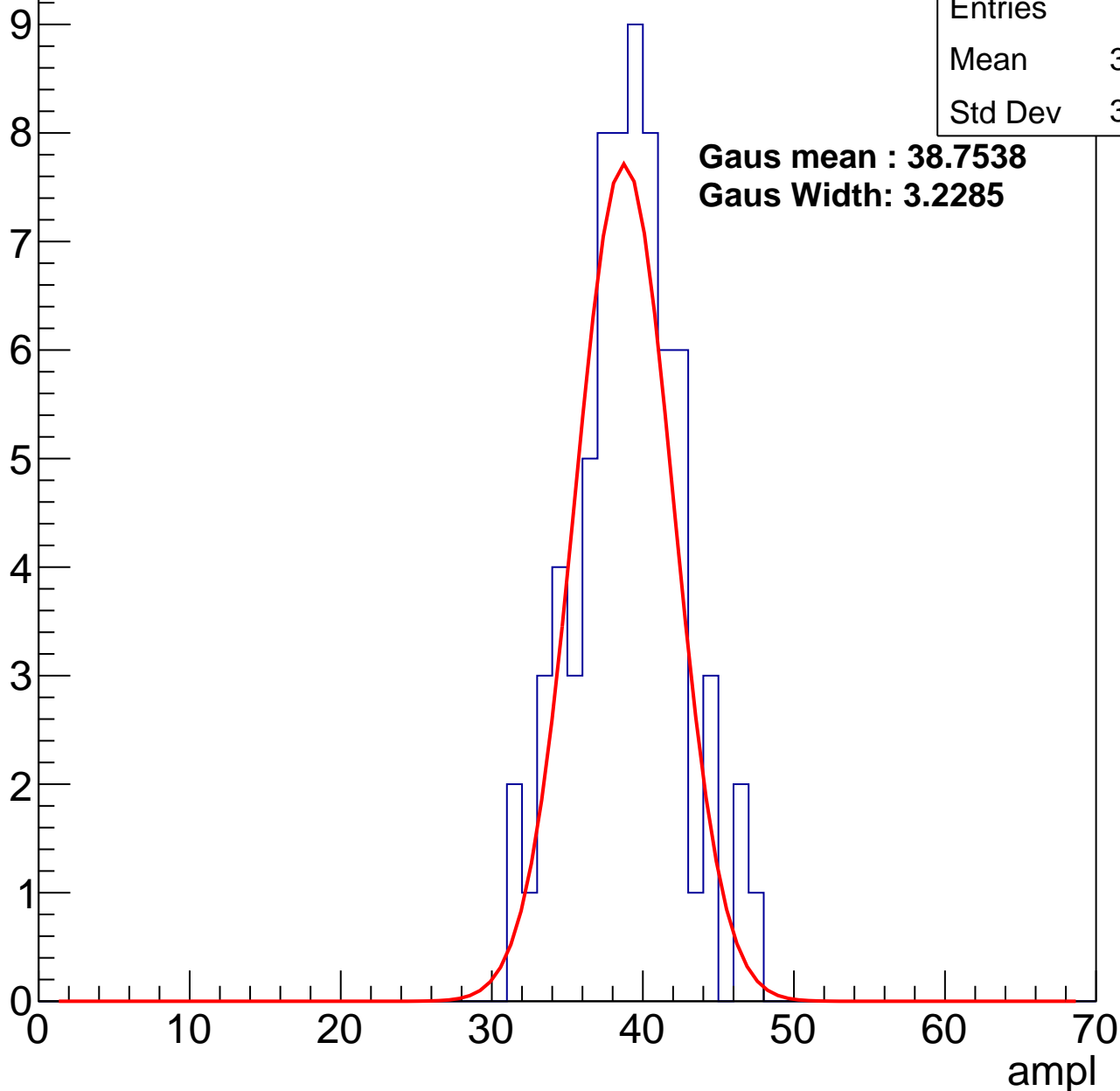
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.53
Std Dev	3.467

Gaus mean : 38.7538

Gaus Width: 3.2285



B0L001S, U23-ch107, adc2

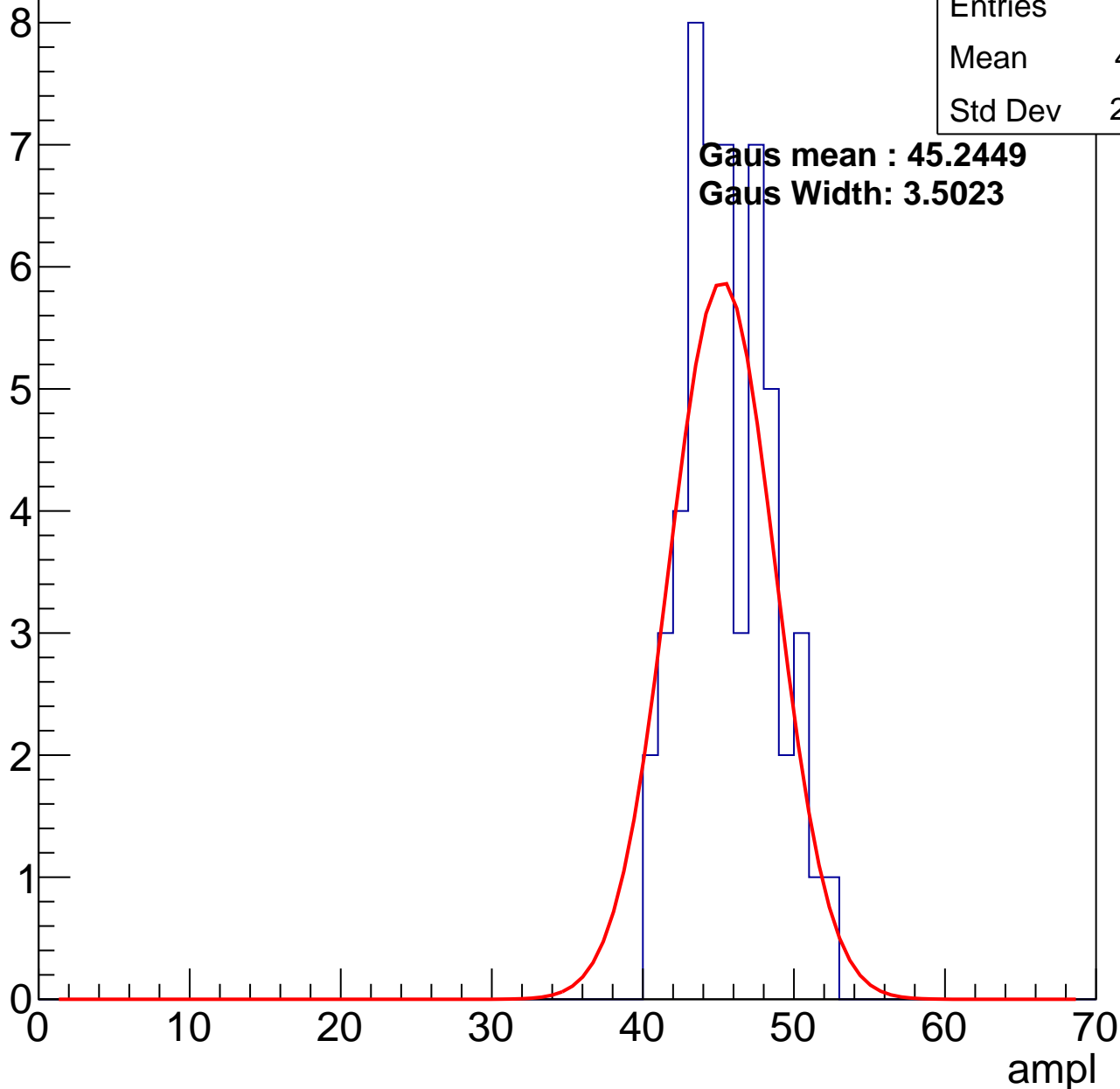
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	45.21
Std Dev	2.877

Gaus mean : 45.2449

Gaus Width: 3.5023

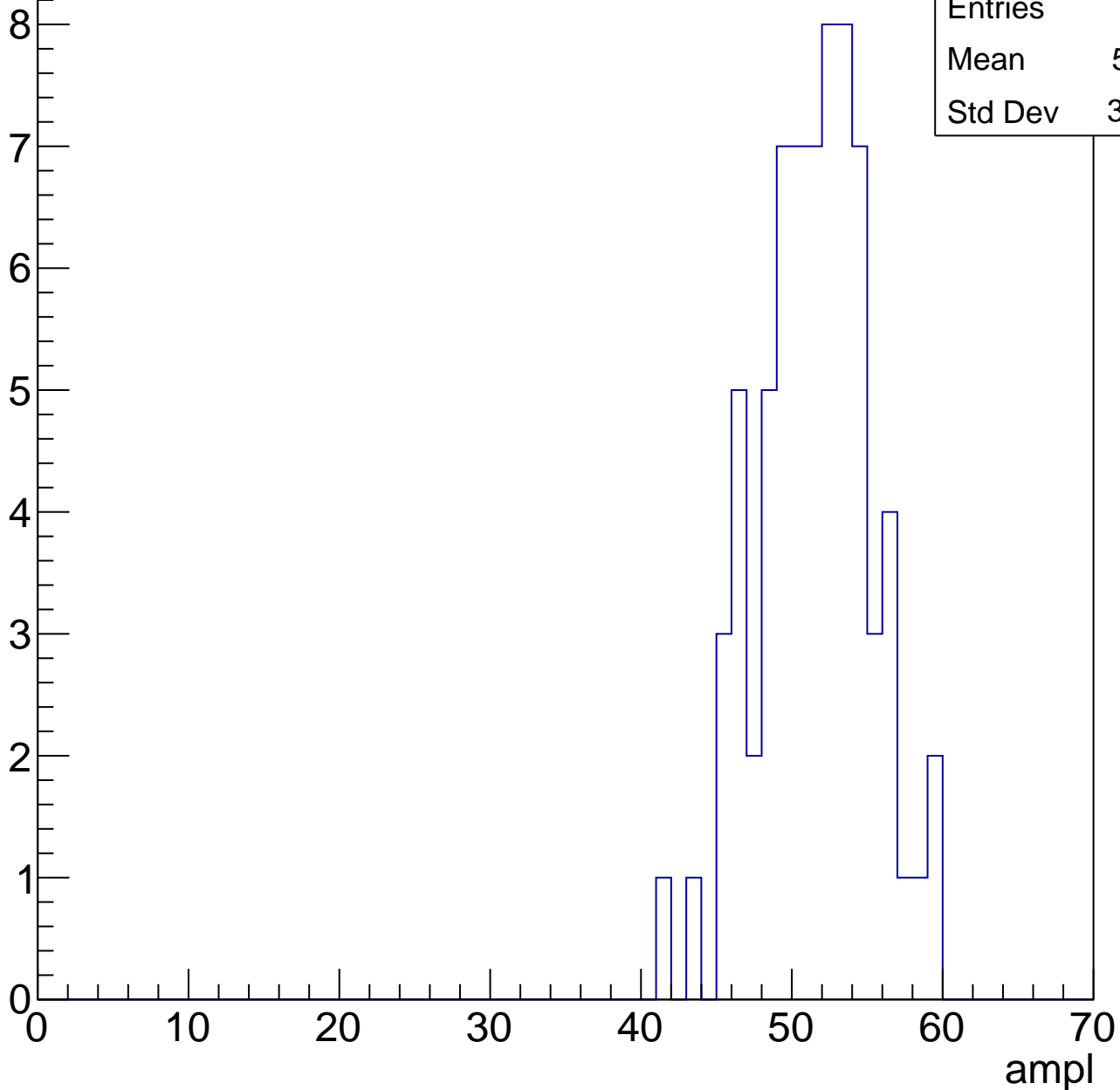


B0L001S, U23-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.01
Std Dev	3.676

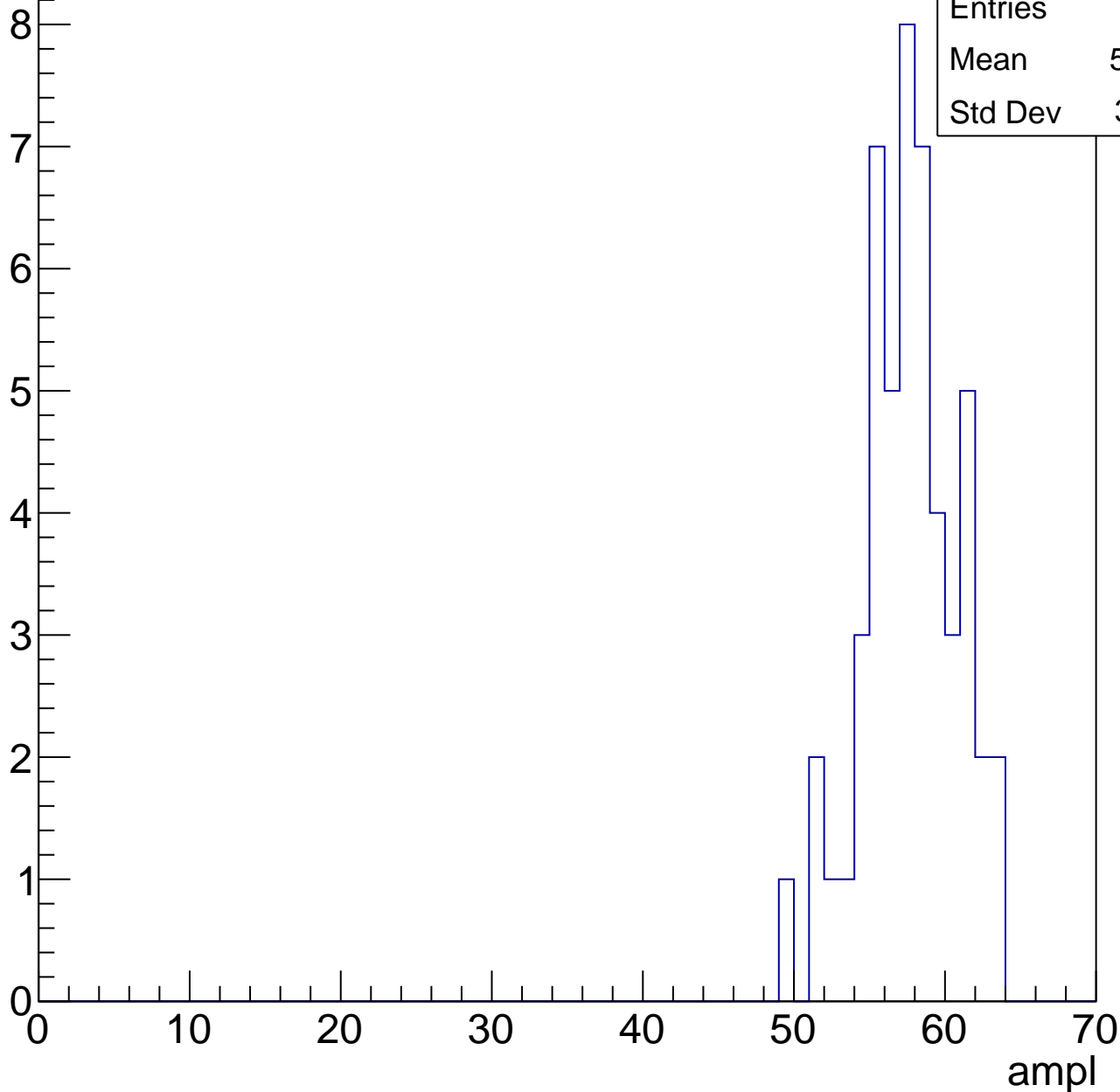


B0L001S, U23-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.18
Std Dev	3.091

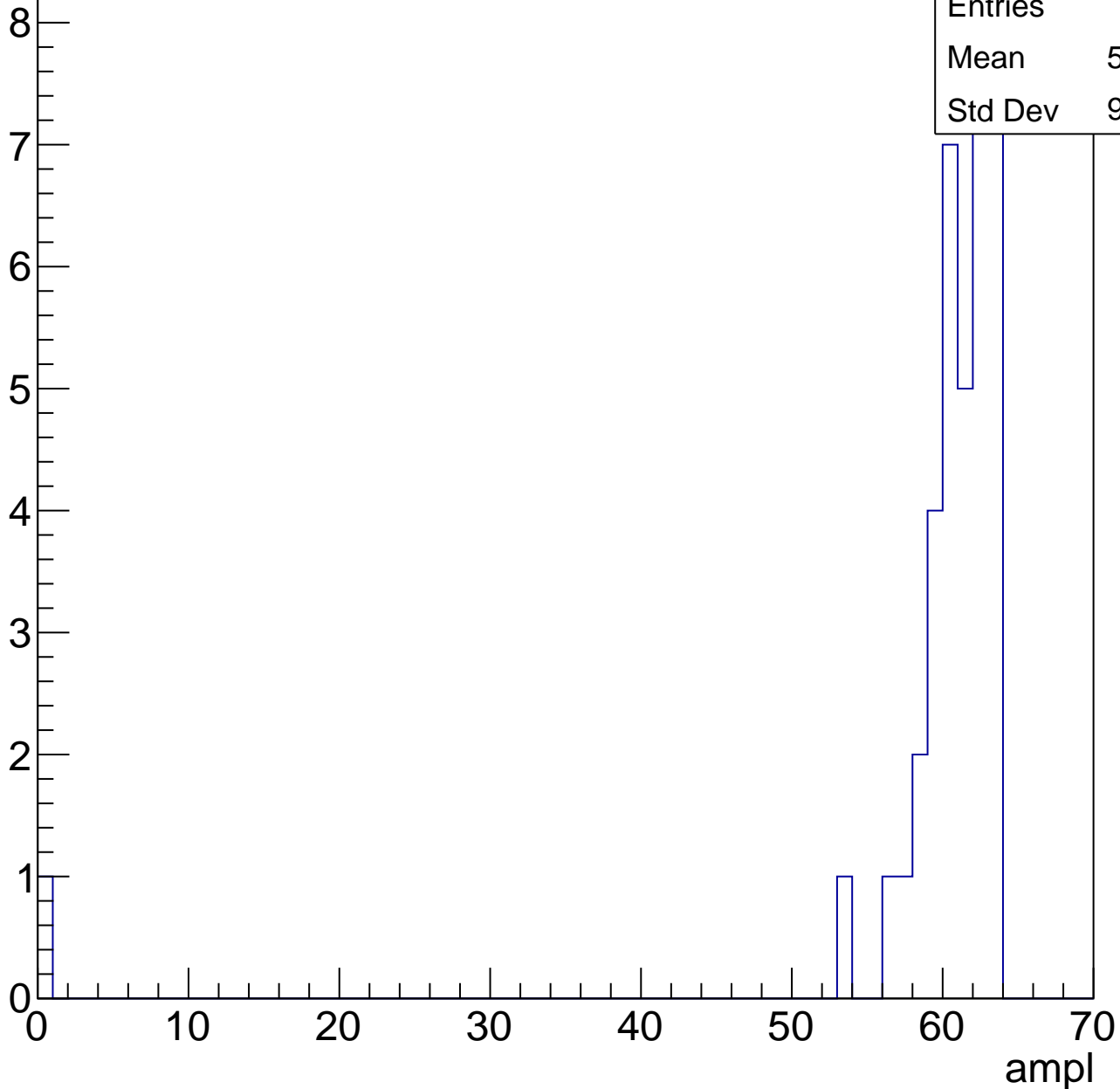


B0L001S, U23-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	59.03
Std Dev	9.946



B0L001S, U23-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch108, adc0

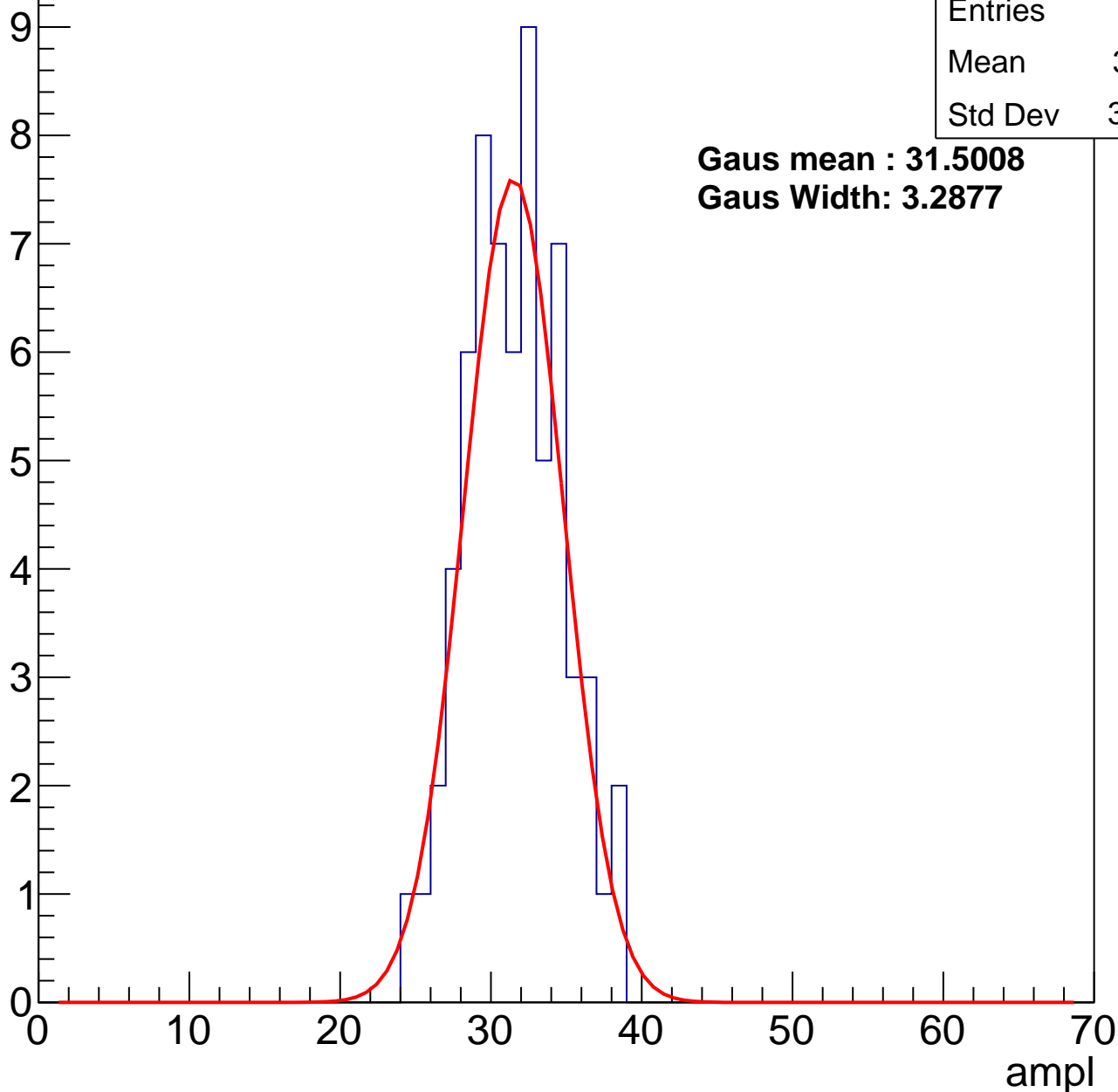
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.11
Std Dev	3.138

Gaus mean : 31.5008

Gaus Width: 3.2877



B0L001S, U23-ch108, adc1

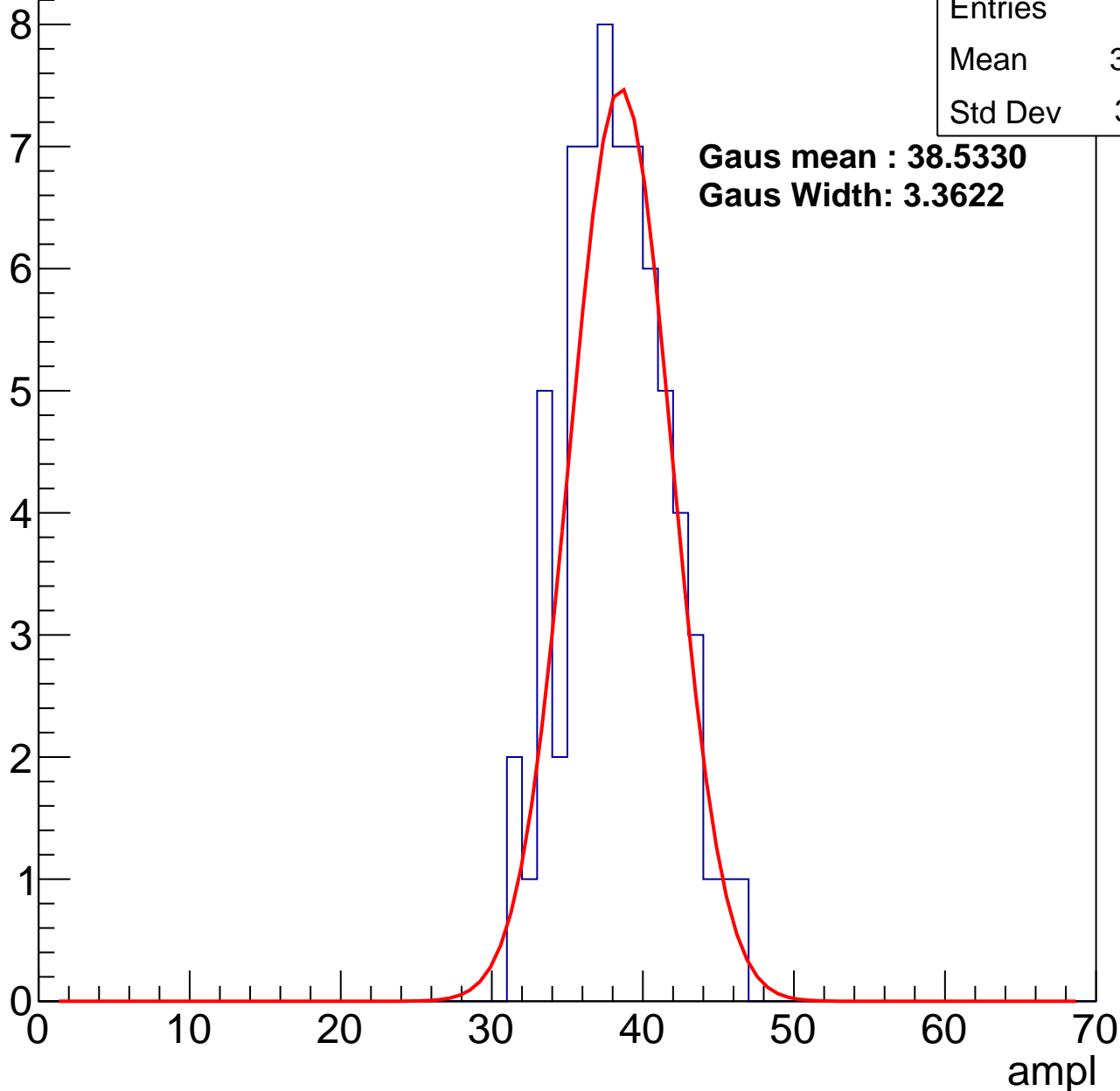
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.85
Std Dev	3.351

Gaus mean : 38.5330

Gaus Width: 3.3622



B0L001S, U23-ch108, adc2

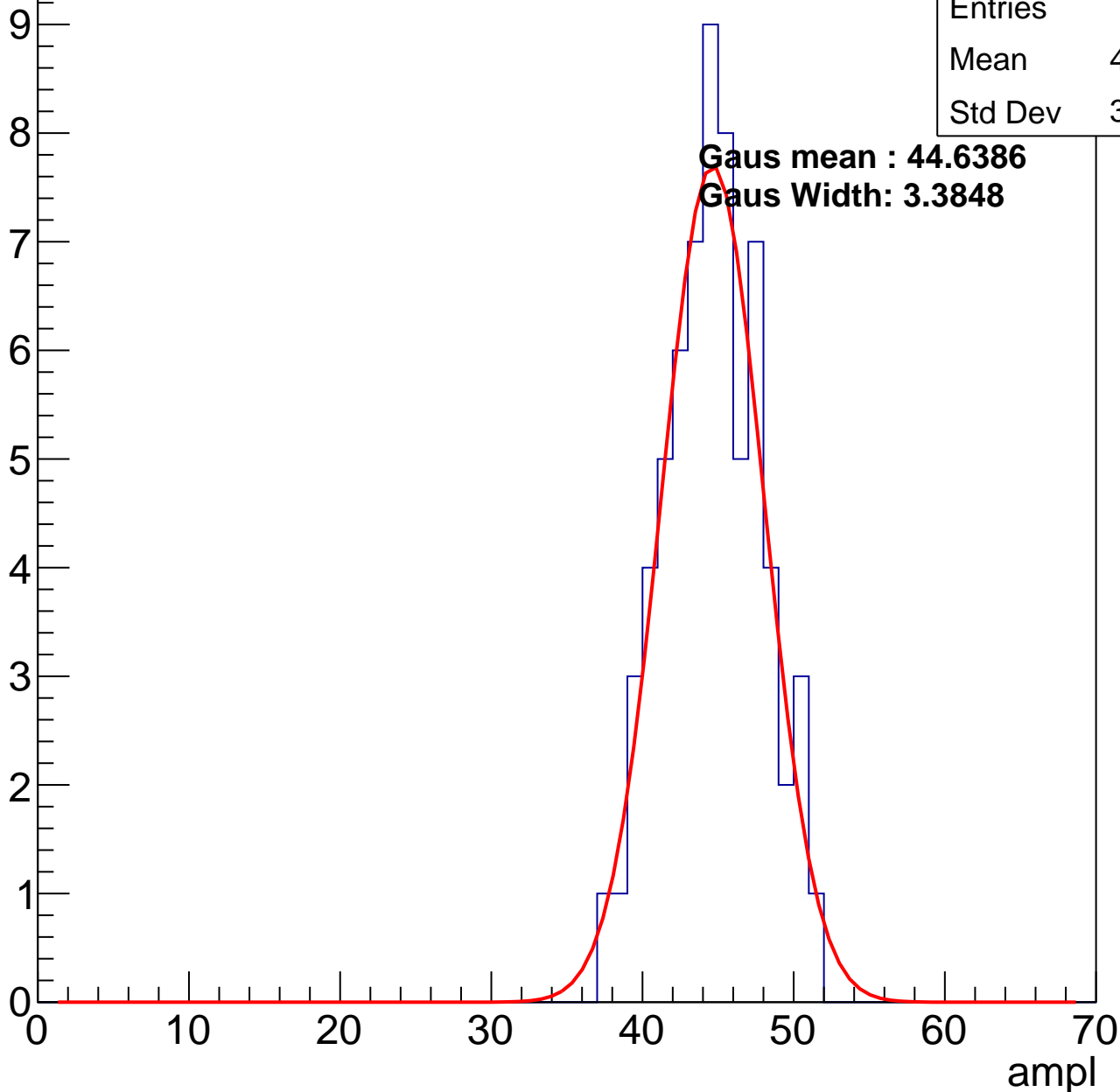
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.18
Std Dev	3.162

Gaus mean : 44.6386

Gaus Width: 3.3848



B0L001S, U23-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

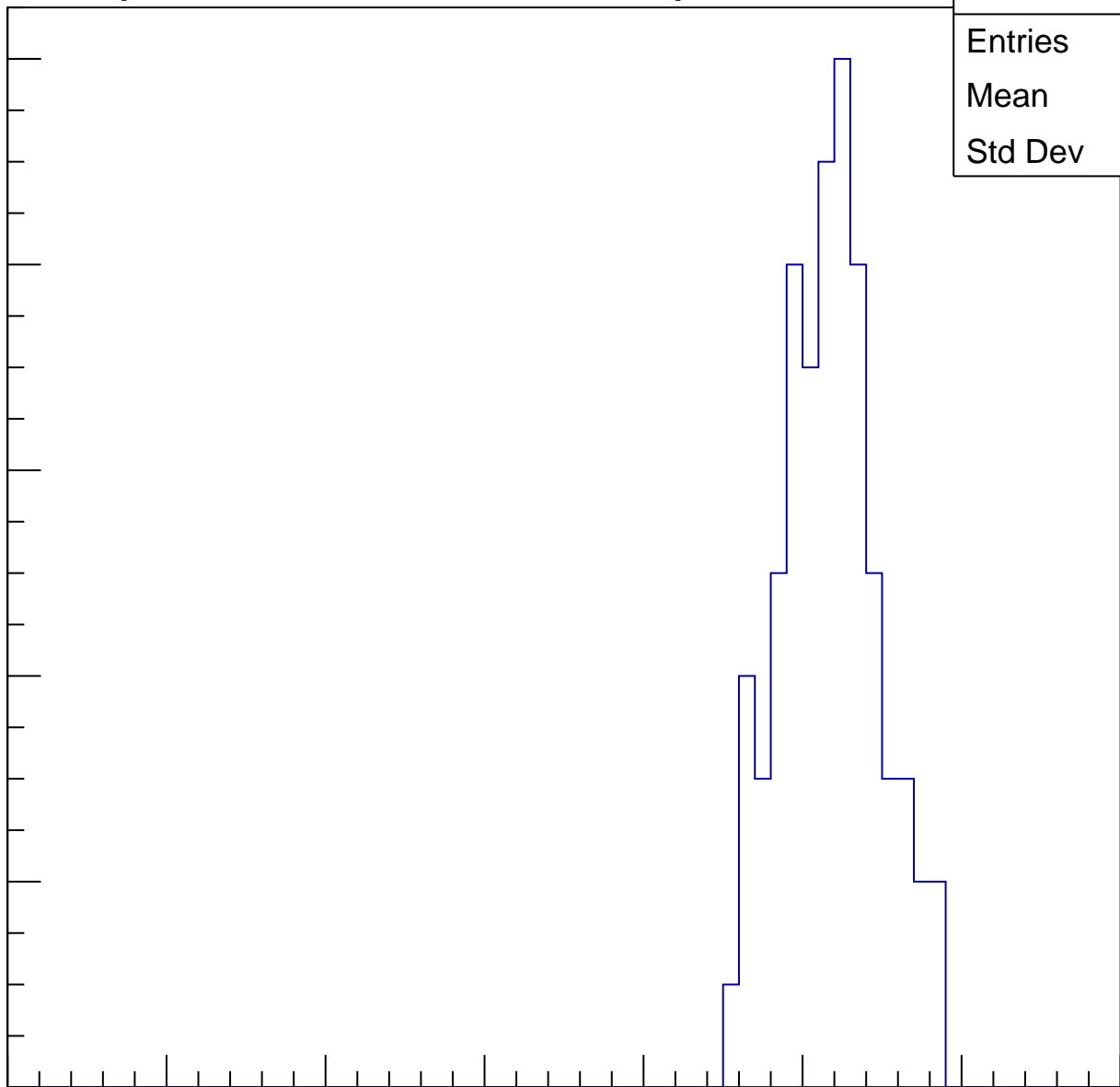
Entries	70
Mean	51.26
Std Dev	3.032

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

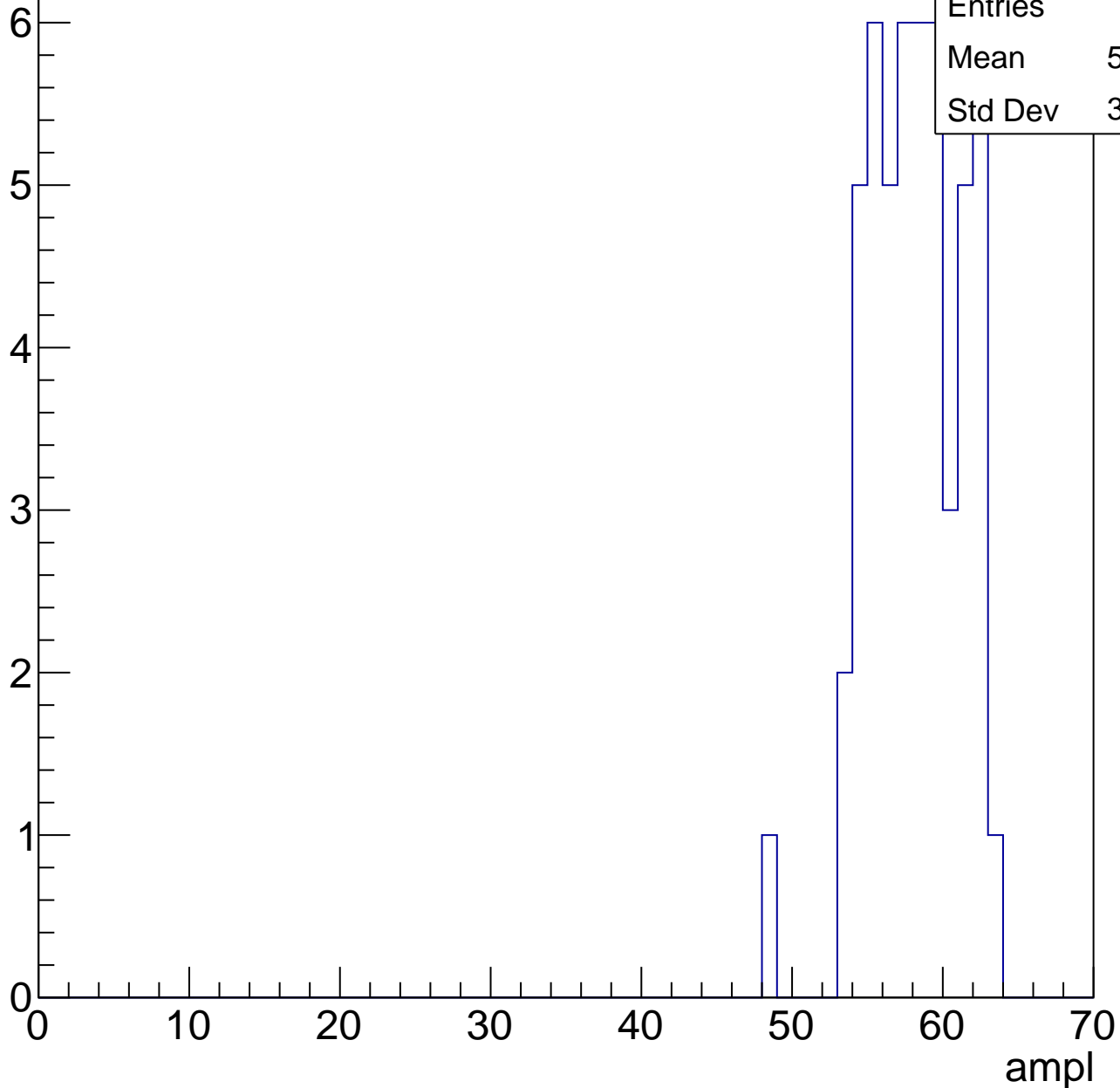
ampl



B0L001S, U23-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

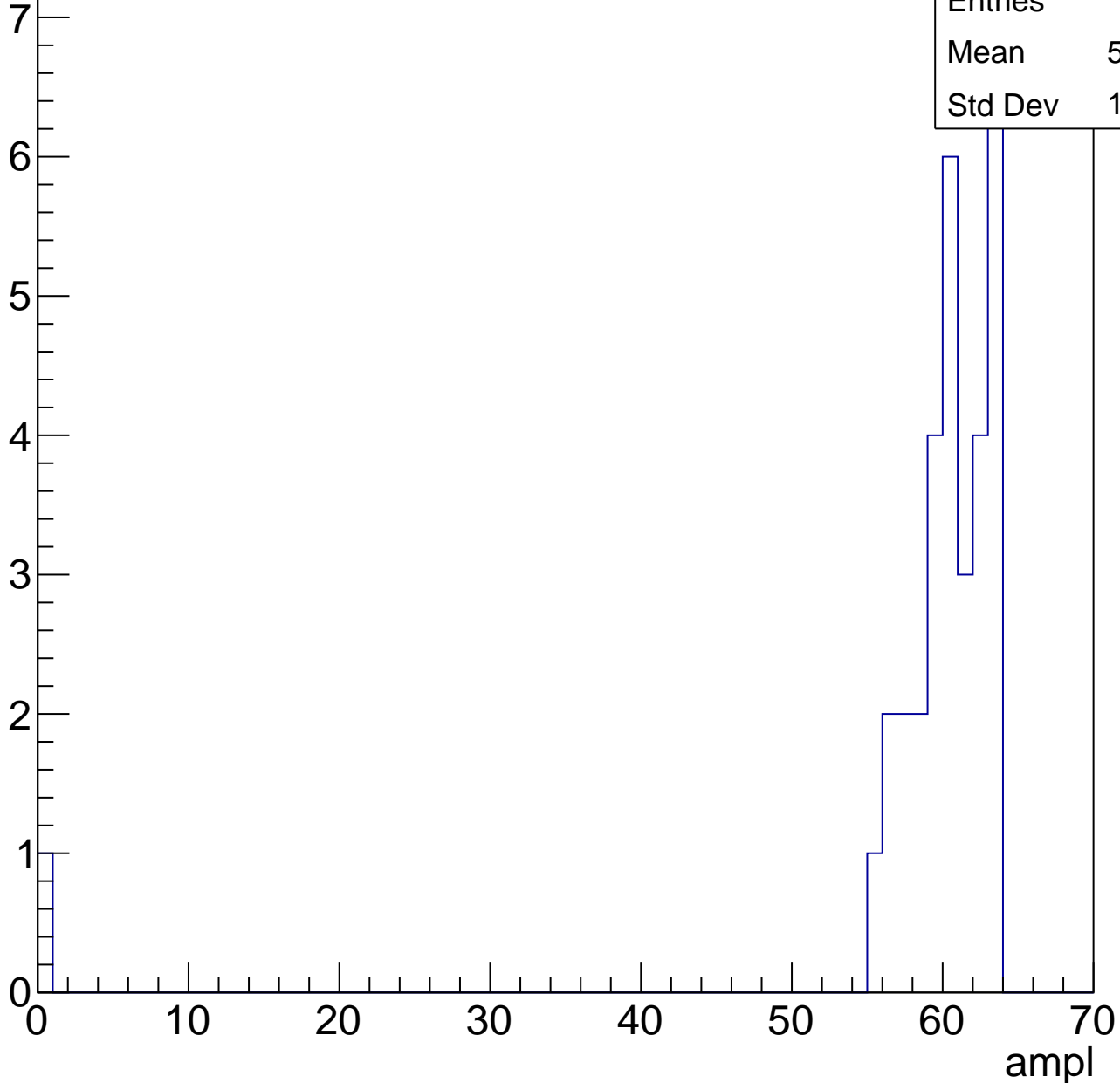


B0L001S, U23-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.28
Std Dev	10.72



B0L001S, U23-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.2

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch109, adc0

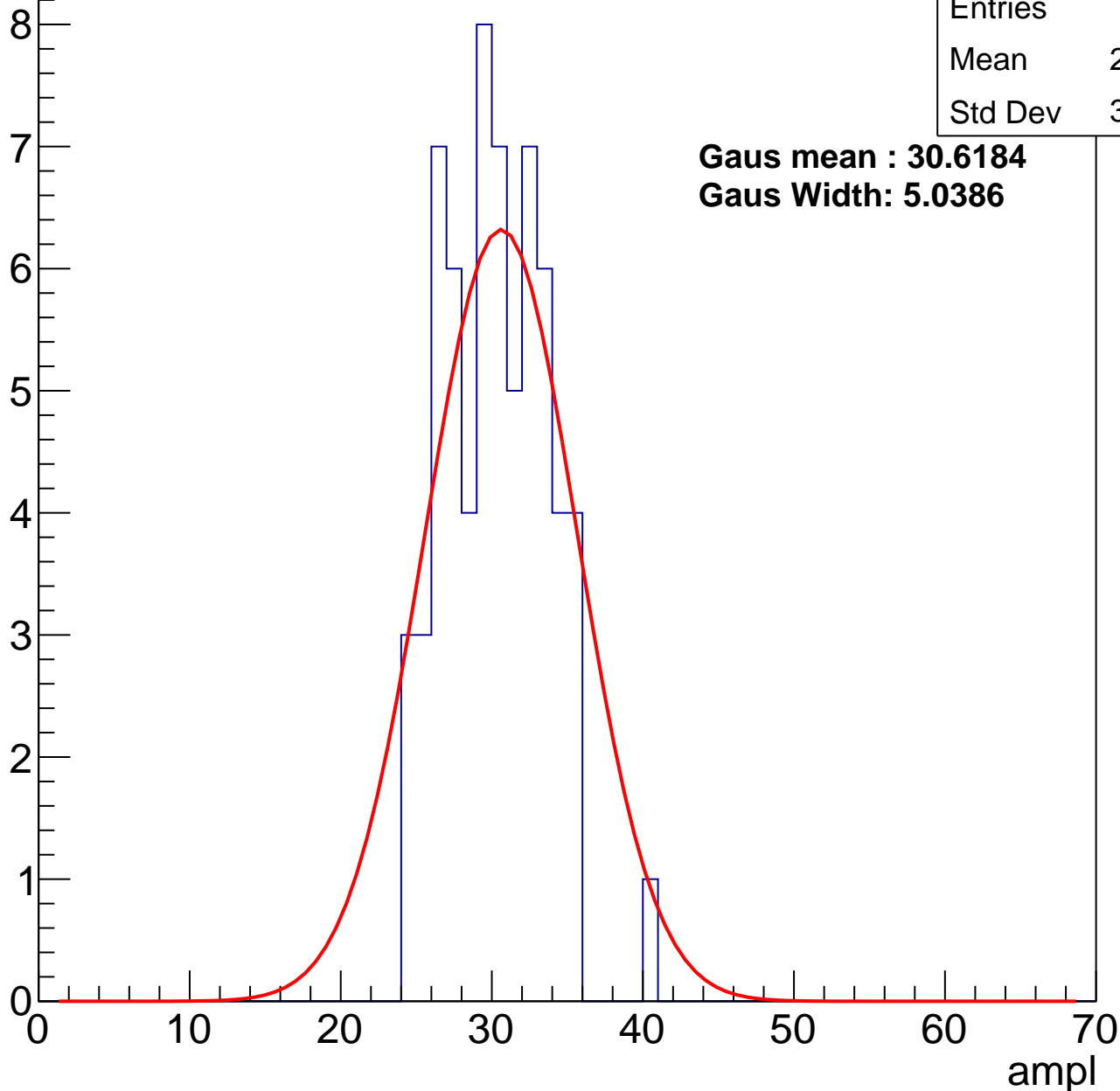
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.82
Std Dev	3.332

Gaus mean : 30.6184

Gaus Width: 5.0386



B0L001S, U23-ch109, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	37.4
Std Dev	3.774

Gaus mean : 37.7898

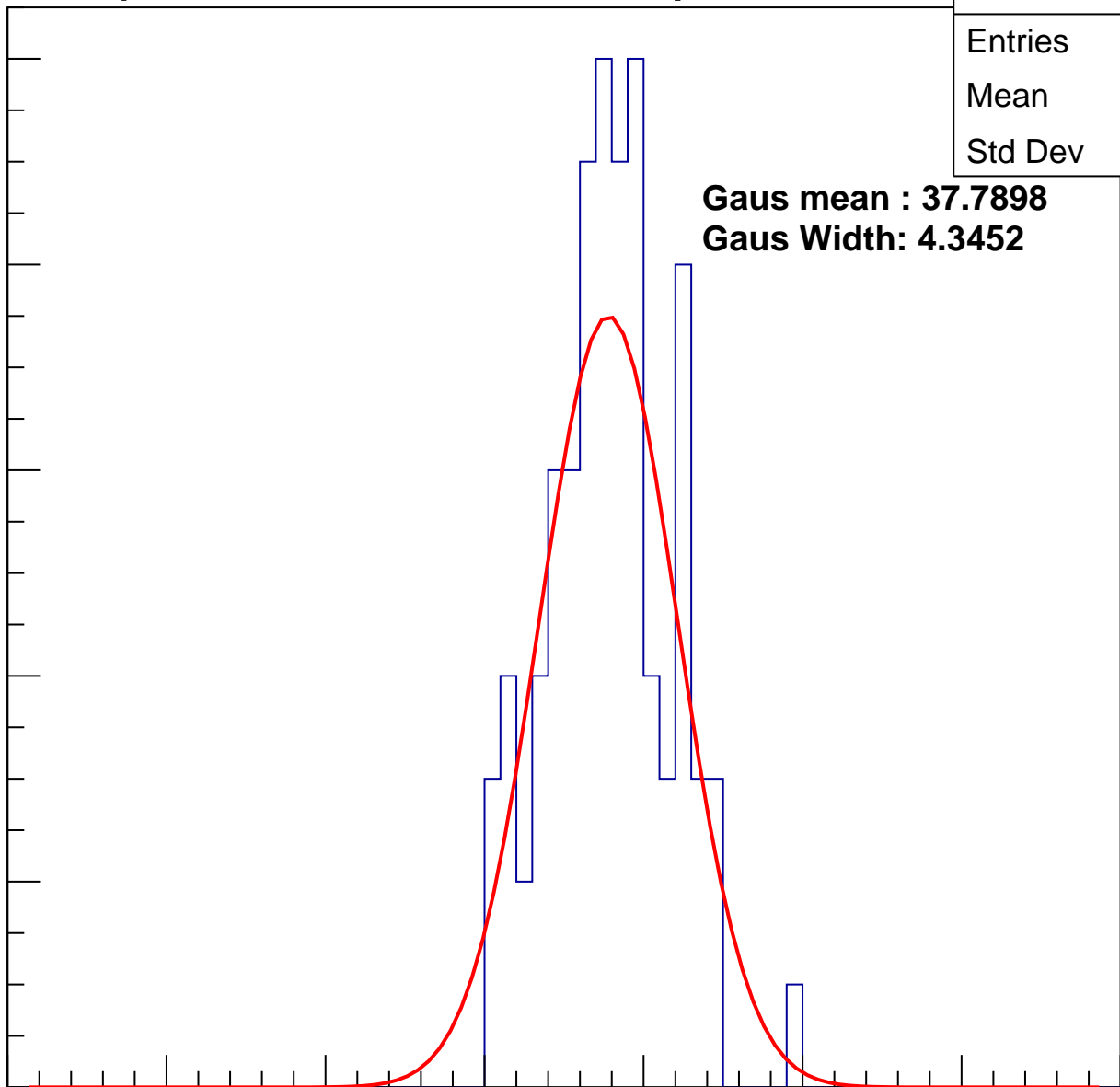
Gaus Width: 4.3452

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch109, adc2

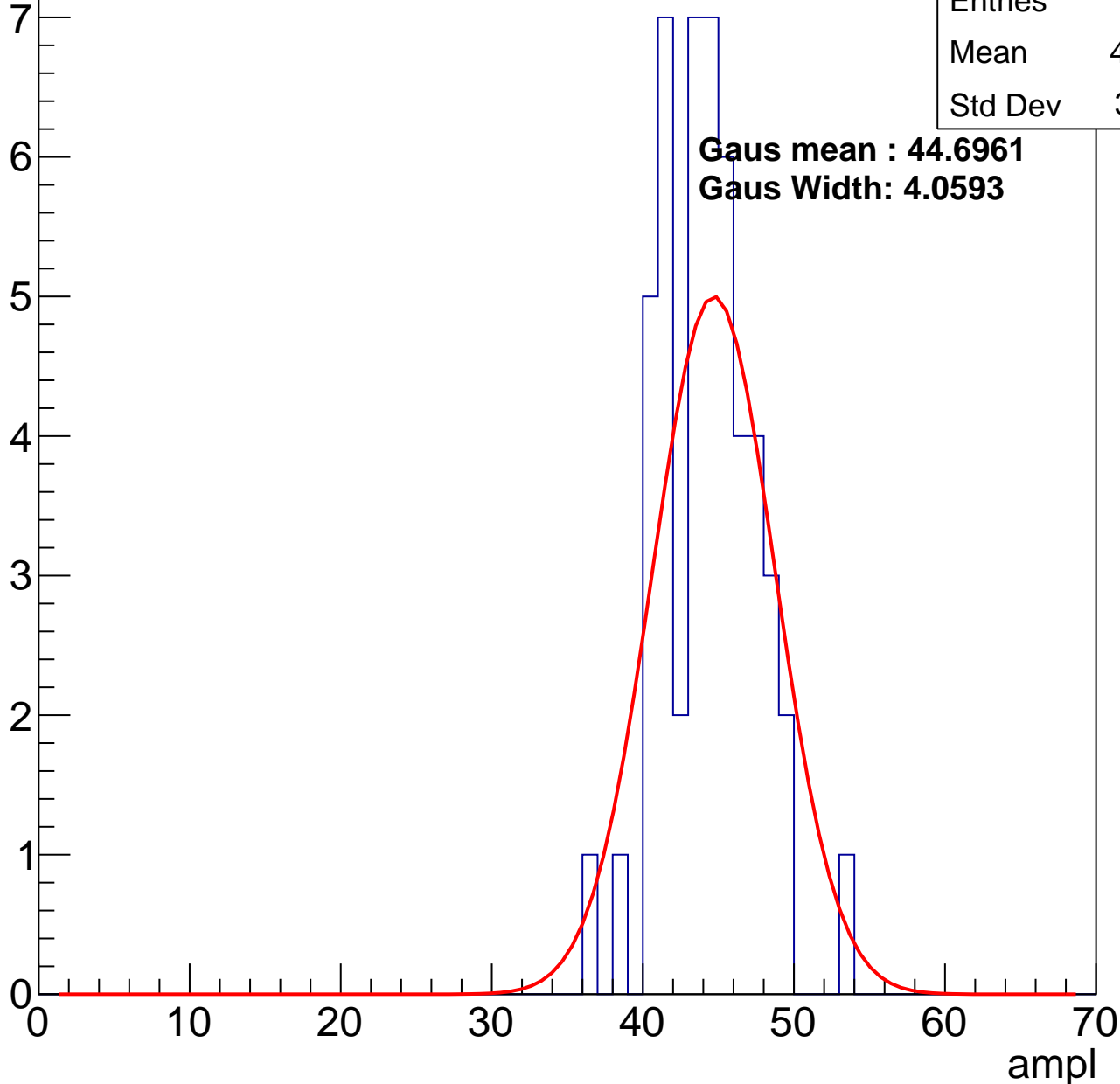
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	43.82
Std Dev	3.141

Gaus mean : 44.6961

Gaus Width: 4.0593

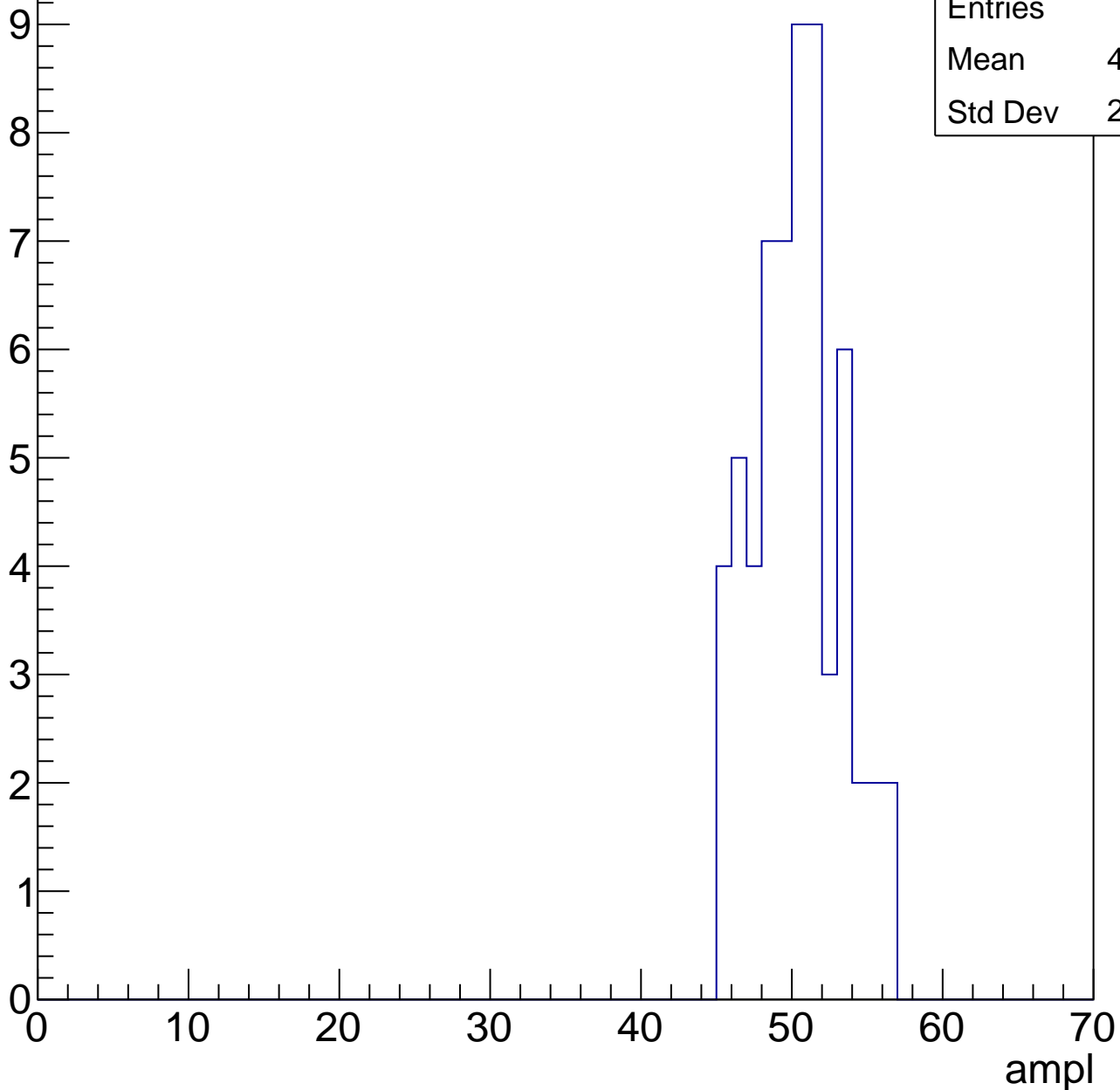


B0L001S, U23-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	49.83
Std Dev	2.824

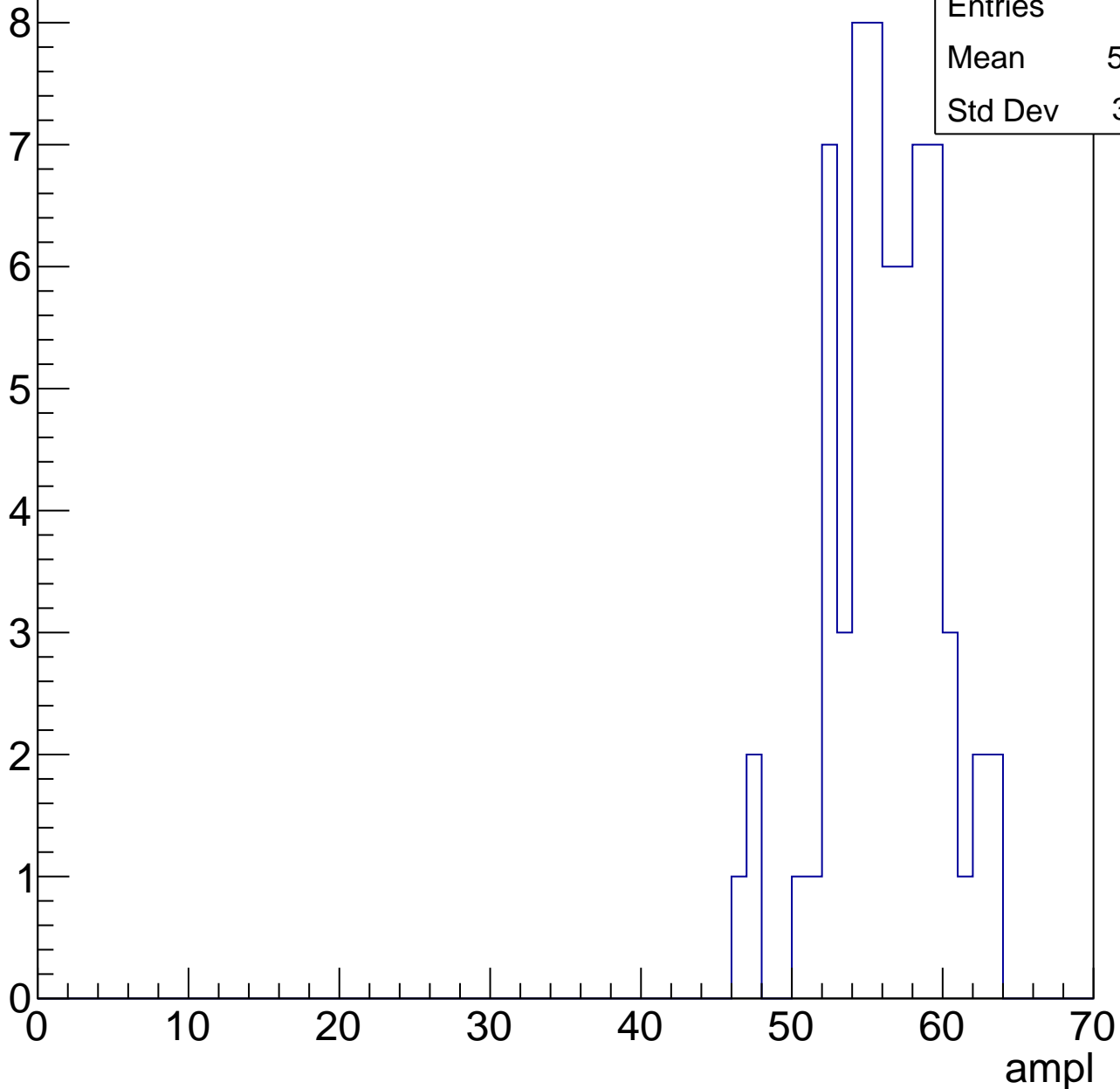


B0L001S, U23-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	55.75
Std Dev	3.591

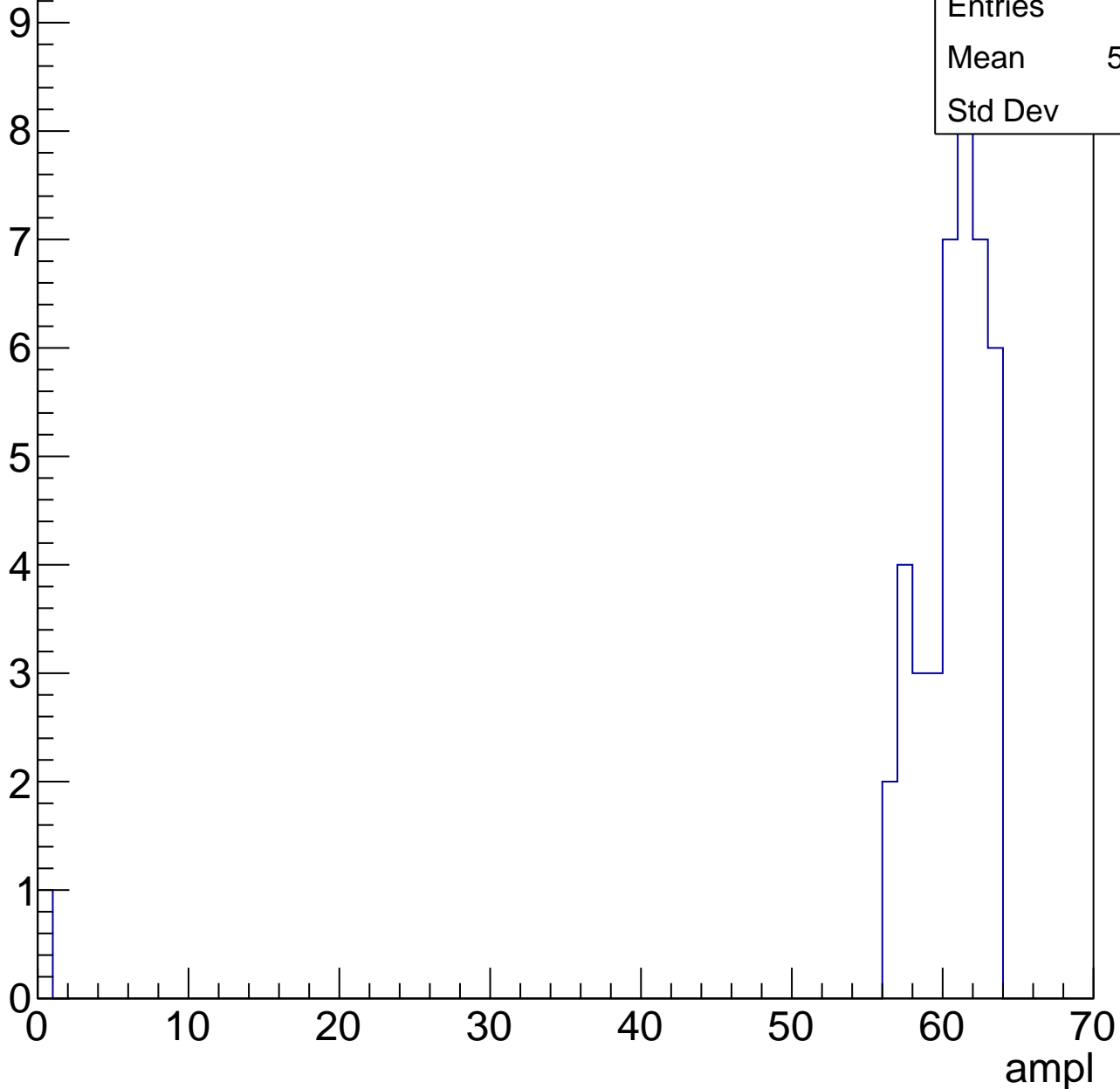


B0L001S, U23-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.86
Std Dev	9.41



B0L001S, U23-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch110, adc0

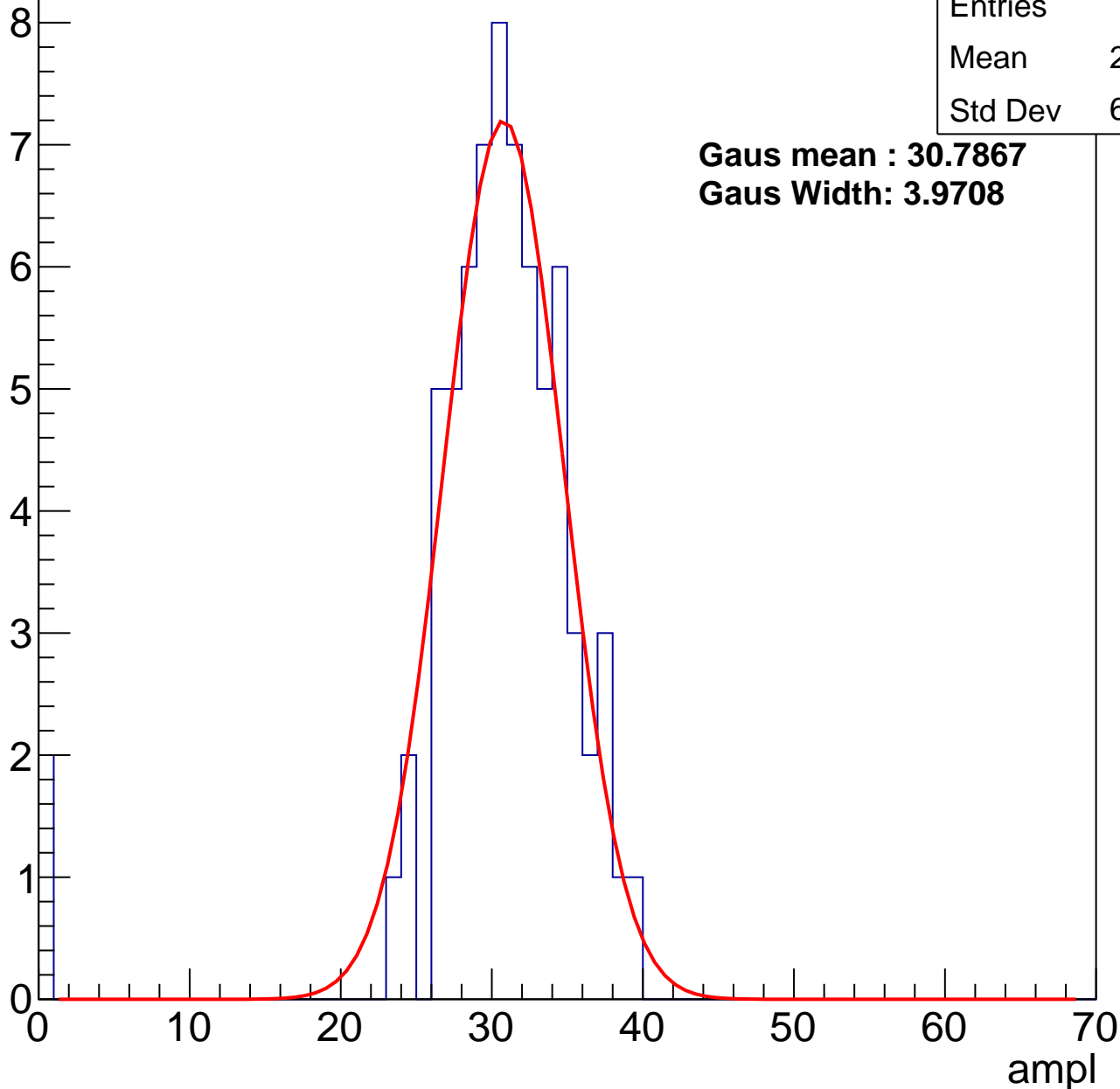
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.86
Std Dev	6.195

Gaus mean : 30.7867

Gaus Width: 3.9708



B0L001S, U23-ch110, adc1

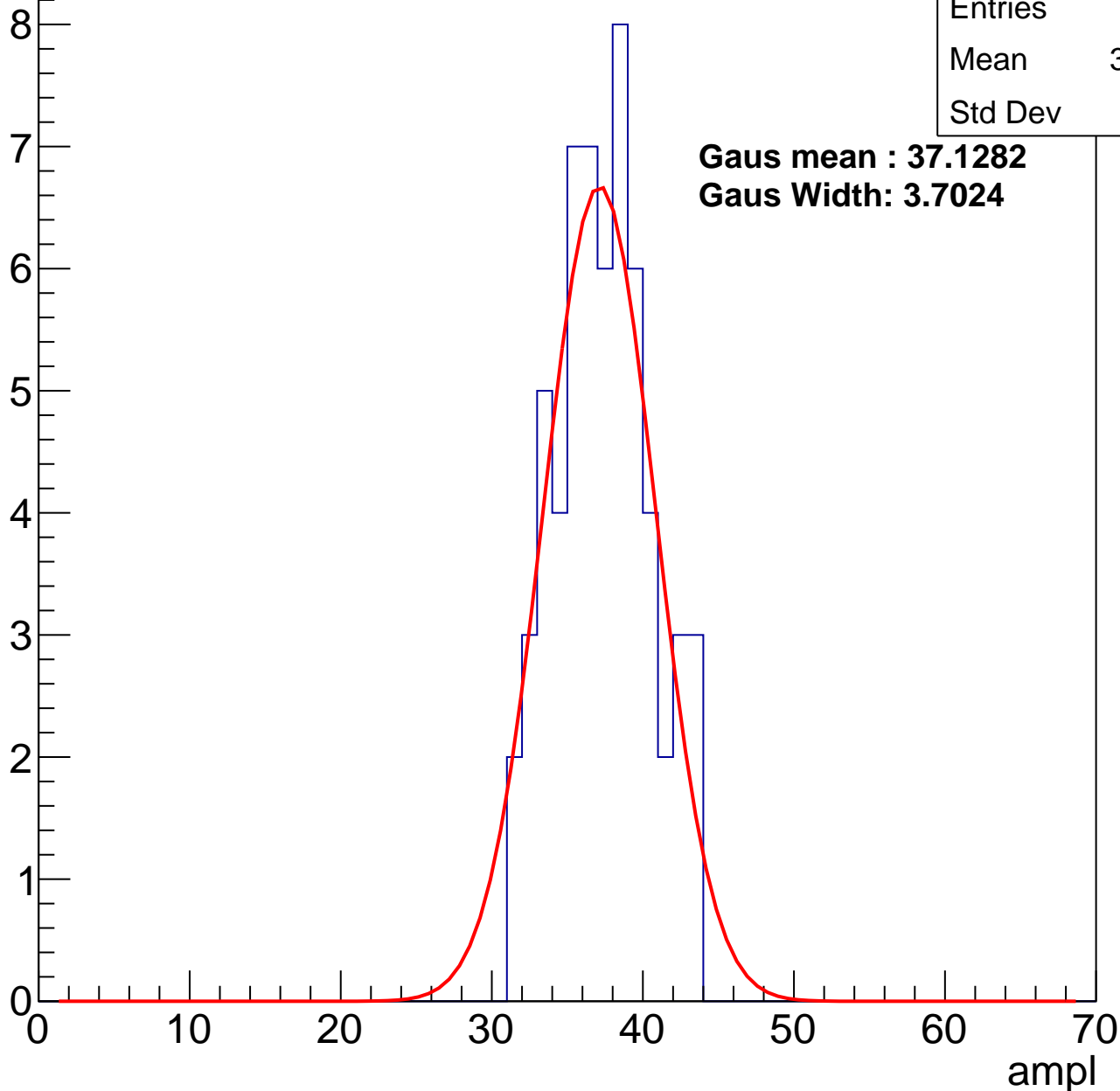
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.88
Std Dev	3.11

Gaus mean : 37.1282

Gaus Width: 3.7024

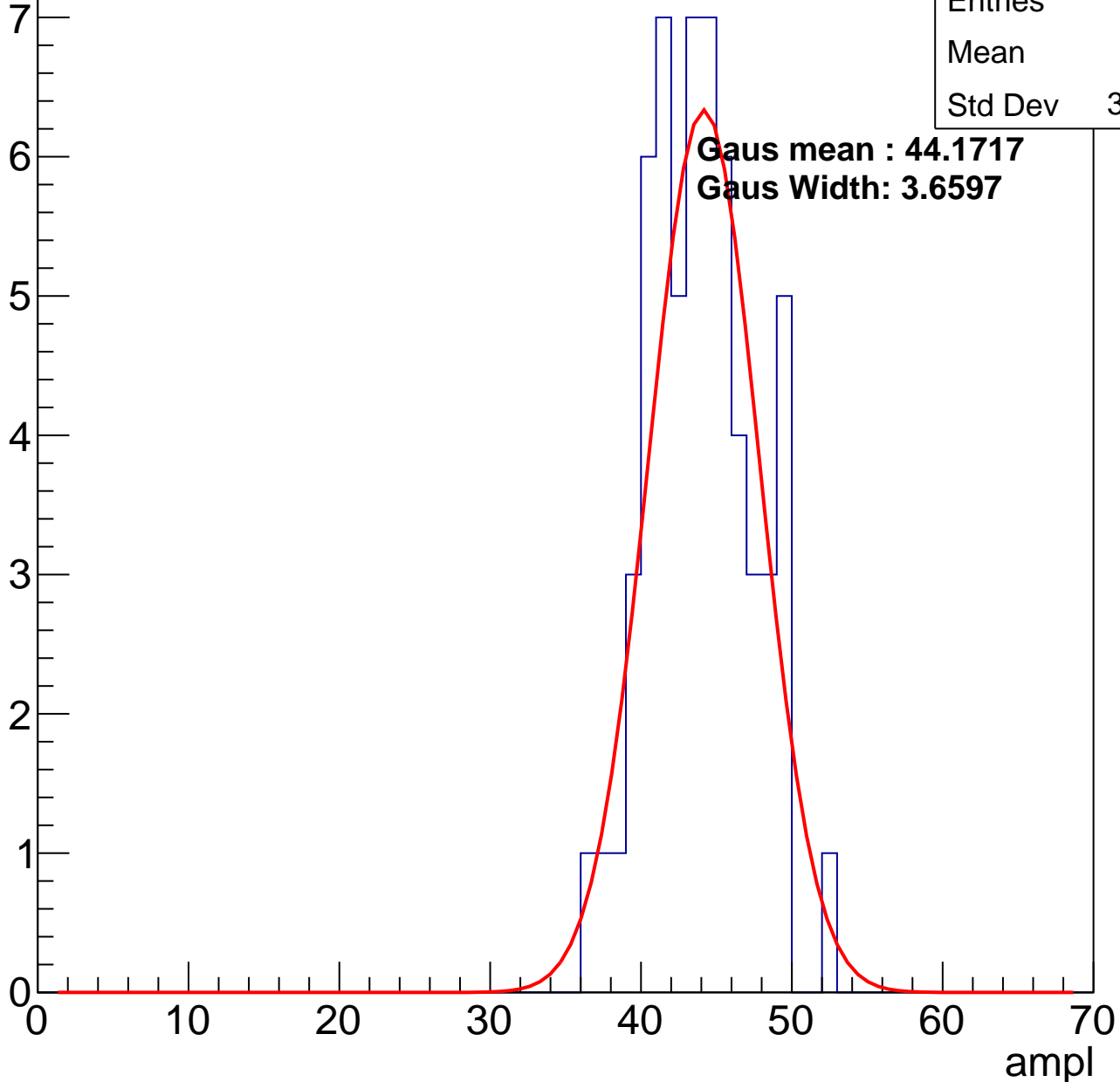


B0L001S, U23-ch110, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.5
Std Dev	3.369



B0L001S, U23-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	49.96
Std Dev	3.694

Entry

10

8

6

4

2

0

0

10

20

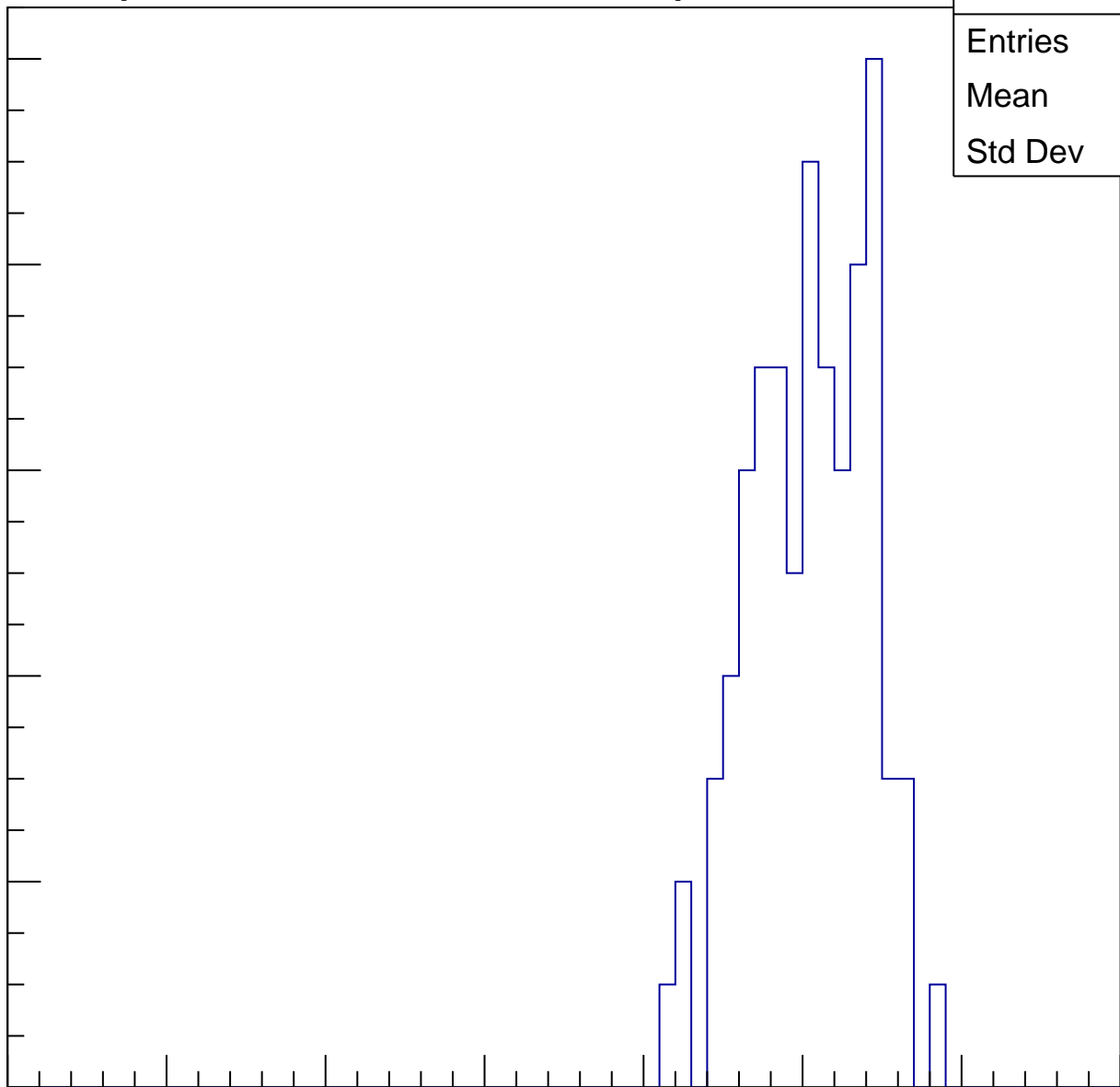
30

40

50

60

ampl

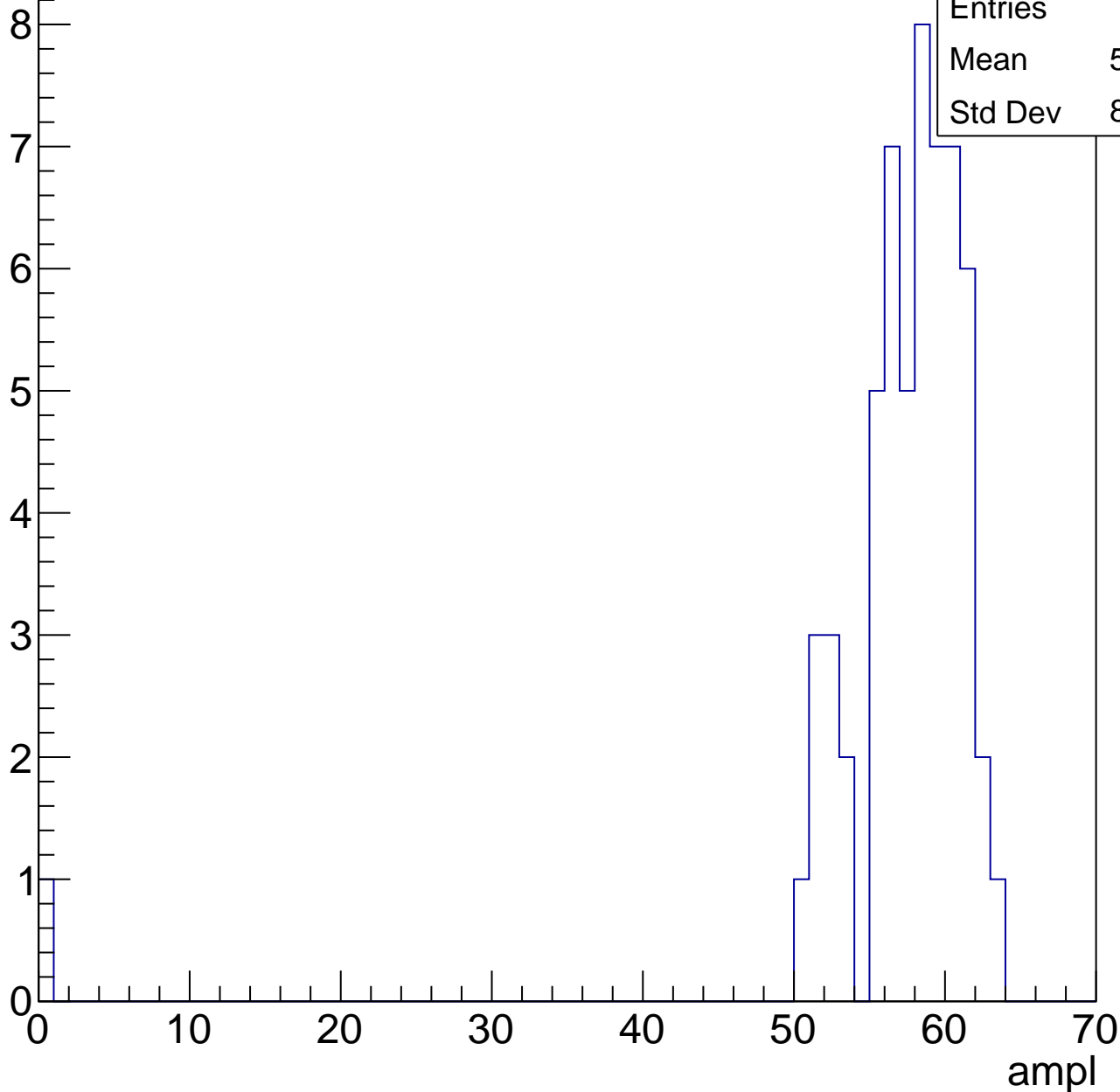


B0L001S, U23-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.33
Std Dev	8.089

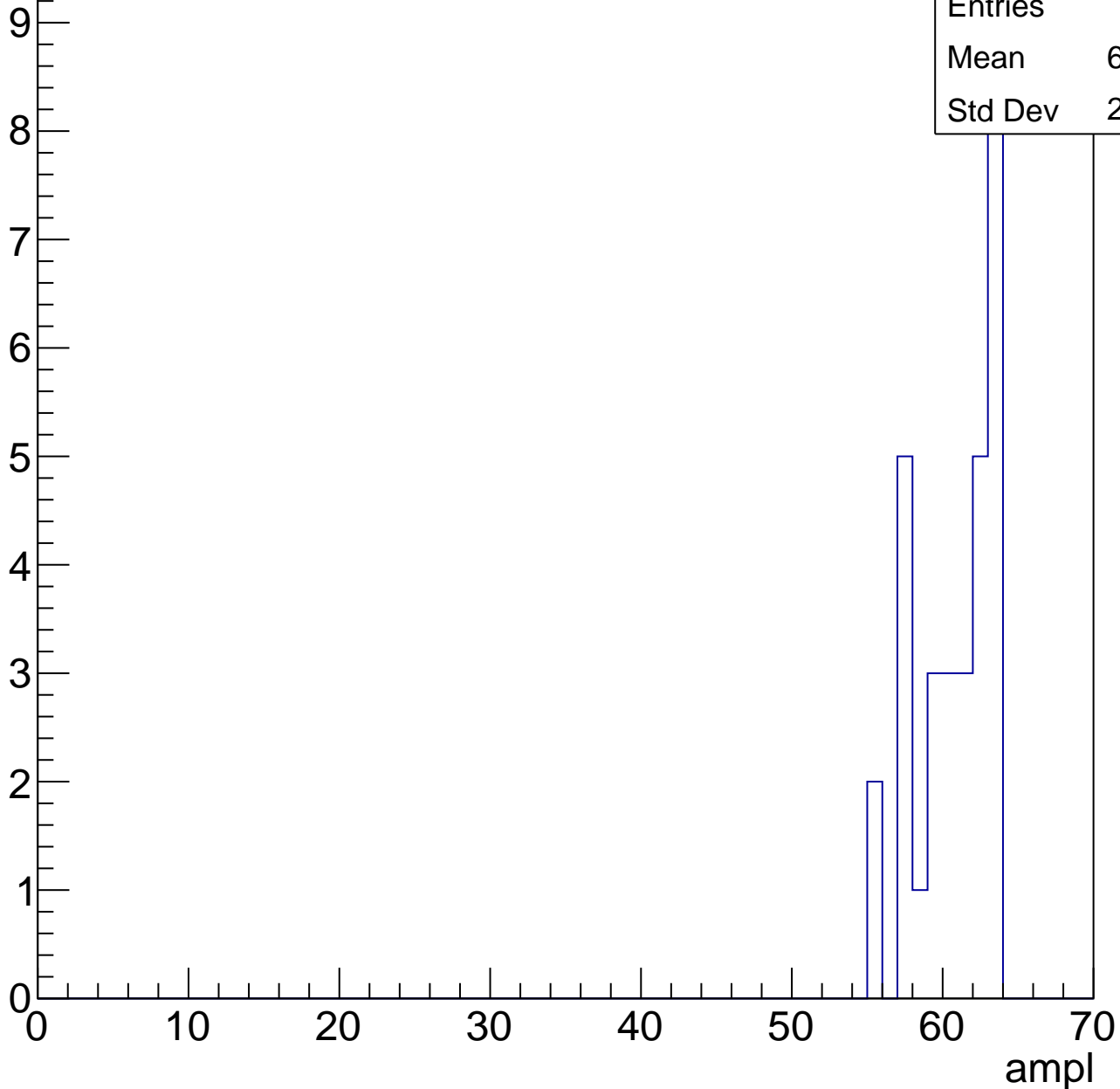


B0L001S, U23-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

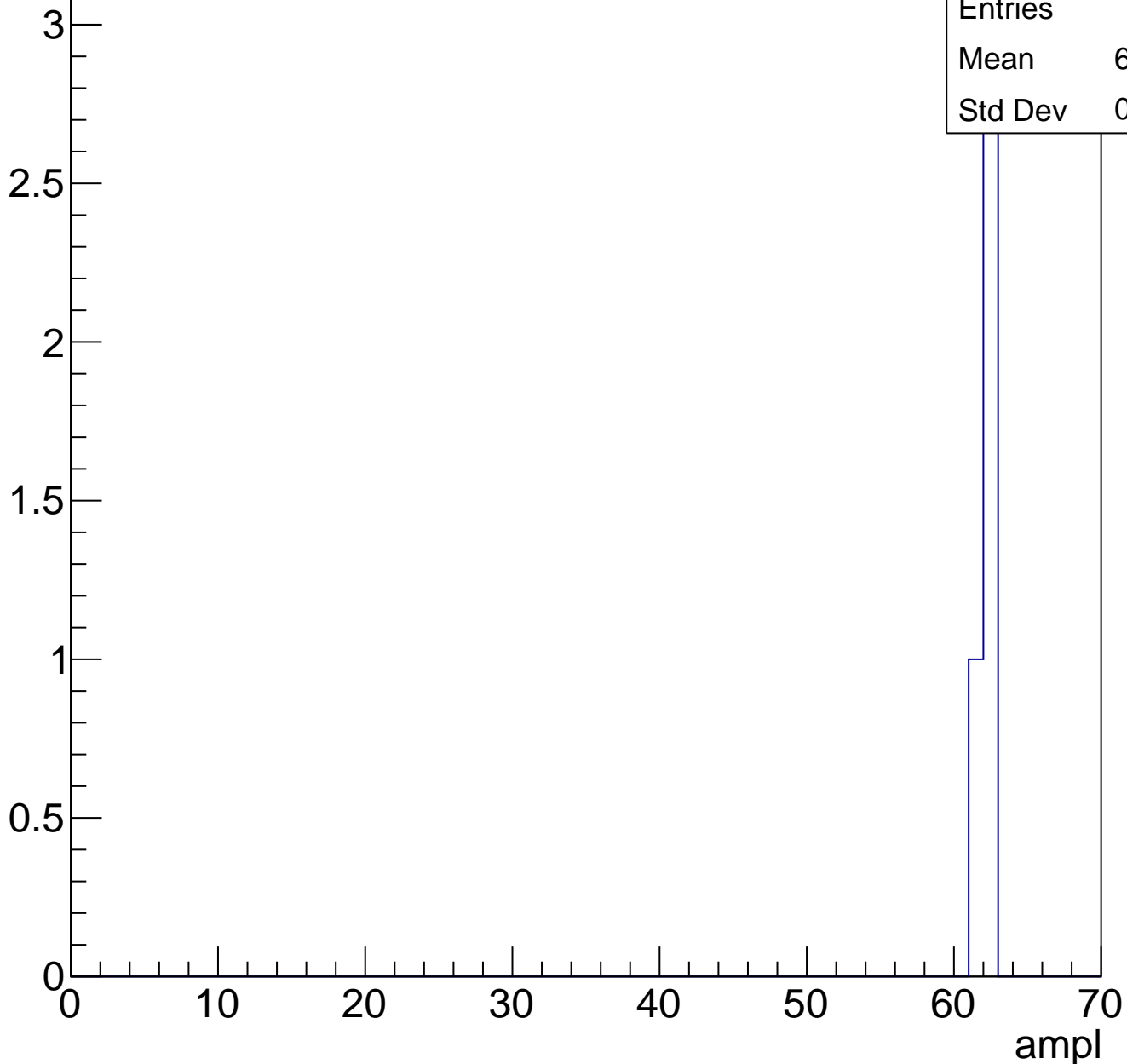
Entries	31
Mean	60.32
Std Dev	2.558



B0L001S, U23-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch111, adc0

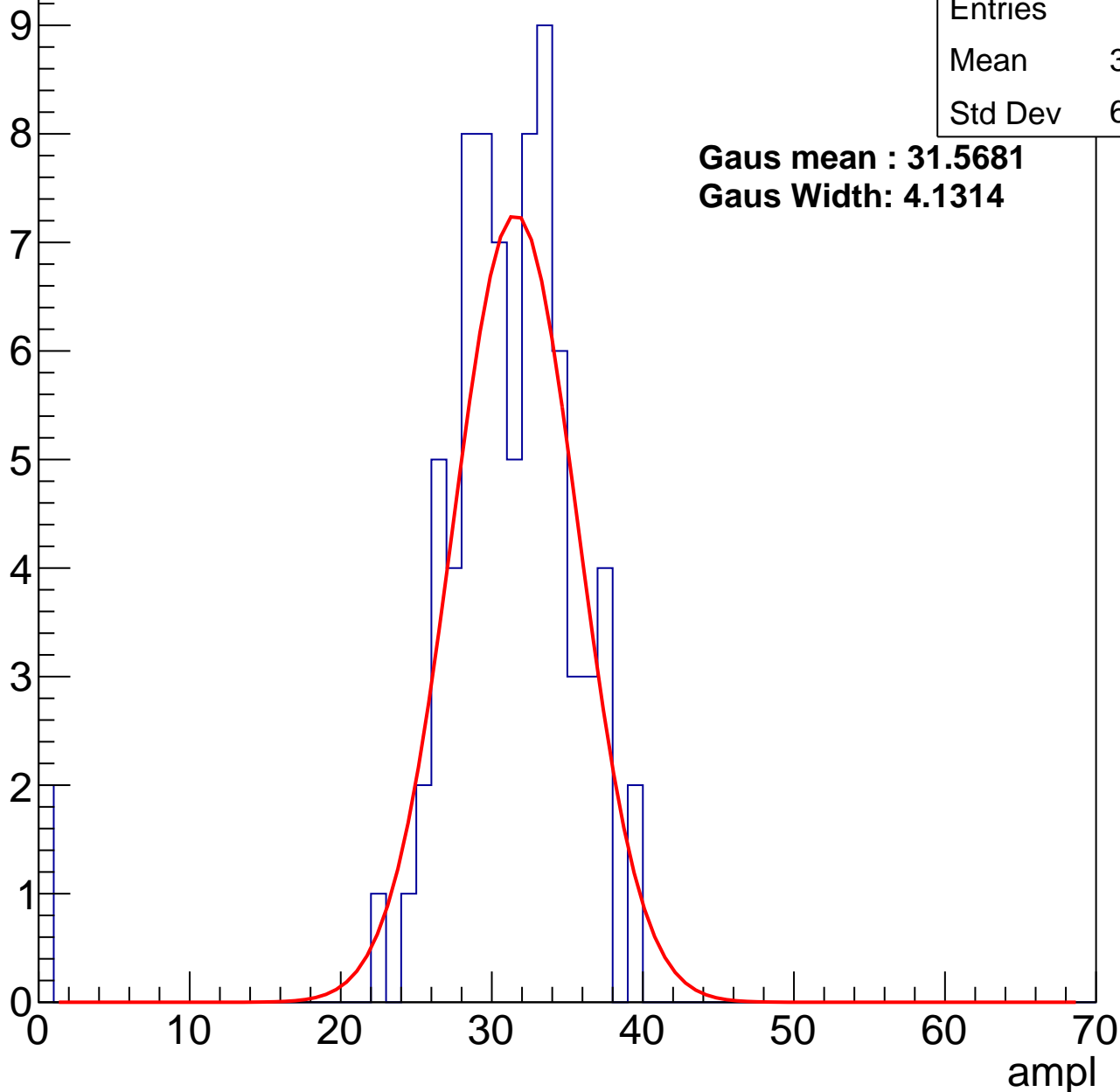
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.14
Std Dev	6.055

Gaus mean : 31.5681

Gaus Width: 4.1314



B0L001S, U23-ch111, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	38.38
Std Dev	3.415

Gaus mean : 39.0498

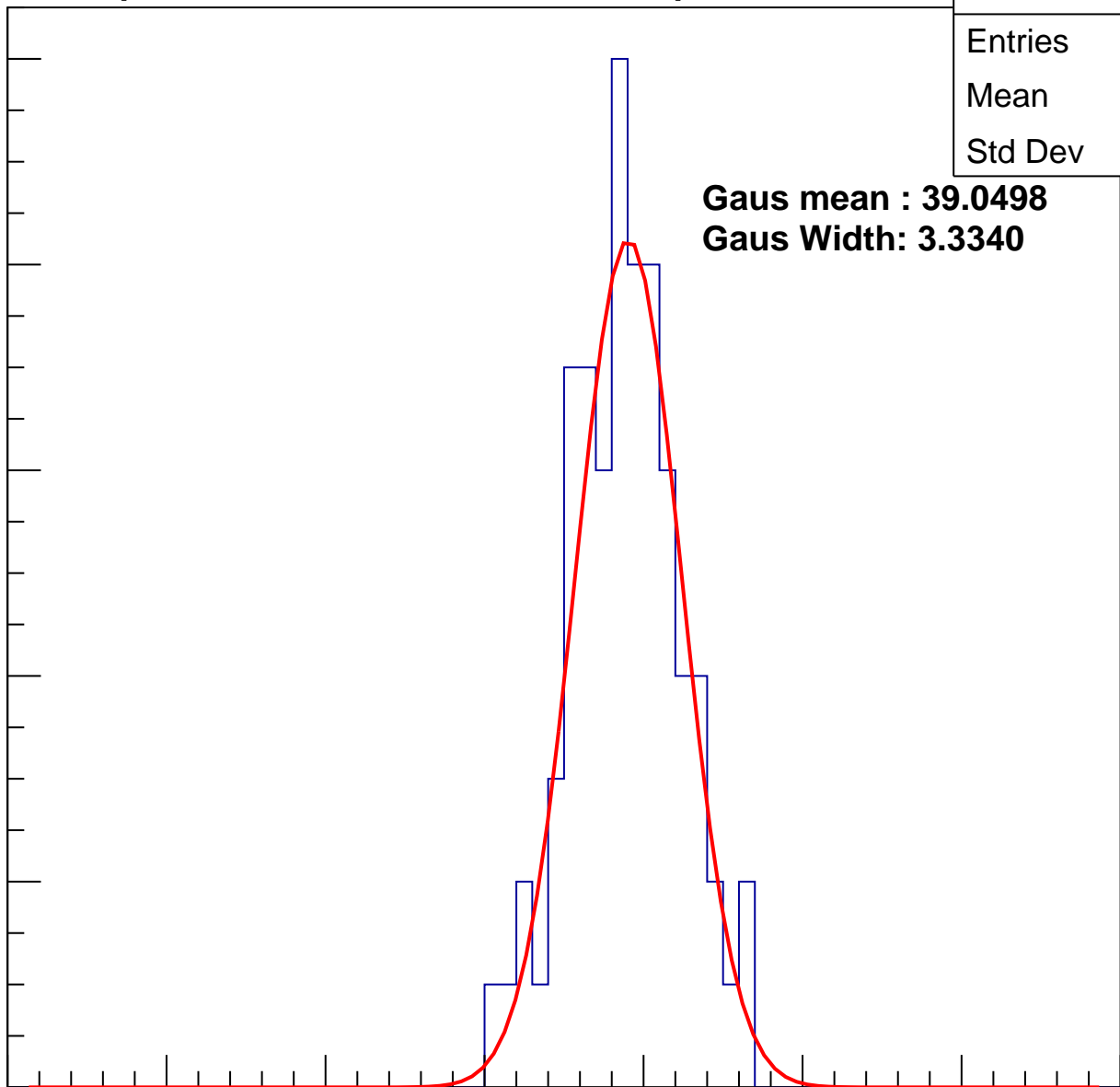
Gaus Width: 3.3340

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch111, adc2

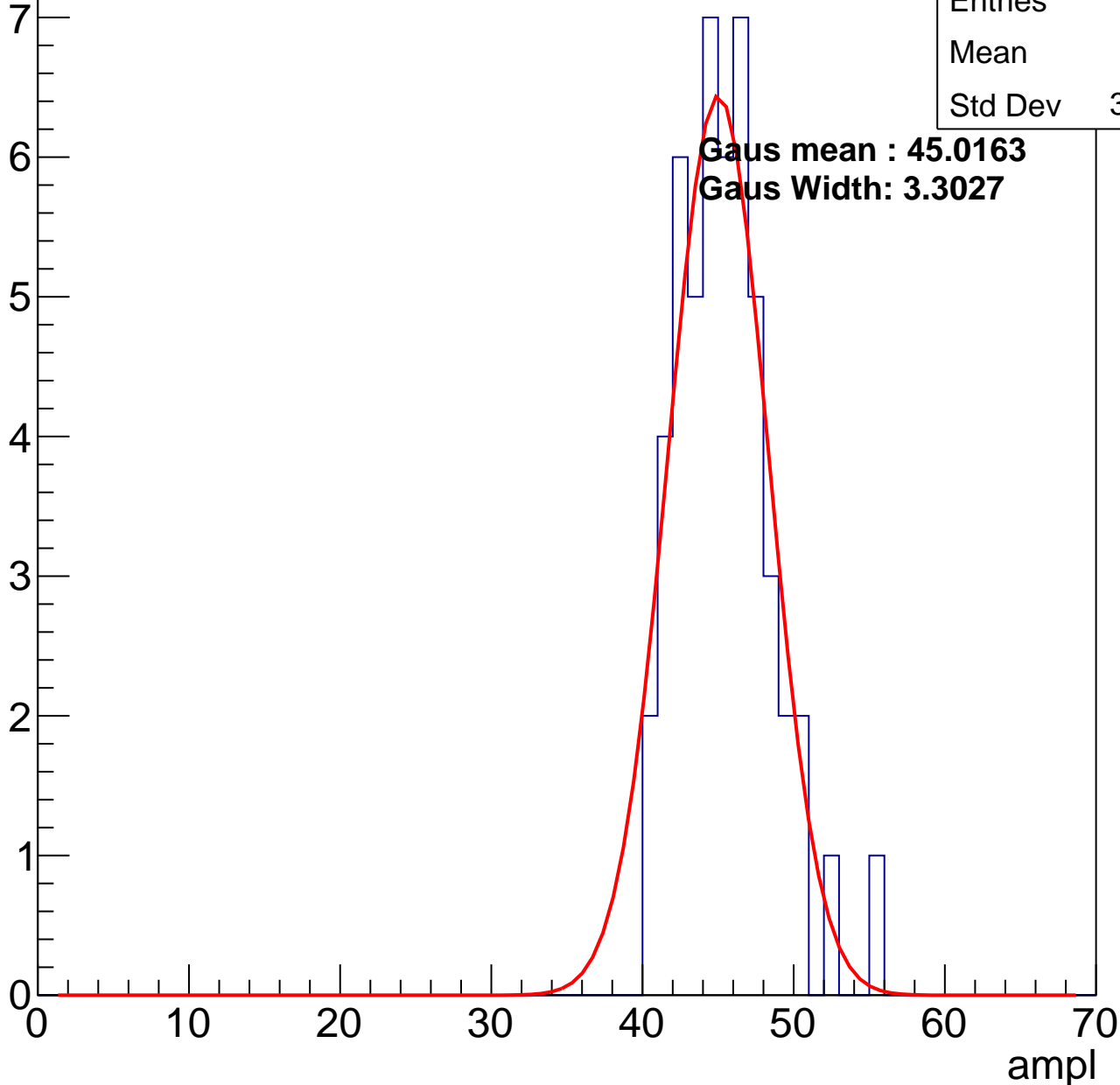
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	45
Std Dev	3.068

Gaus mean : 45.0163

Gaus Width: 3.3027

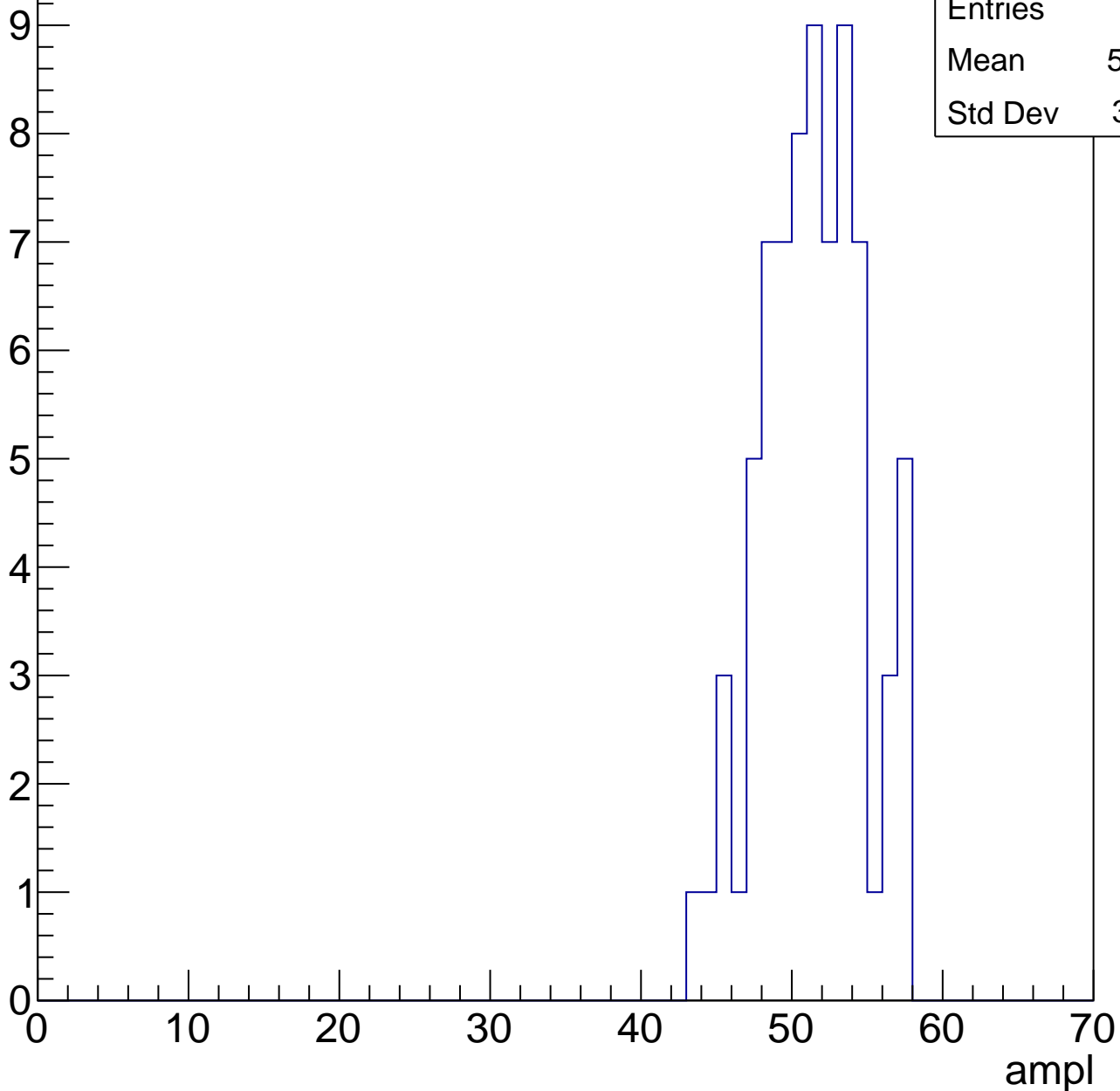


B0L001S, U23-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	50.92
Std Dev	3.291

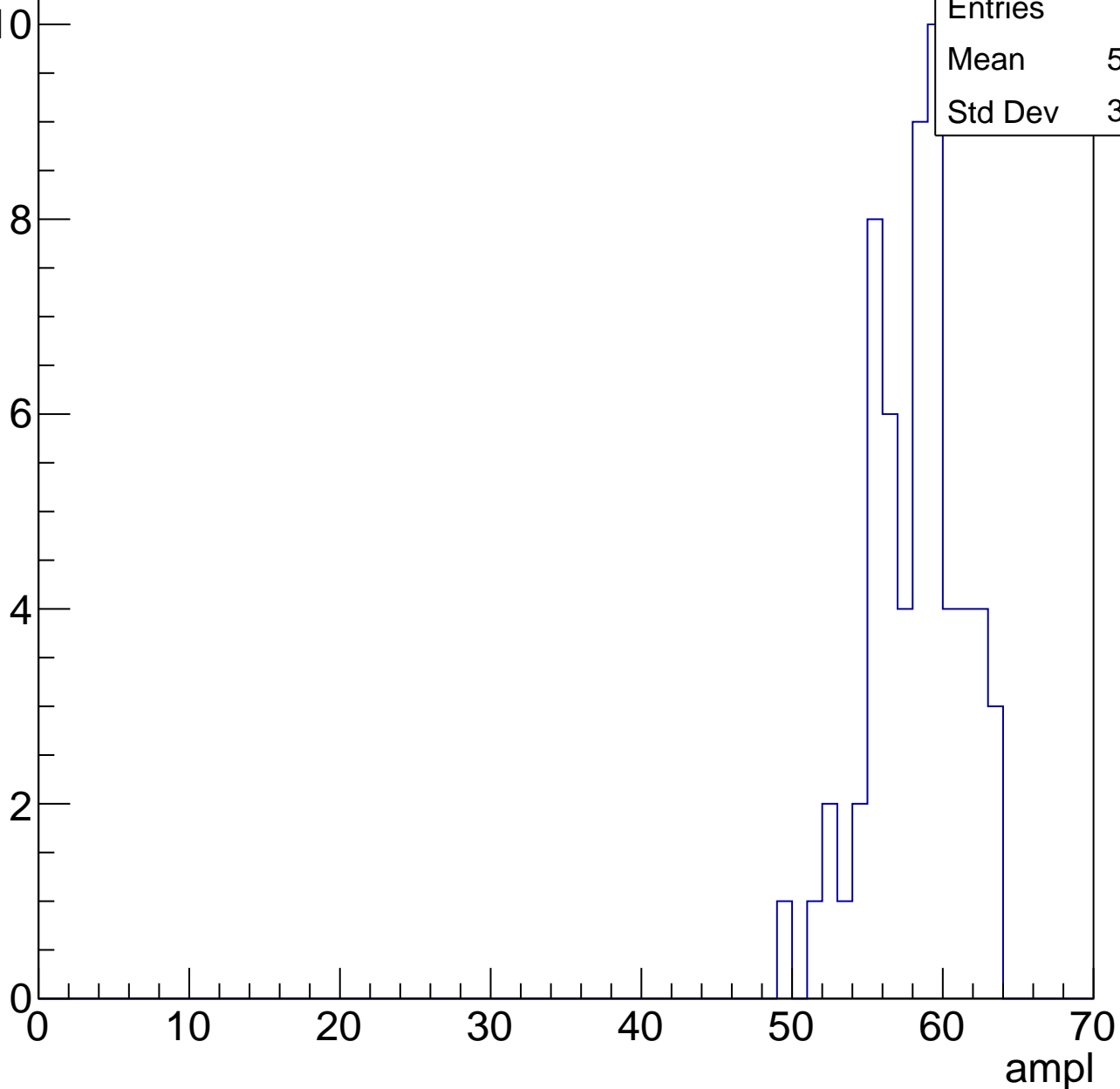


B0L001S, U23-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.66
Std Dev	3.062

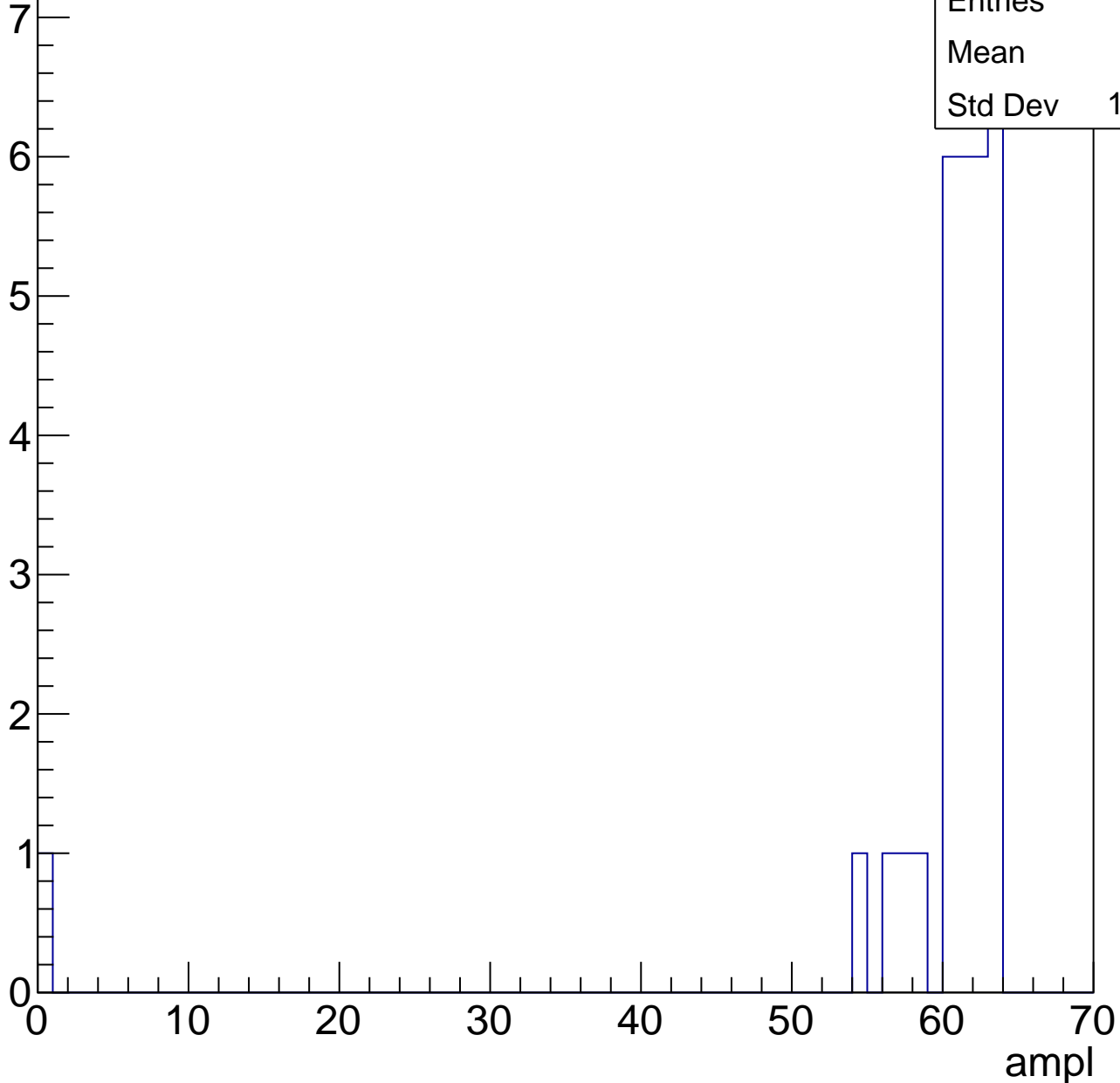


B0L001S, U23-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.8
Std Dev	11.13



B0L001S, U23-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch112, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	29.69
Std Dev	5.012

Gaus mean : 30.9542

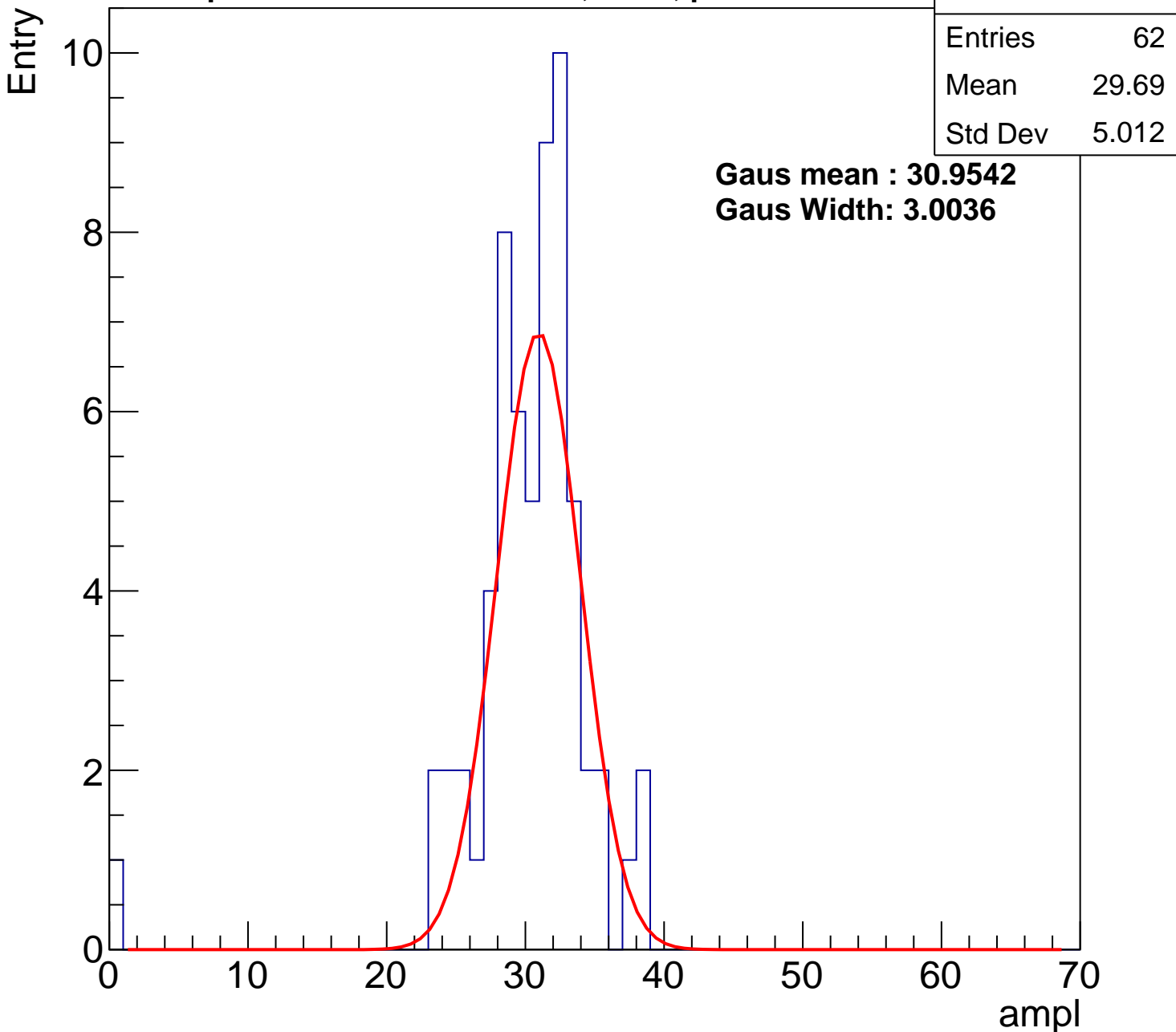
Gaus Width: 3.0036

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch112, adc1

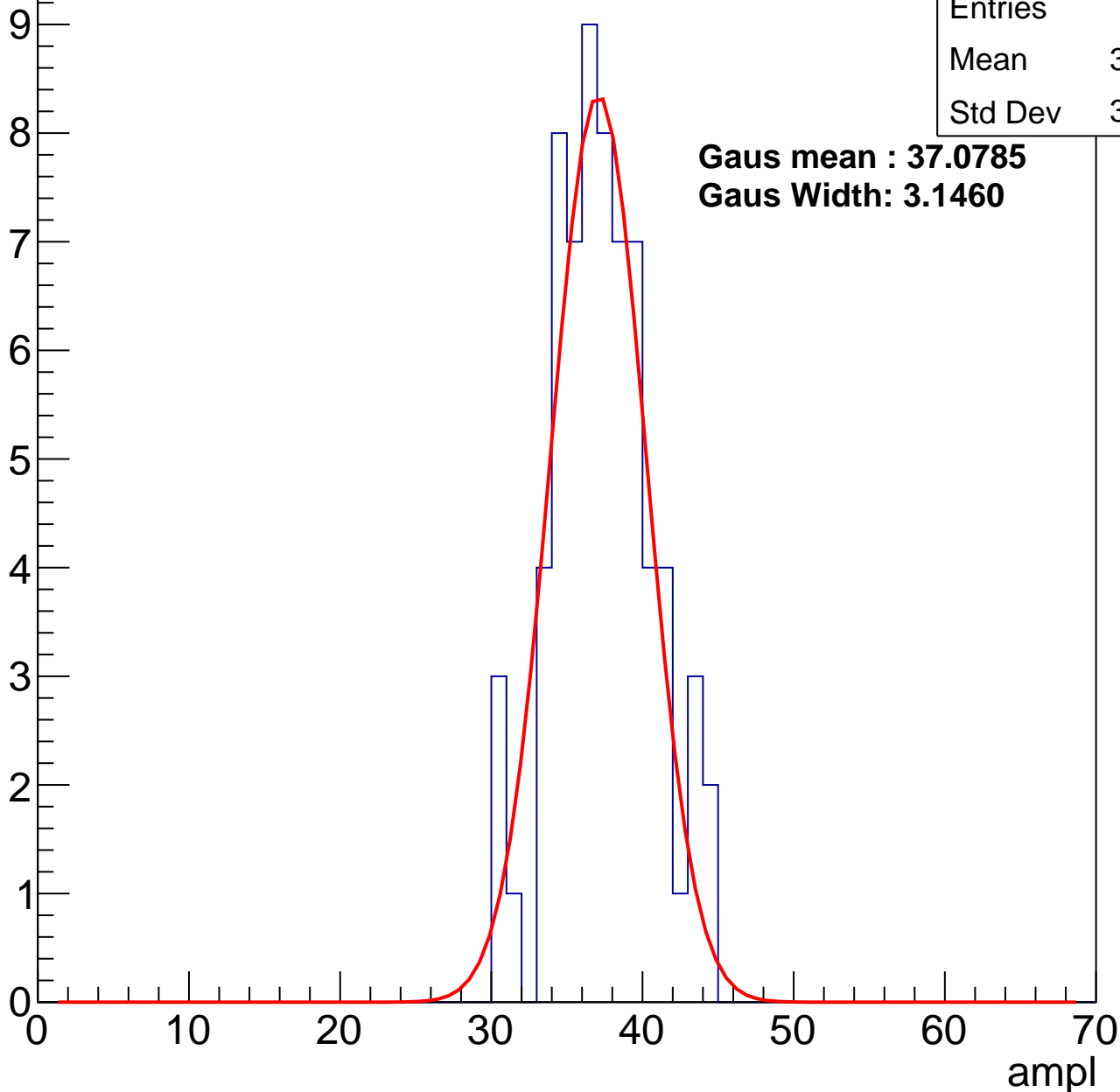
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.94
Std Dev	3.258

Gaus mean : 37.0785

Gaus Width: 3.1460



B0L001S, U23-ch112, adc2

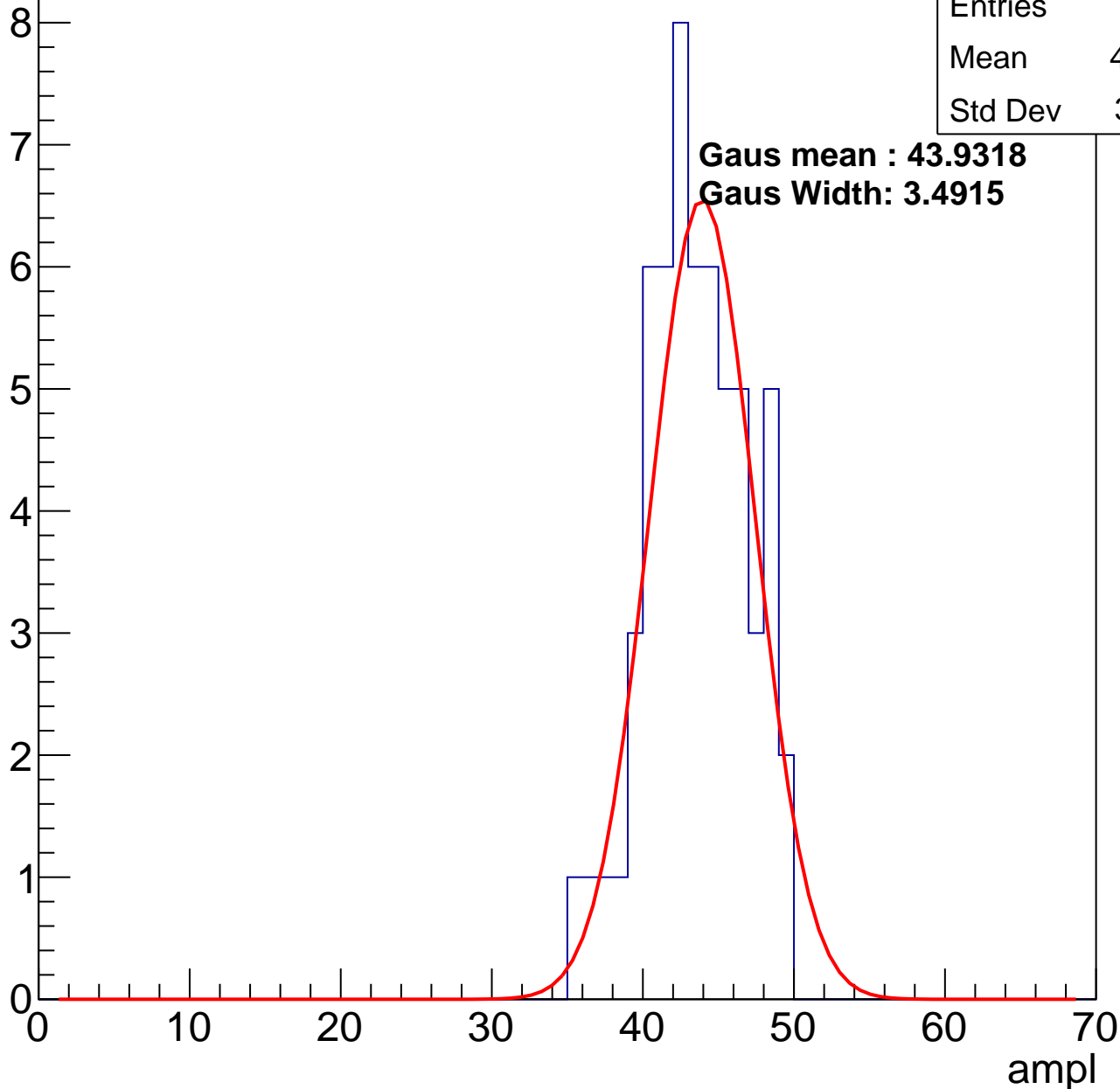
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.07
Std Dev	3.251

Gaus mean : 43.9318

Gaus Width: 3.4915

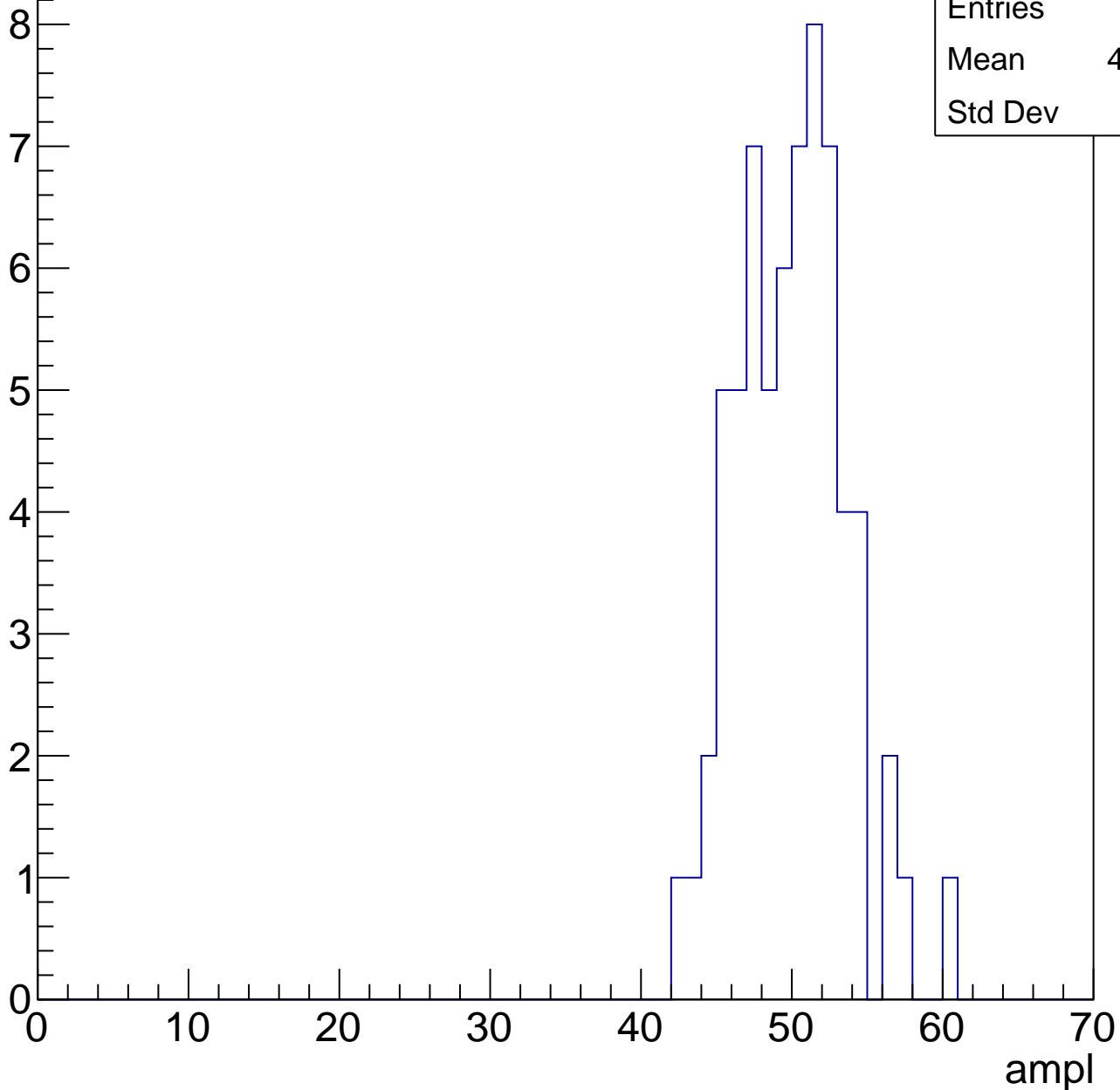


B0L001S, U23-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.55
Std Dev	3.53

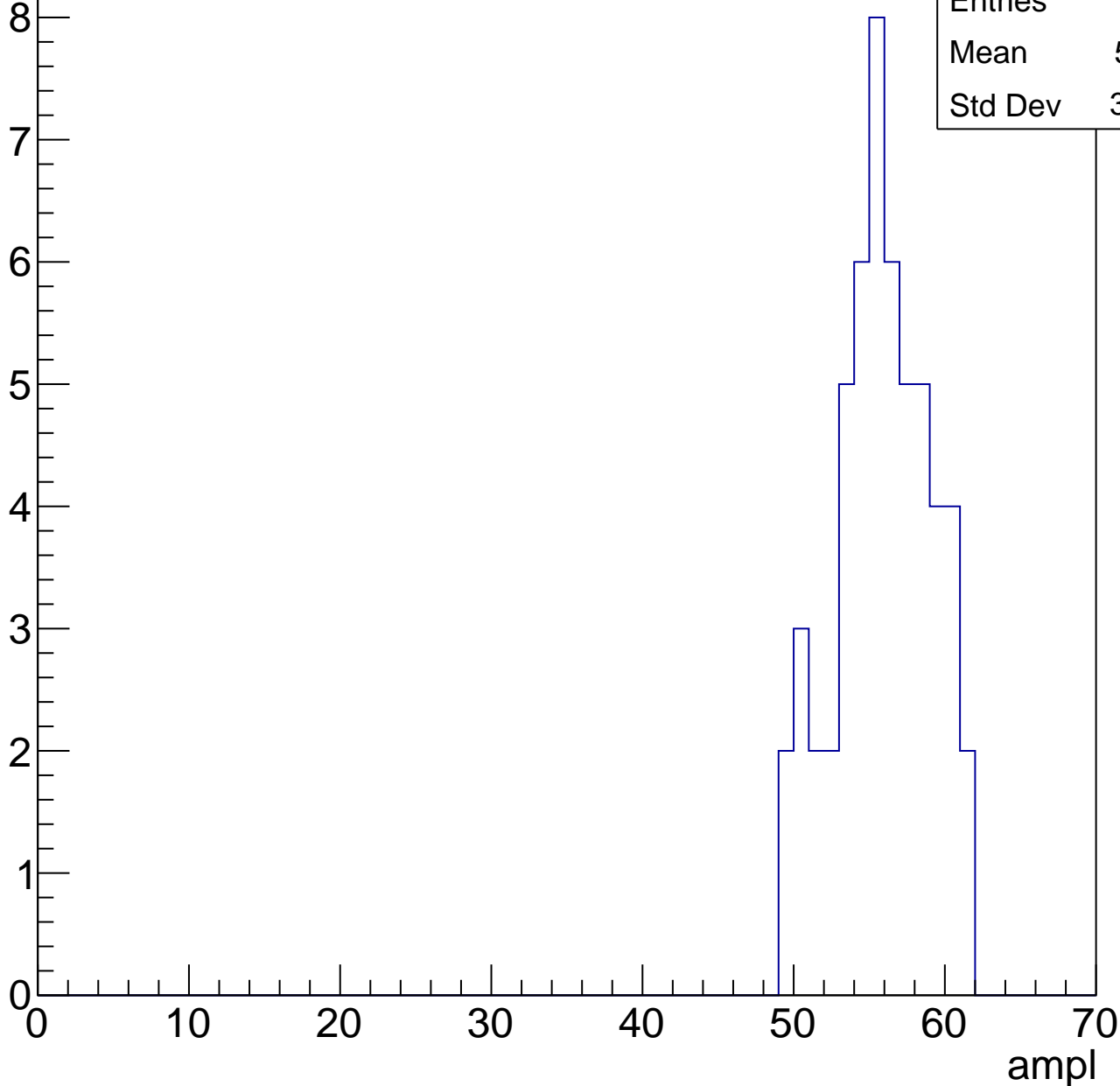


B0L001S, U23-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

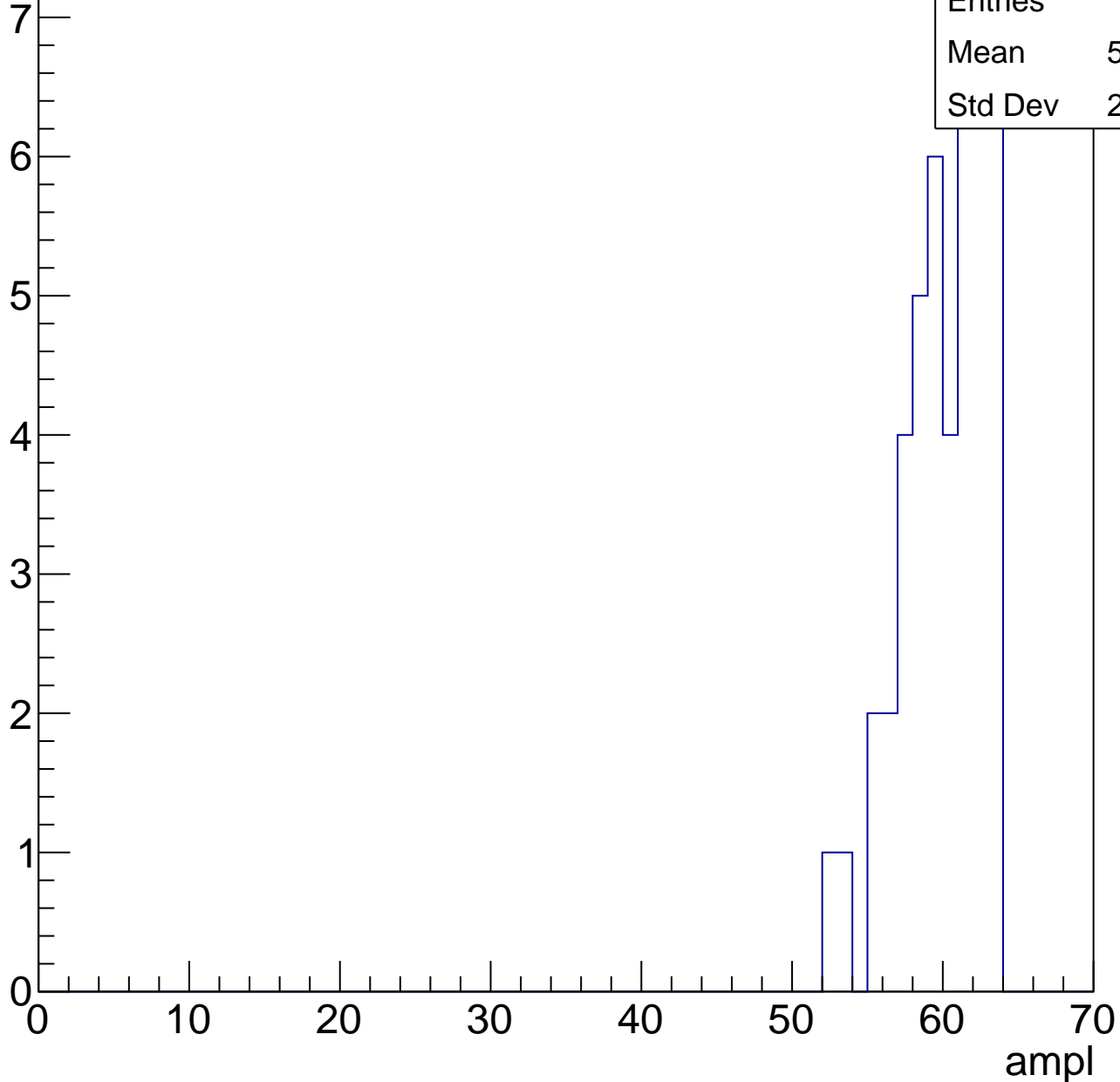
Entries	54
Mean	55.41
Std Dev	3.106



B0L001S, U23-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

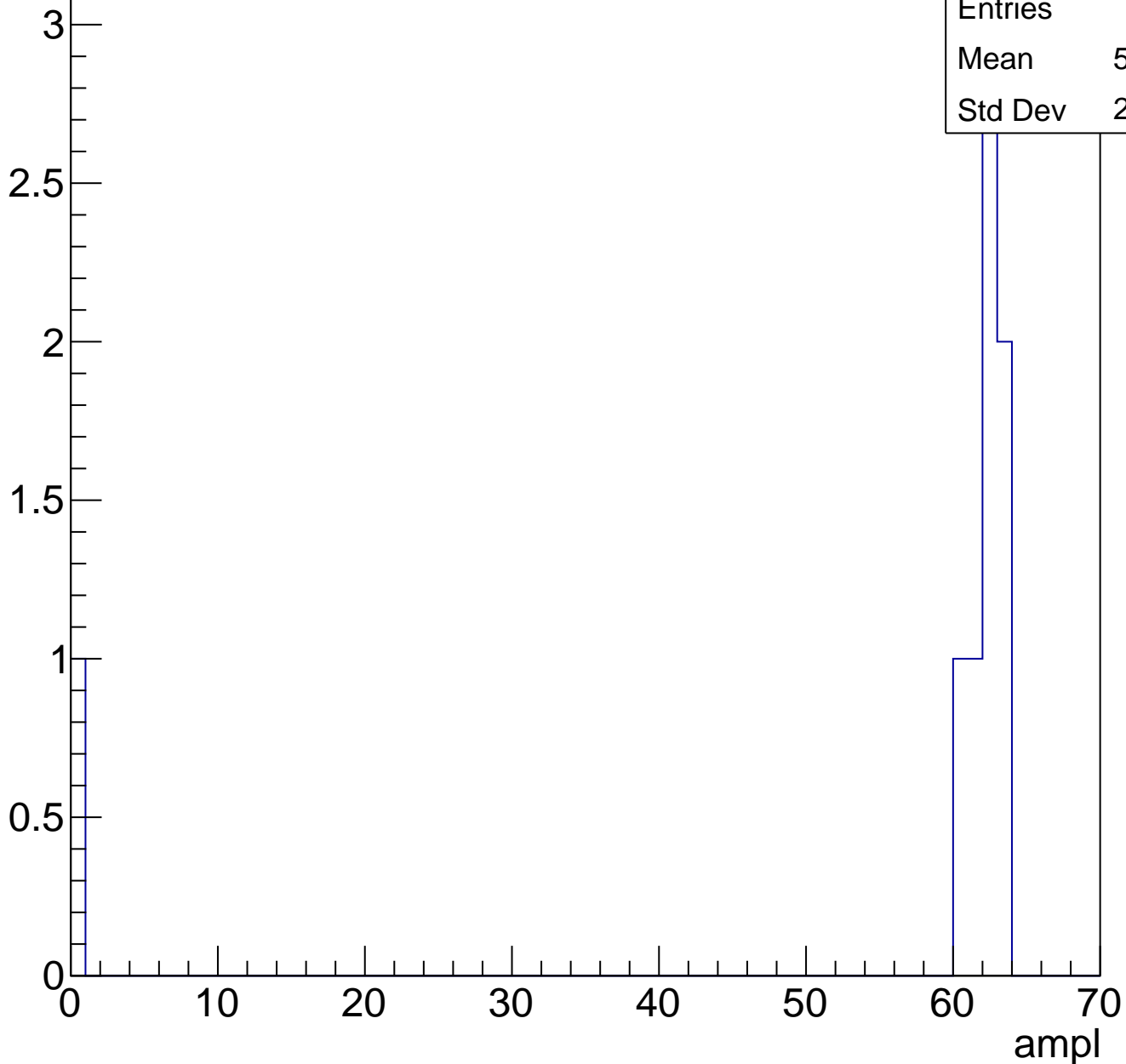
Entry



B0L001S, U23-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L001S, U23-ch113, adc0

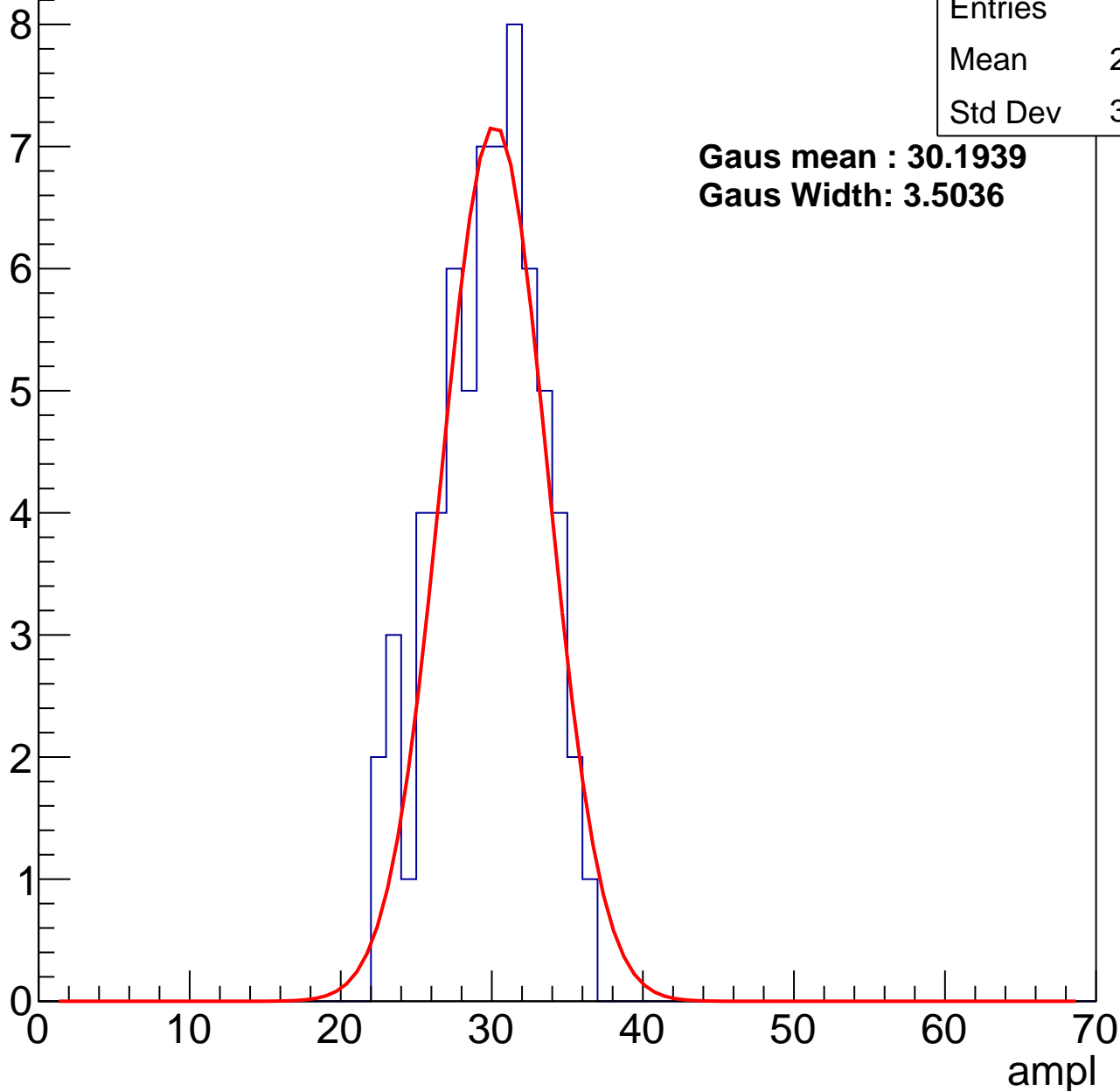
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.28
Std Dev	3.395

Gaus mean : 30.1939

Gaus Width: 3.5036



B0L001S, U23-ch113, adc1

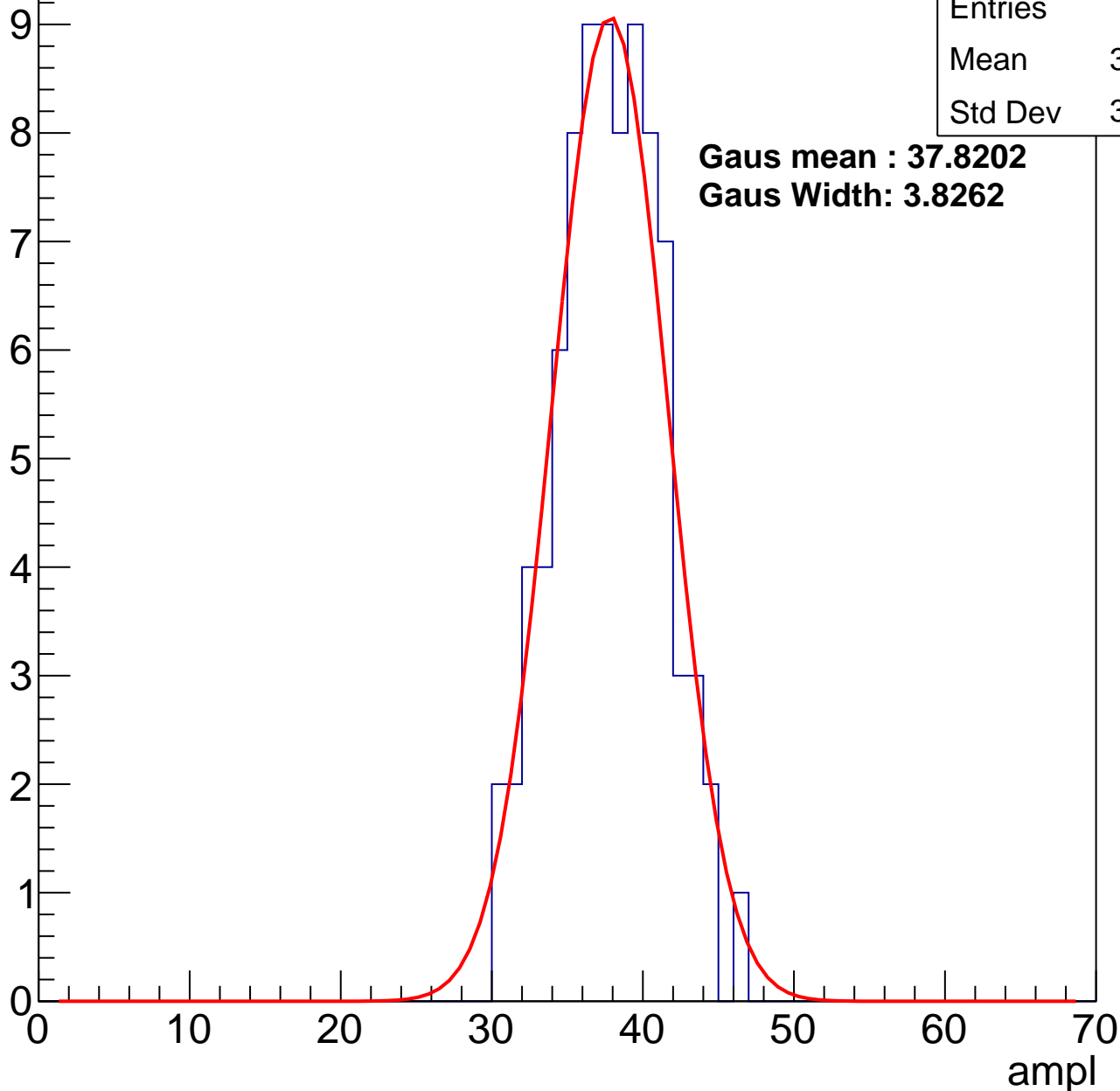
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	37.34
Std Dev	3.446

Gaus mean : 37.8202

Gaus Width: 3.8262



B0L001S, U23-ch113, adc2

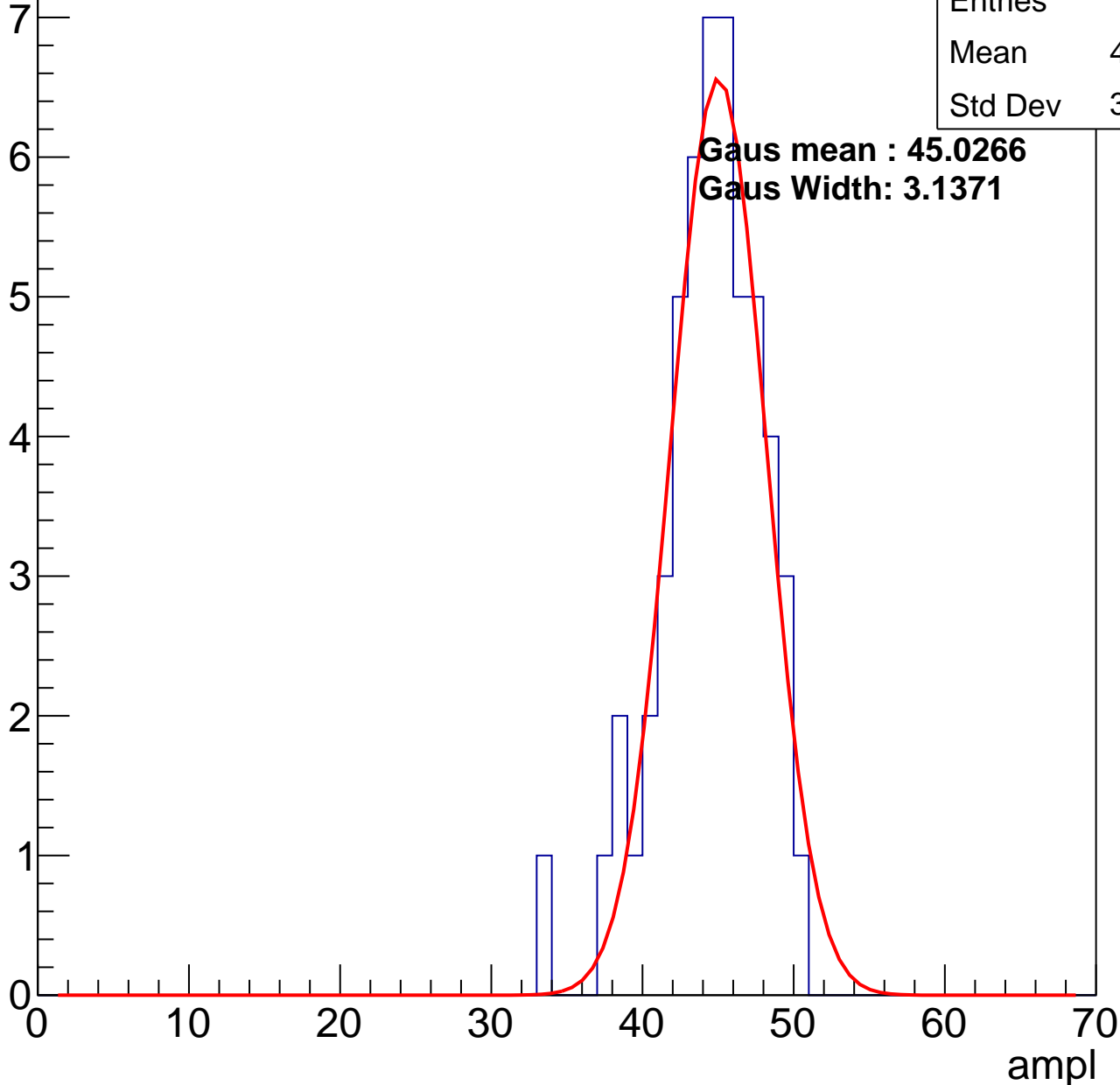
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.02
Std Dev	3.362

Gaus mean : 45.0266

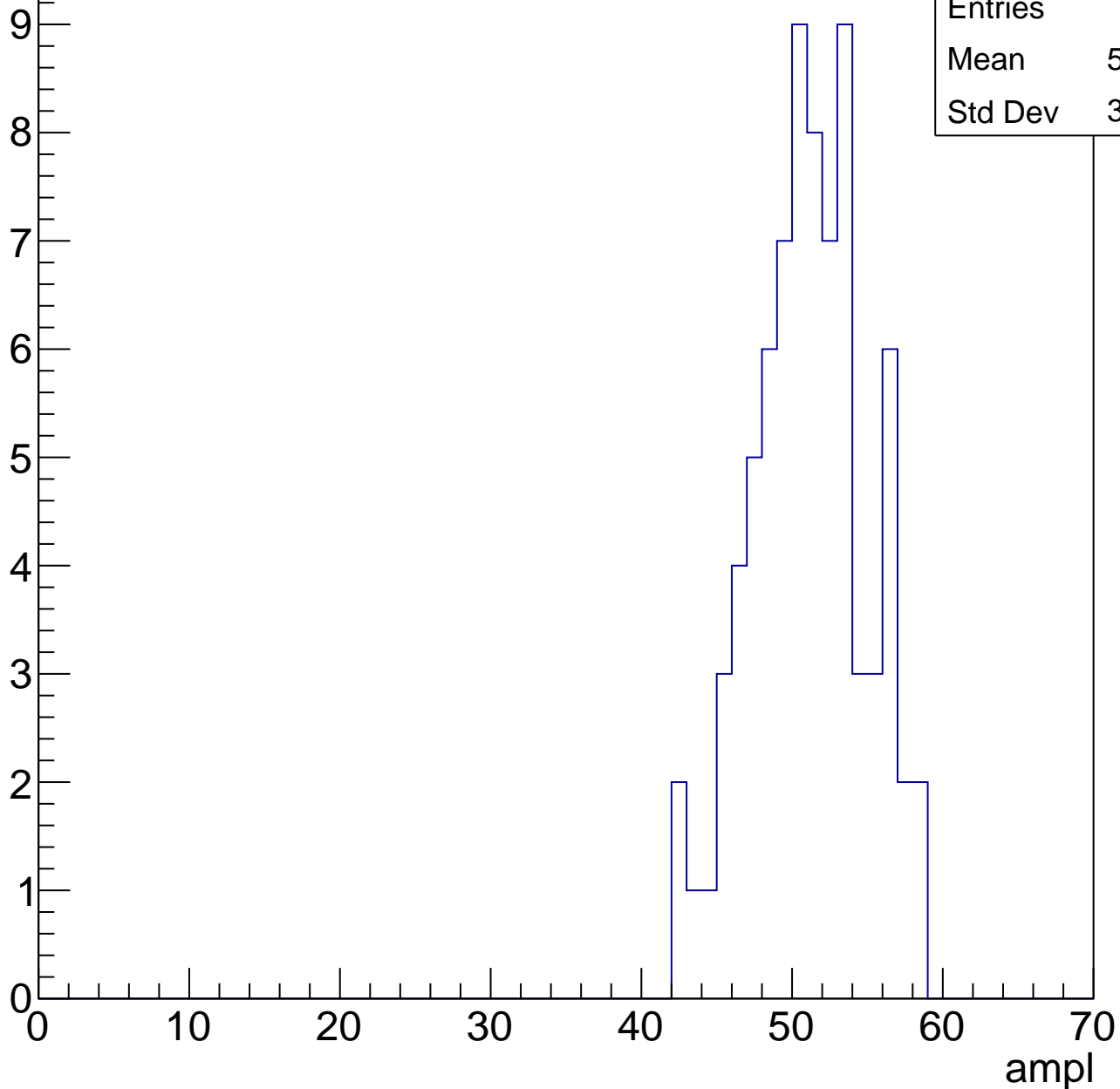
Gaus Width: 3.1371



B0L001S, U23-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



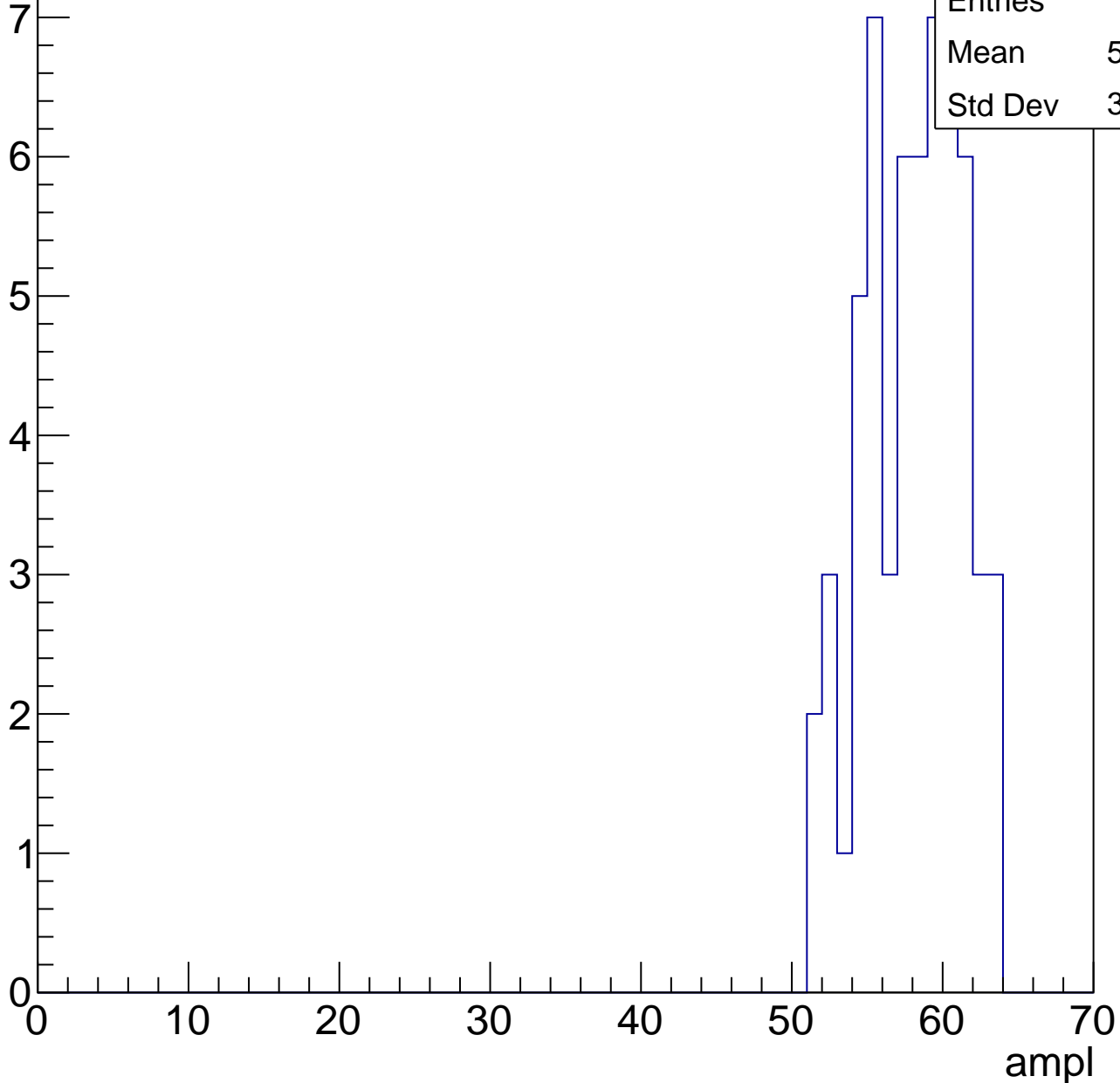
Entries	78
Mean	50.62
Std Dev	3.722

B0L001S, U23-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.59
Std Dev	3.174

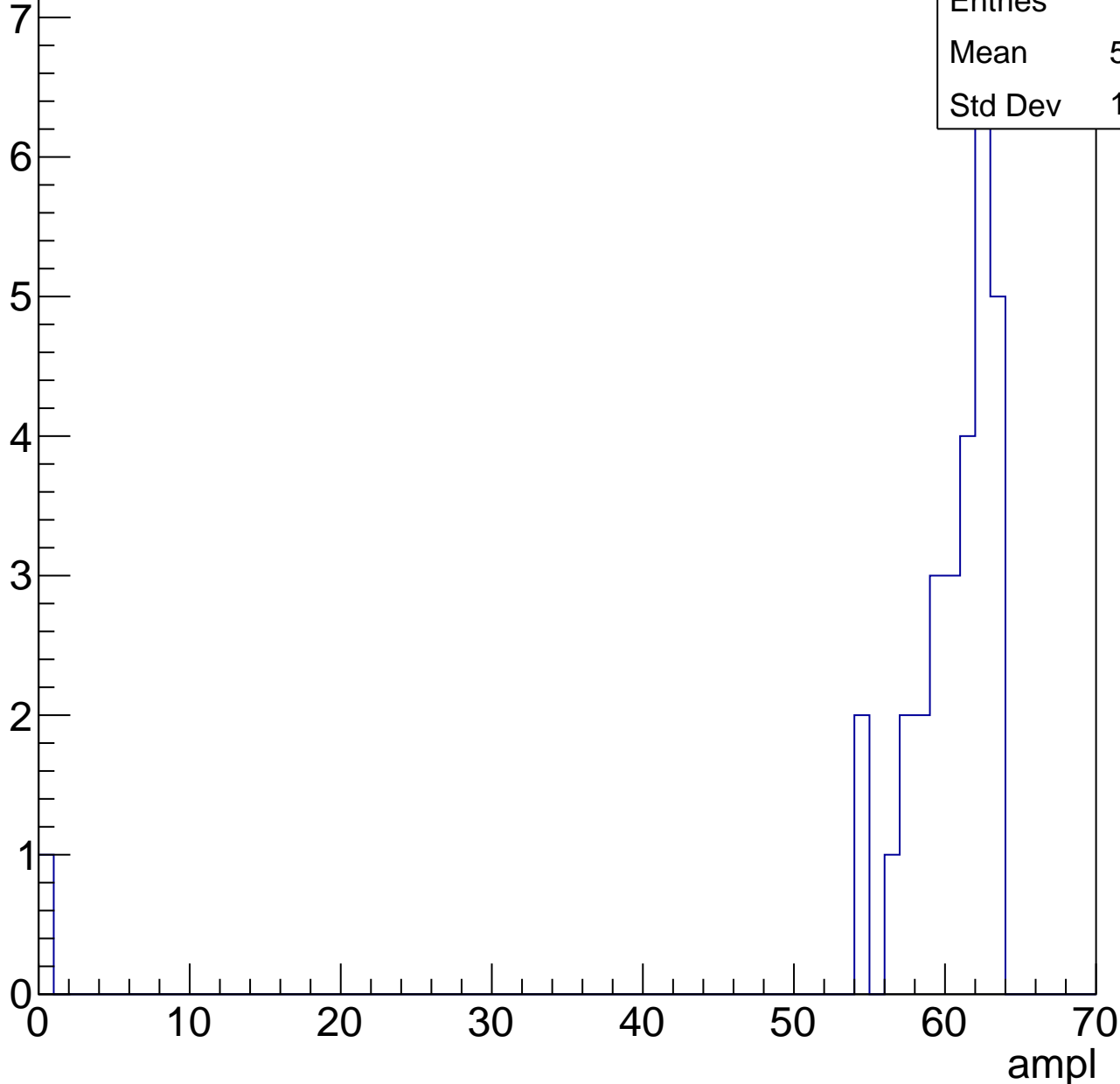


B0L001S, U23-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.13
Std Dev	11.09



B0L001S, U23-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	63
Std Dev	0

B0L001S, U23-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch114, adc0

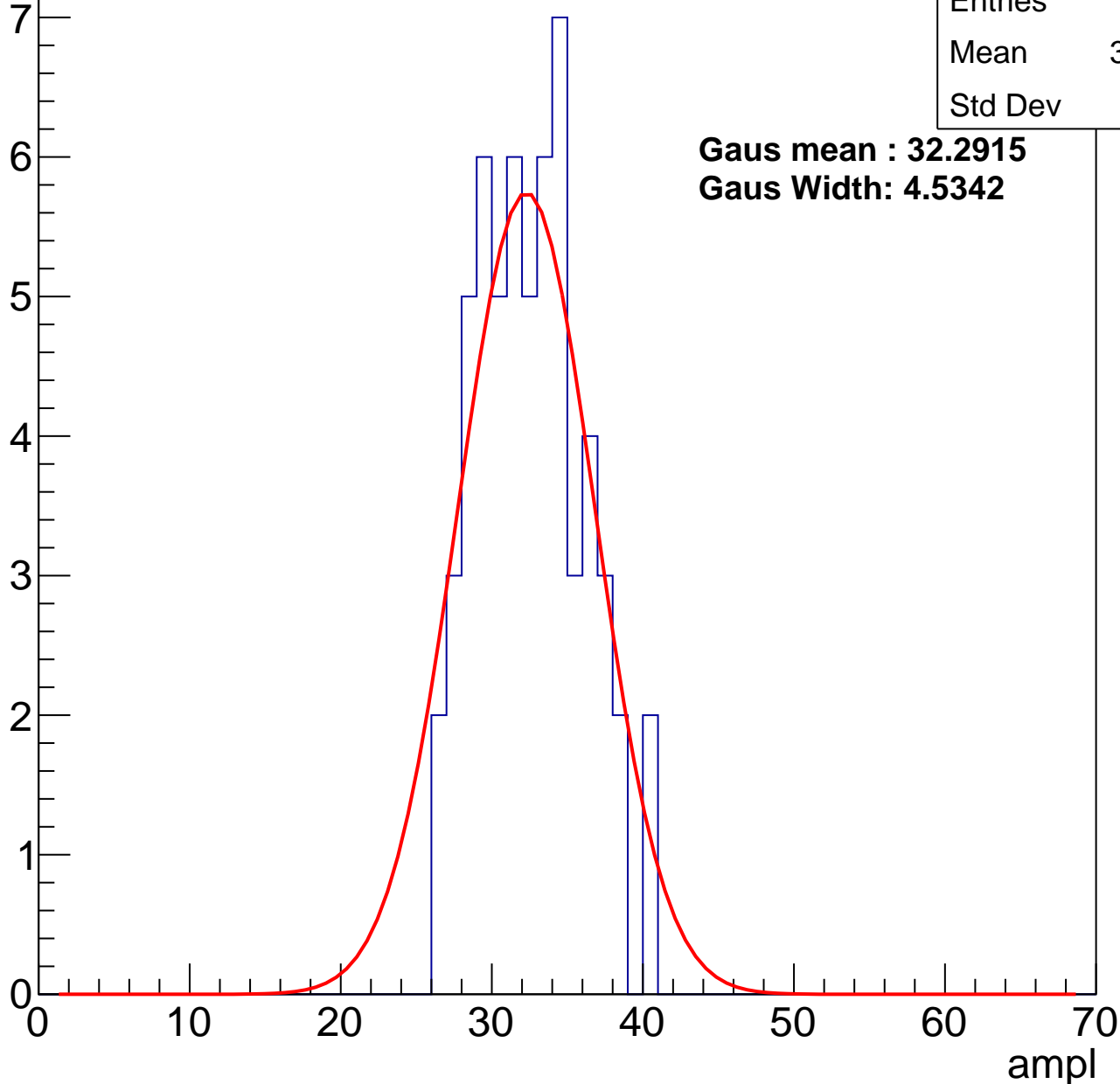
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	32.12
Std Dev	3.46

Gaus mean : 32.2915

Gaus Width: 4.5342



B0L001S, U23-ch114, adc1

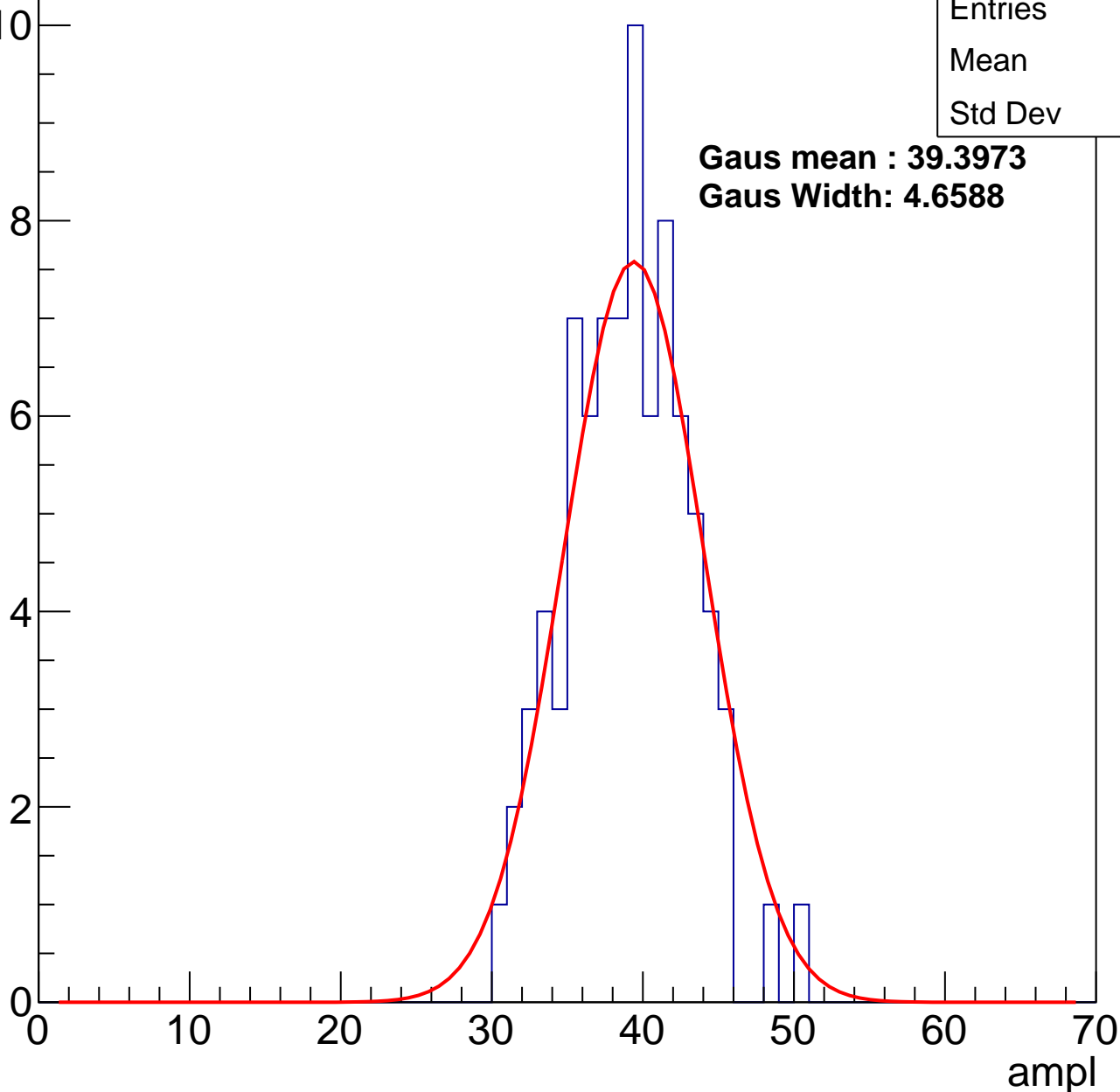
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	38.6
Std Dev	4

Gaus mean : 39.3973

Gaus Width: 4.6588



B0L001S, U23-ch114, adc2

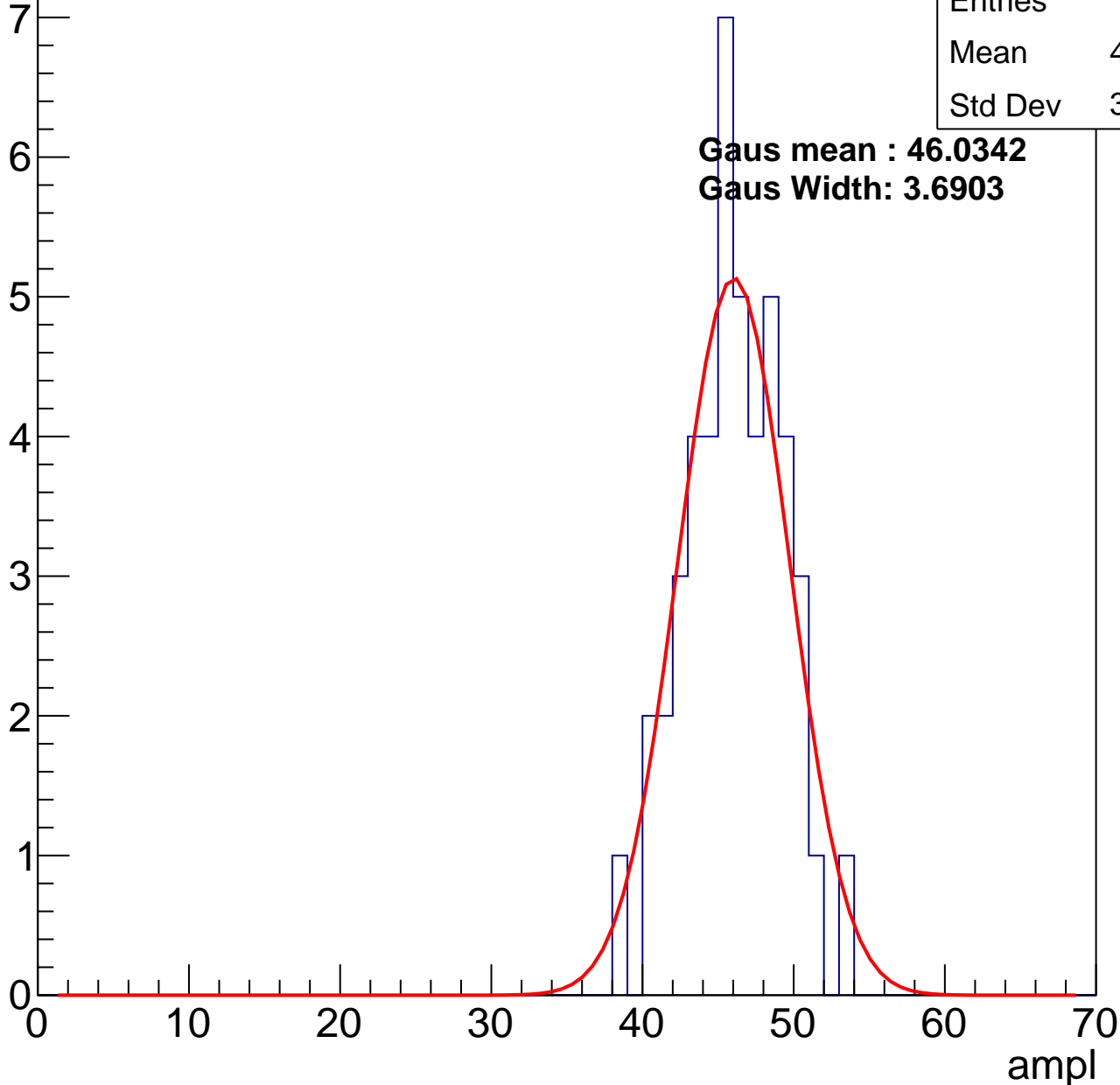
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	45.59
Std Dev	3.187

Gaus mean : 46.0342

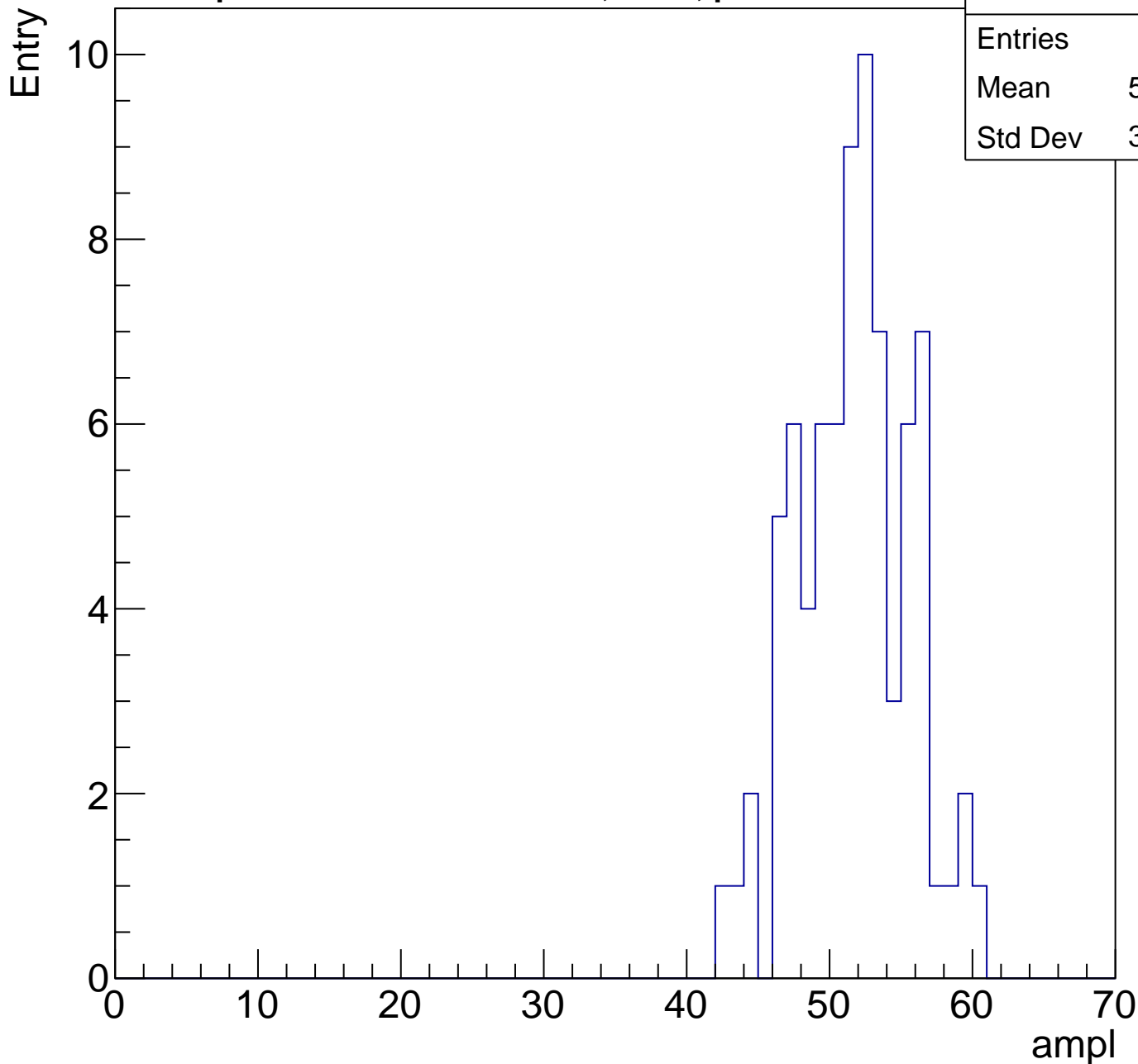
Gaus Width: 3.6903



B0L001S, U23-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

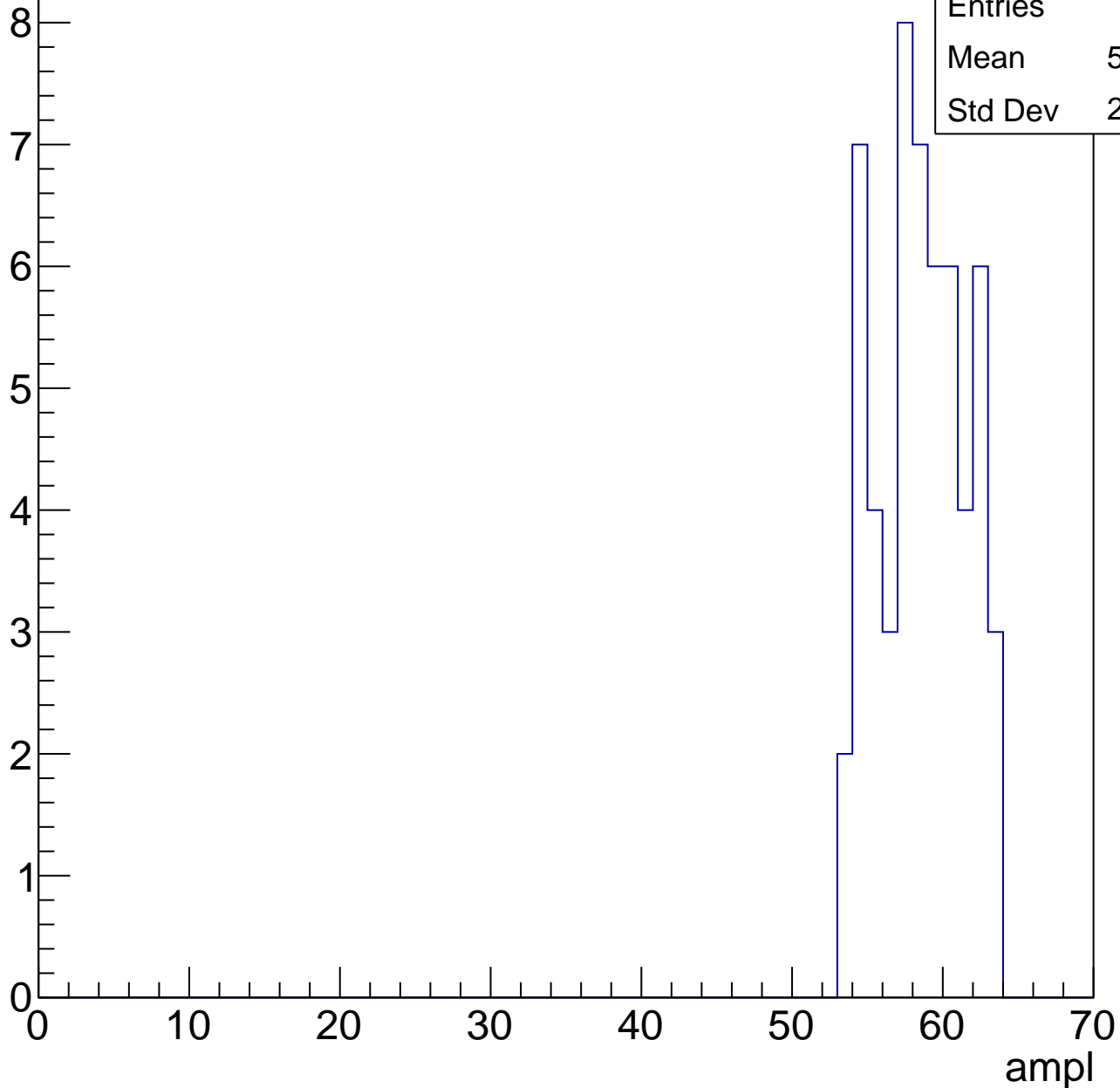
Entries	78
Mean	51.26
Std Dev	3.848



B0L001S, U23-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

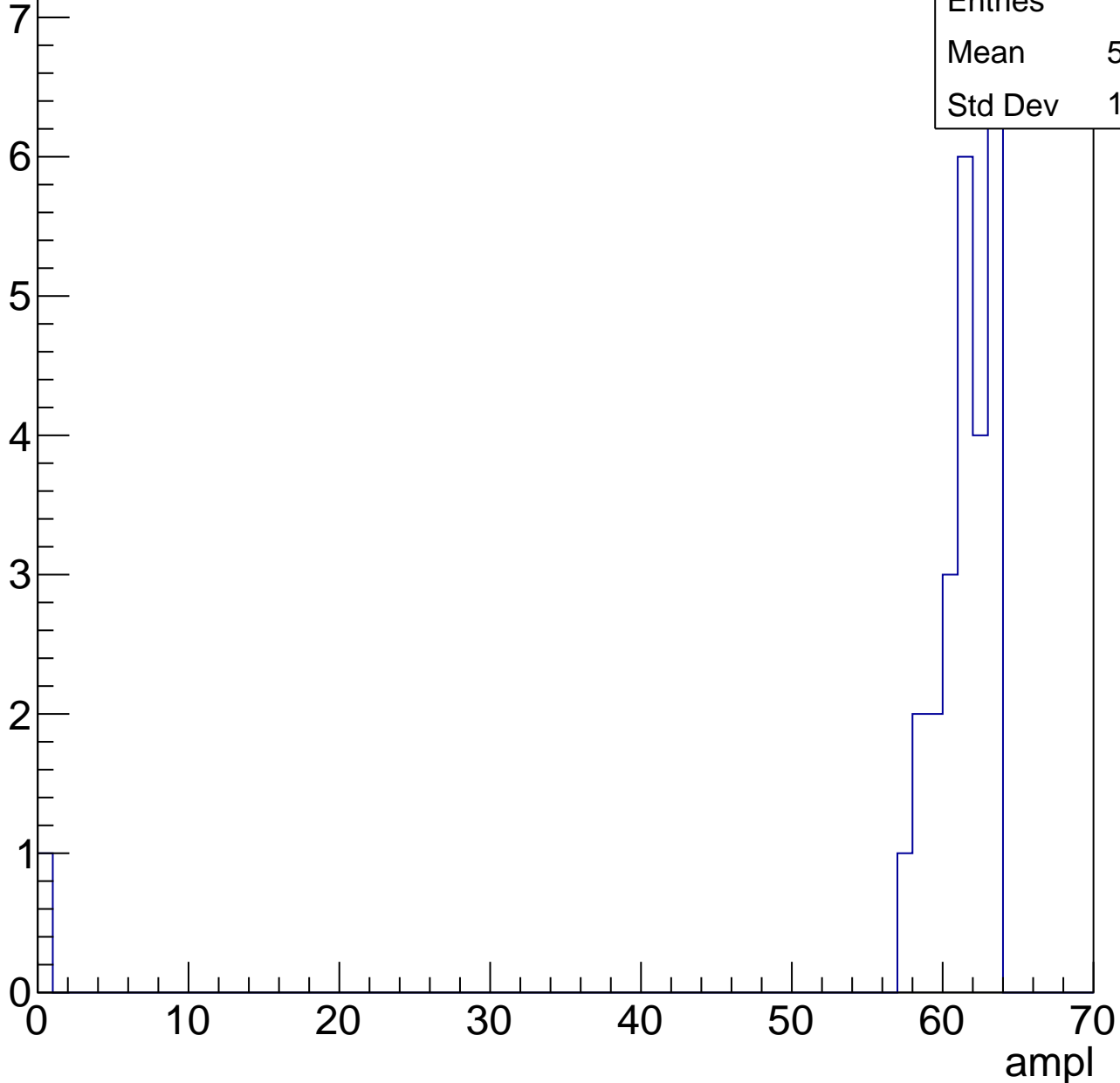


B0L001S, U23-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.69
Std Dev	11.86



B0L001S, U23-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch115, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	29.76
Std Dev	7.291

Gaus mean : 31.9653

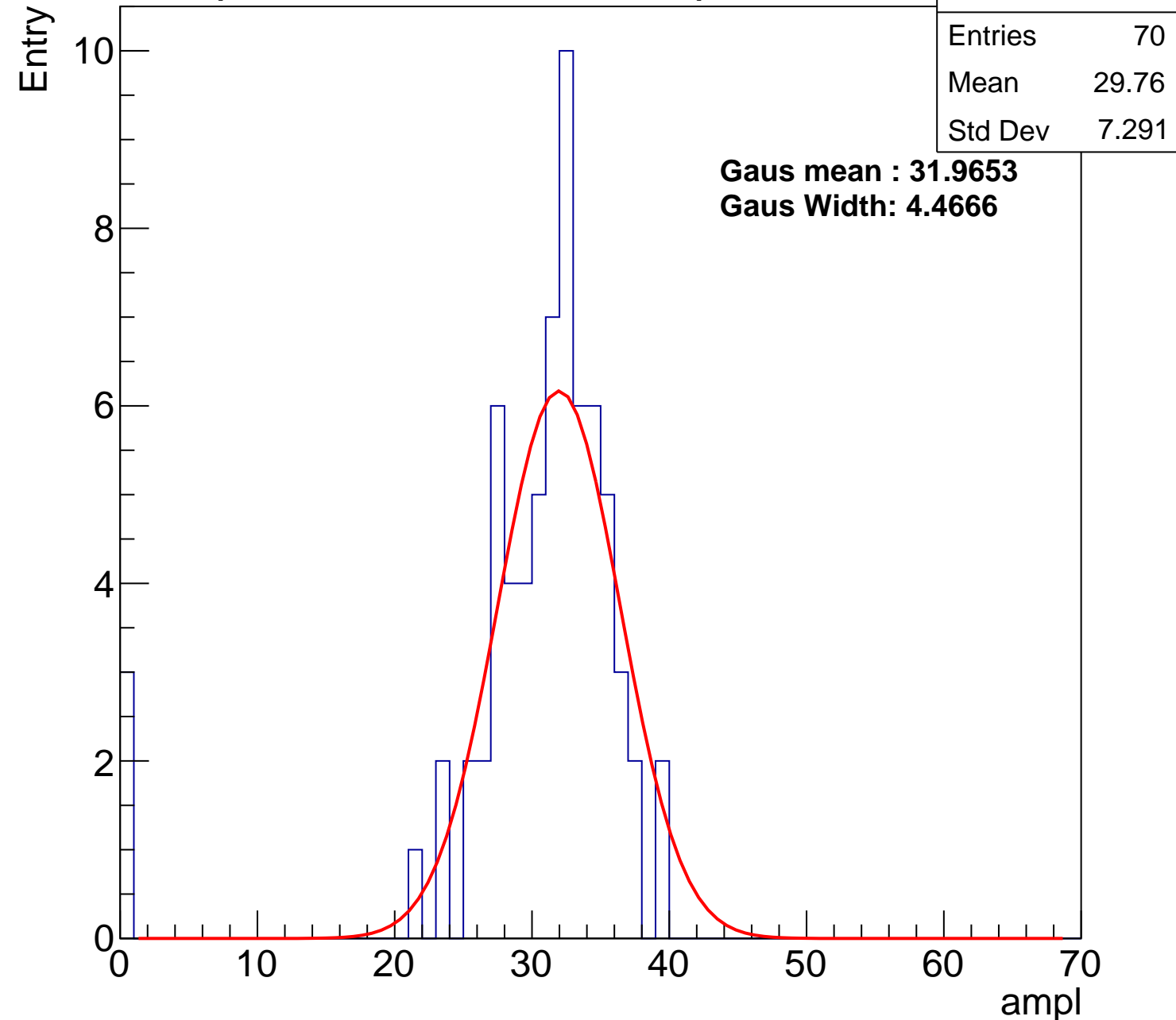
Gaus Width: 4.4666

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch115, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	39.15
Std Dev	3.622

Gaus mean : 39.6526

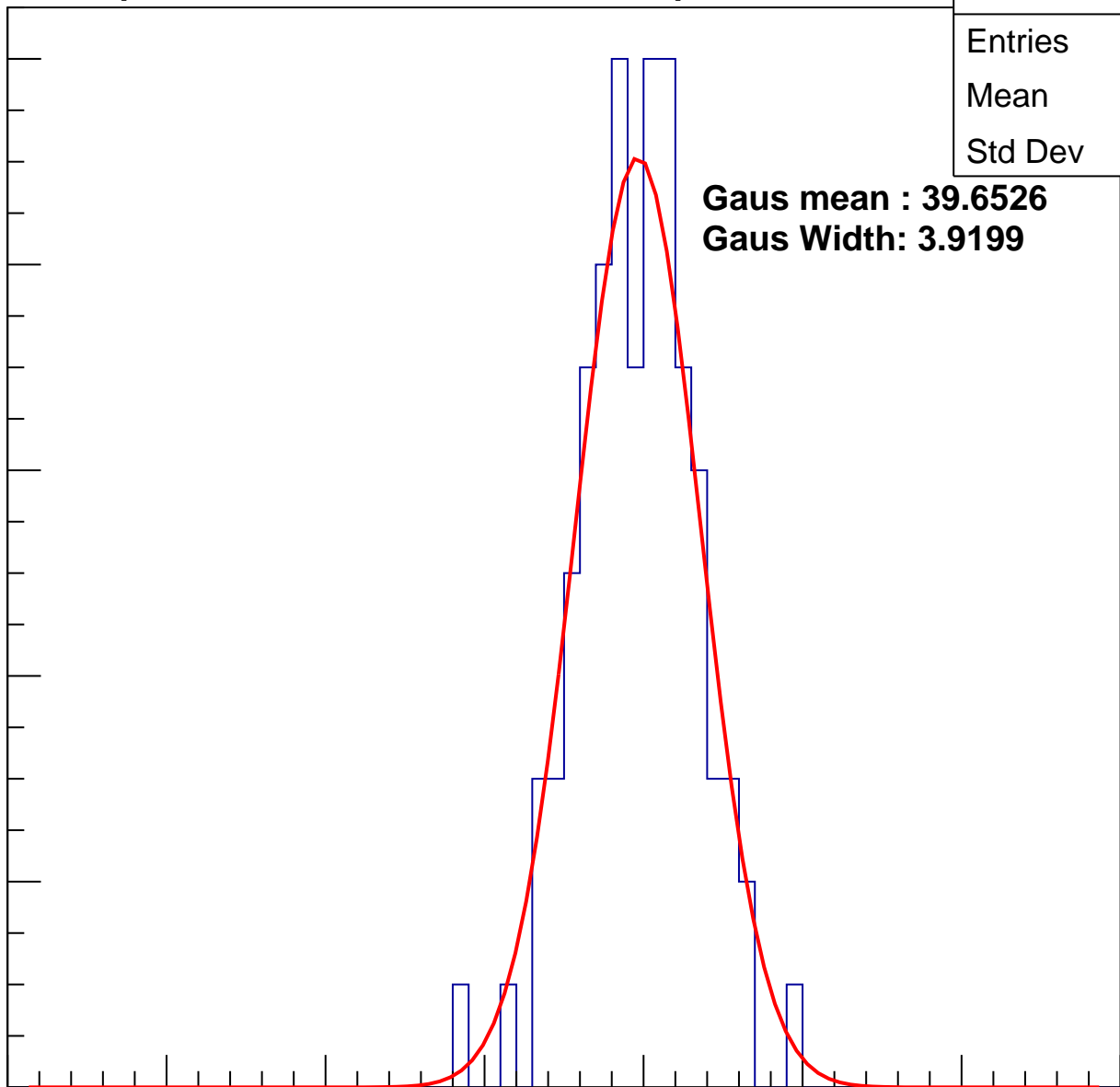
Gaus Width: 3.9199

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



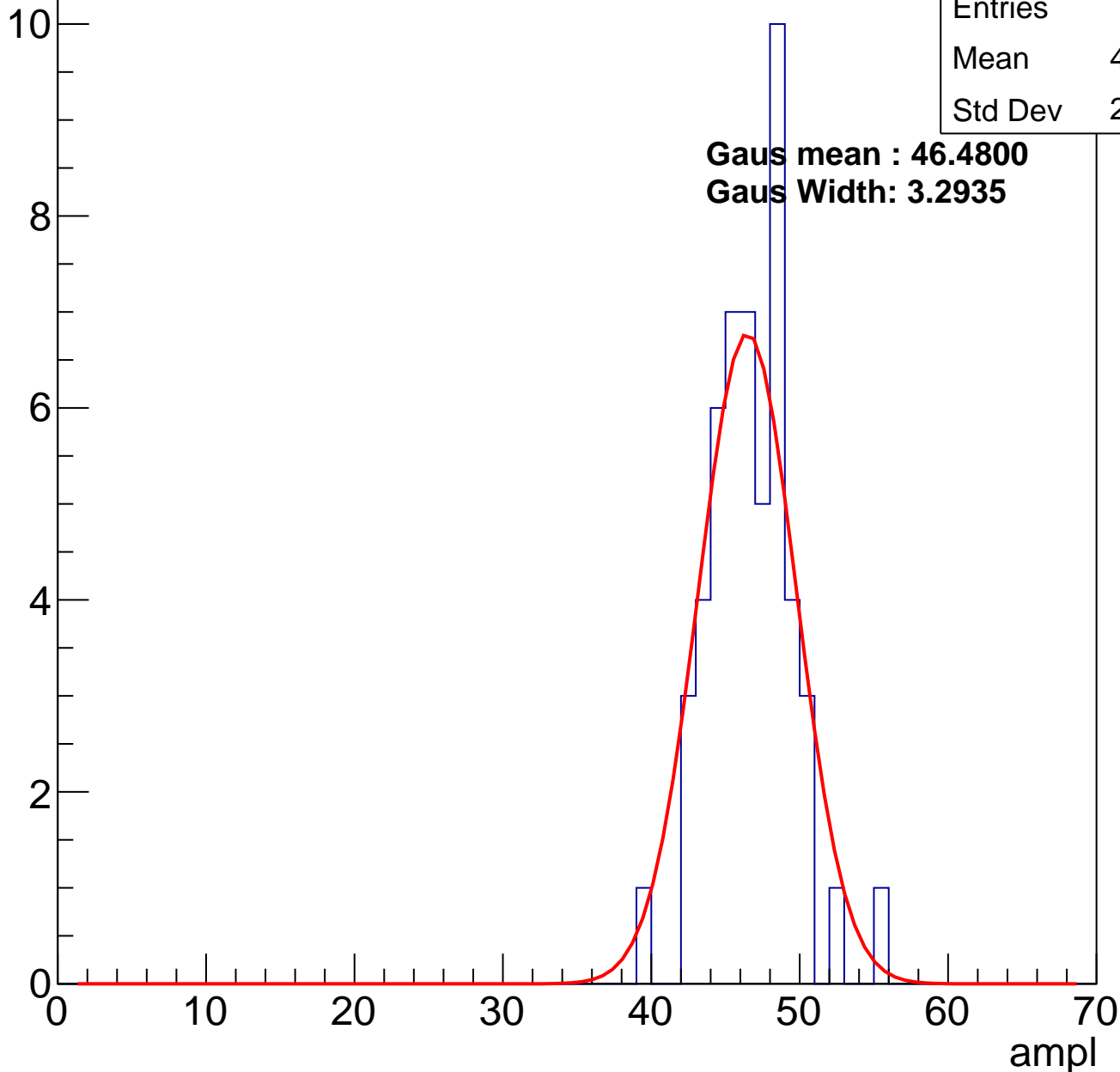
B0L001S, U23-ch115, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	52
Mean	46.27
Std Dev	2.795

Gaus mean : 46.4800
Gaus Width: 3.2935

Entry



B0L001S, U23-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

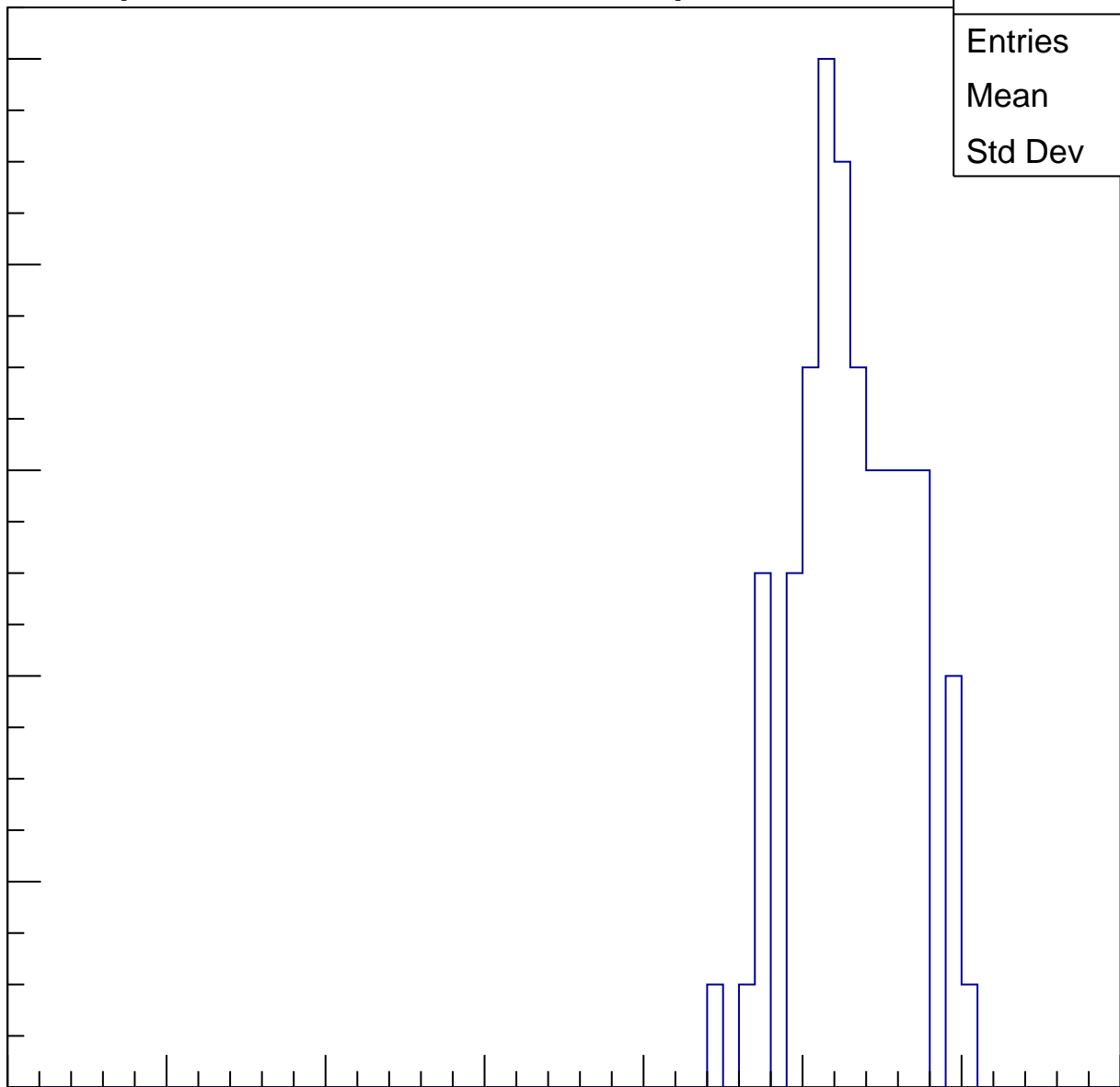
Entries	74
Mean	52.66
Std Dev	3.422

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

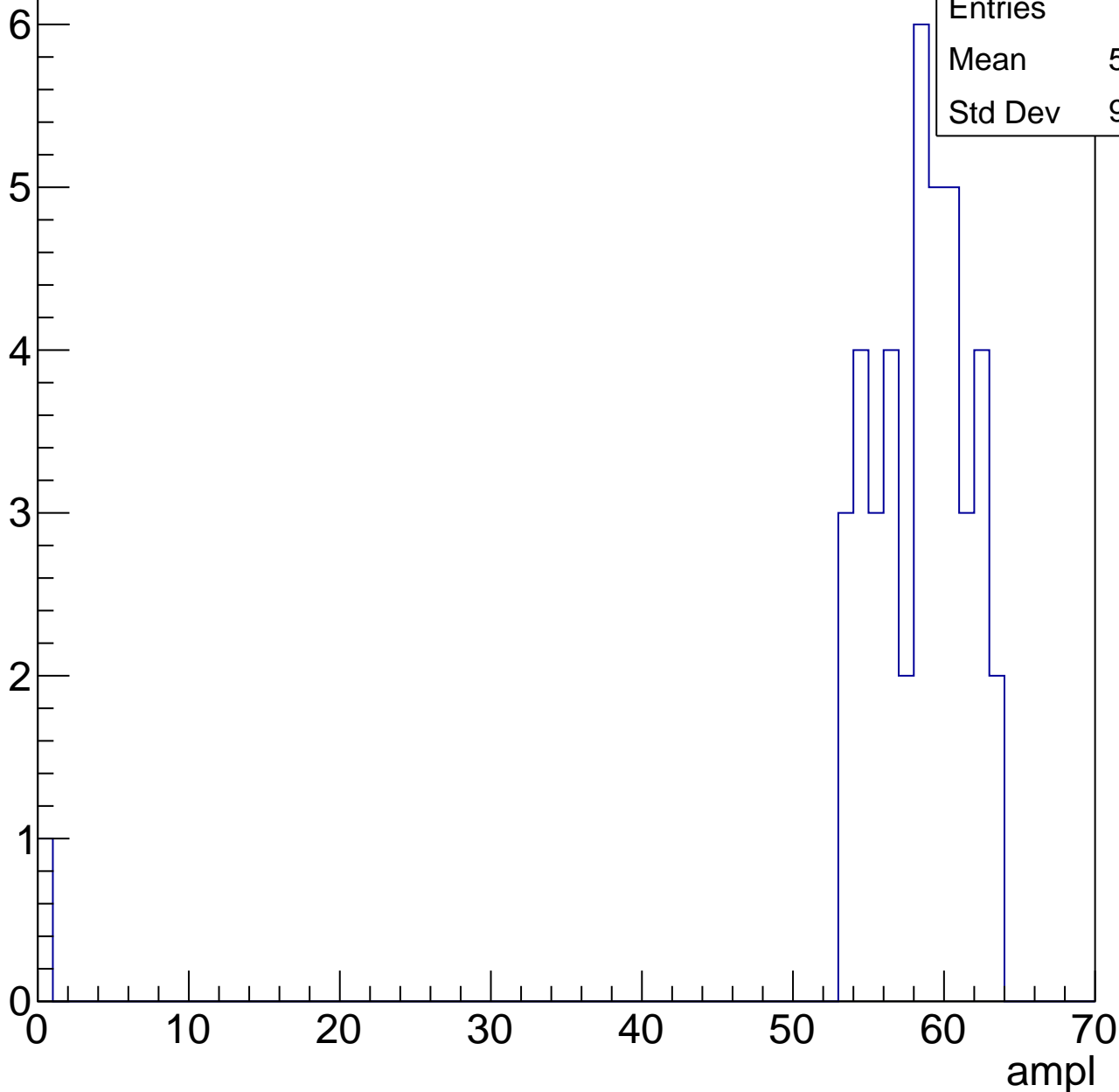


B0L001S, U23-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	56.62
Std Dev	9.302

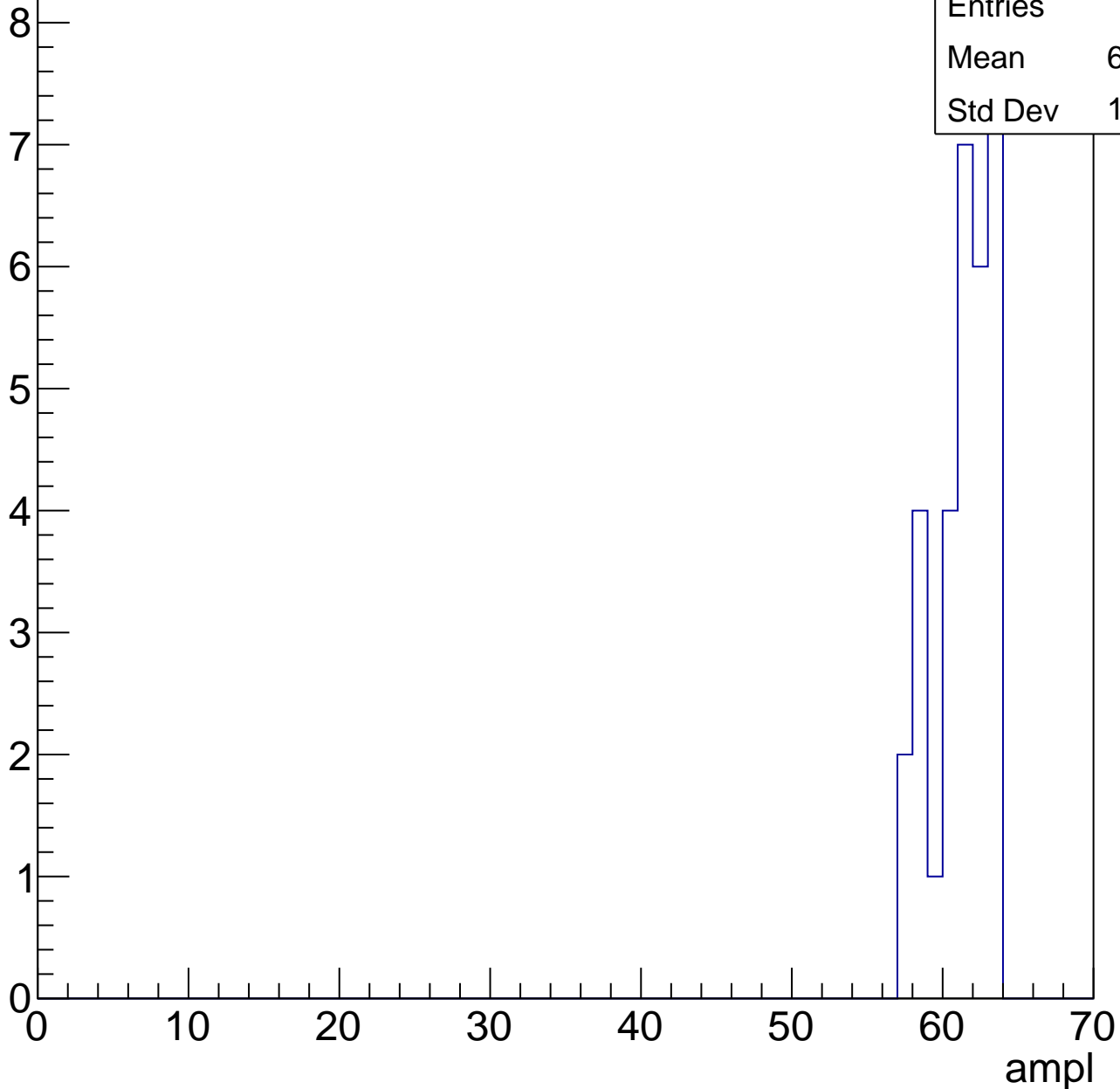


B0L001S, U23-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.88
Std Dev	1.883



B0L001S, U23-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch116, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	31.42
Std Dev	3.6

Gaus mean : 32.0804

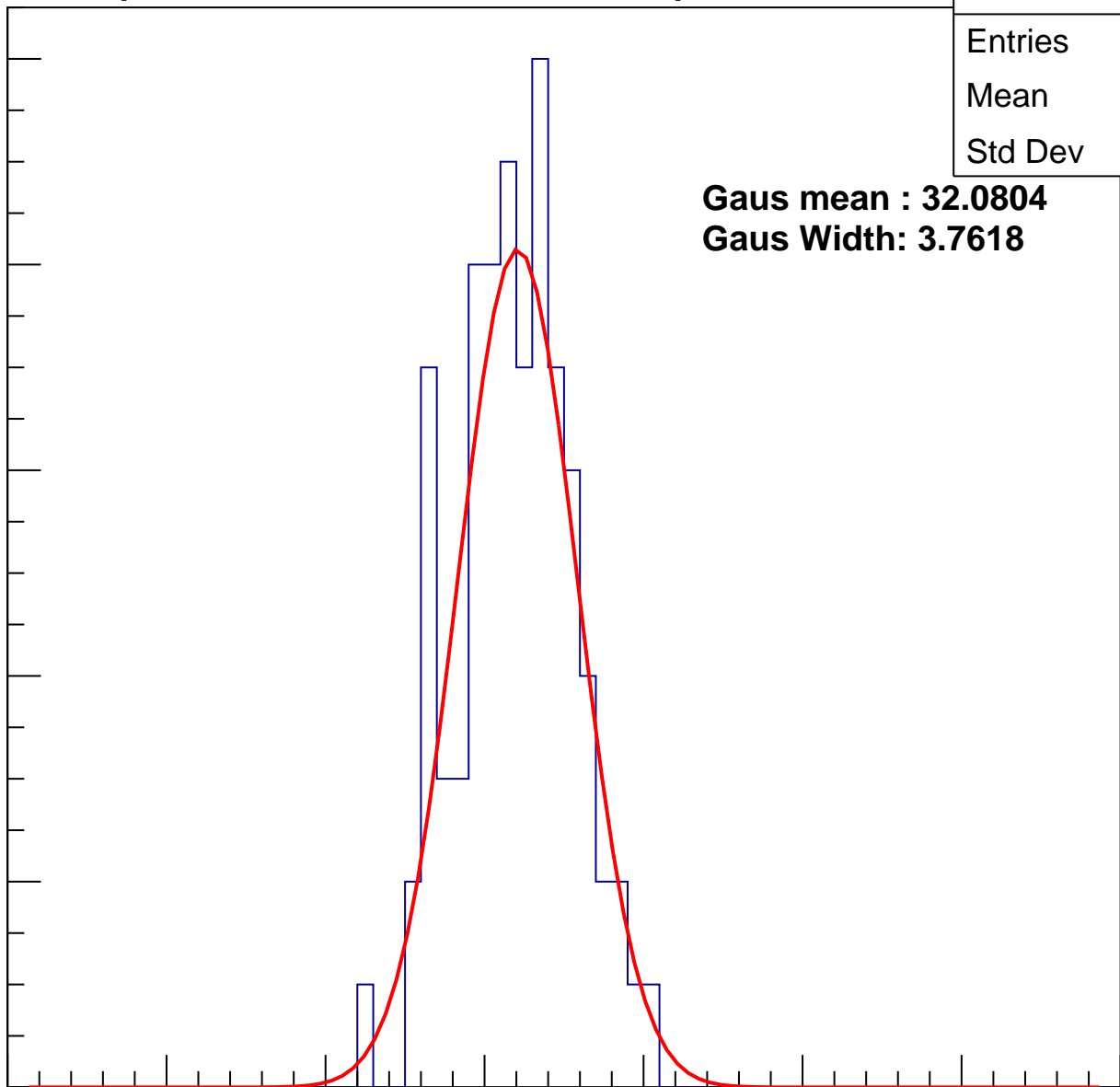
Gaus Width: 3.7618

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch116, adc1

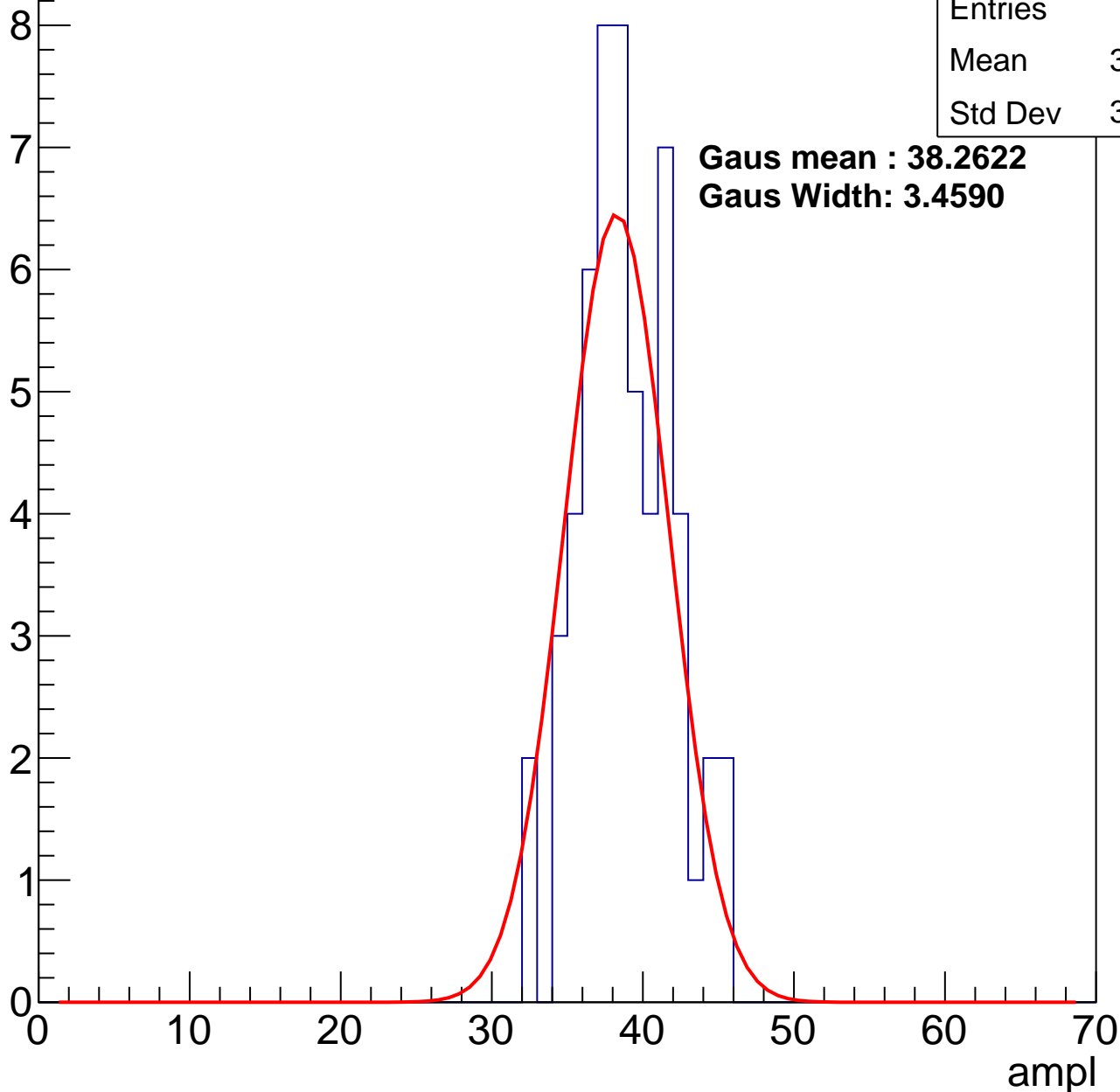
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	38.45
Std Dev	3.047

Gaus mean : 38.2622

Gaus Width: 3.4590



B0L001S, U23-ch116, adc2

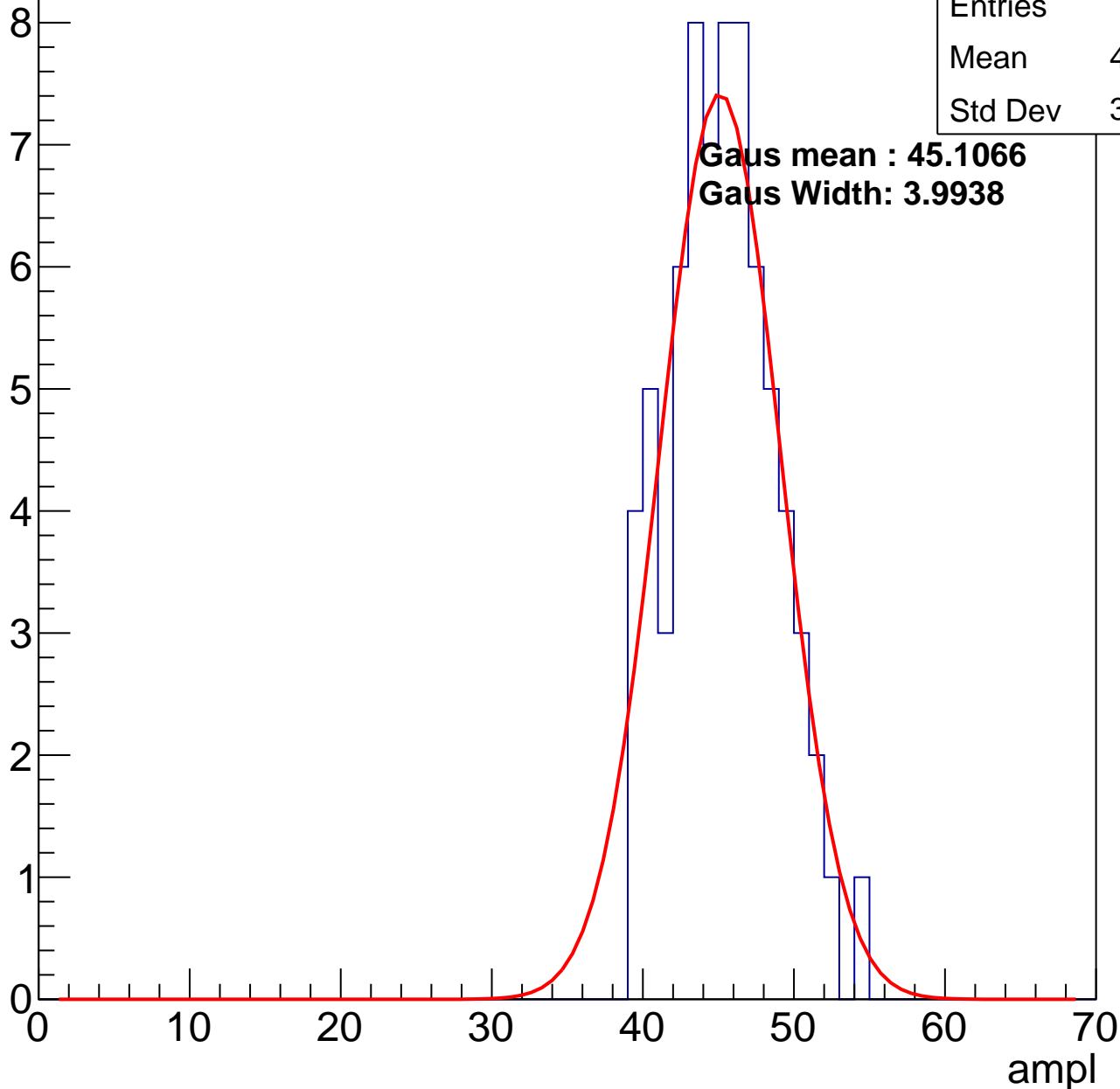
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.89
Std Dev	3.413

Gaus mean : 45.1066

Gaus Width: 3.9938

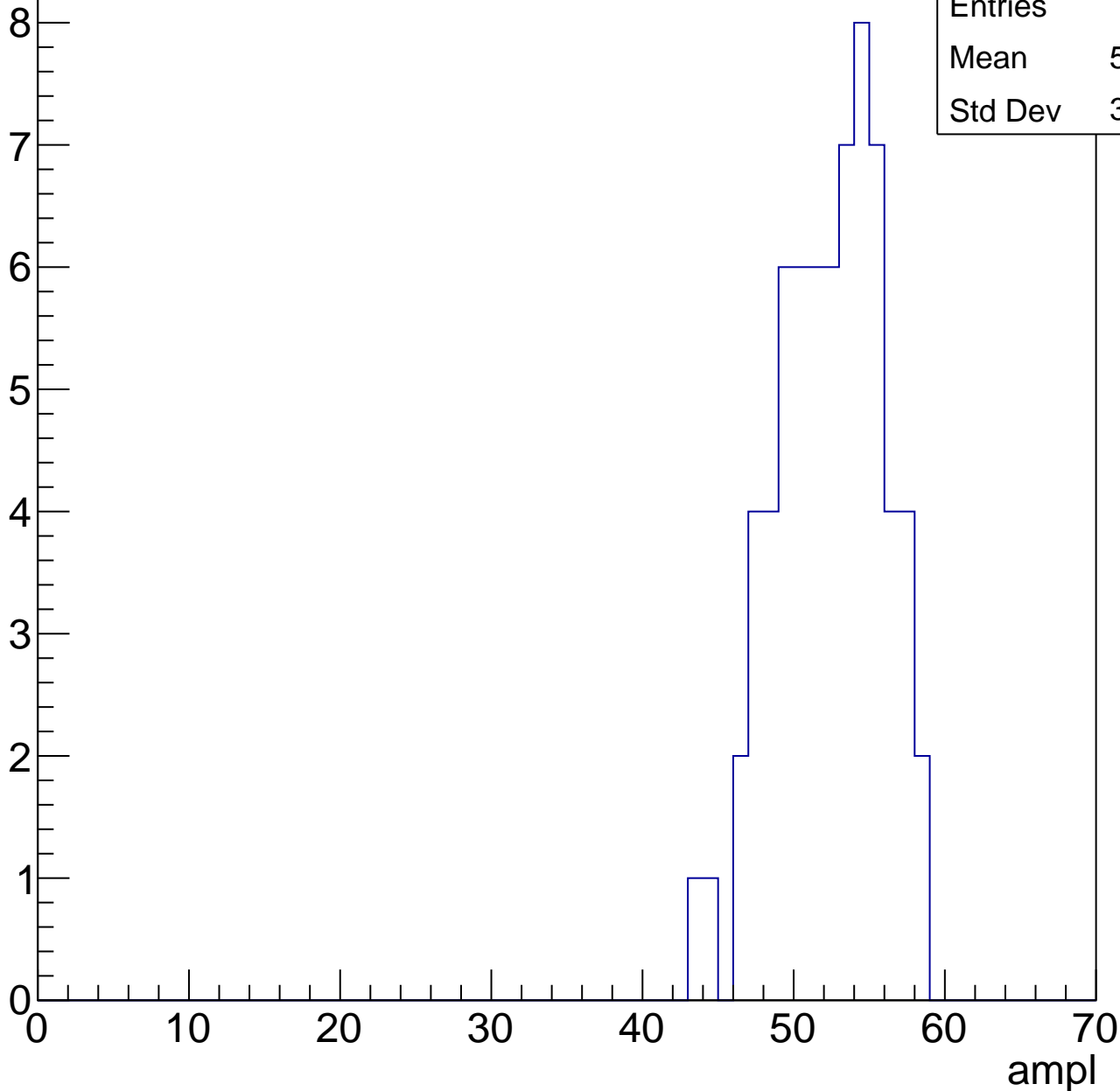


B0L001S, U23-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

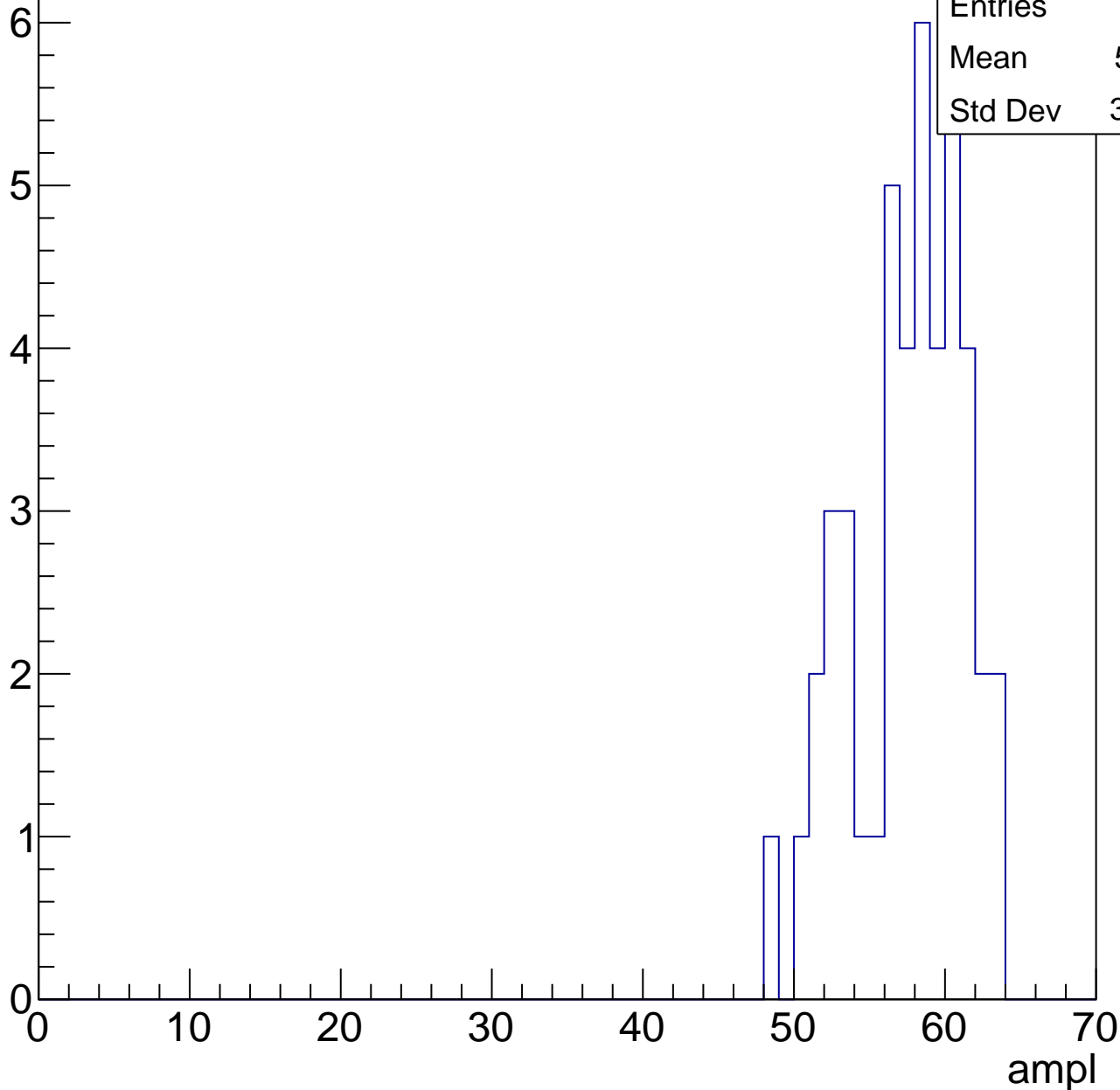
Entries	68
Mean	51.87
Std Dev	3.434



B0L001S, U23-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

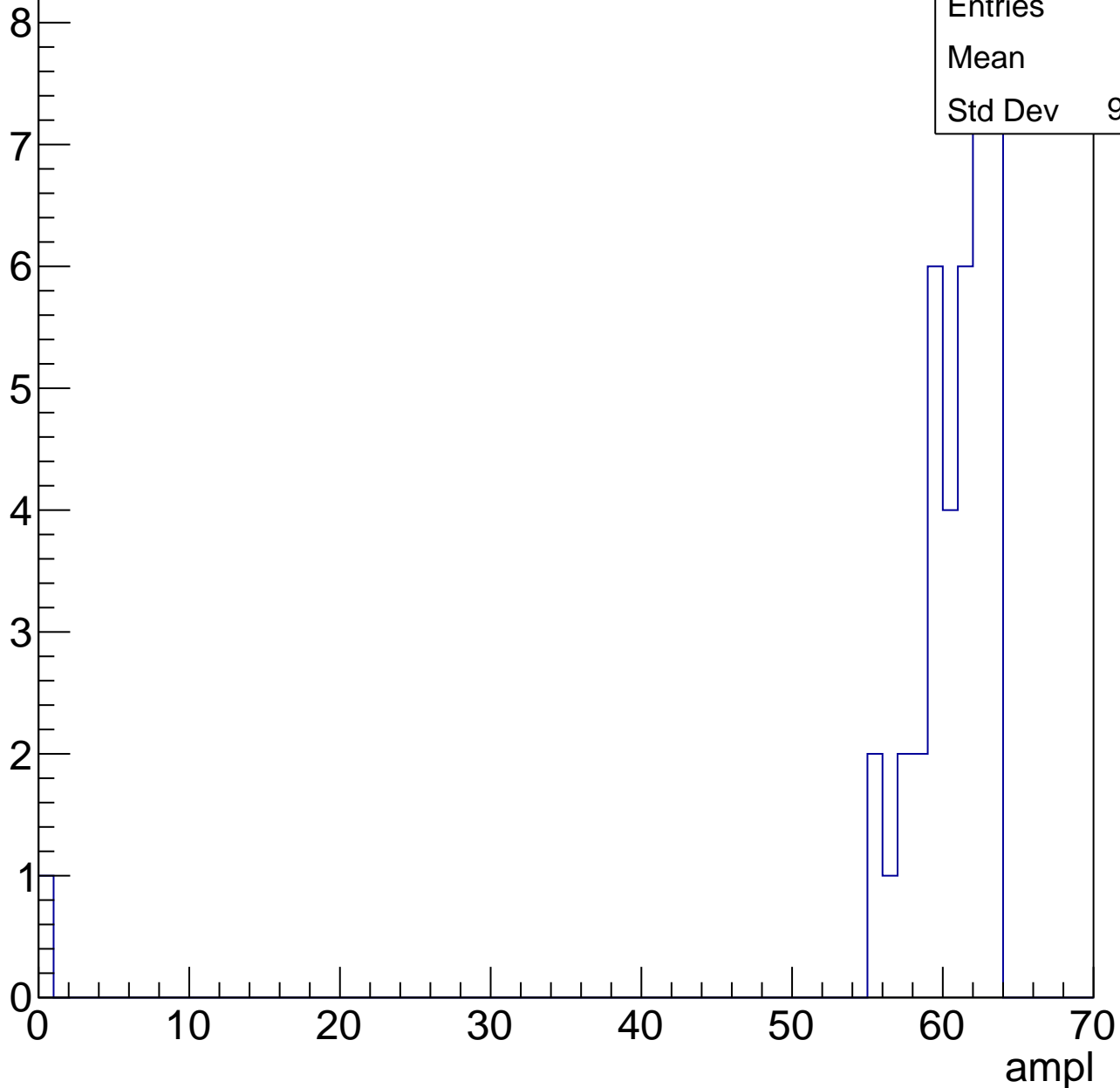


B0L001S, U23-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.9
Std Dev	9.695



B0L001S, U23-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch117, adc0

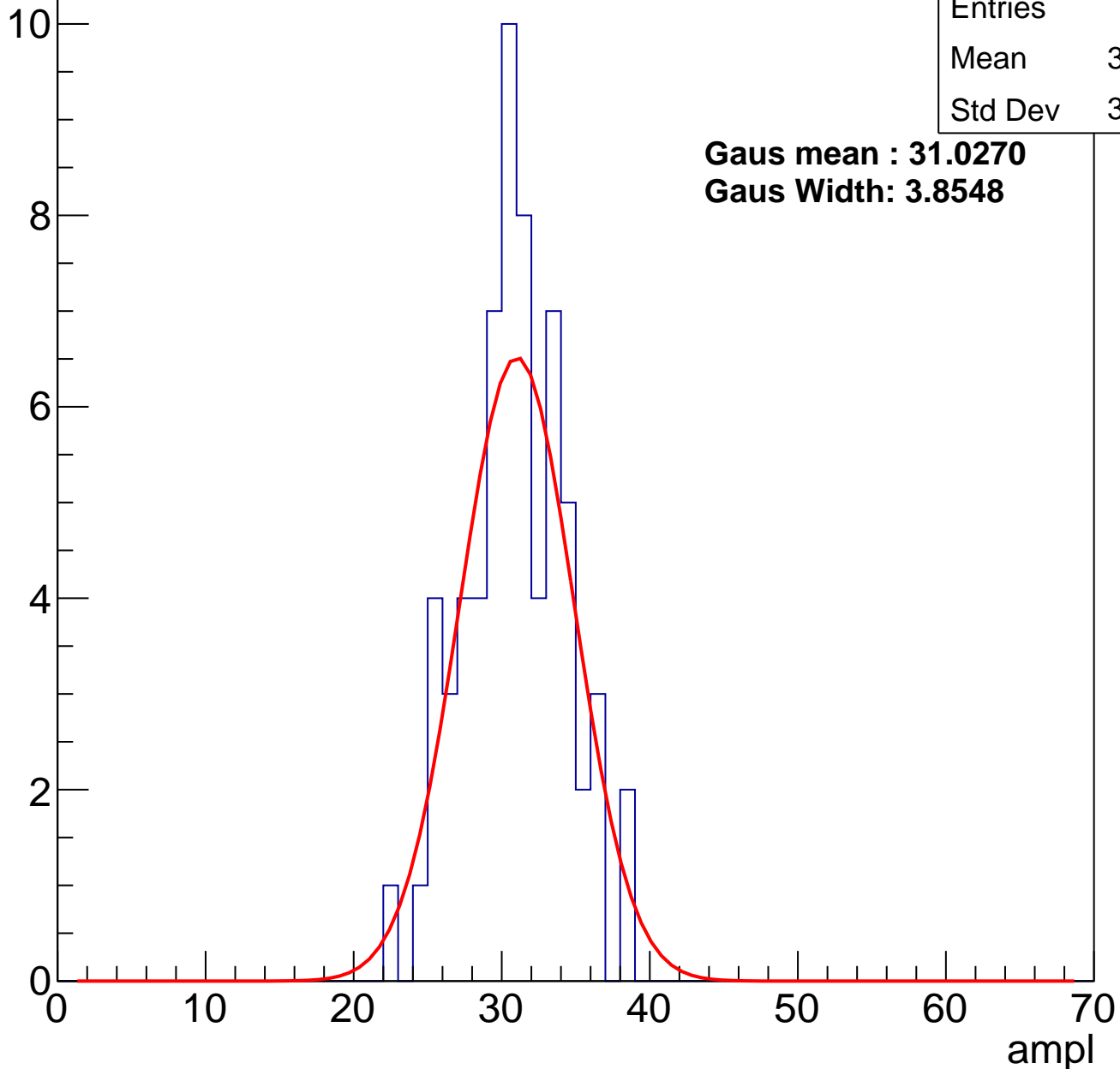
calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	30.43
Std Dev	3.392

Gaus mean : 31.0270

Gaus Width: 3.8548

Entry



B0L001S, U23-ch117, adc1

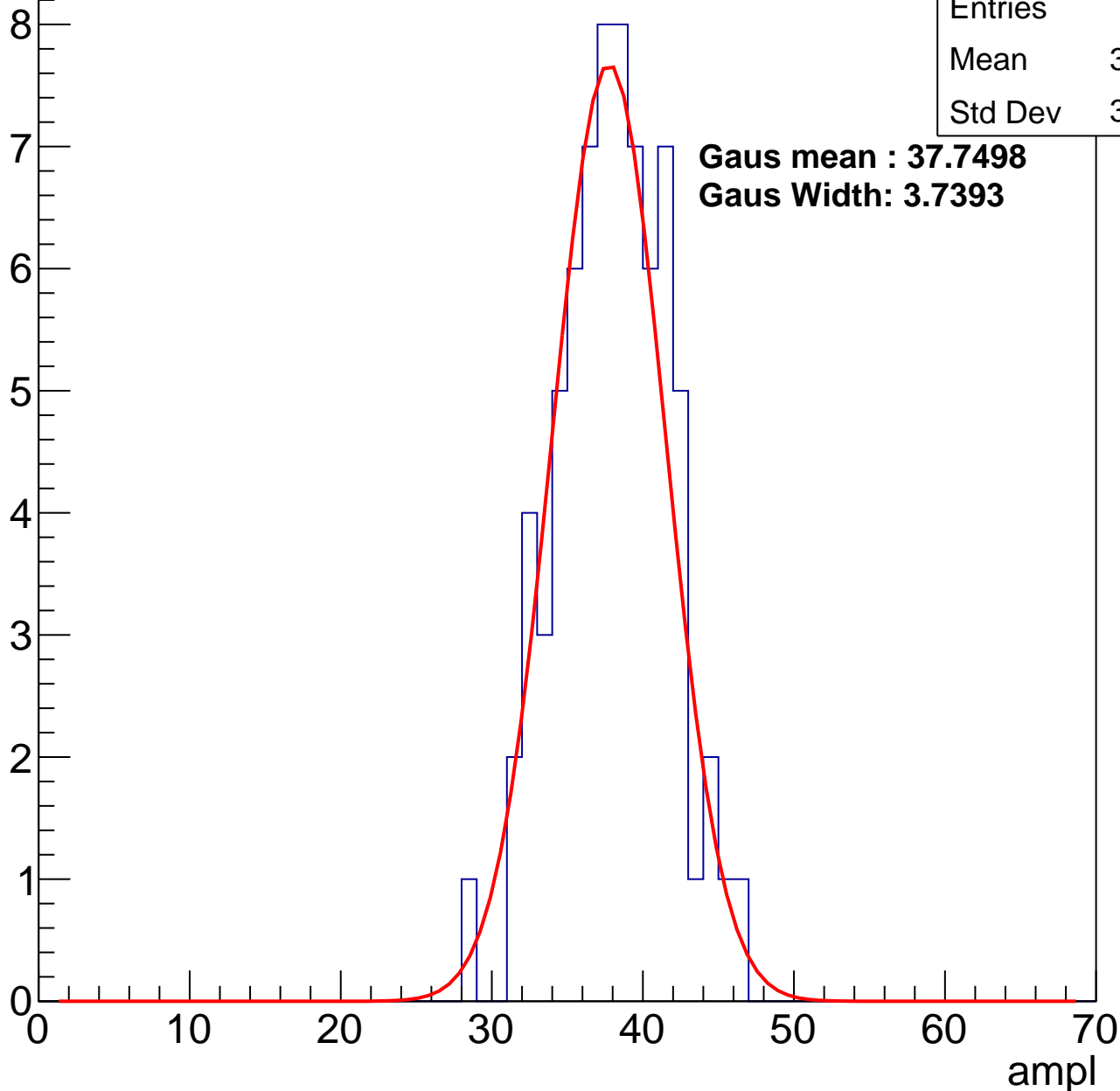
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	37.58
Std Dev	3.598

Gaus mean : 37.7498

Gaus Width: 3.7393



B0L001S, U23-ch117, adc2

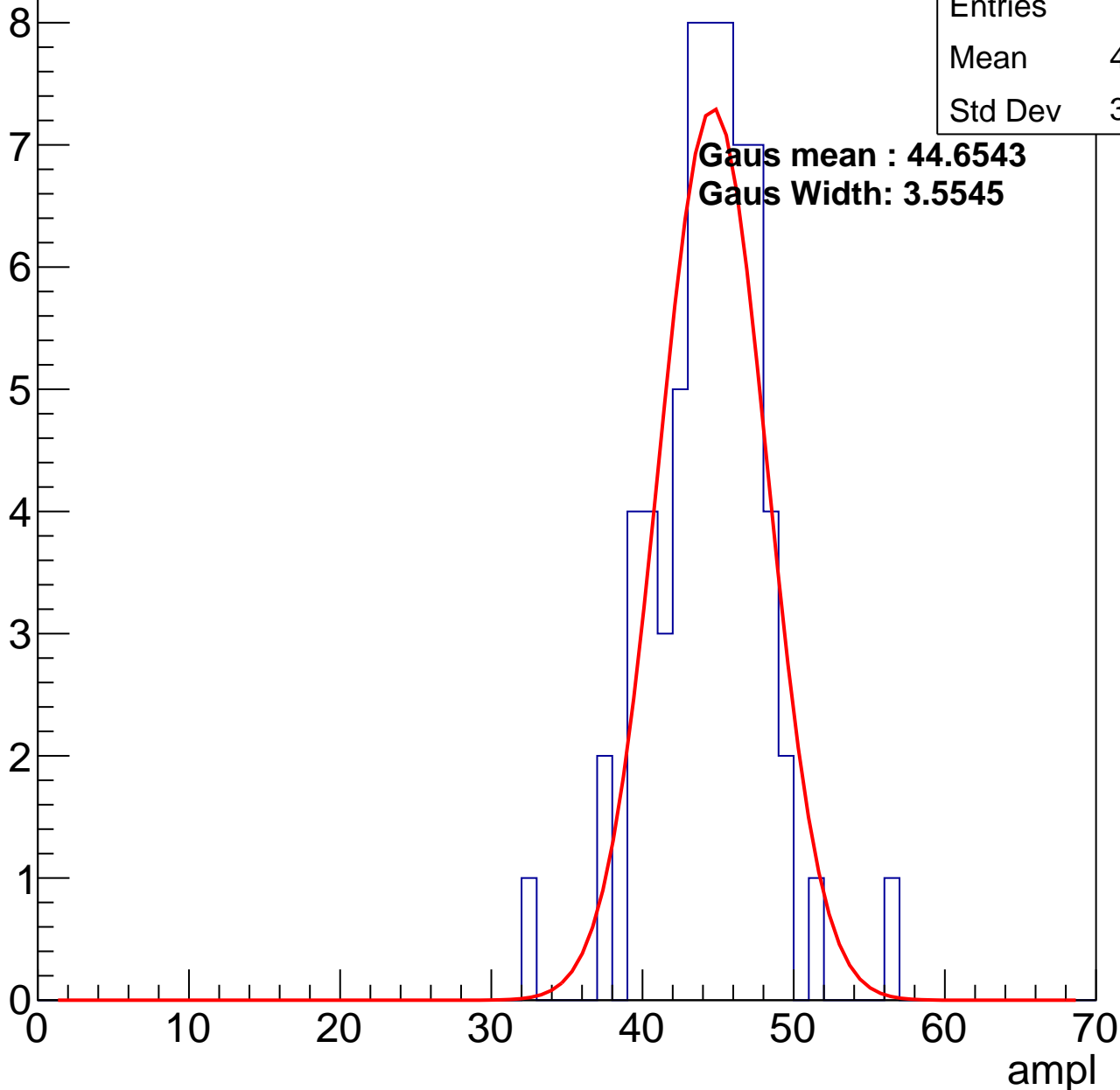
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.98
Std Dev	3.652

Gaus mean : 44.6543

Gaus Width: 3.5545

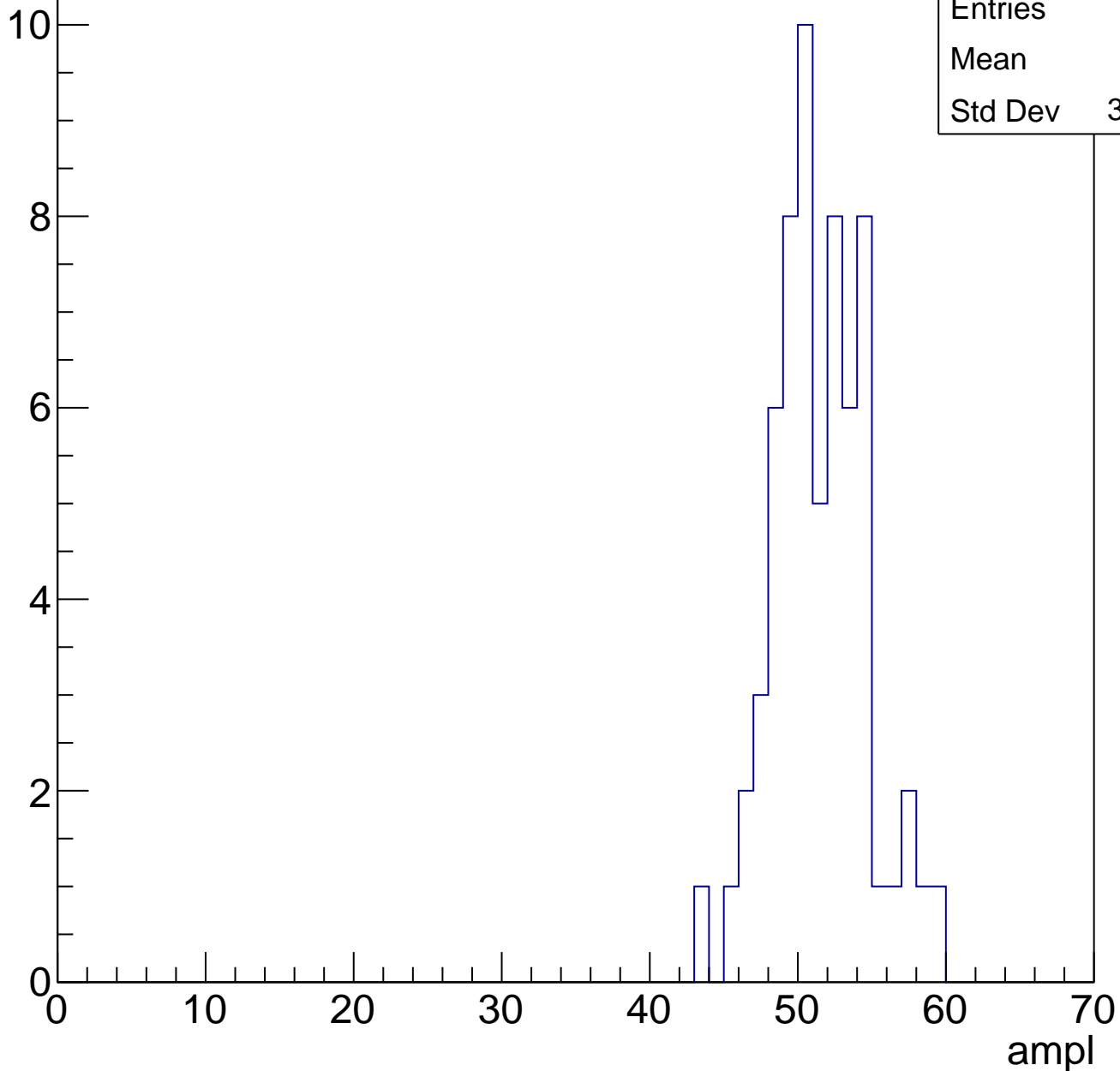


B0L001S, U23-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	51
Std Dev	3.122

Entry

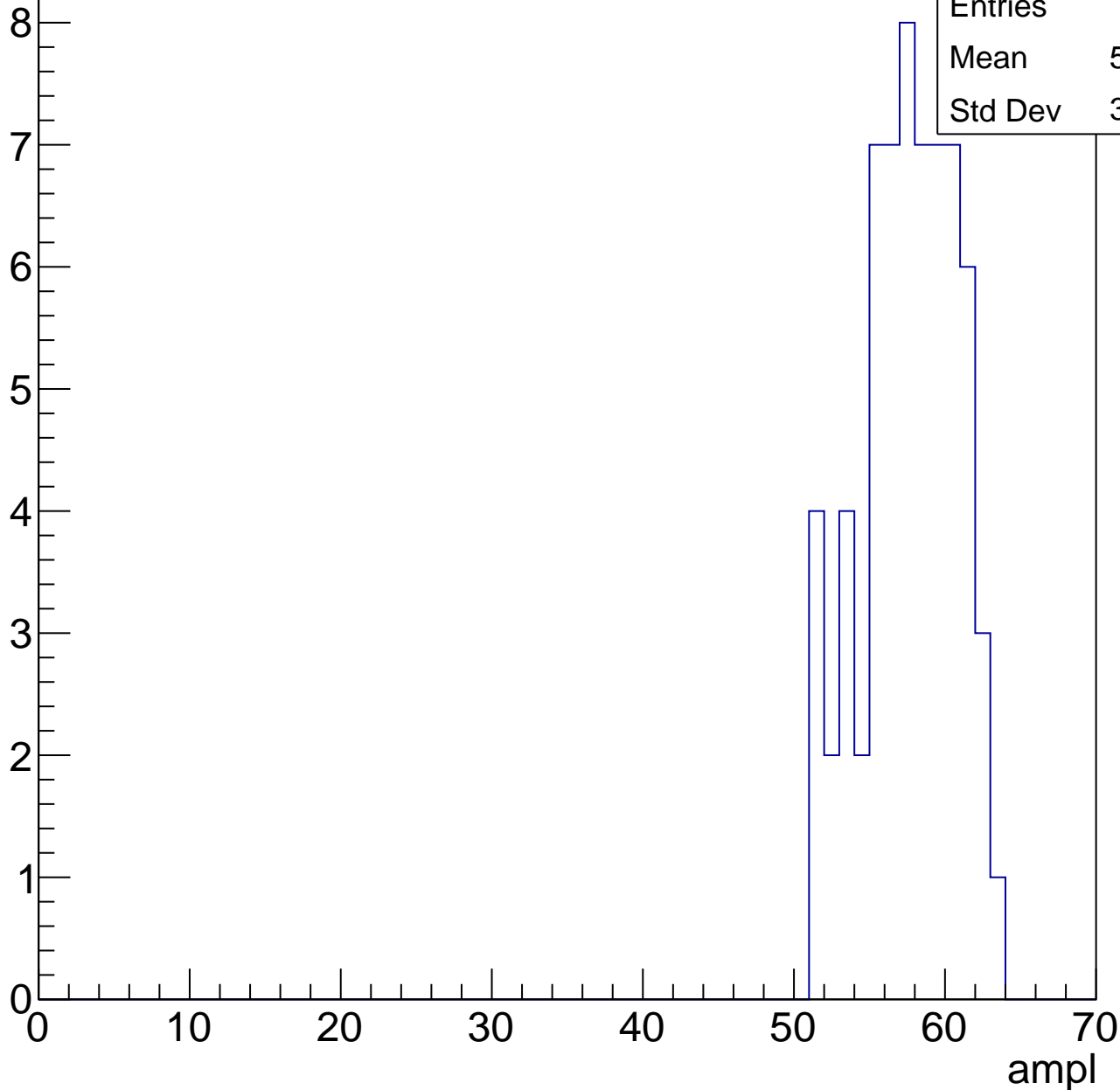


B0L001S, U23-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	57.15
Std Dev	3.075

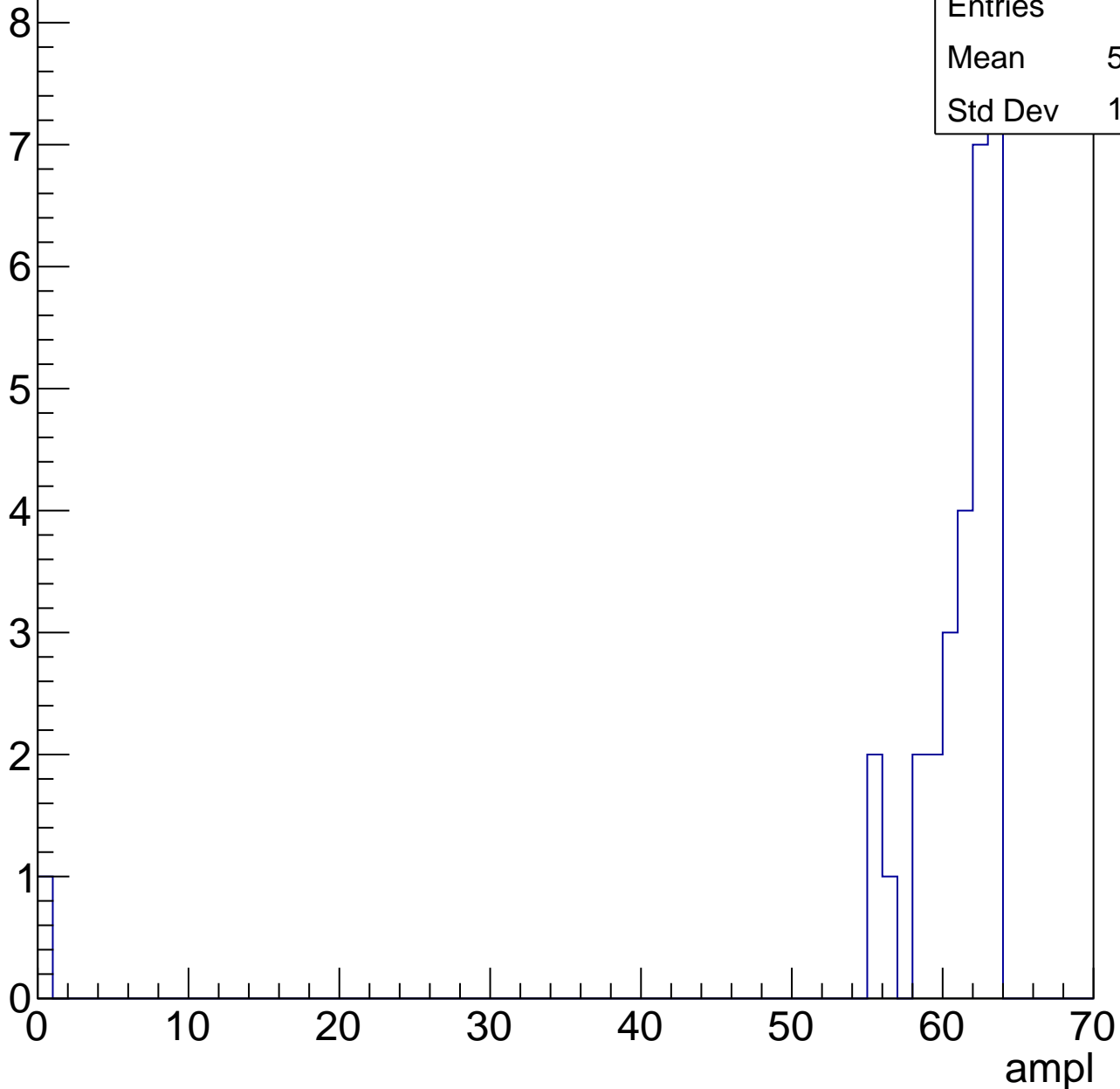


B0L001S, U23-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.73
Std Dev	11.15



B0L001S, U23-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch118, adc0

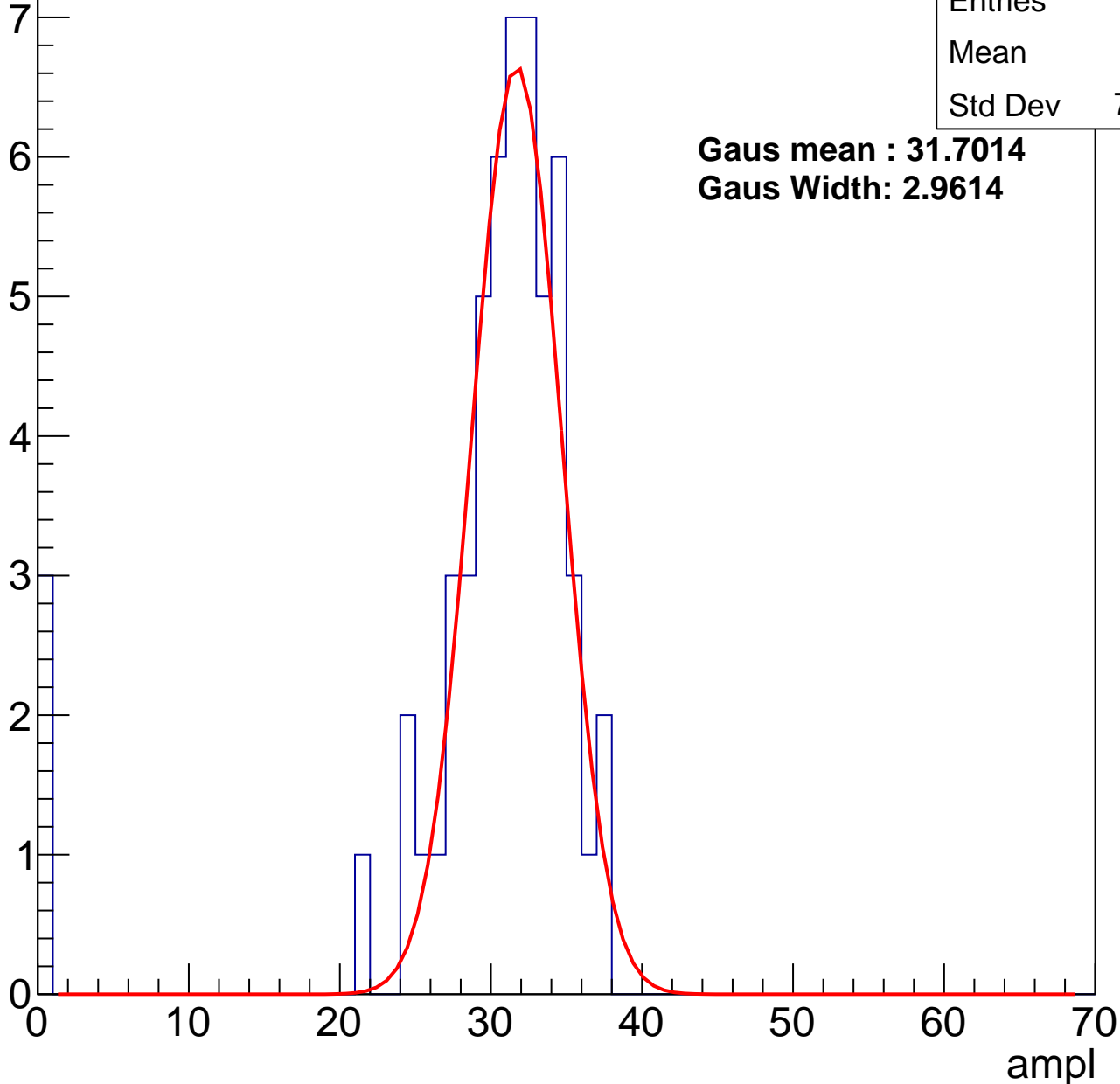
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.2
Std Dev	7.661

Gaus mean : 31.7014

Gaus Width: 2.9614



B0L001S, U23-ch118, adc1

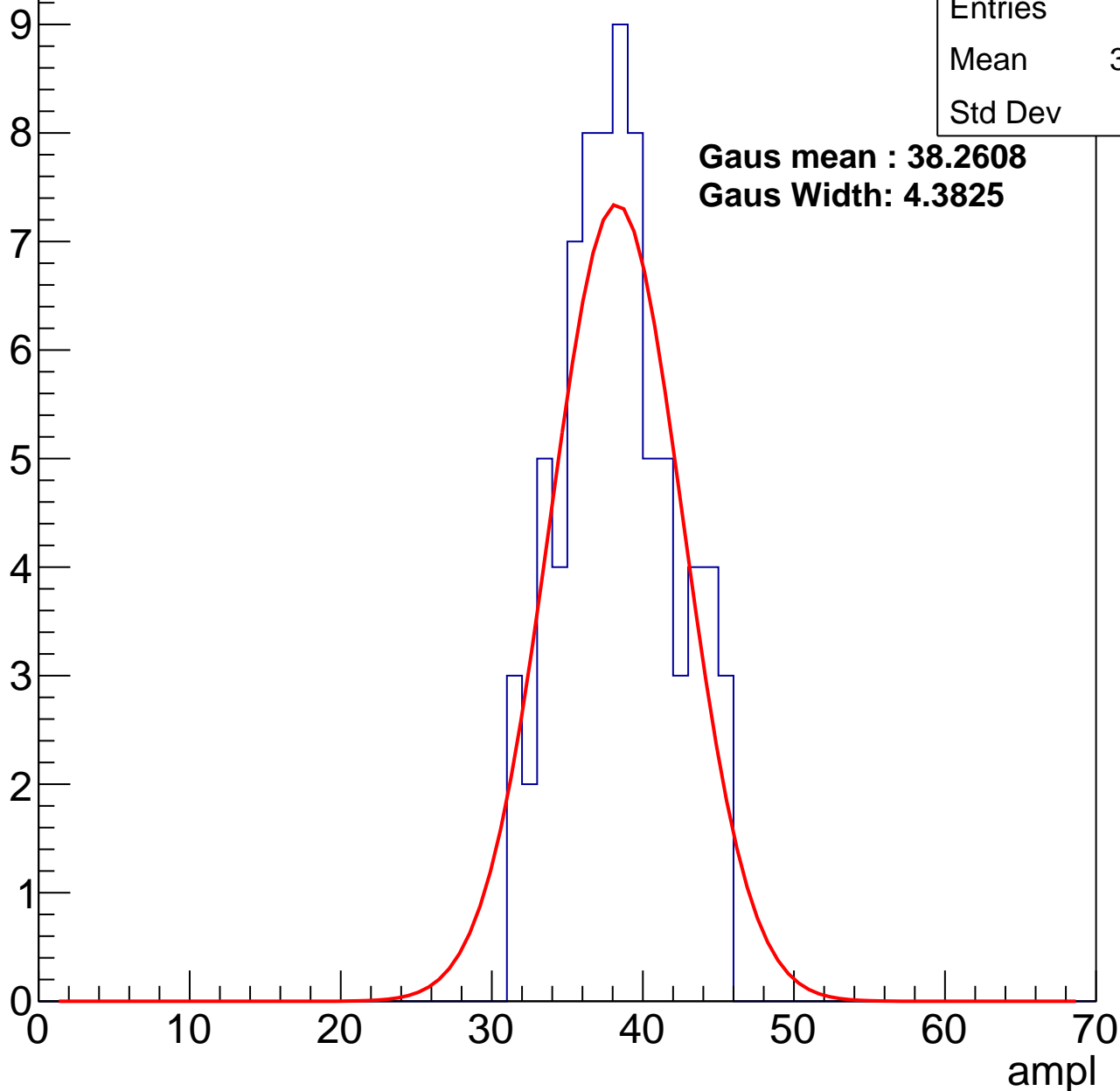
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.88
Std Dev	3.62

Gaus mean : 38.2608

Gaus Width: 4.3825



B0L001S, U23-ch118, adc2

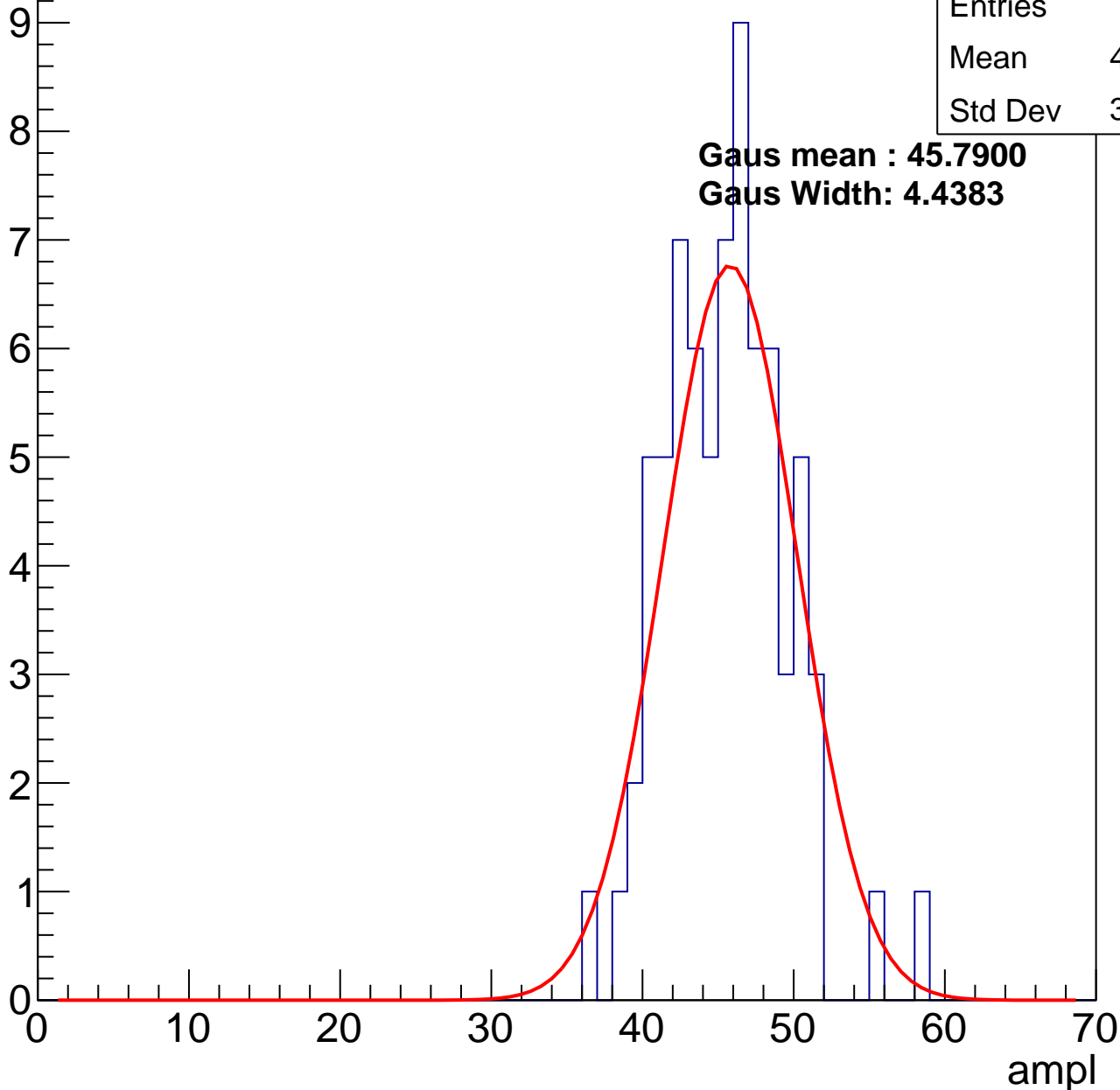
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.08
Std Dev	3.944

Gaus mean : 45.7900

Gaus Width: 4.4383

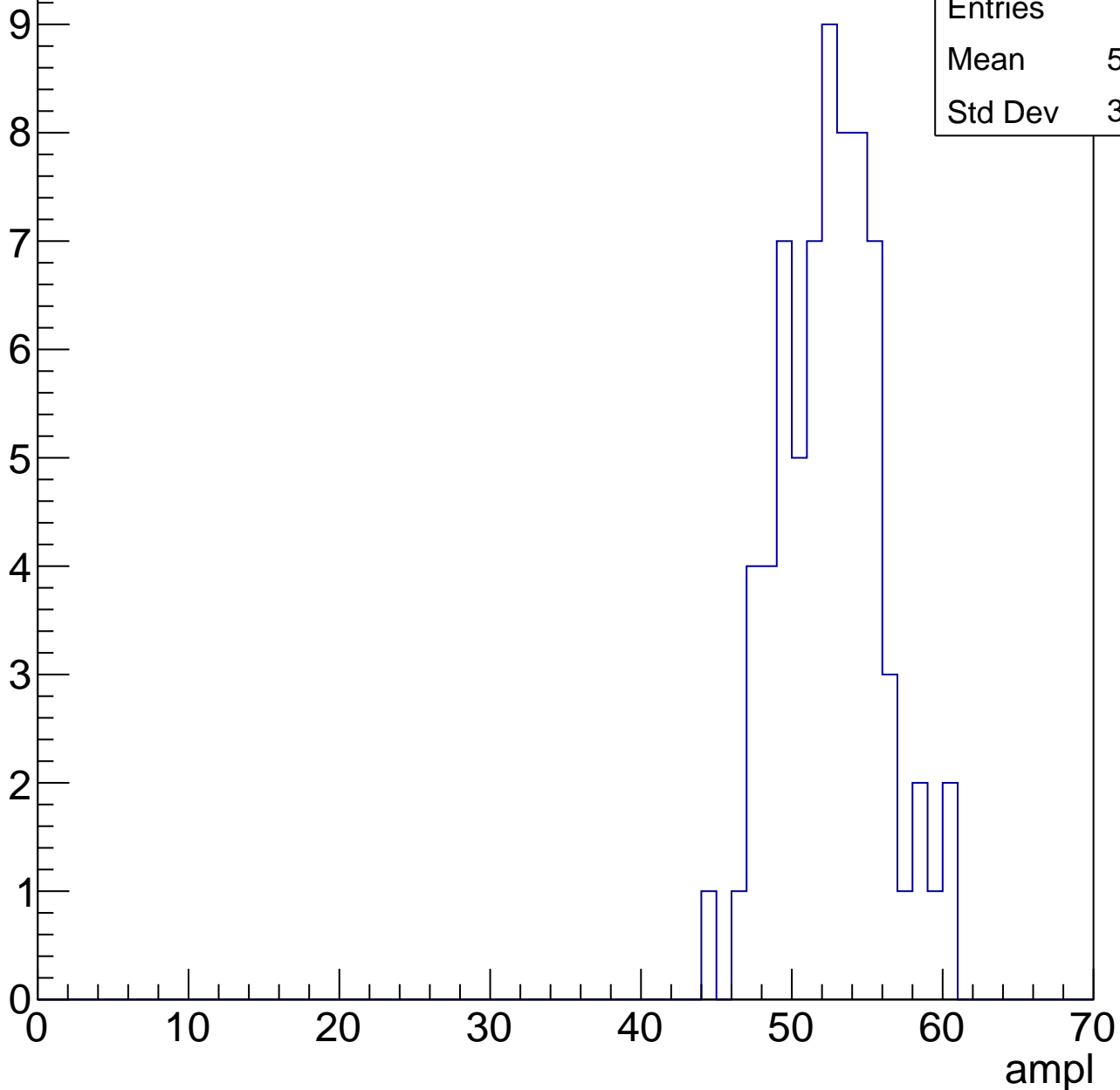


B0L001S, U23-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.13
Std Dev	3.333

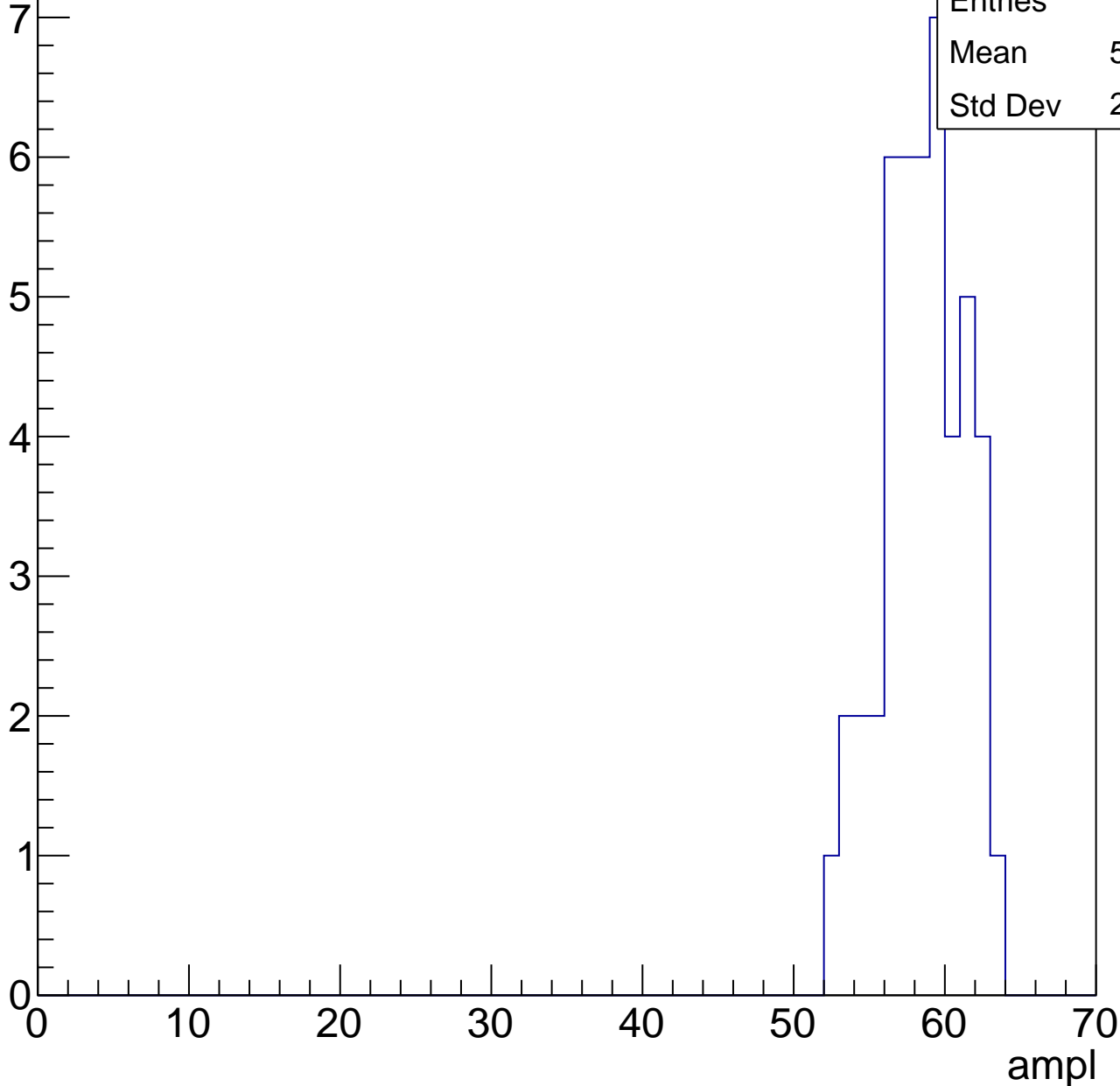


B0L001S, U23-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.07
Std Dev	2.649

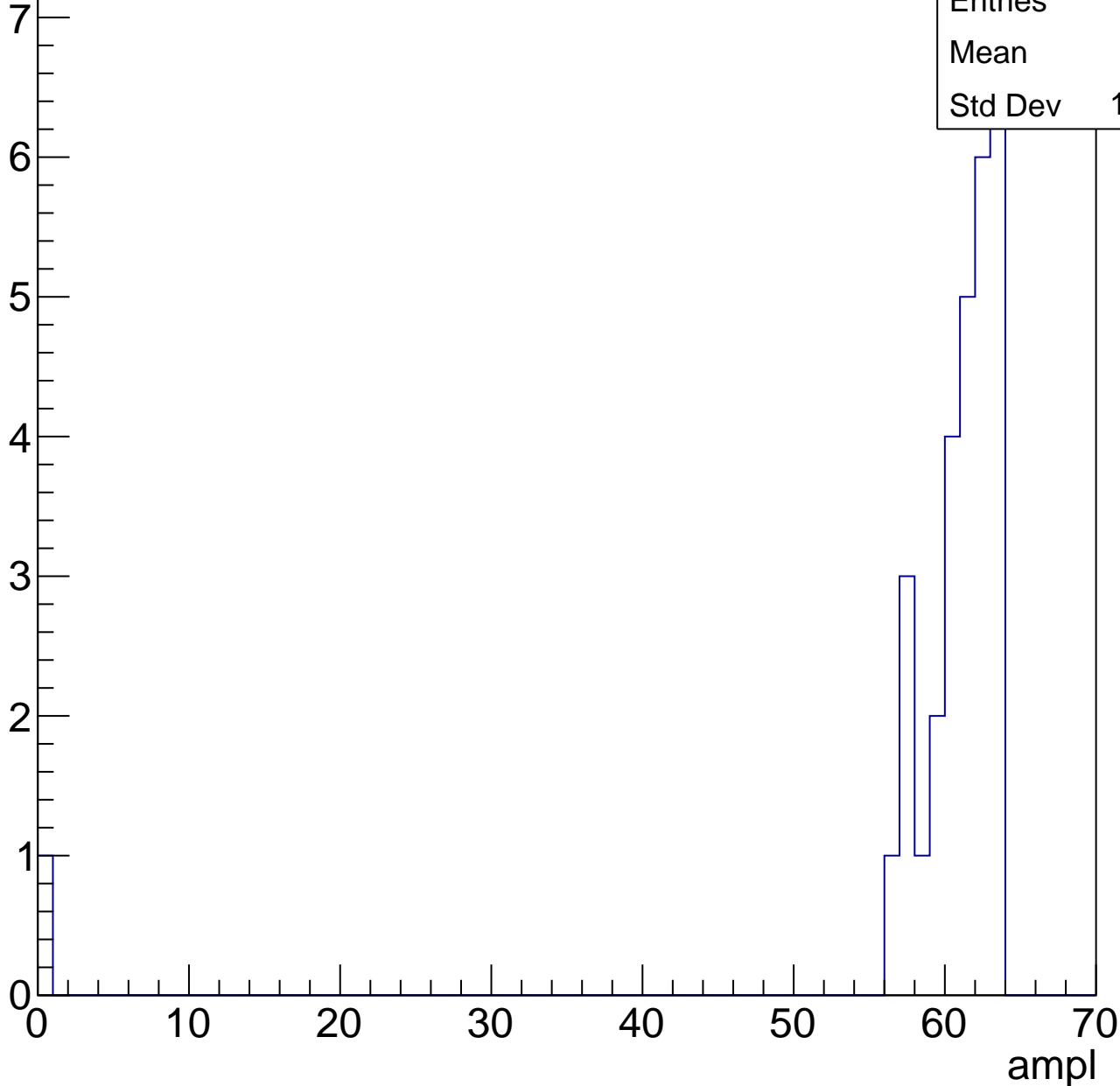


B0L001S, U23-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.7
Std Dev	11.09



B0L001S, U23-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U23-ch119, adc0

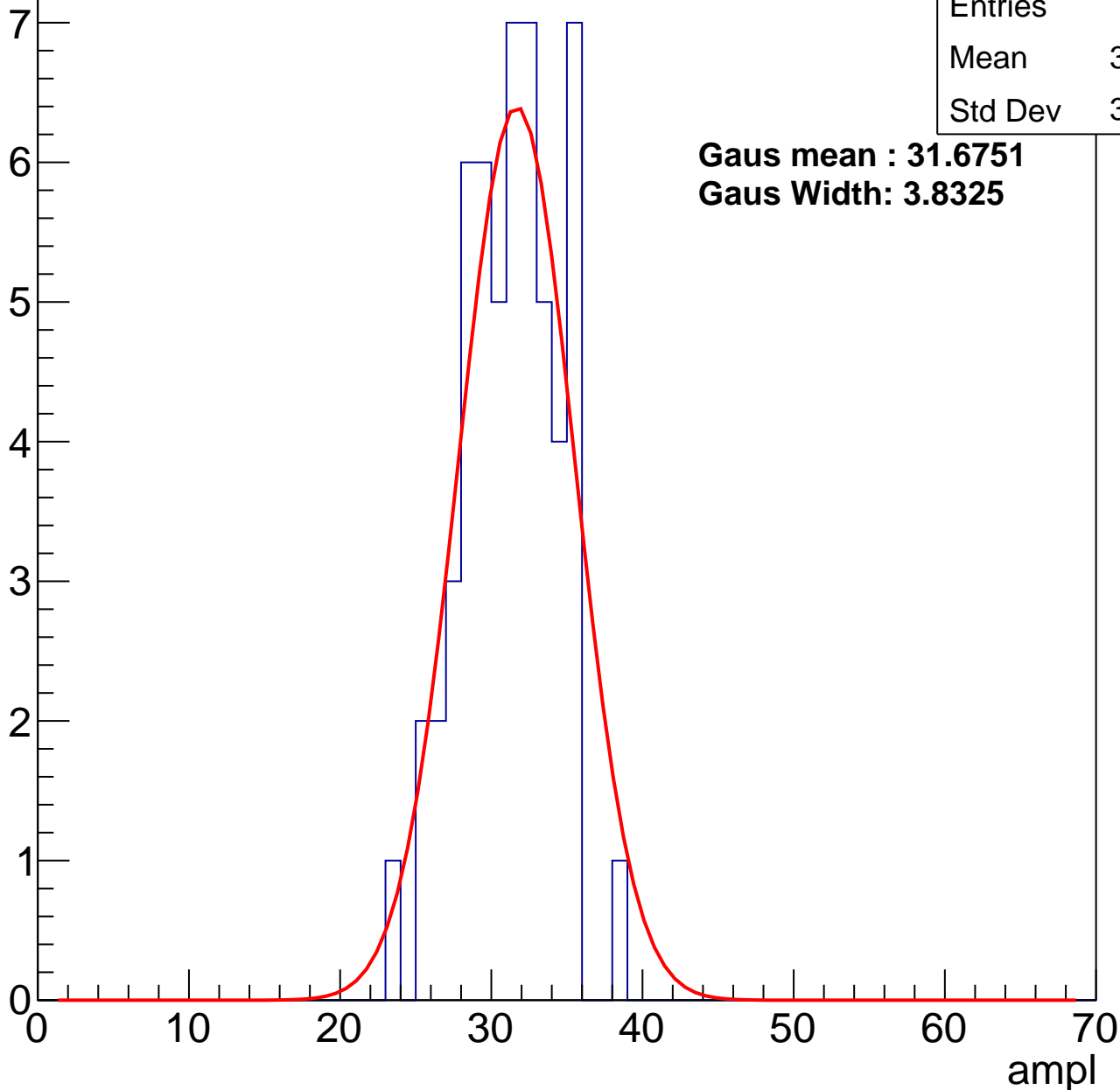
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.77
Std Dev	3.099

Gaus mean : 31.6751

Gaus Width: 3.8325



B0L001S, U23-ch119, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	90
Mean	38.82
Std Dev	3.548

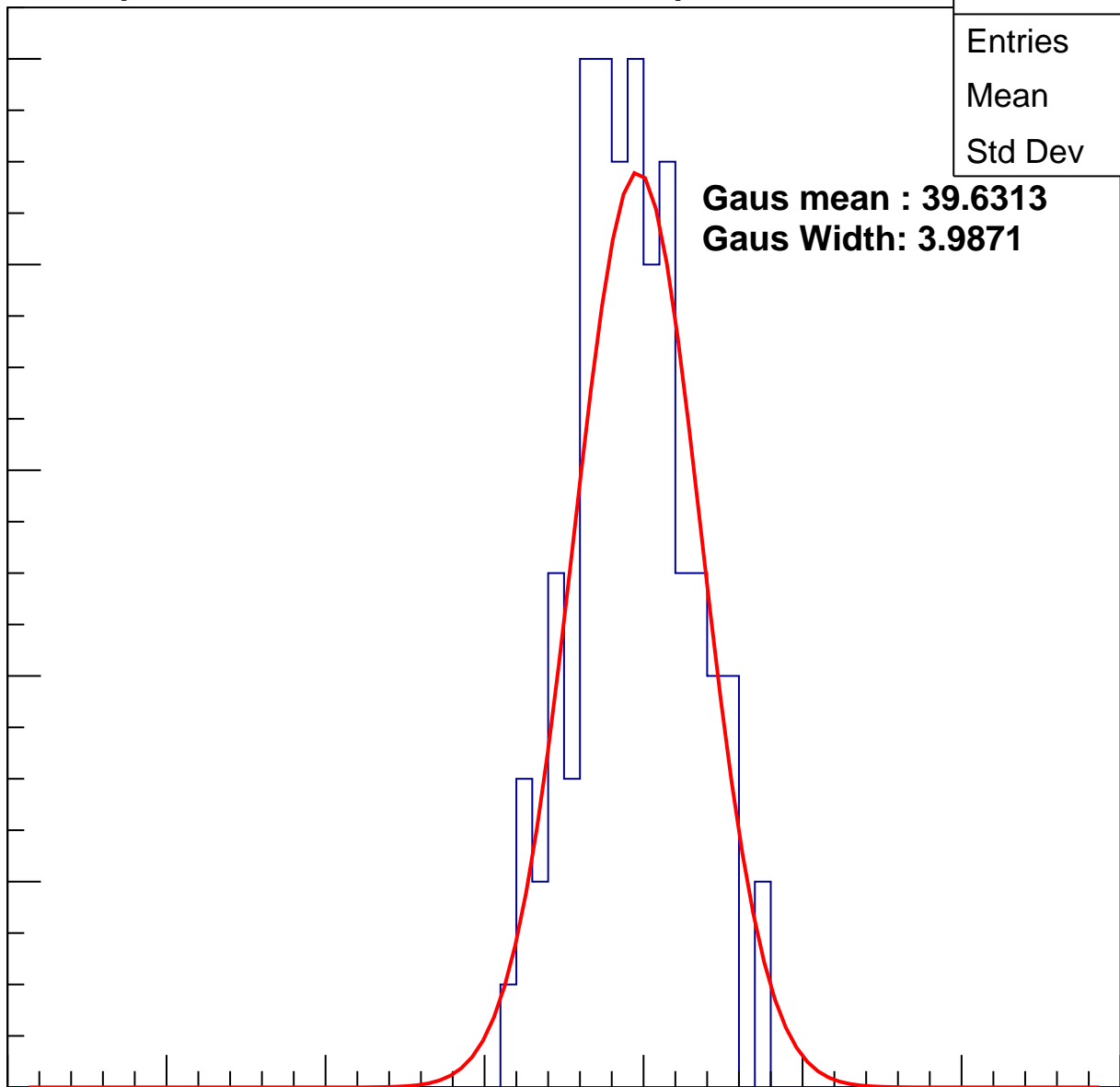
Gaus mean : 39.6313
Gaus Width: 3.9871

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U23-ch119, adc2

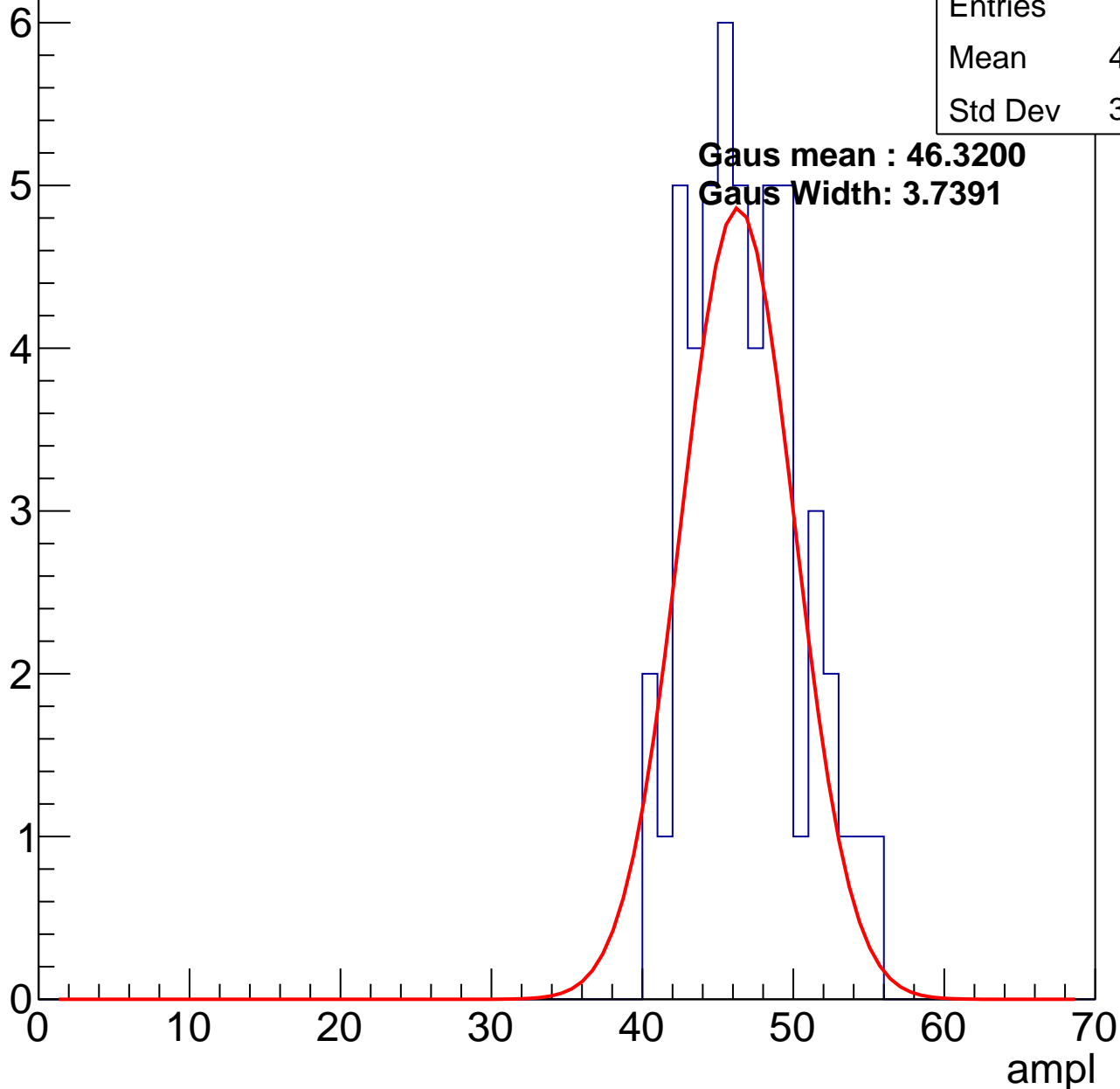
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	46.37
Std Dev	3.592

Gaus mean : 46.3200

Gaus Width: 3.7391

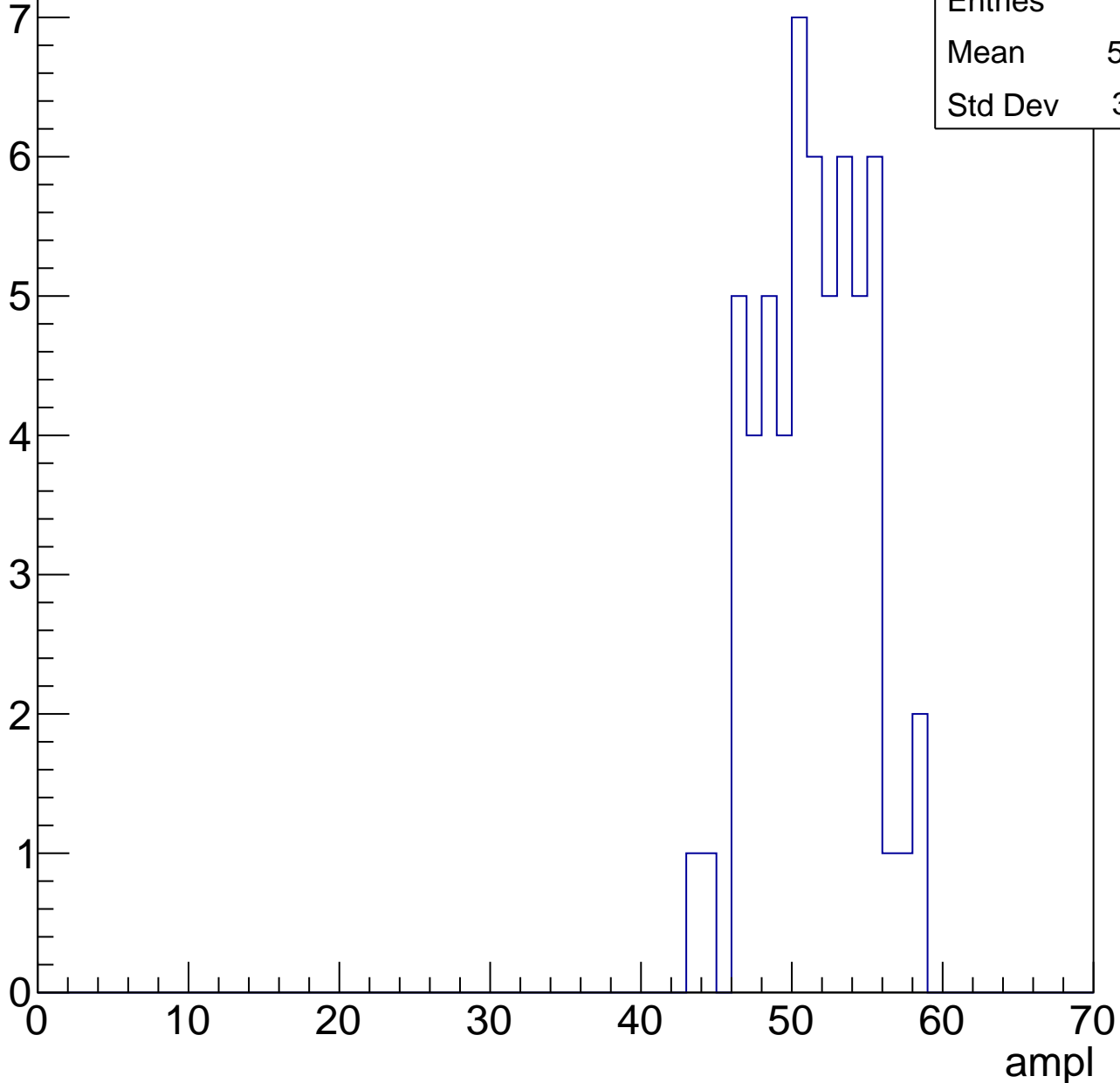


B0L001S, U23-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

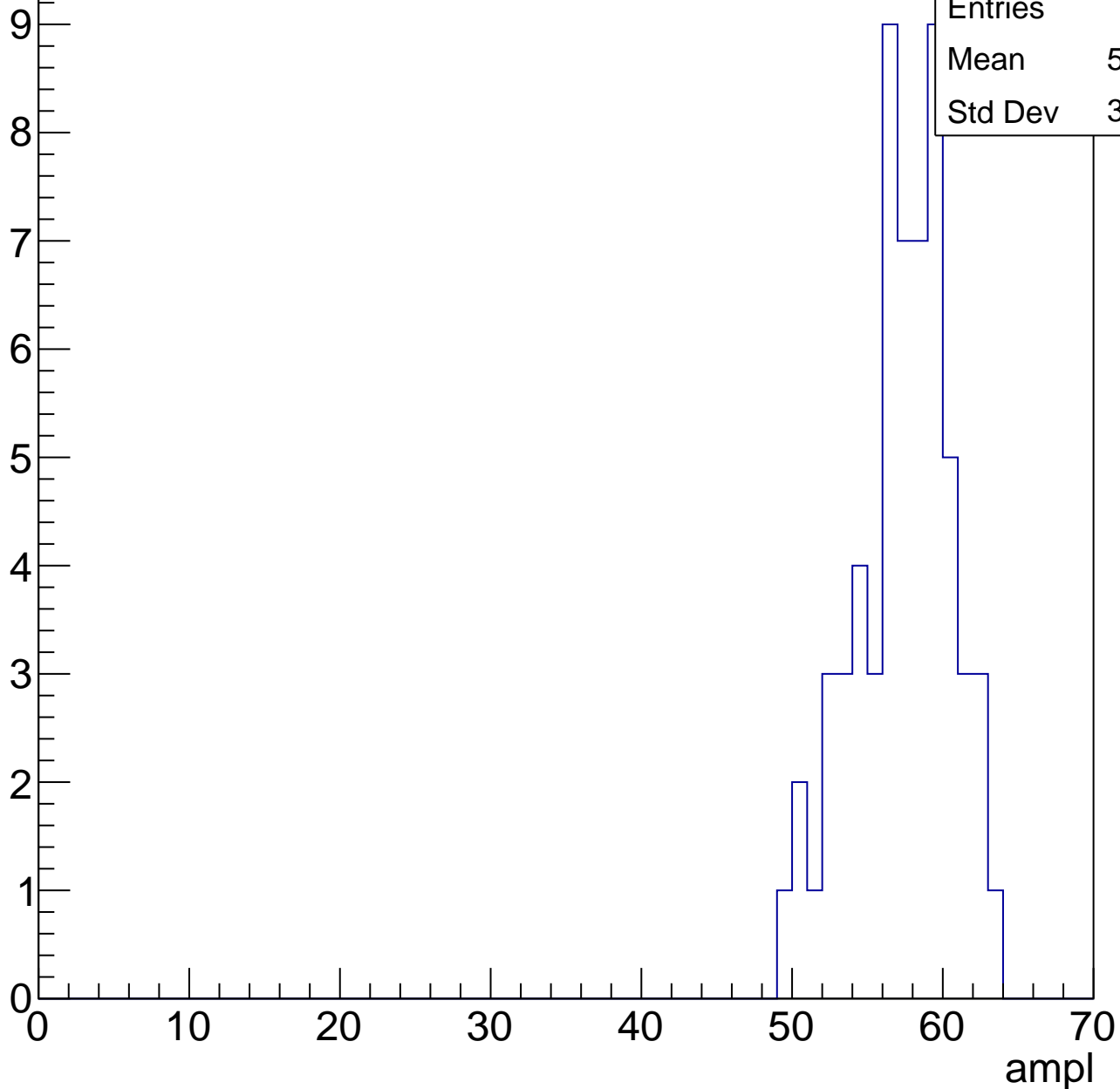
Entries	59
Mean	50.92
Std Dev	3.441



B0L001S, U23-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



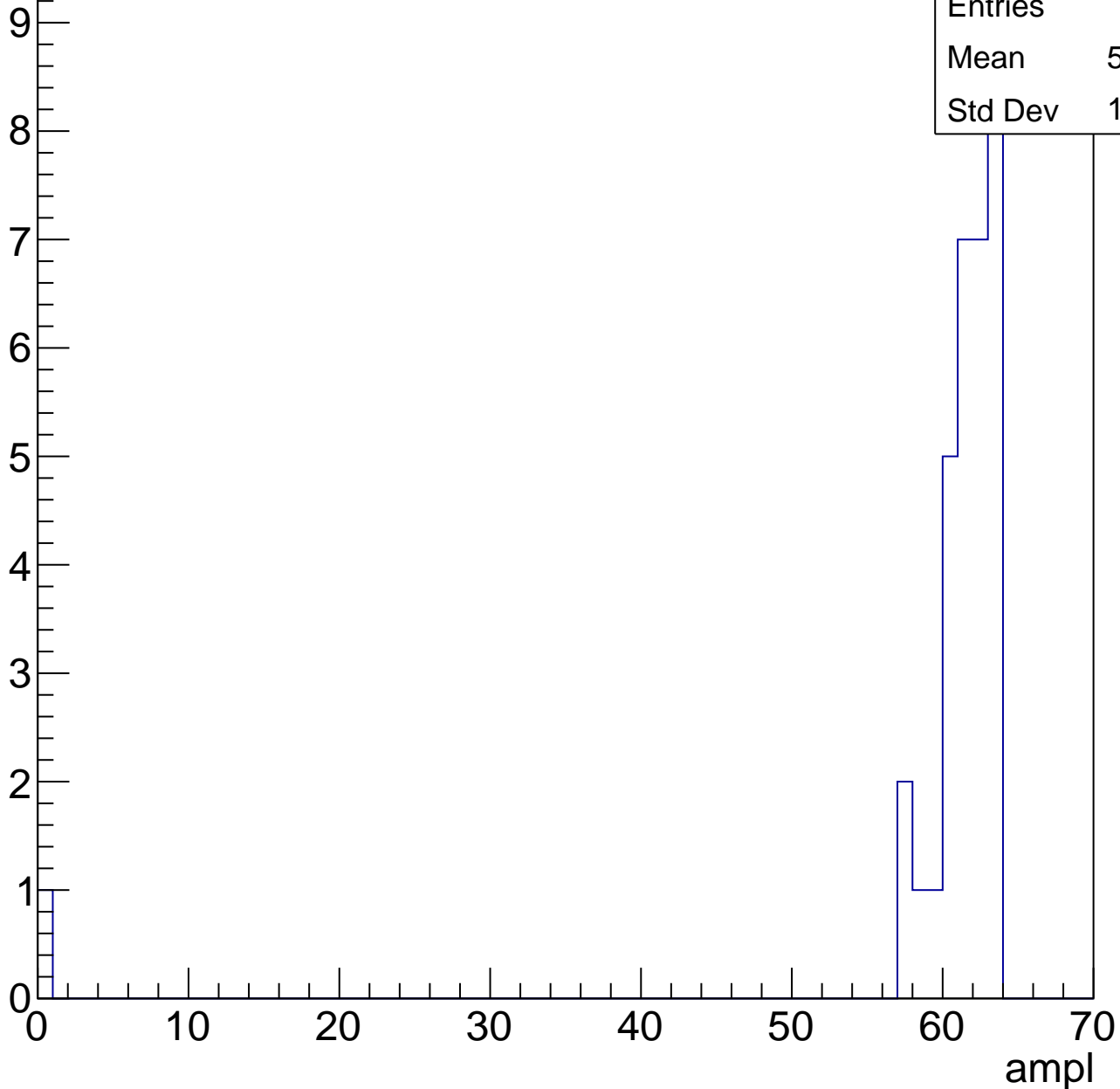
Entries	61
Mean	56.85
Std Dev	3.198

B0L001S, U23-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.36
Std Dev	10.63



B0L001S, U23-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch120, adc0

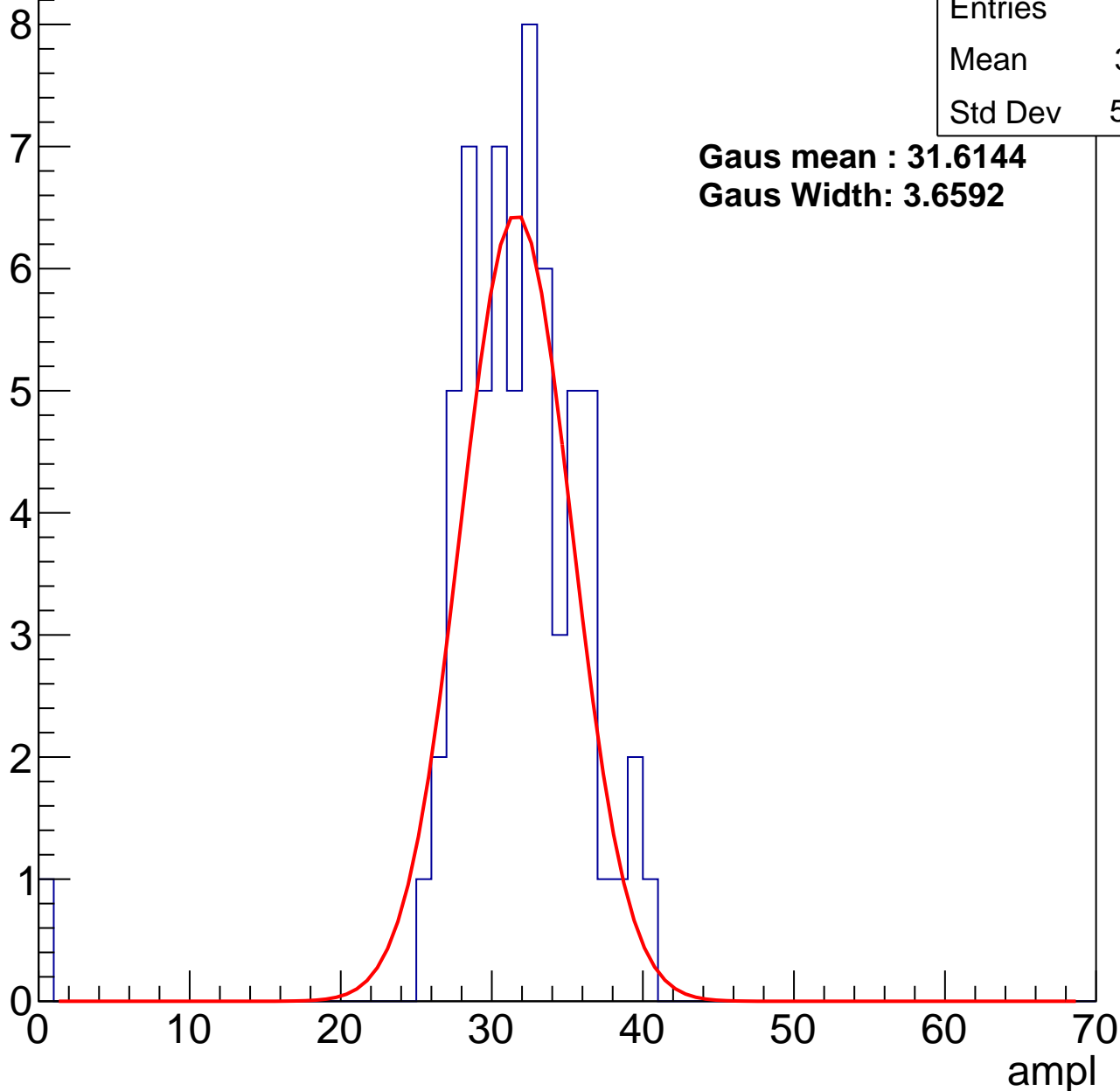
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.11
Std Dev	5.225

Gaus mean : 31.6144

Gaus Width: 3.6592



B0L001S, U23-ch120, adc1

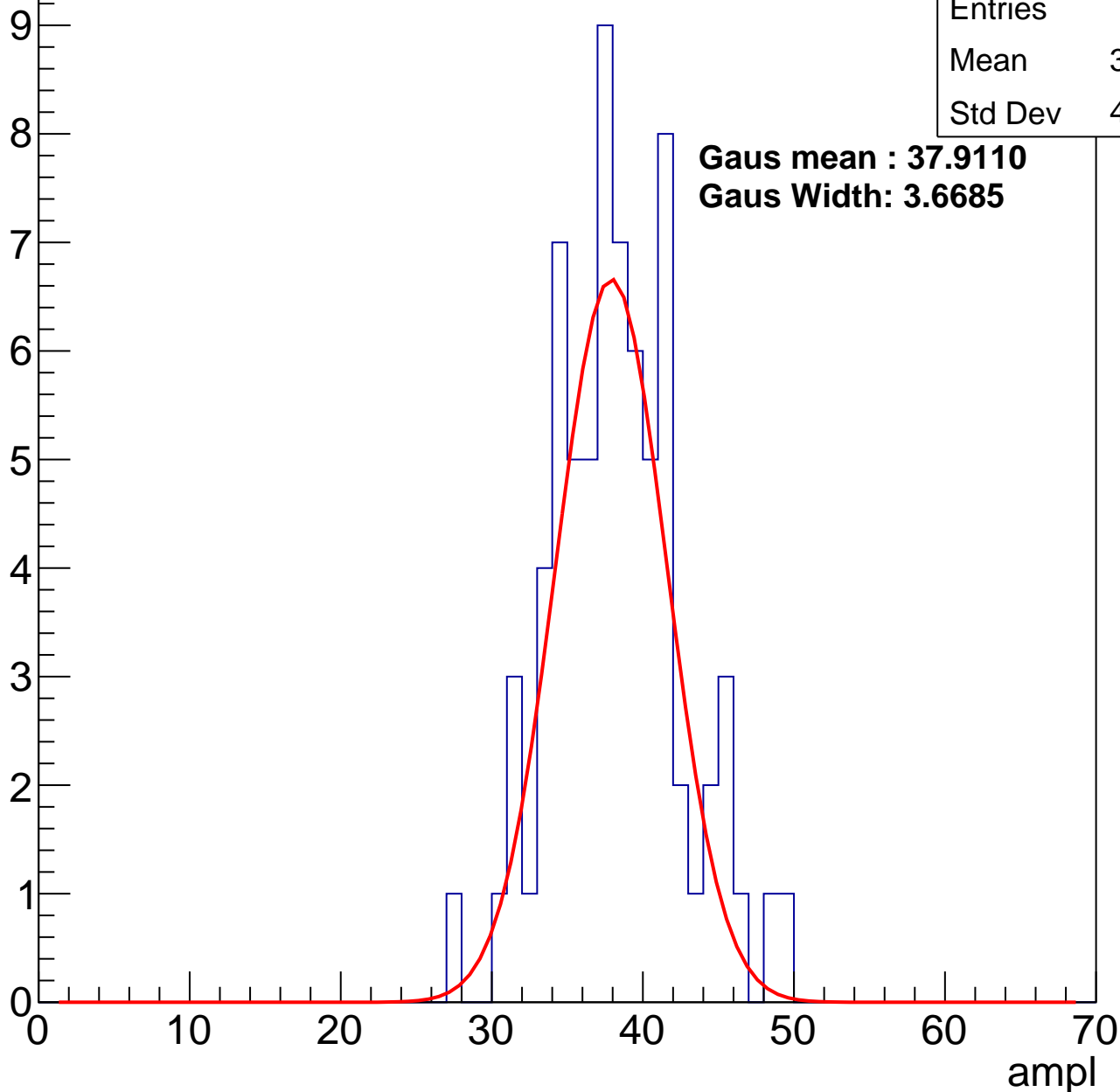
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.82
Std Dev	4.237

Gaus mean : 37.9110

Gaus Width: 3.6685



B0L001S, U23-ch120, adc2

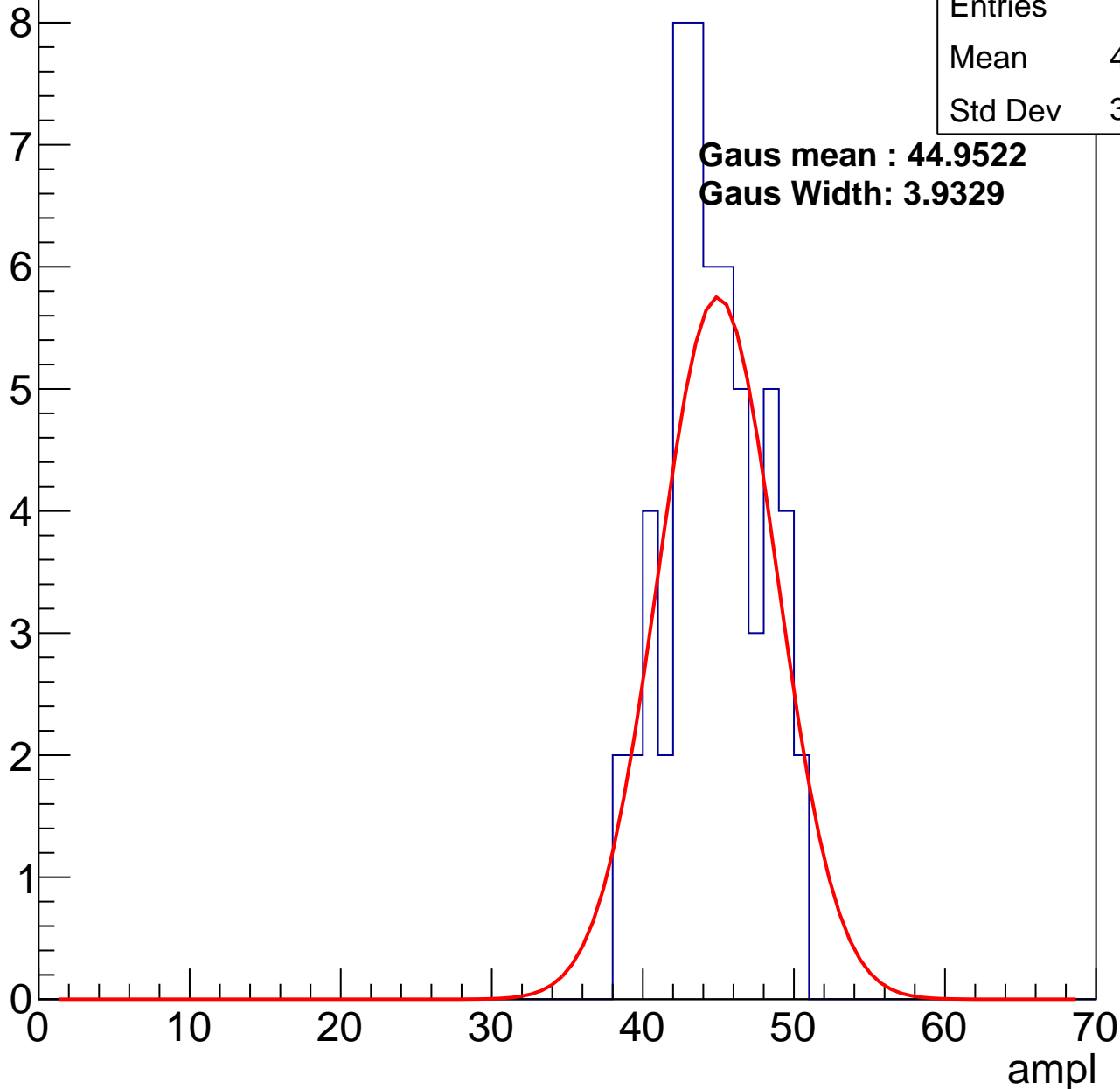
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.16
Std Dev	3.099

Gaus mean : 44.9522

Gaus Width: 3.9329

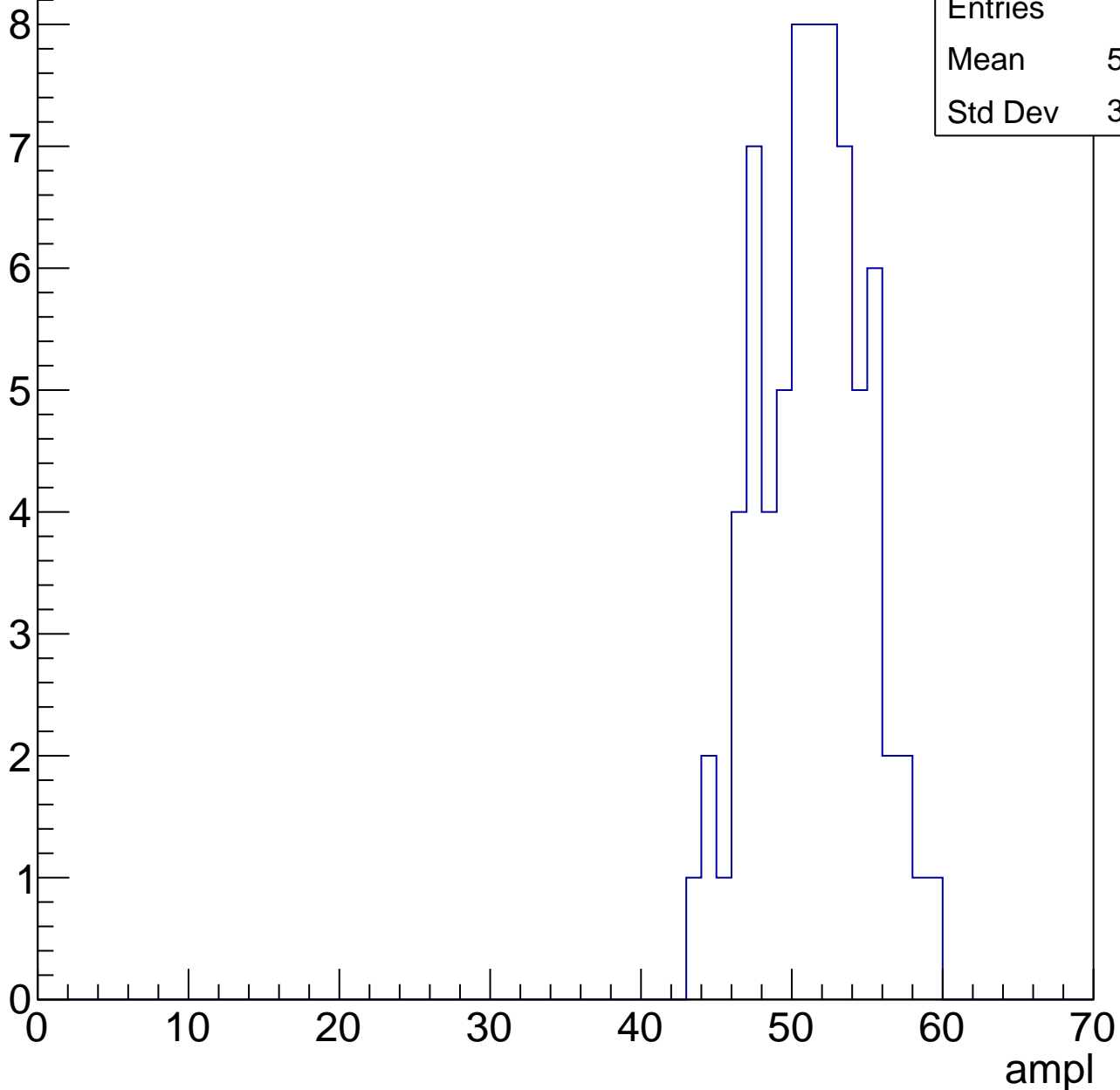


B0L001S, U23-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	50.89
Std Dev	3.506

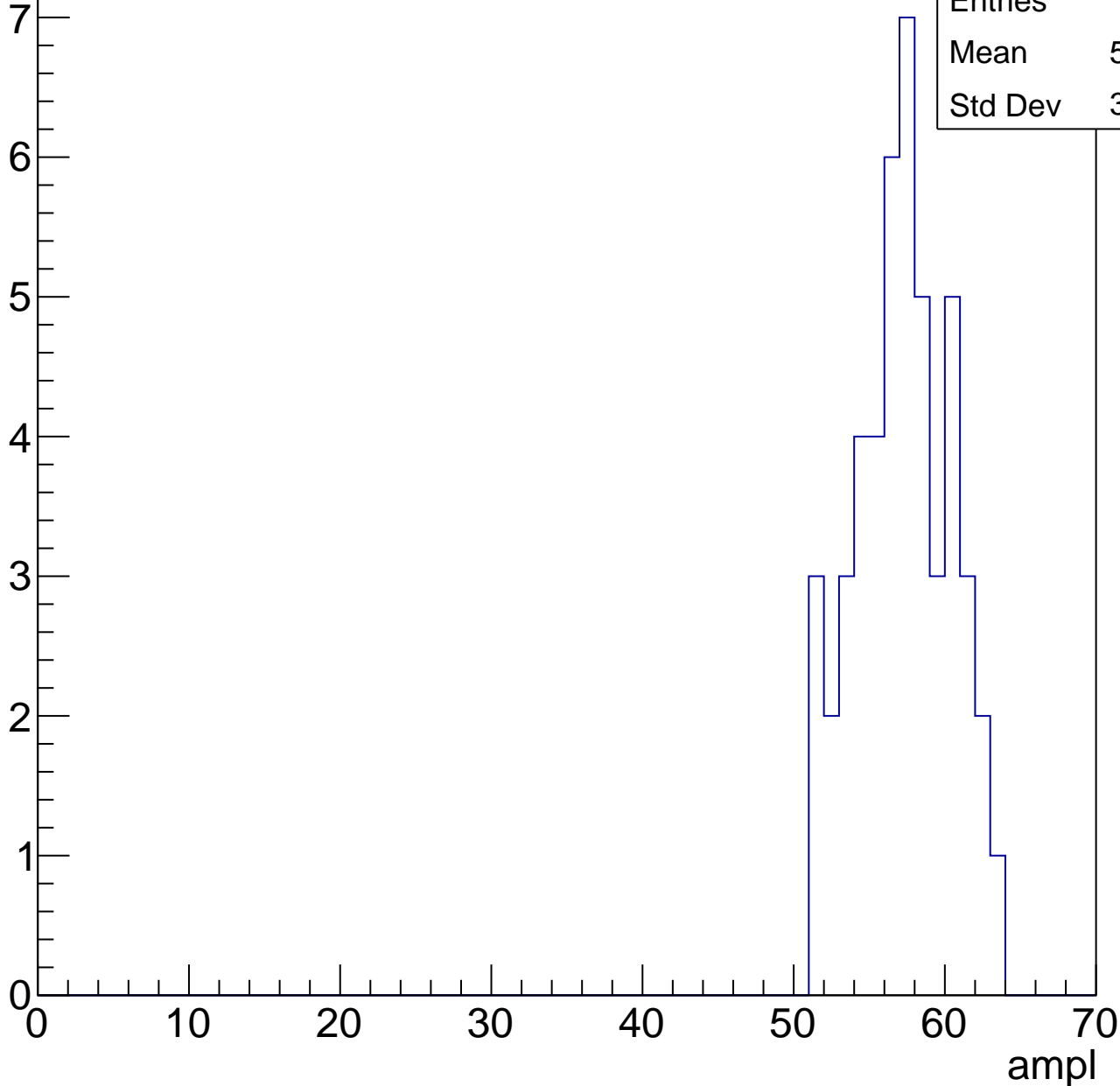


B0L001S, U23-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.75
Std Dev	3.086

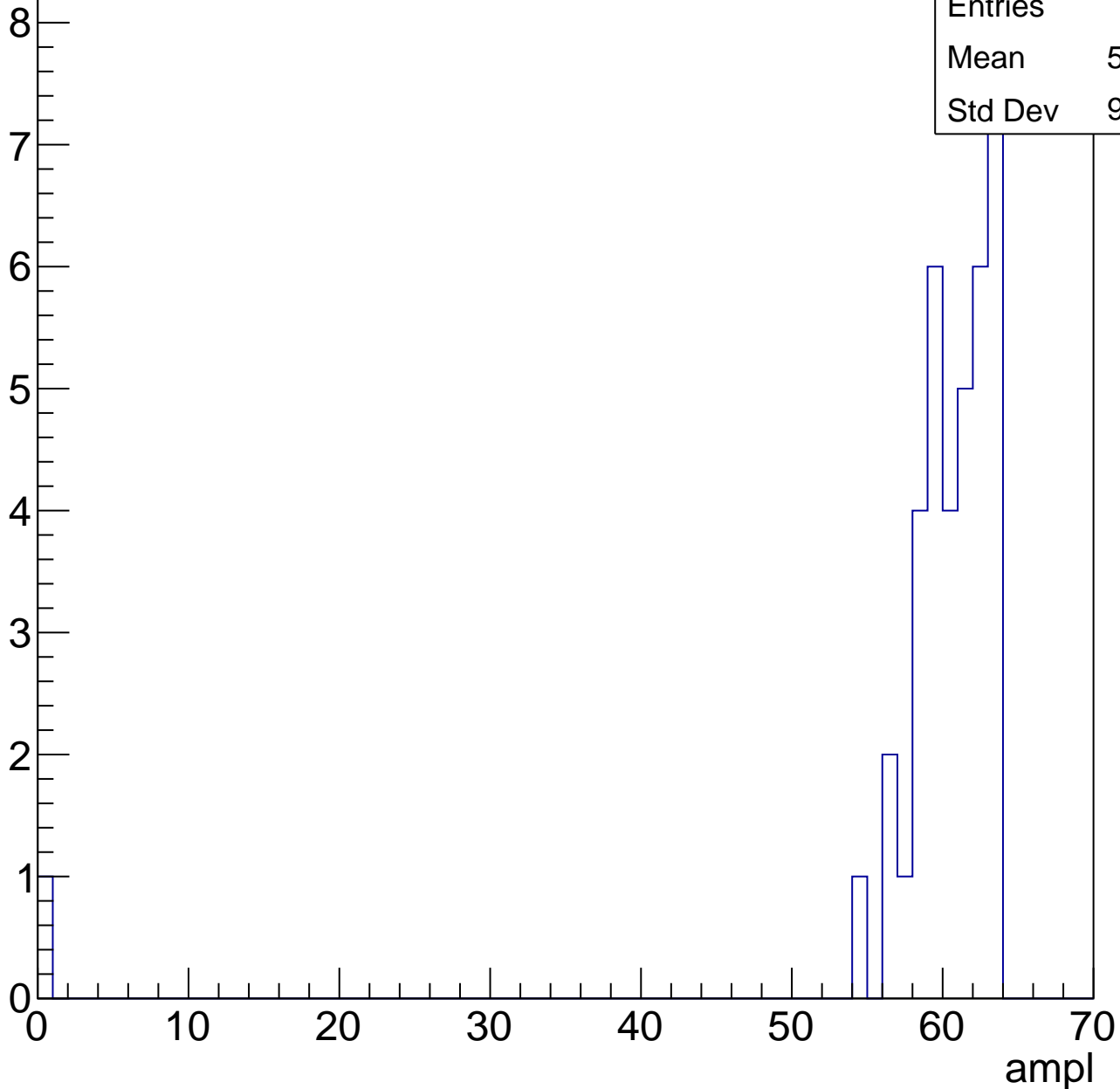


B0L001S, U23-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.68
Std Dev	9.913



B0L001S, U23-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70

B0L001S, U23-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch121, adc0

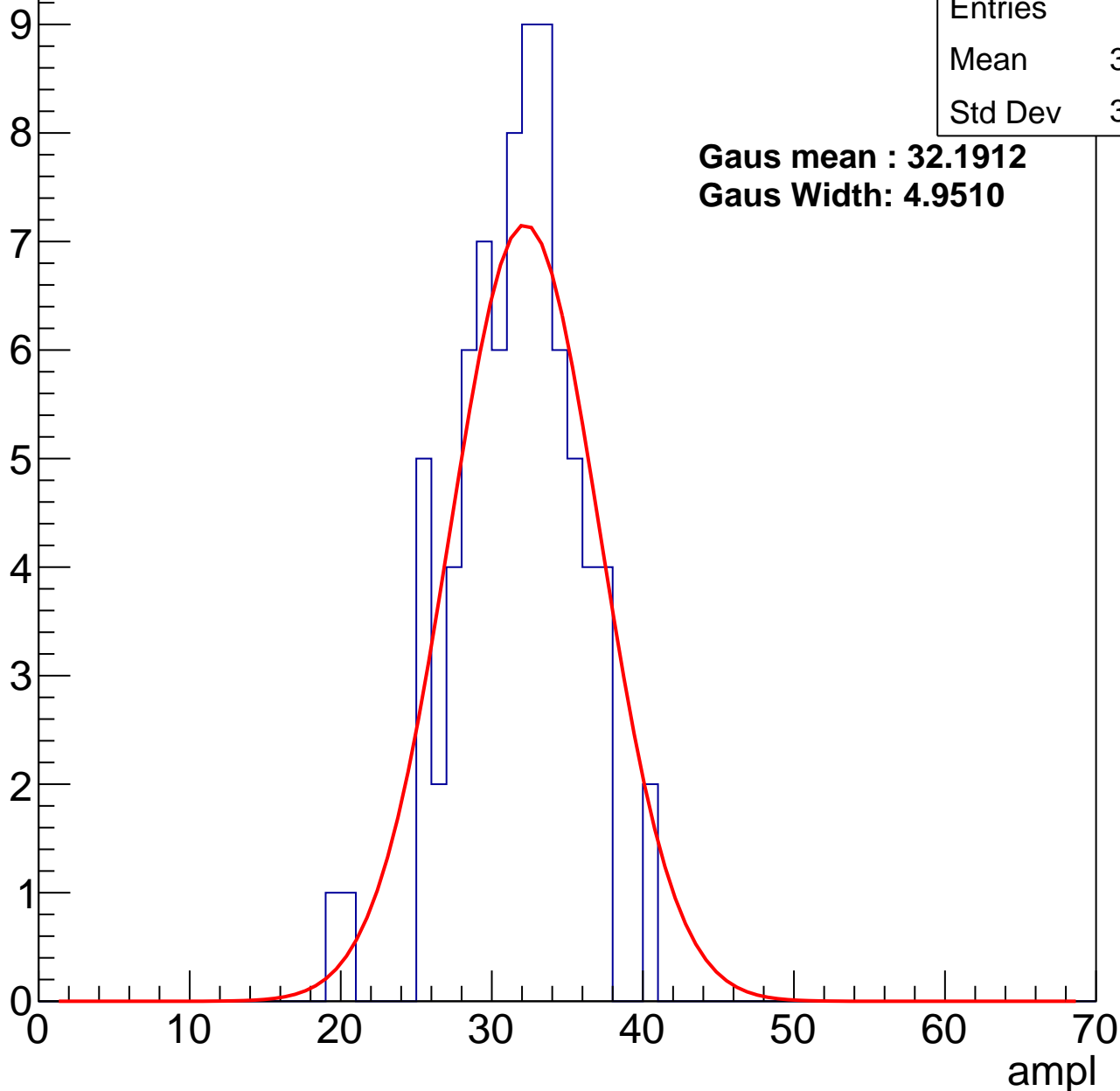
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	31.13
Std Dev	3.947

Gaus mean : 32.1912

Gaus Width: 4.9510



B0L001S, U23-ch121, adc1

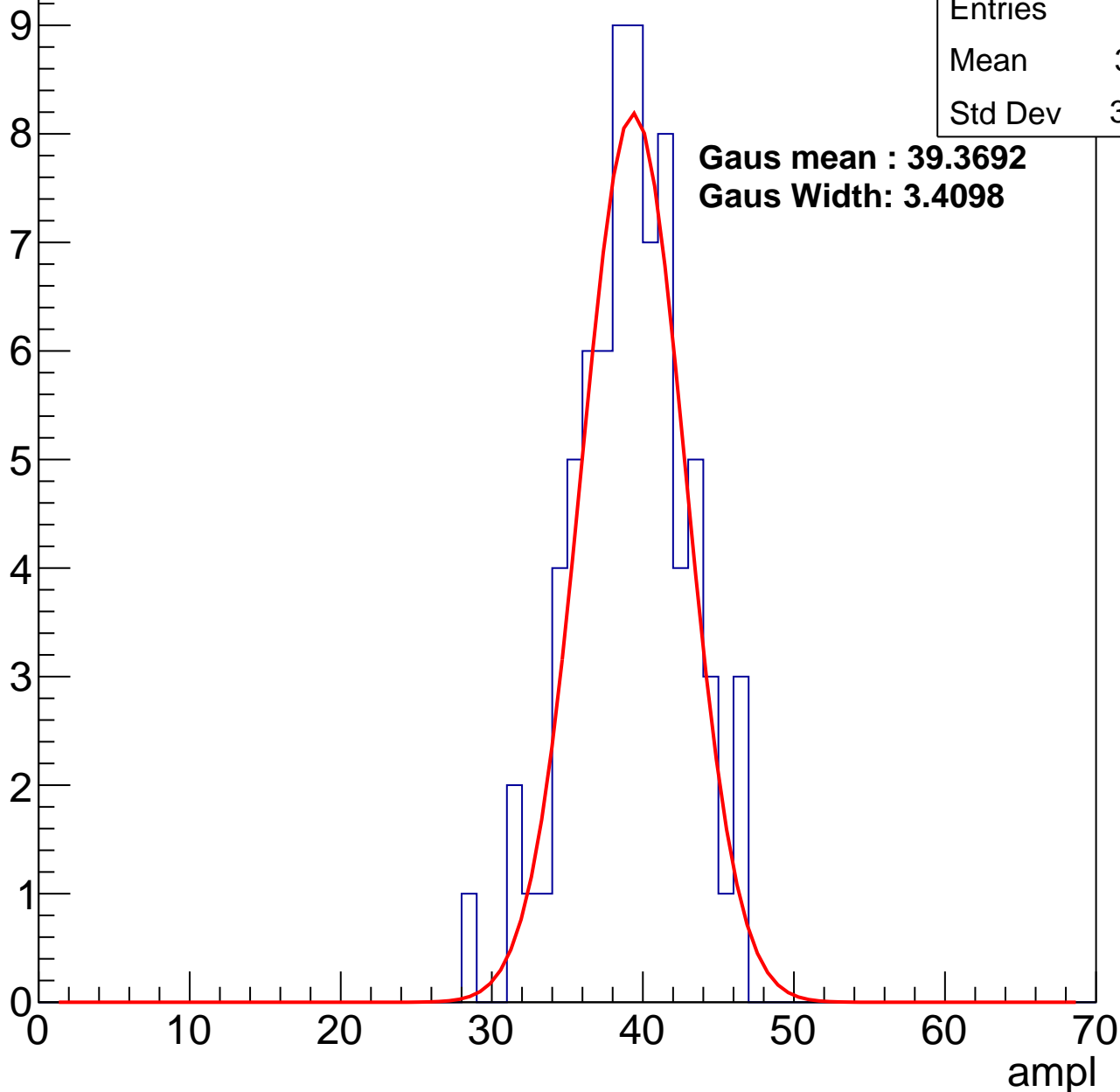
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	38.71
Std Dev	3.665

Gaus mean : 39.3692

Gaus Width: 3.4098



B0L001S, U23-ch121, adc2

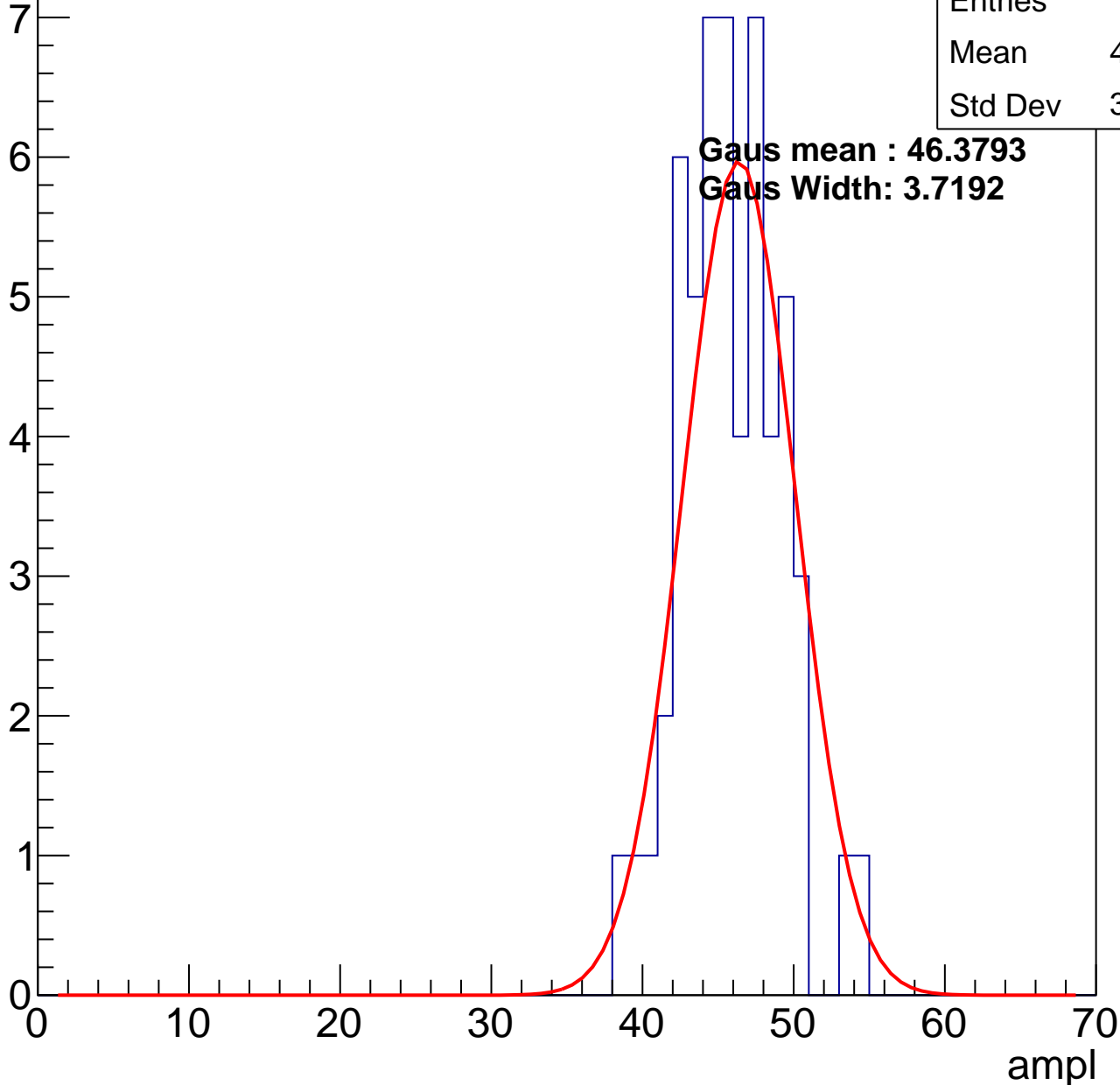
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	45.38
Std Dev	3.256

Gaus mean : 46.3793

Gaus Width: 3.7192

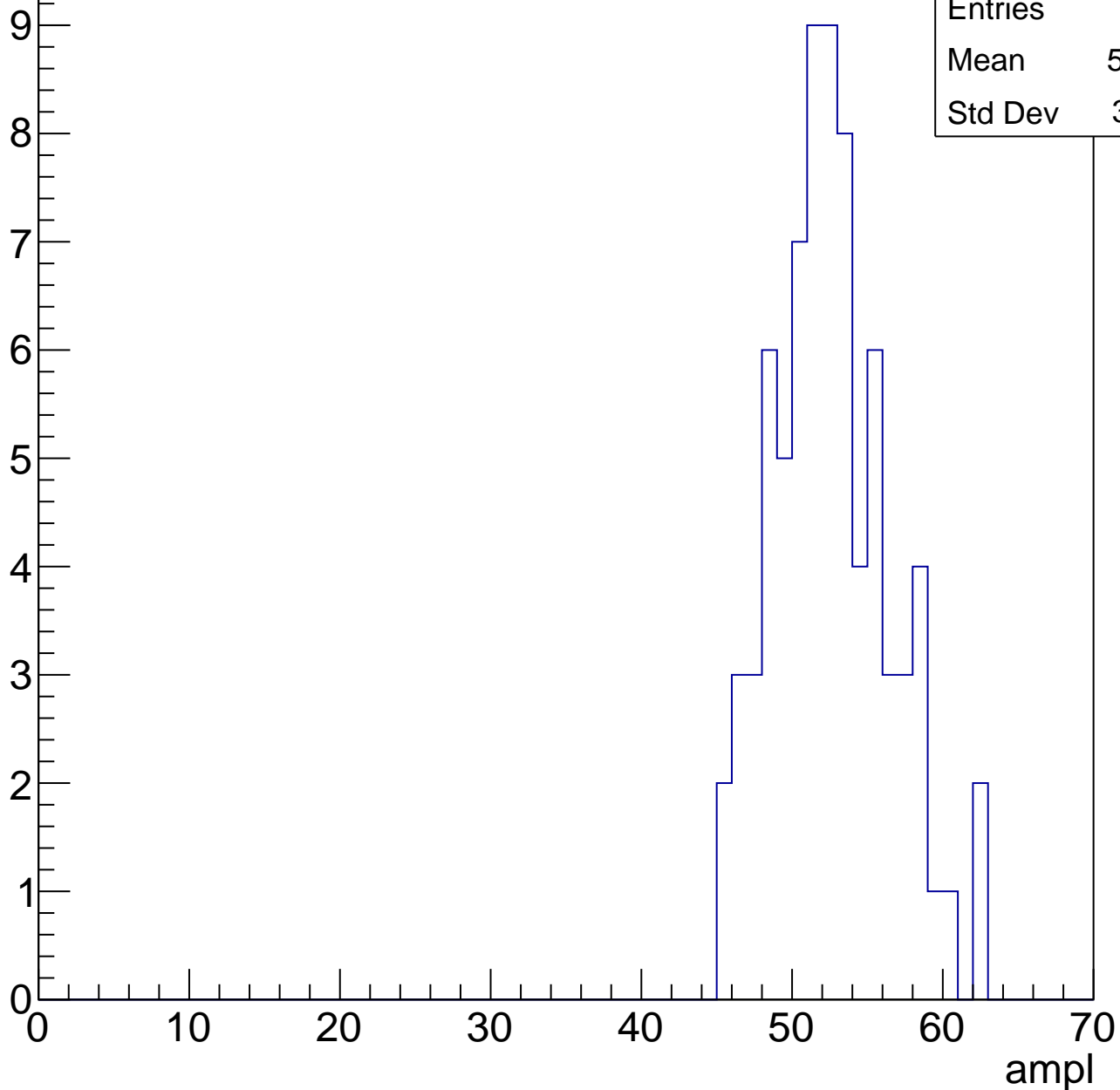


B0L001S, U23-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	52.14
Std Dev	3.831

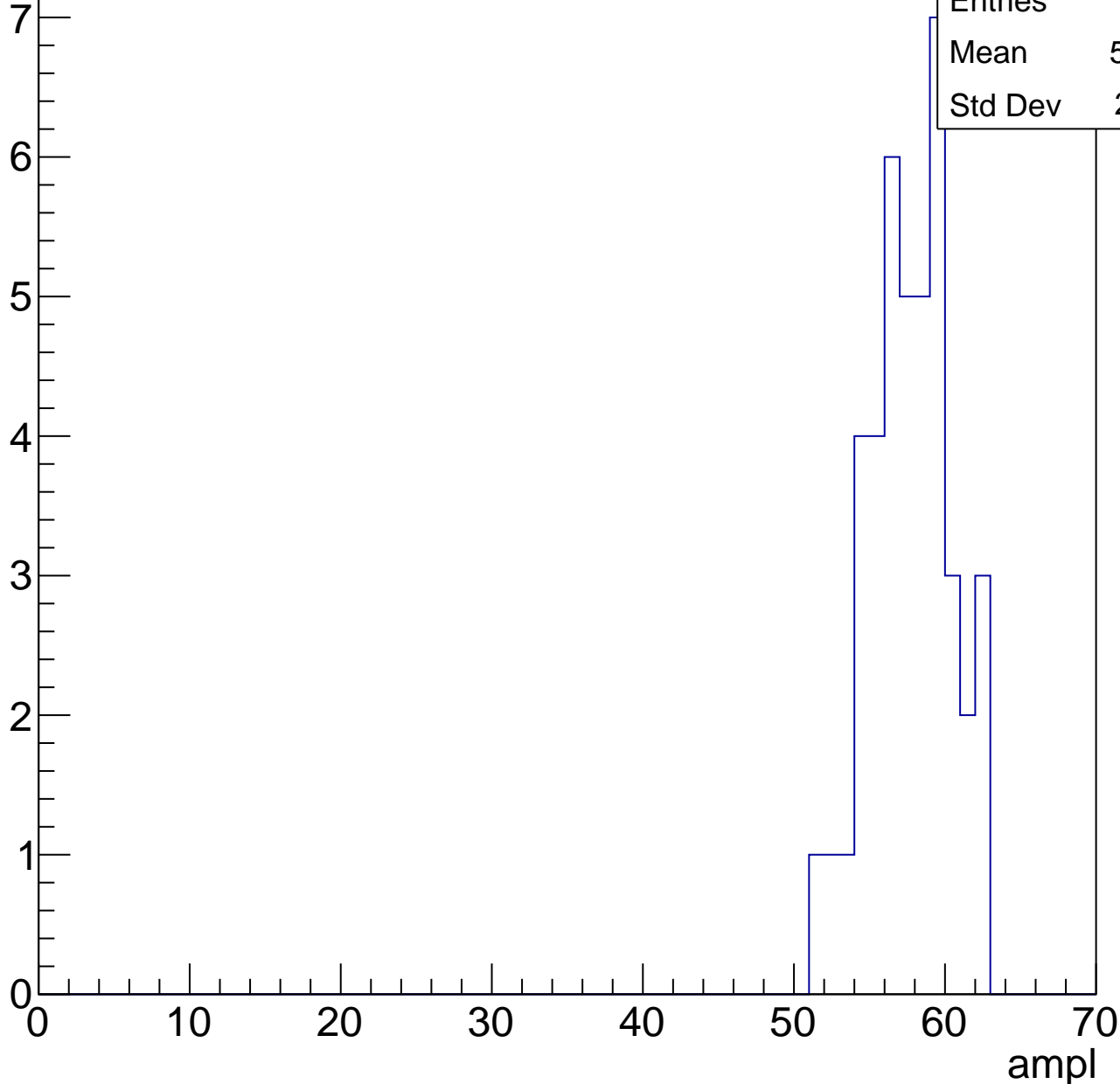


B0L001S, U23-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	57.24
Std Dev	2.671



B0L001S, U23-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	37
Mean	59.14
Std Dev	10.09

Entry

10

8

6

4

2

0

0

10

20

30

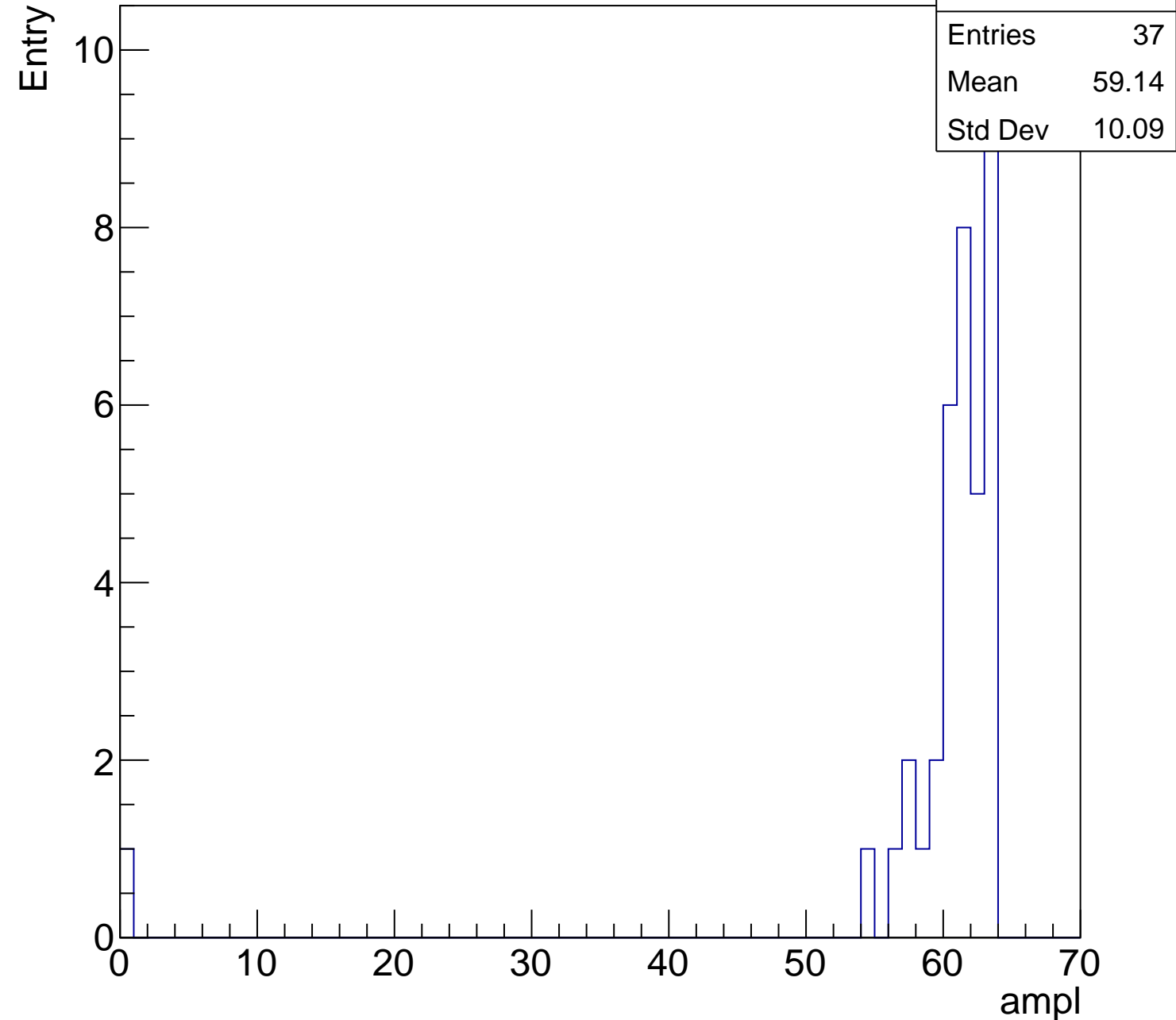
40

50

60

ampl

70



B0L001S, U23-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch122, adc0

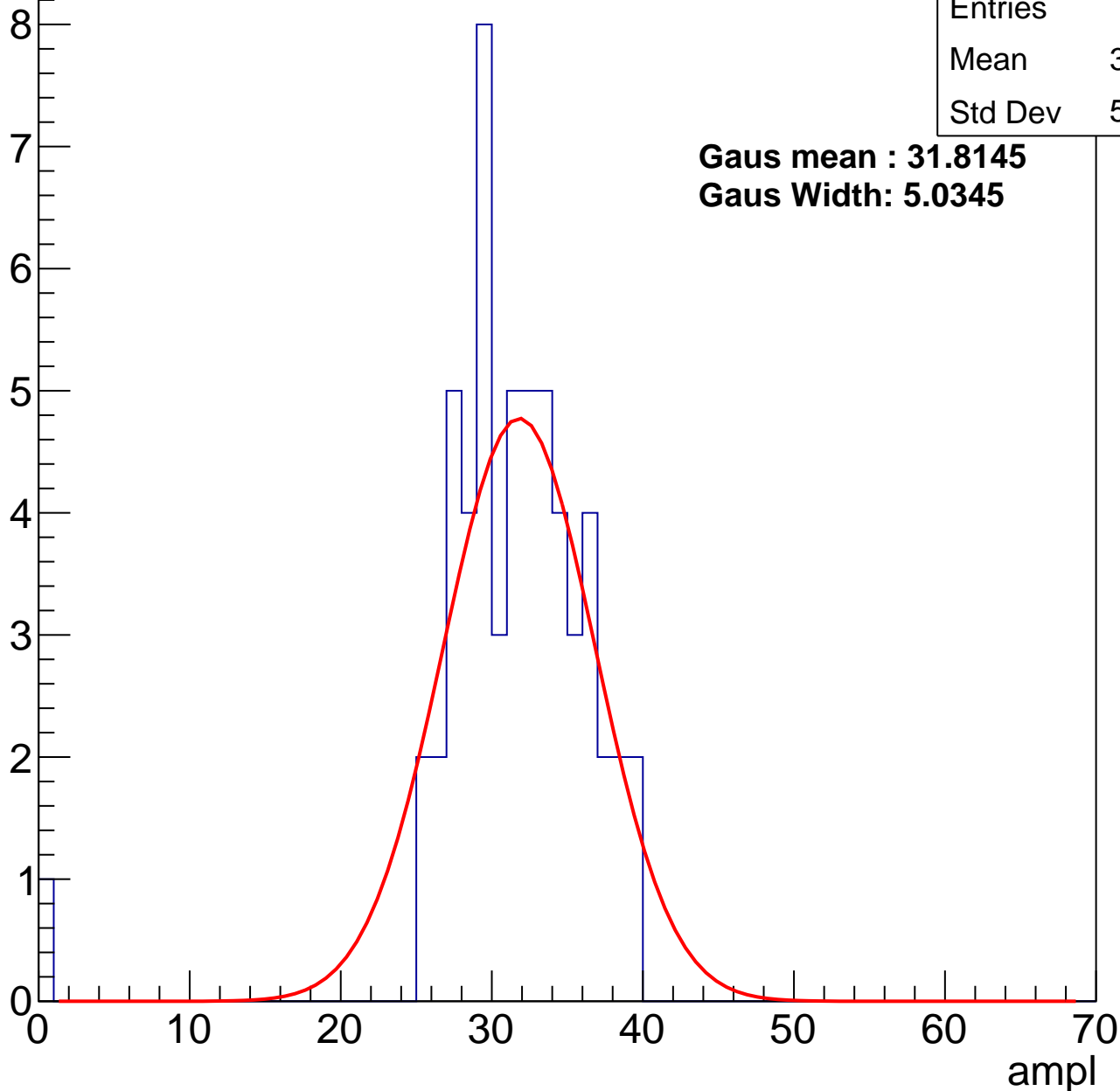
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.95
Std Dev	5.526

Gaus mean : 31.8145

Gaus Width: 5.0345



B0L001S, U23-ch122, adc1

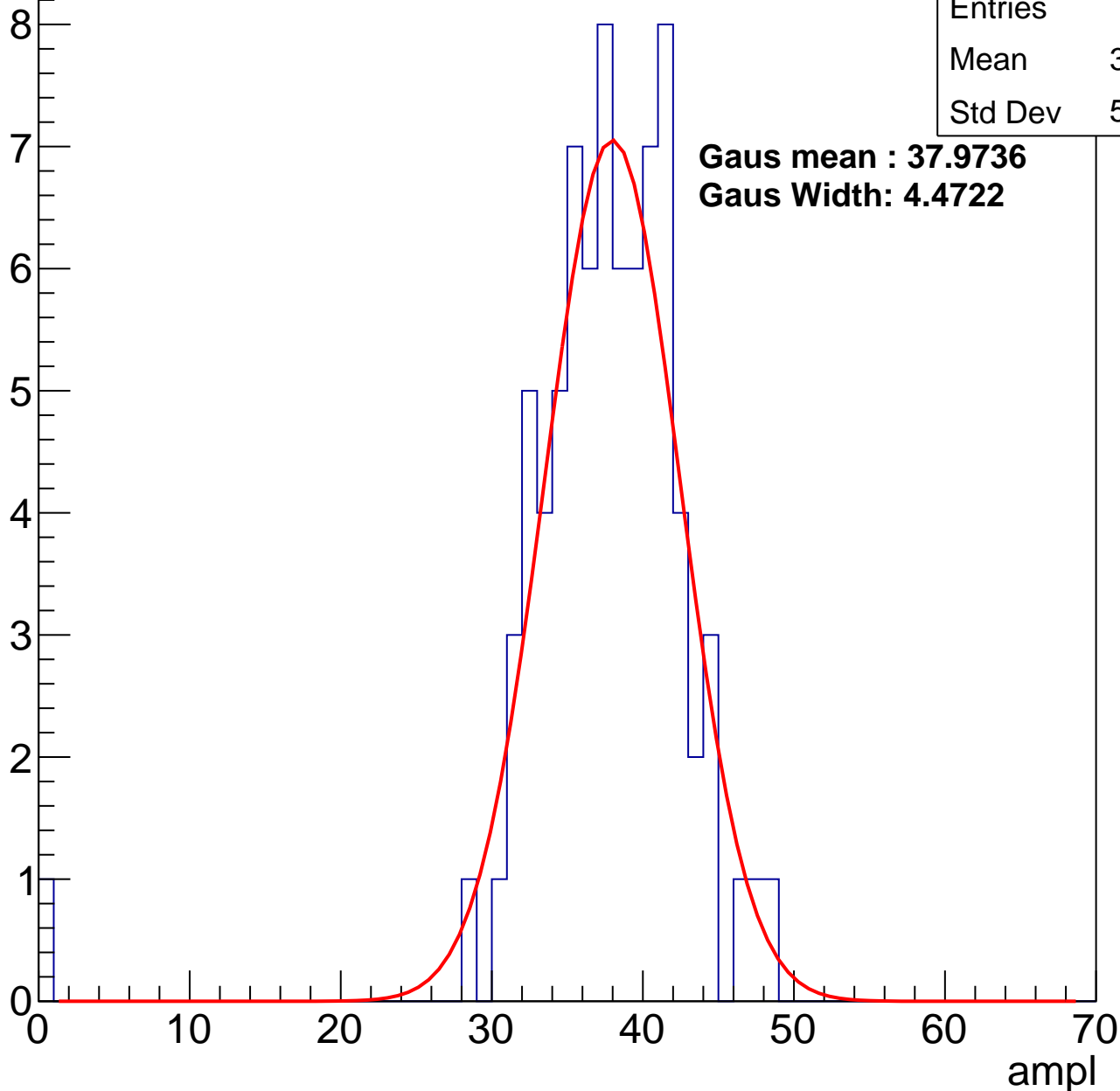
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	37.09
Std Dev	5.827

Gaus mean : 37.9736

Gaus Width: 4.4722



B0L001S, U23-ch122, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	44.78
Std Dev	3.429

Gaus mean : 45.5841

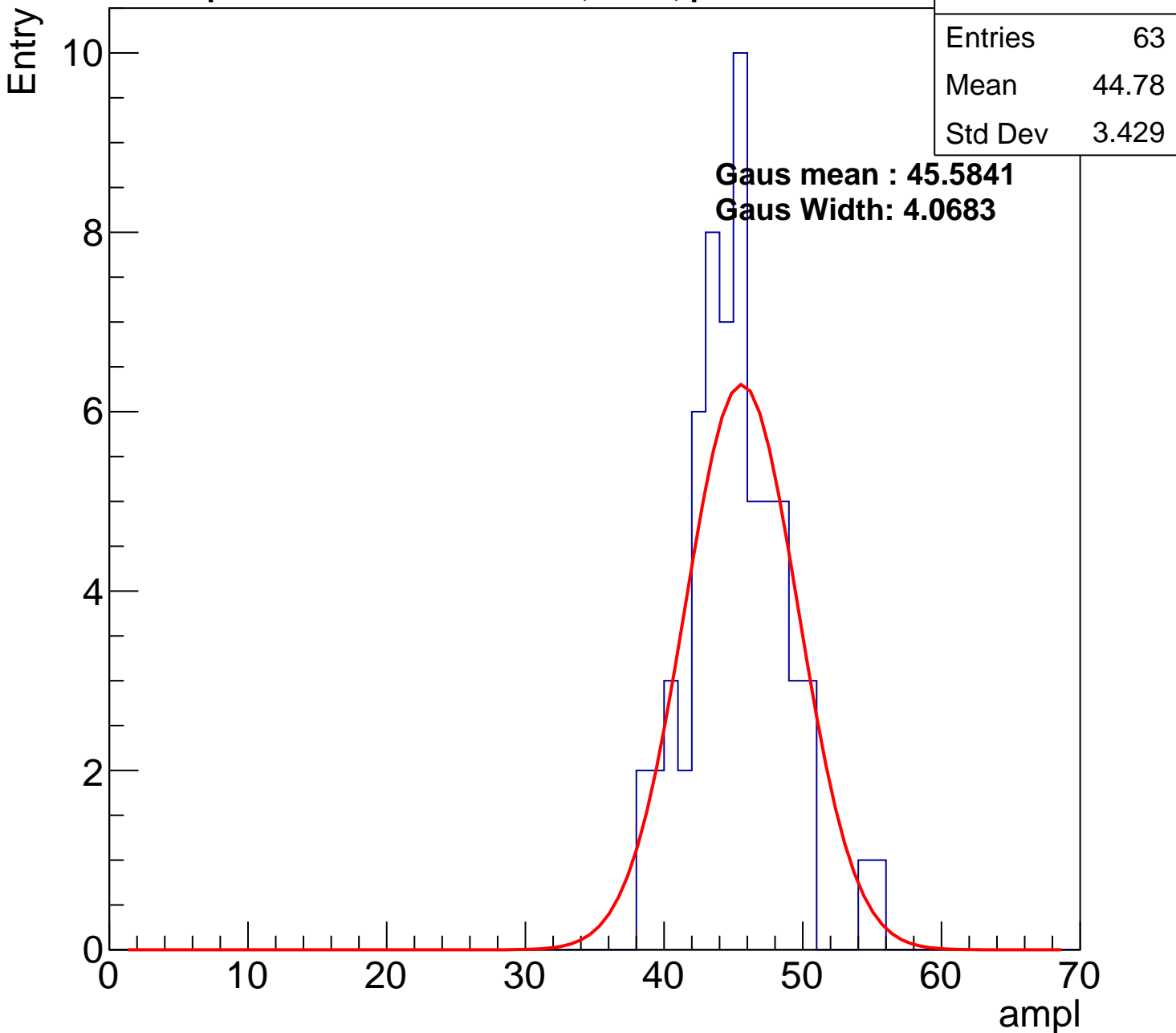
Gaus Width: 4.0683

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

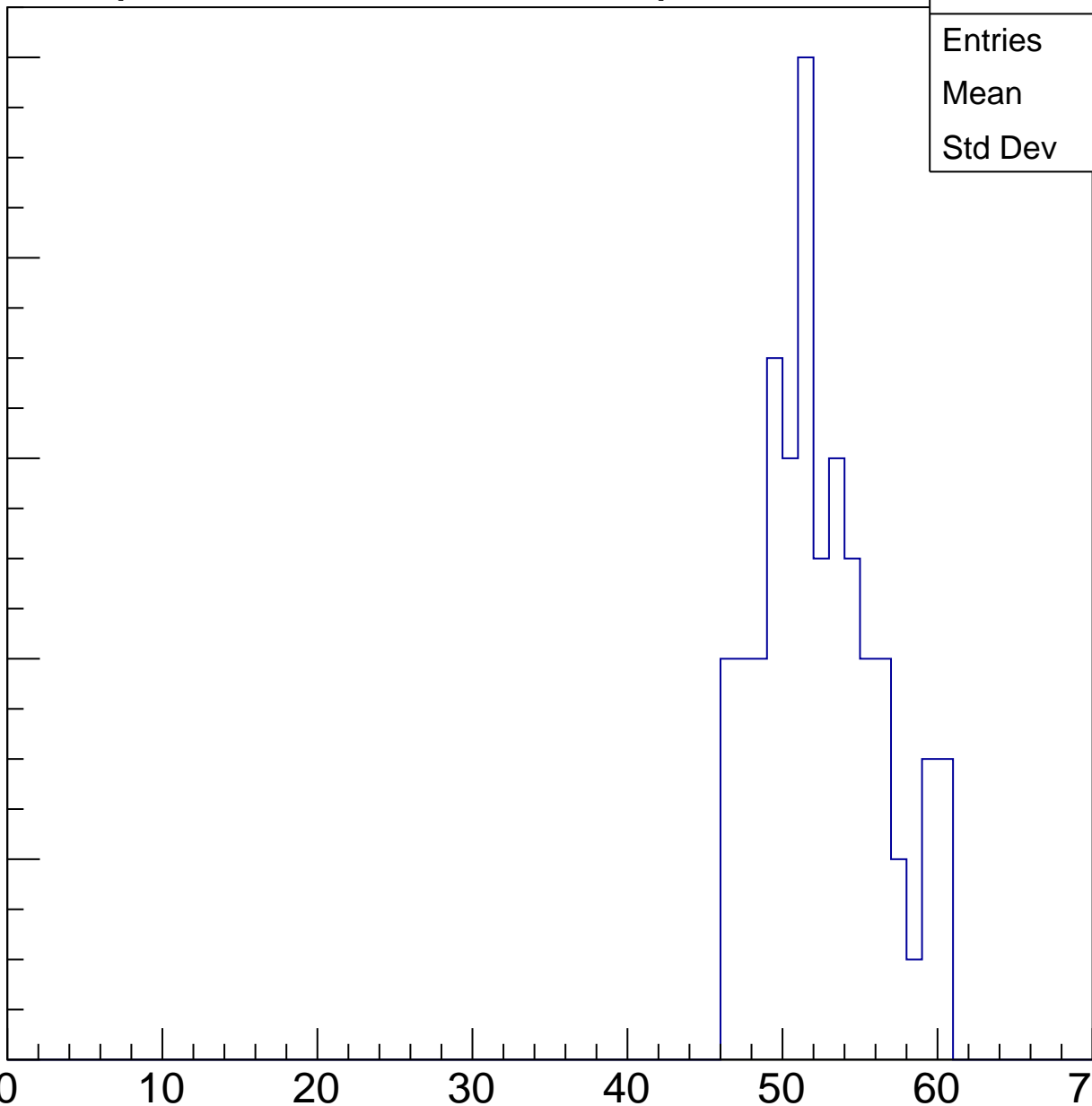
Entries	68
Mean	52.03
Std Dev	3.749

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

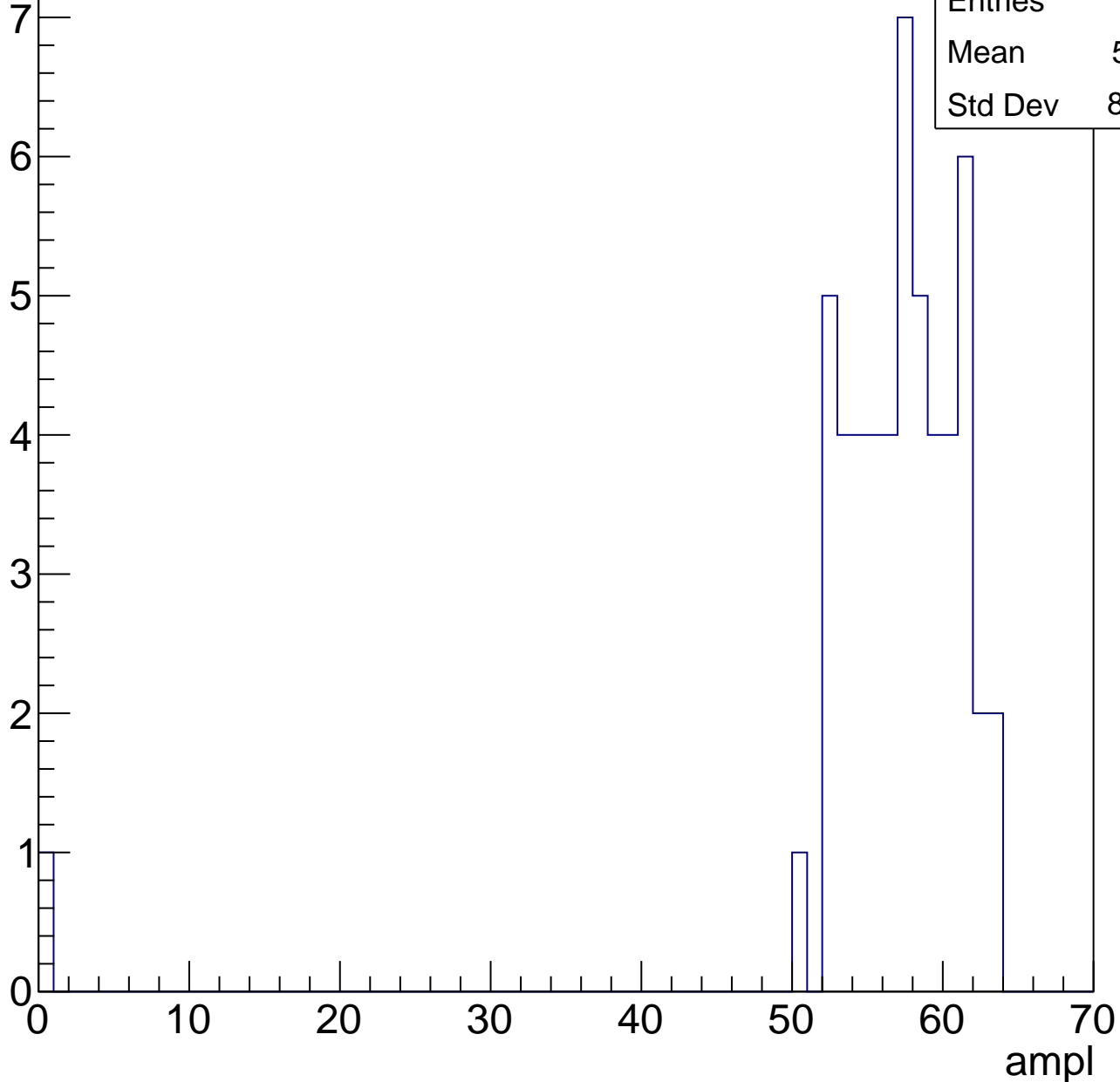


B0L001S, U23-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.91
Std Dev	8.417

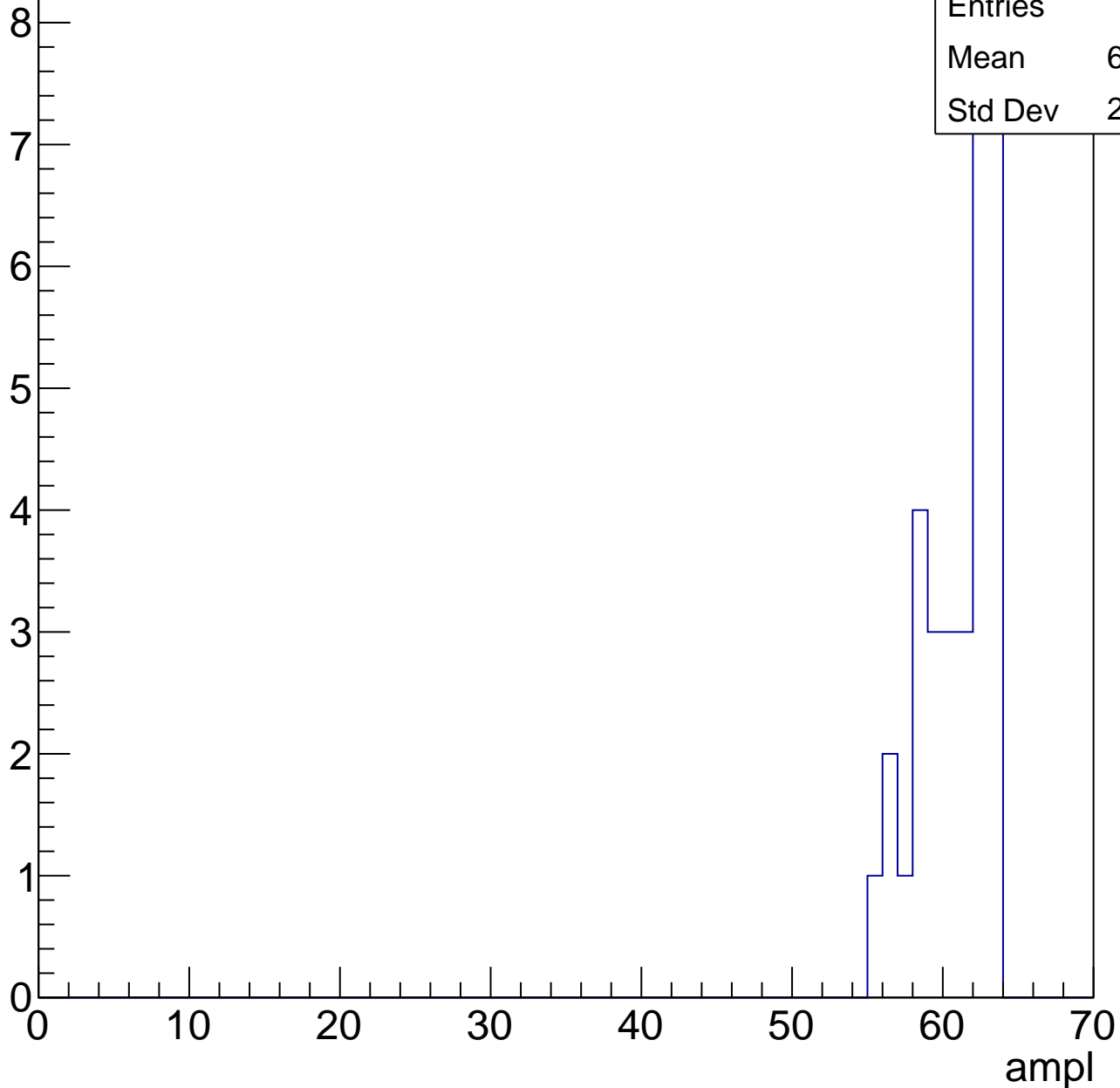


B0L001S, U23-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	60.48
Std Dev	2.363



B0L001S, U23-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch123, adc0

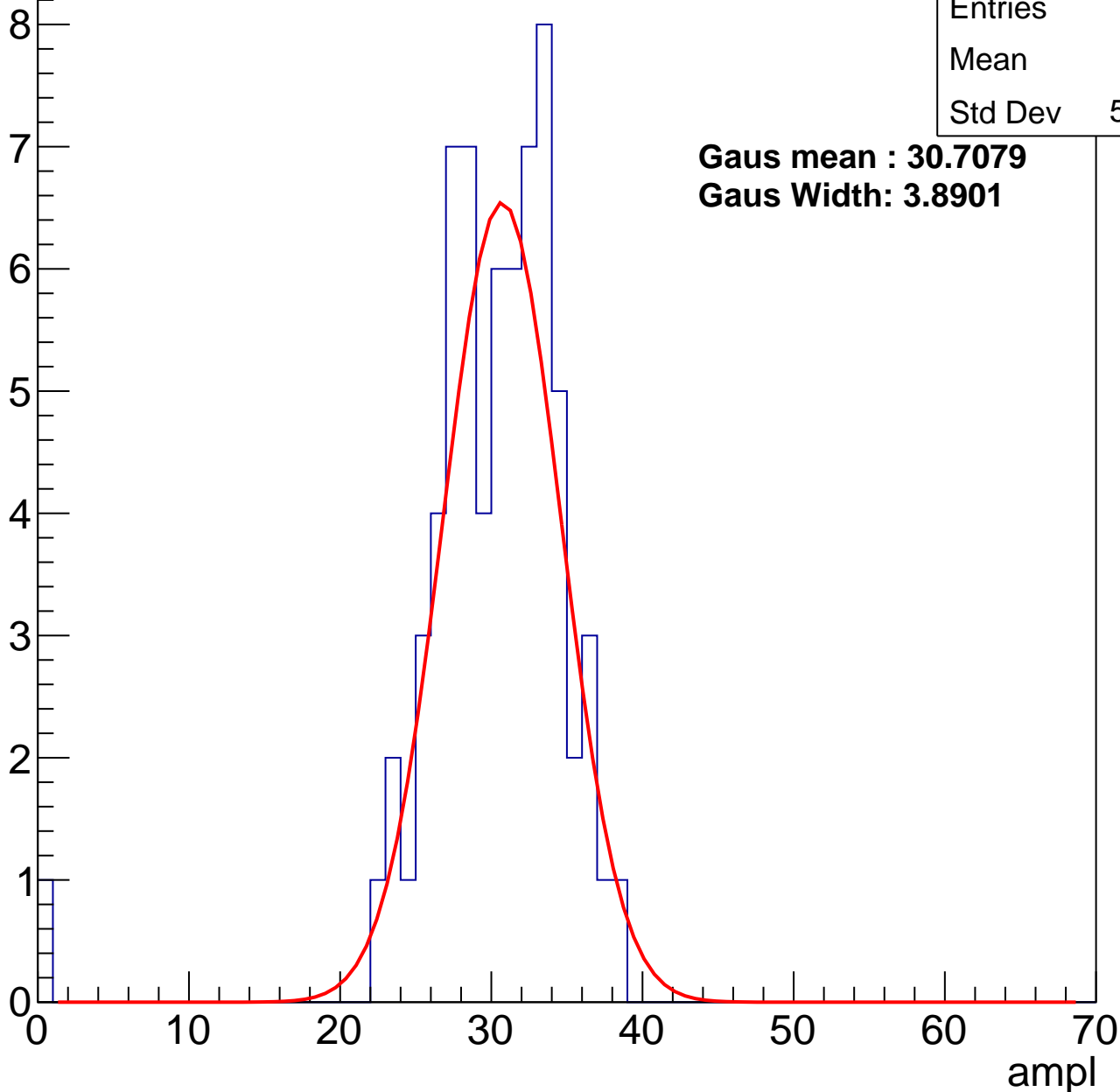
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	29.7
Std Dev	5.083

Gaus mean : 30.7079

Gaus Width: 3.8901



B0L001S, U23-ch123, adc1

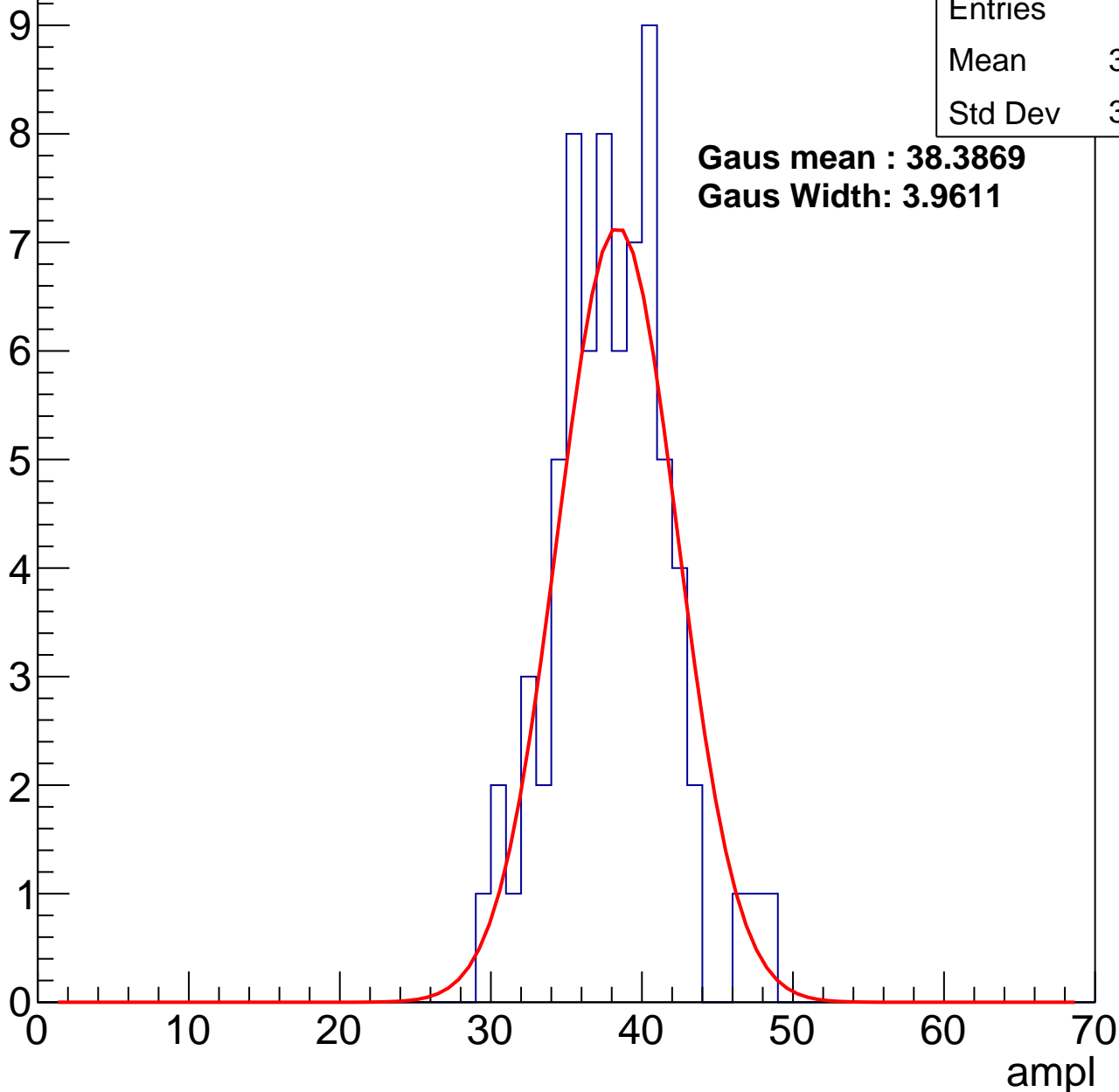
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.57
Std Dev	3.789

Gaus mean : 38.3869

Gaus Width: 3.9611



B0L001S, U23-ch123, adc2

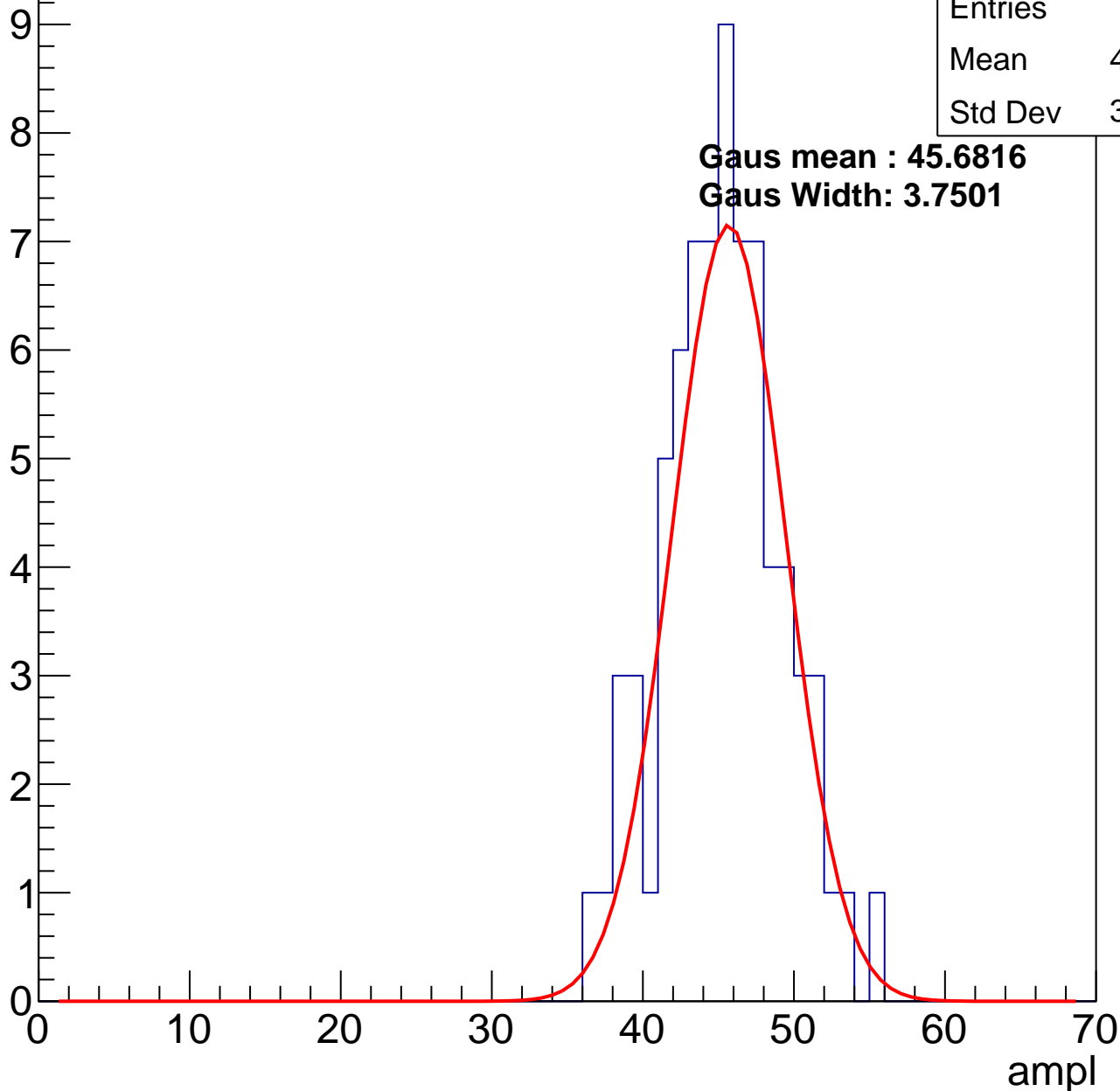
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	44.82
Std Dev	3.895

Gaus mean : 45.6816

Gaus Width: 3.7501

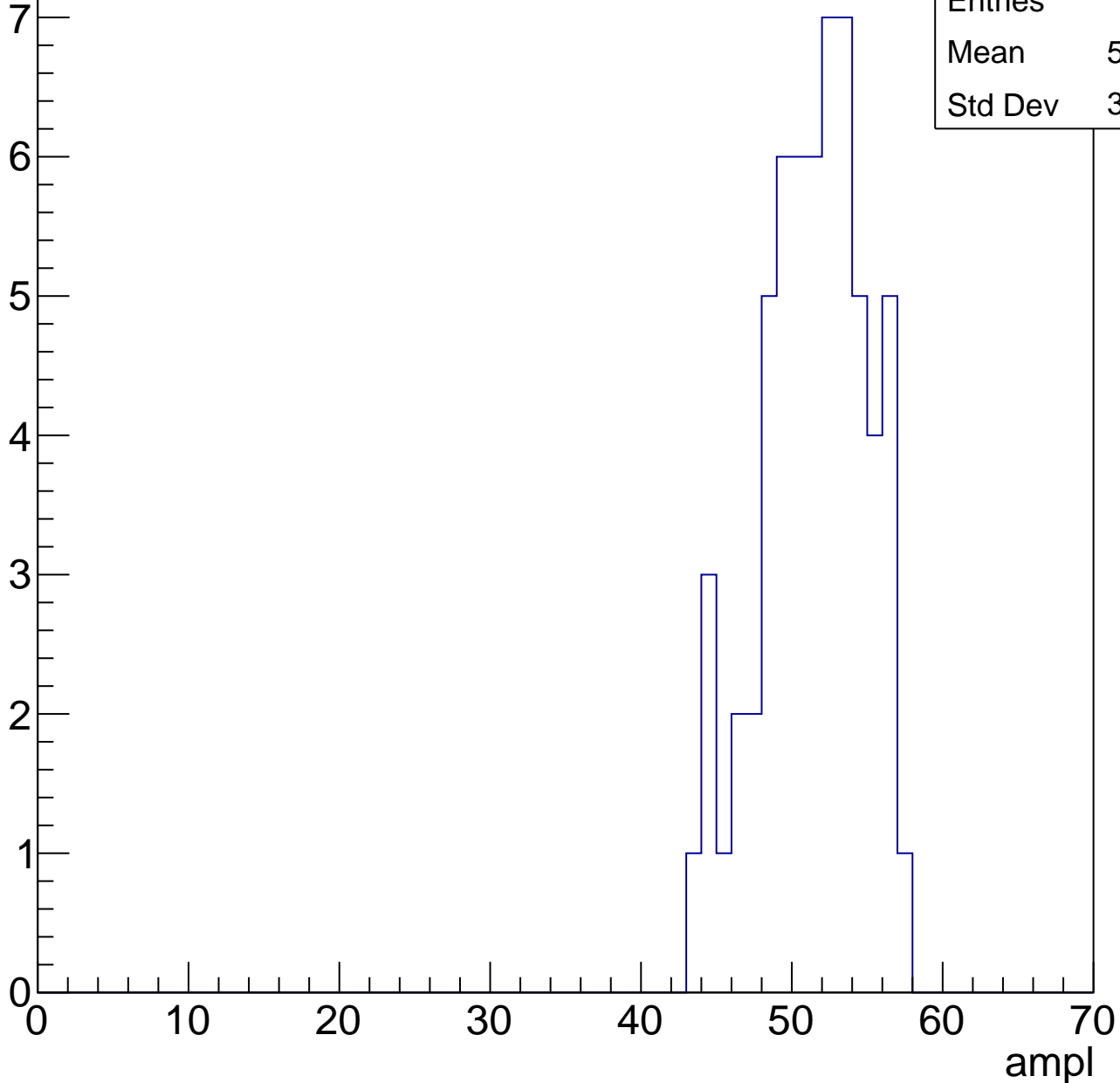


B0L001S, U23-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.95
Std Dev	3.409

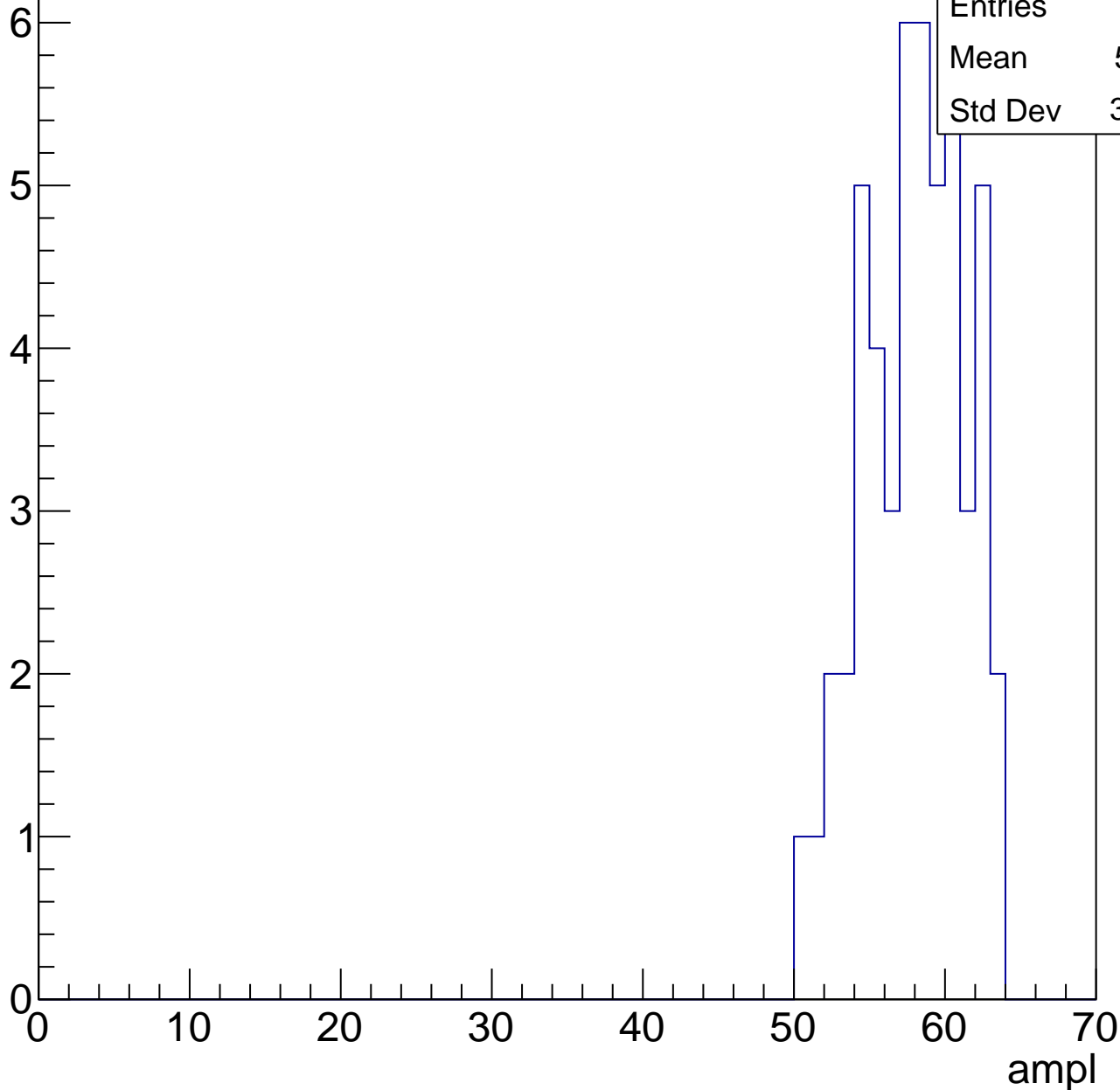


B0L001S, U23-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.51
Std Dev	3.262

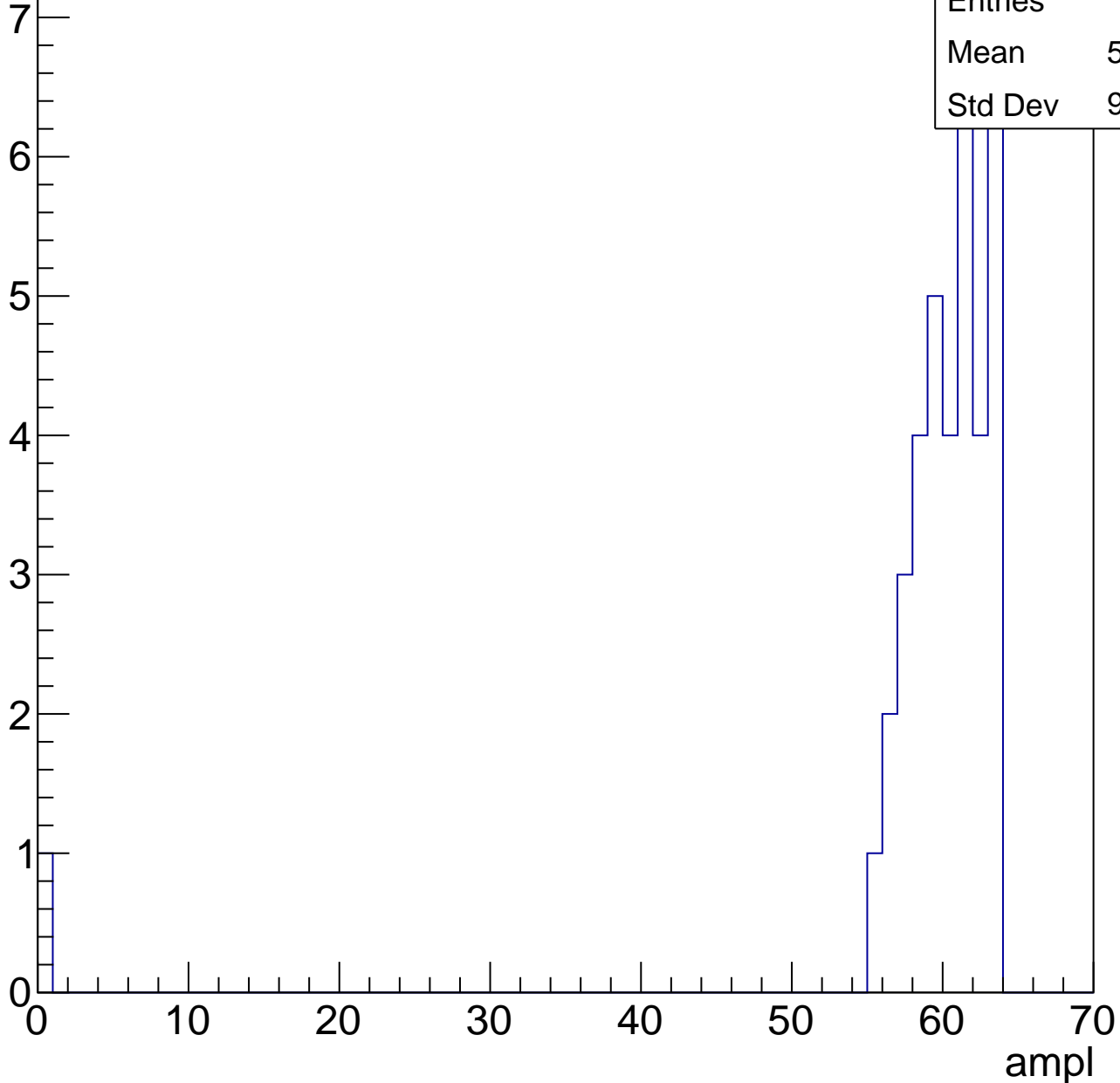


B0L001S, U23-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.45
Std Dev	9.867



B0L001S, U23-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch124, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	29.64
Std Dev	4.835

Gaus mean : 30.7907

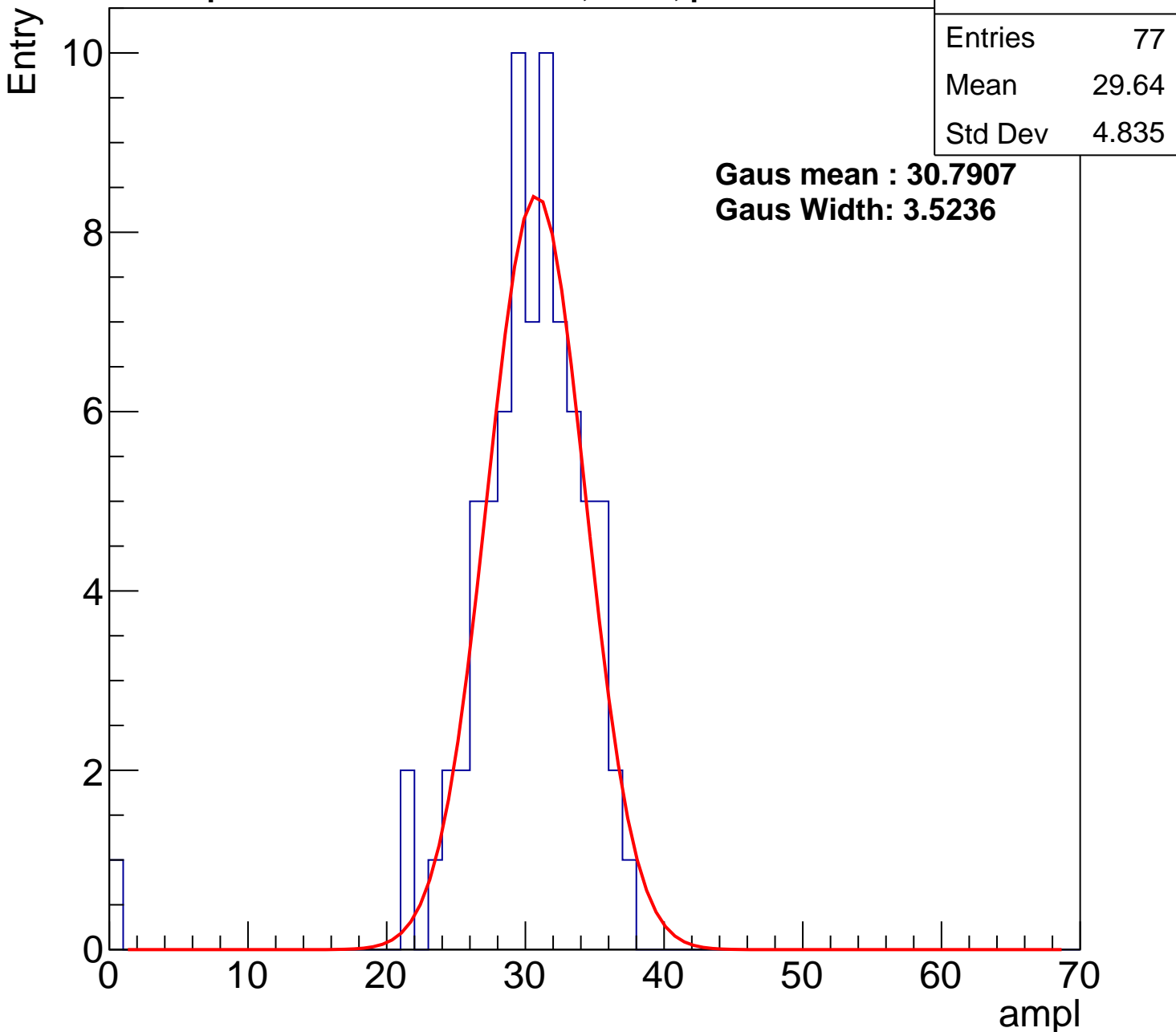
Gaus Width: 3.5236

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U23-ch124, adc1

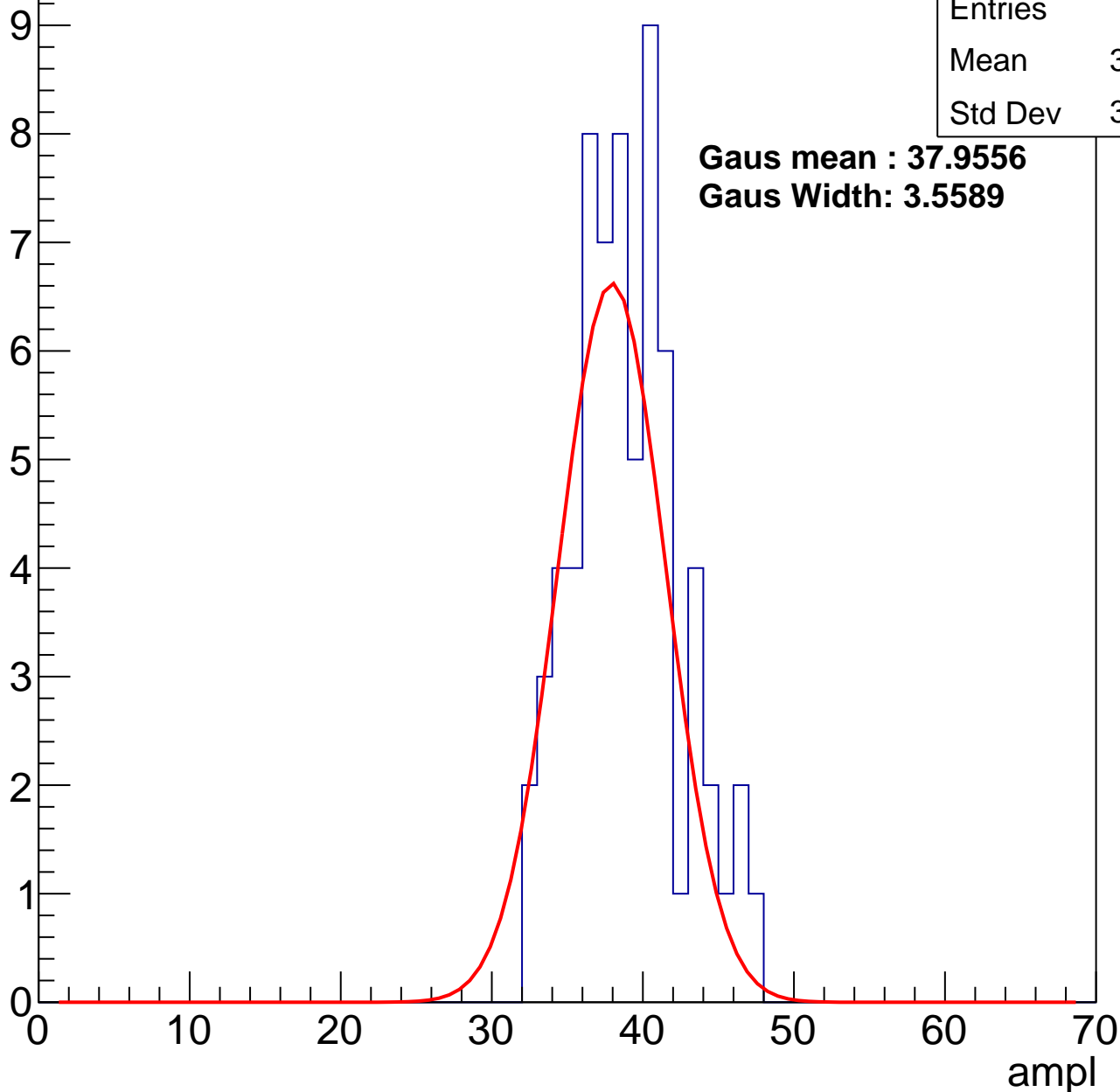
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.46
Std Dev	3.483

Gaus mean : 37.9556

Gaus Width: 3.5589



B0L001S, U23-ch124, adc2

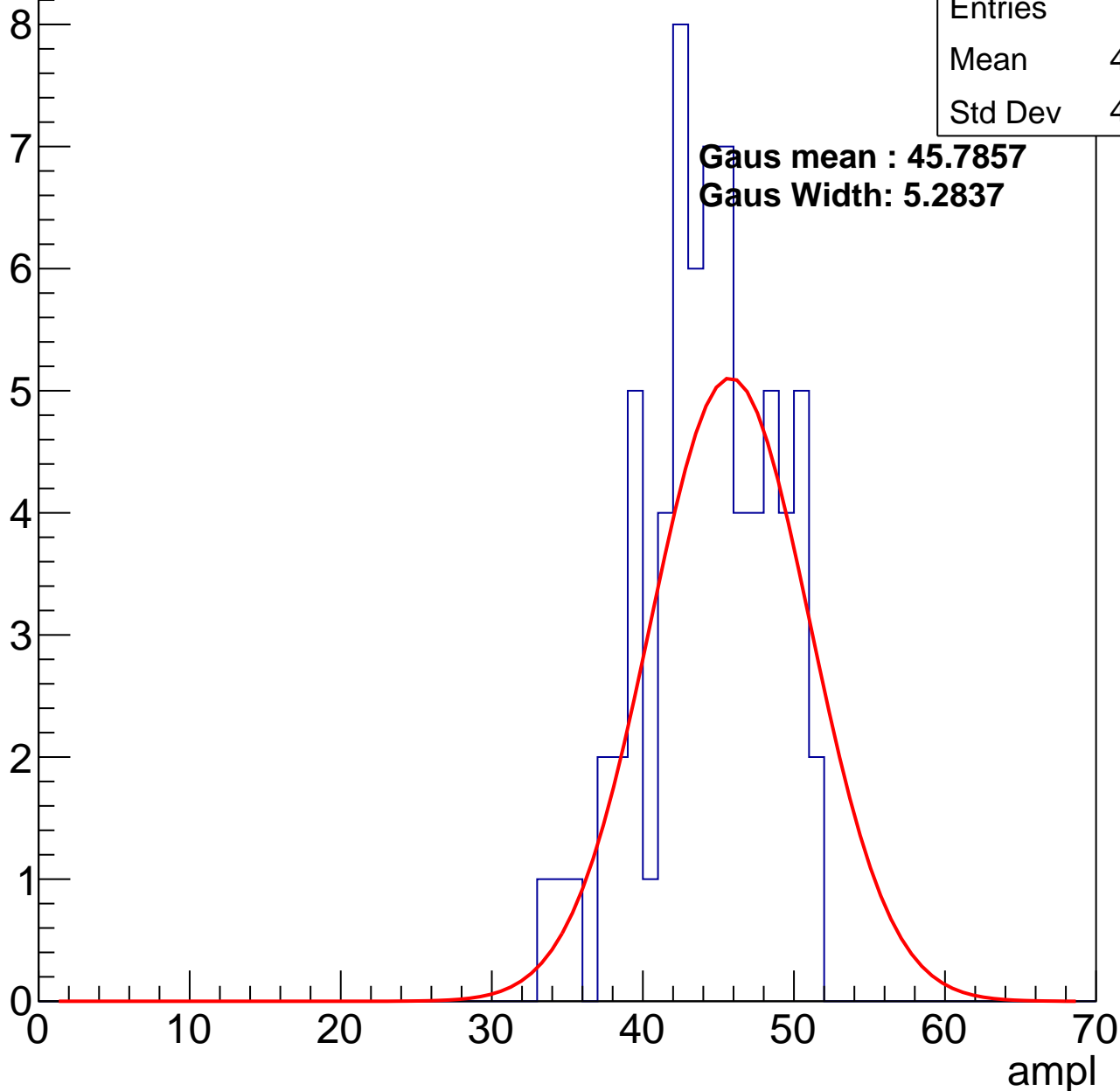
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.88
Std Dev	4.176

Gaus mean : 45.7857

Gaus Width: 5.2837

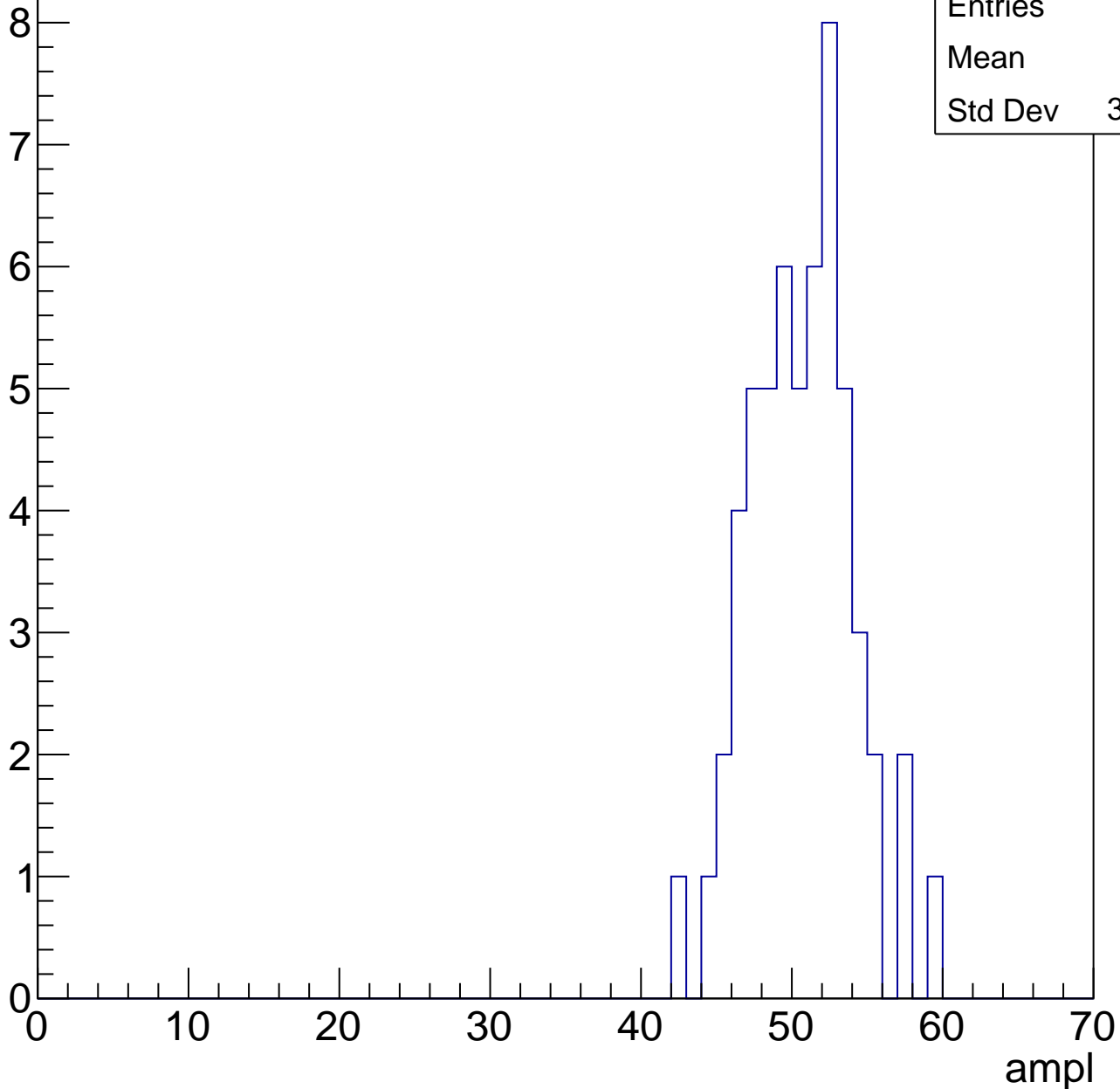


B0L001S, U23-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	50.2
Std Dev	3.388

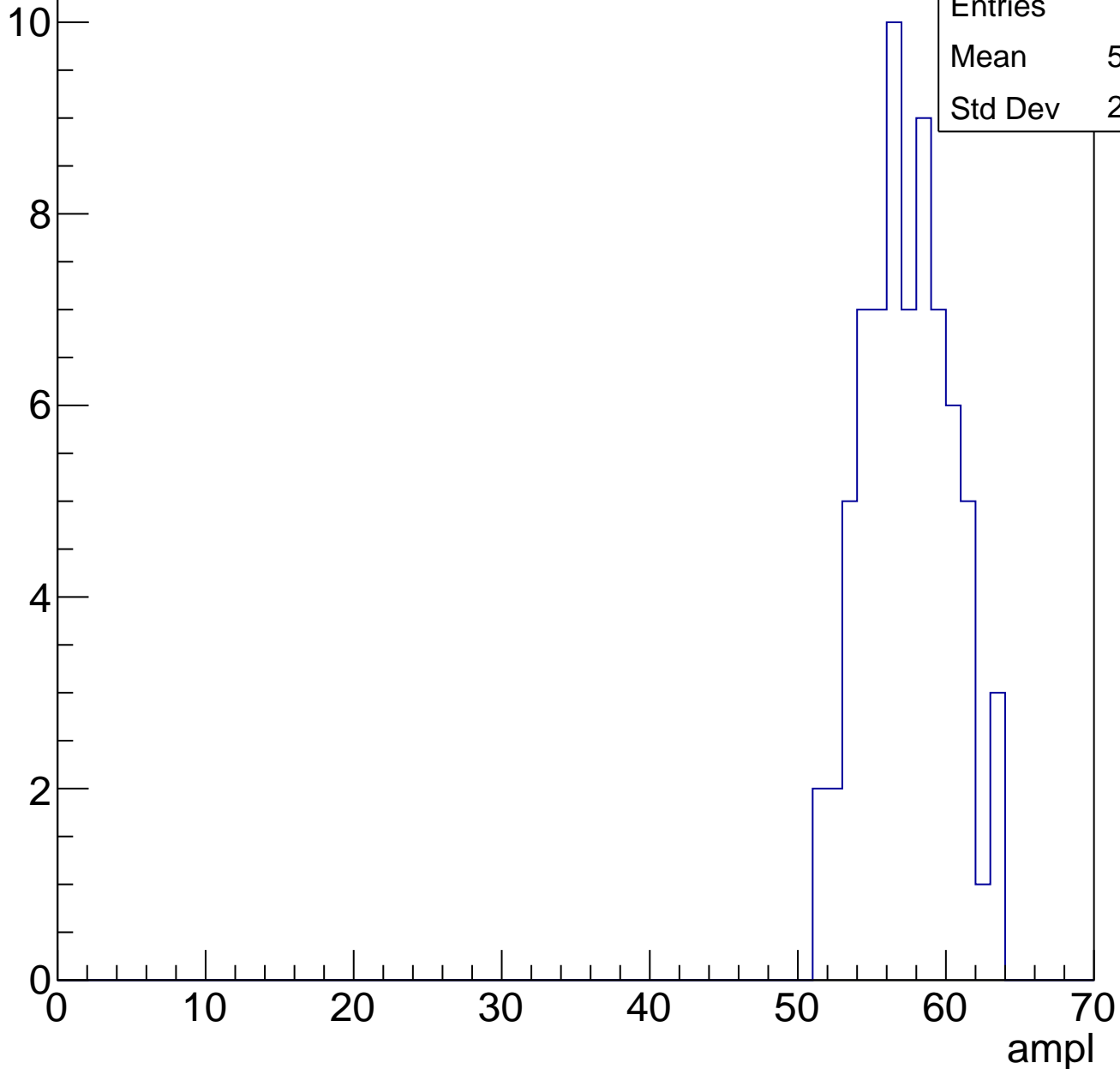


B0L001S, U23-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	56.96
Std Dev	2.924

Entry

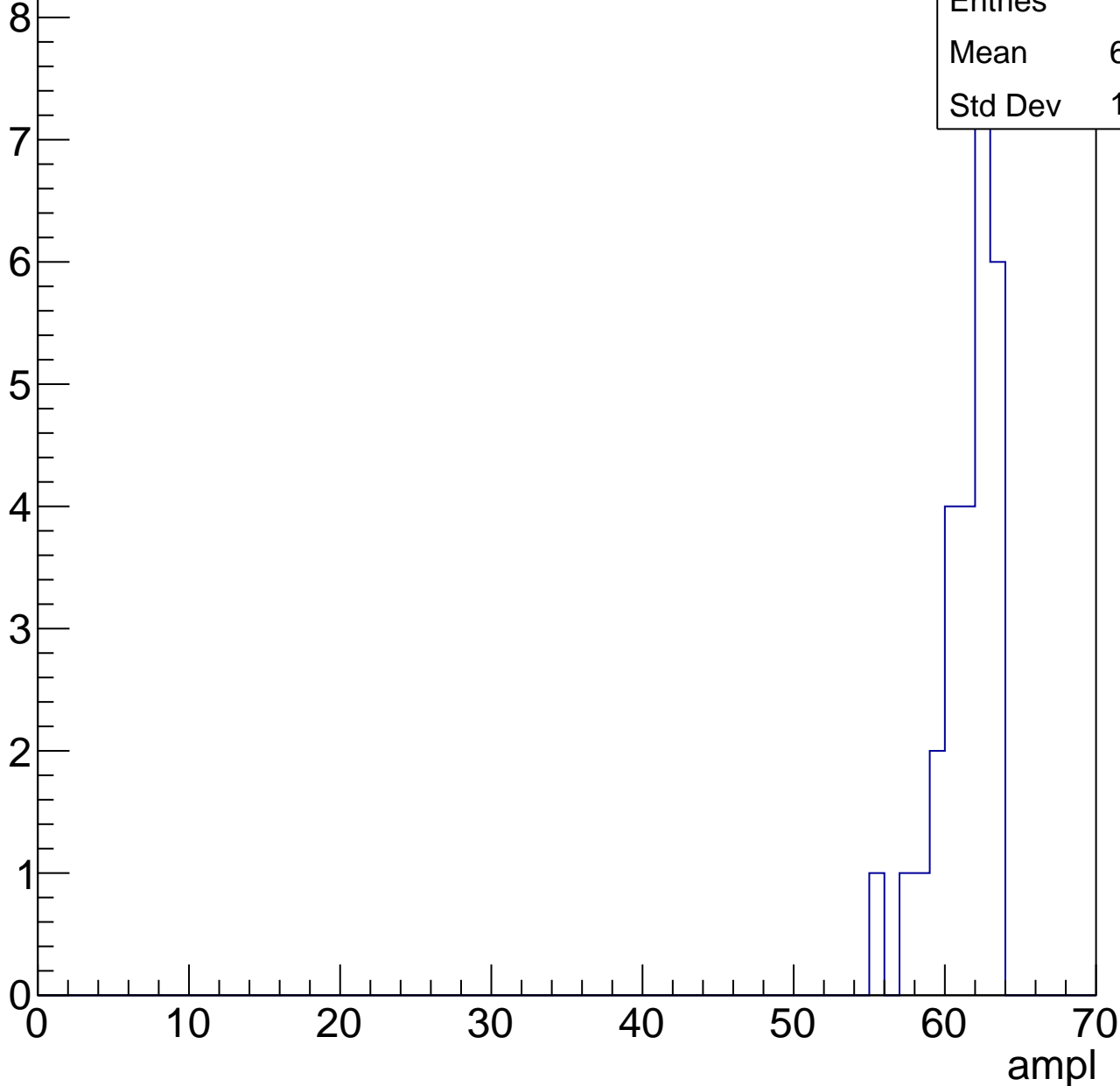


B0L001S, U23-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

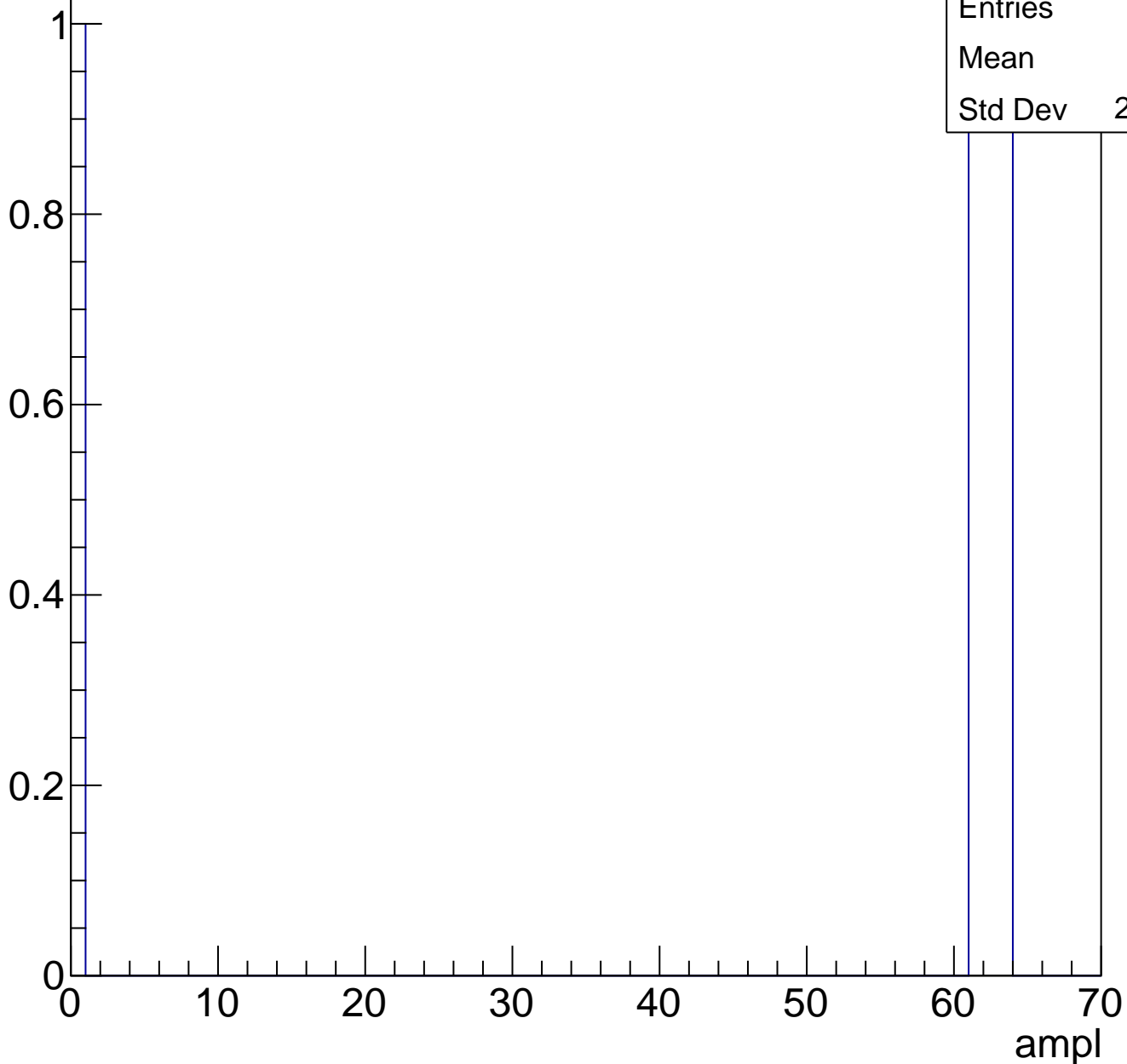
Entries	27
Mean	60.96
Std Dev	1.972



B0L001S, U23-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch125, adc0

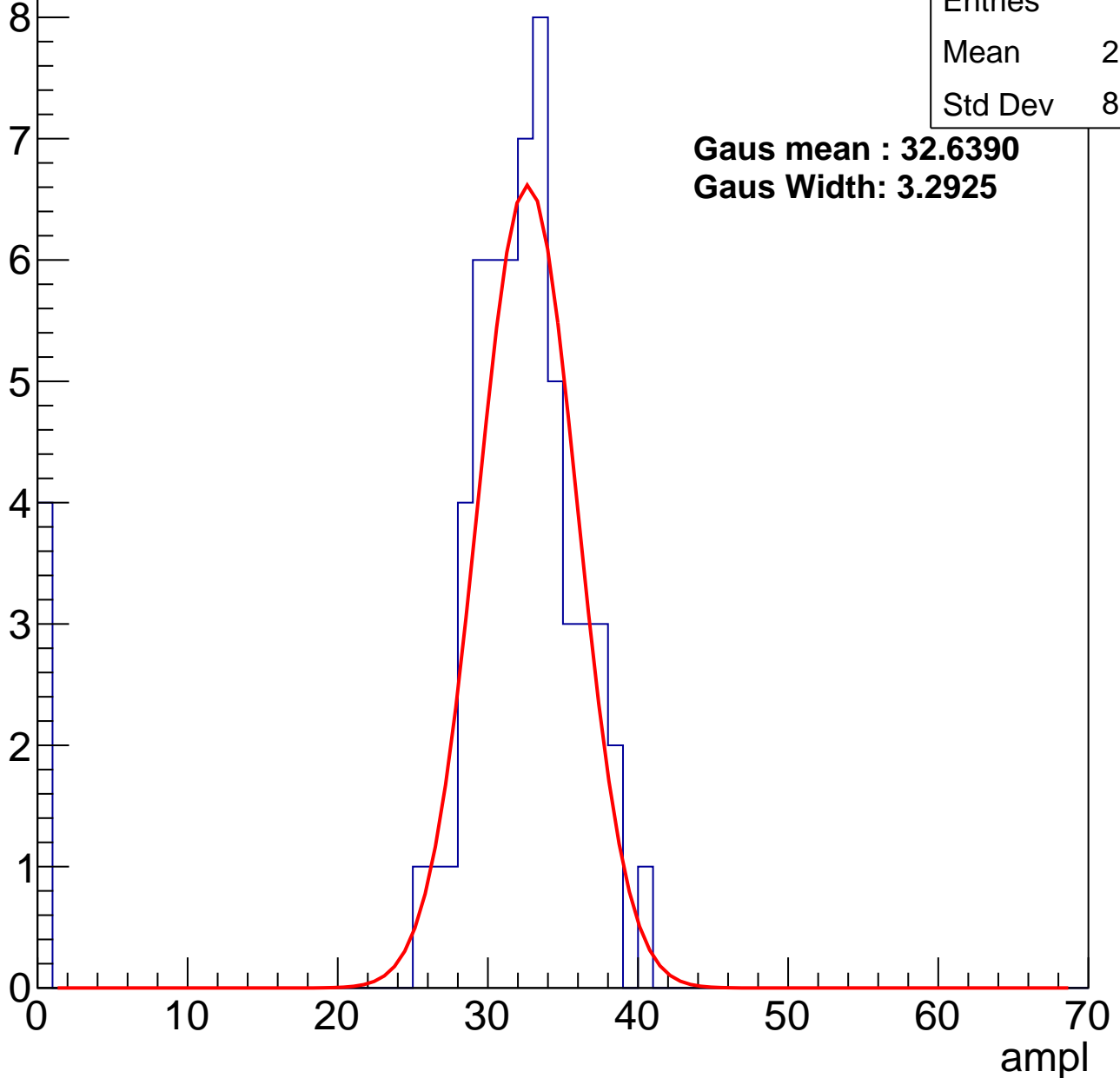
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	29.97
Std Dev	8.508

Gaus mean : 32.6390

Gaus Width: 3.2925



B0L001S, U23-ch125, adc1

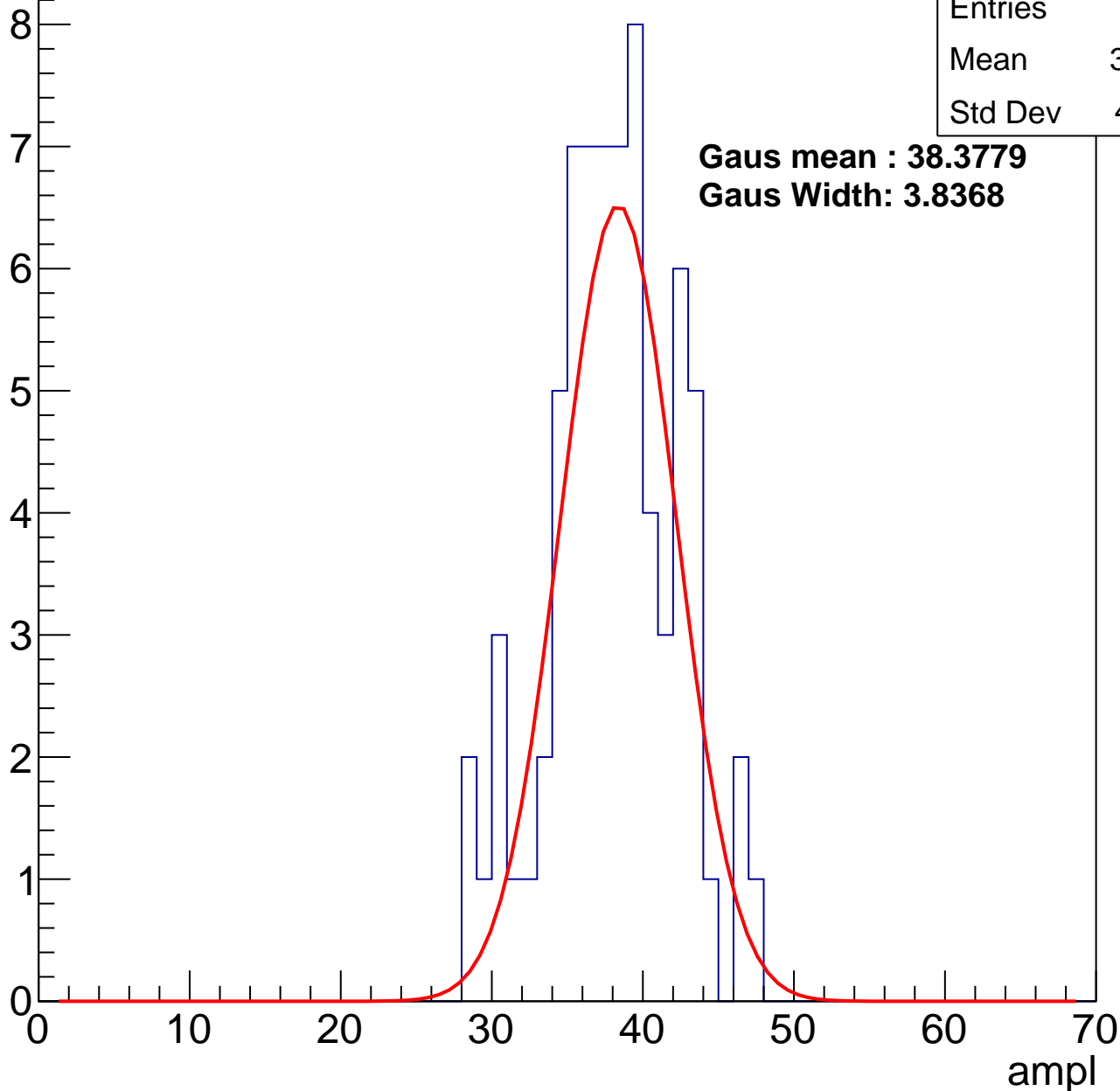
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.55
Std Dev	4.201

Gaus mean : 38.3779

Gaus Width: 3.8368



B0L001S, U23-ch125, adc2

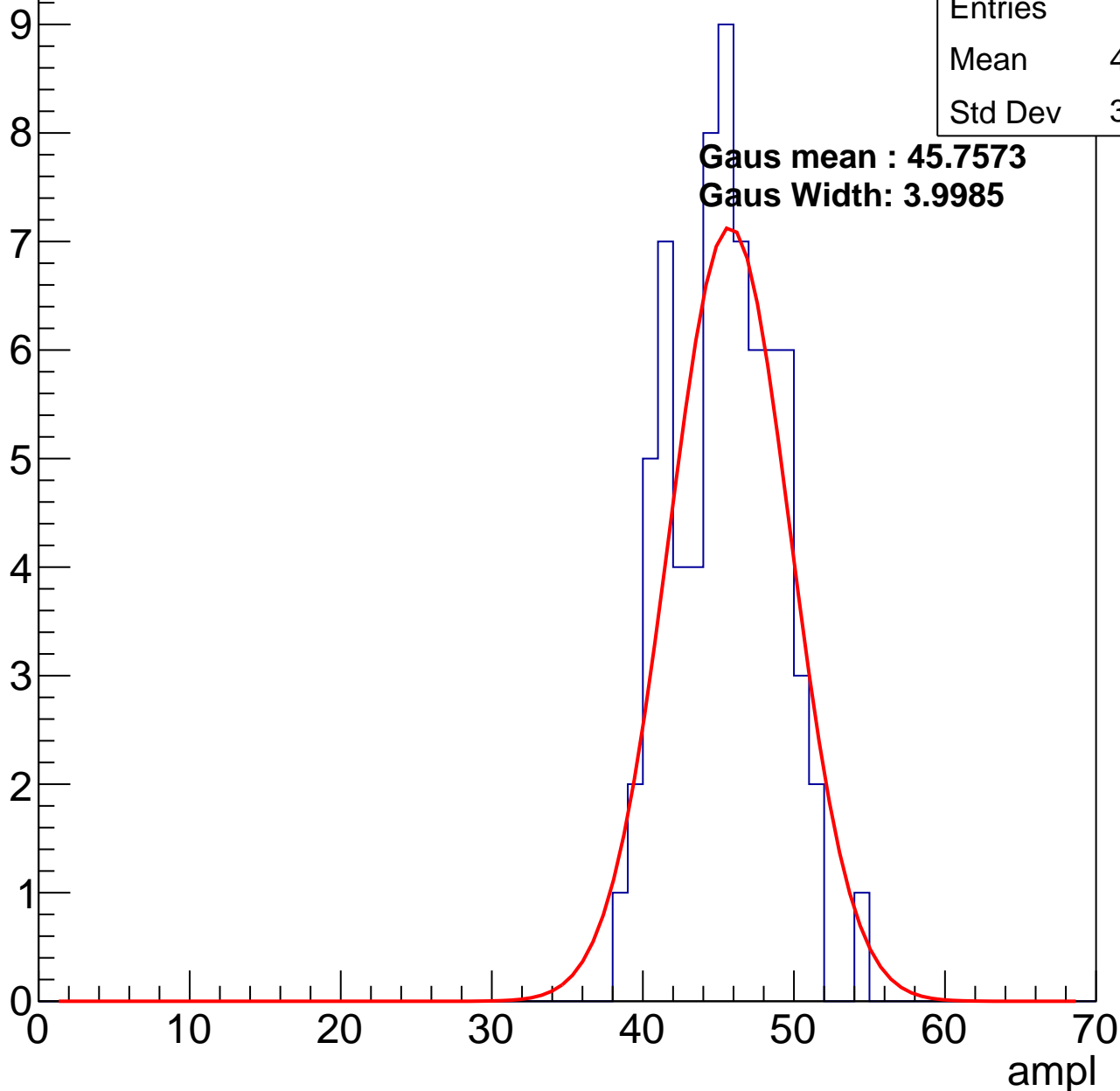
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.96
Std Dev	3.413

Gaus mean : 45.7573

Gaus Width: 3.9985

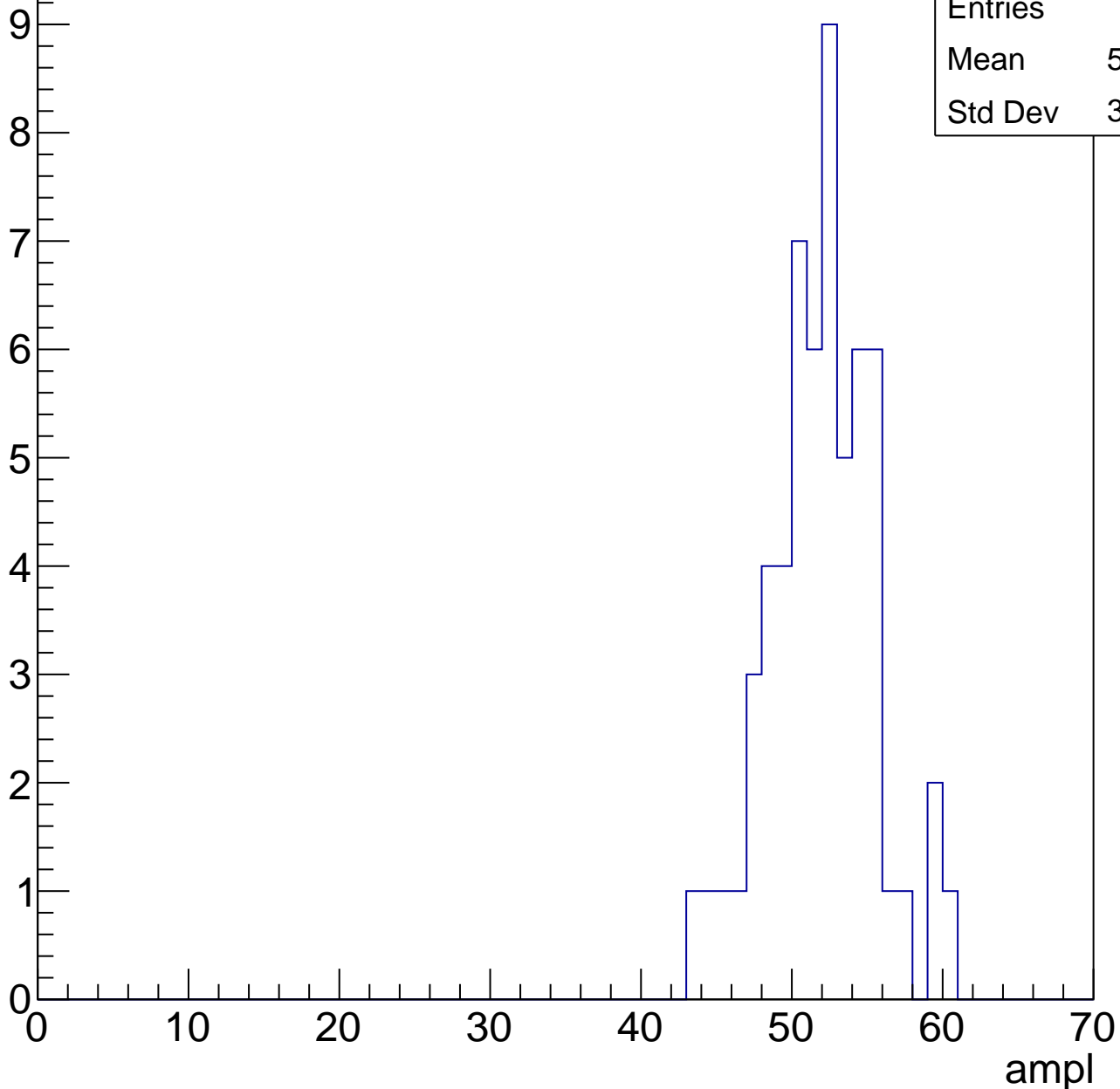


B0L001S, U23-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	51.54
Std Dev	3.475

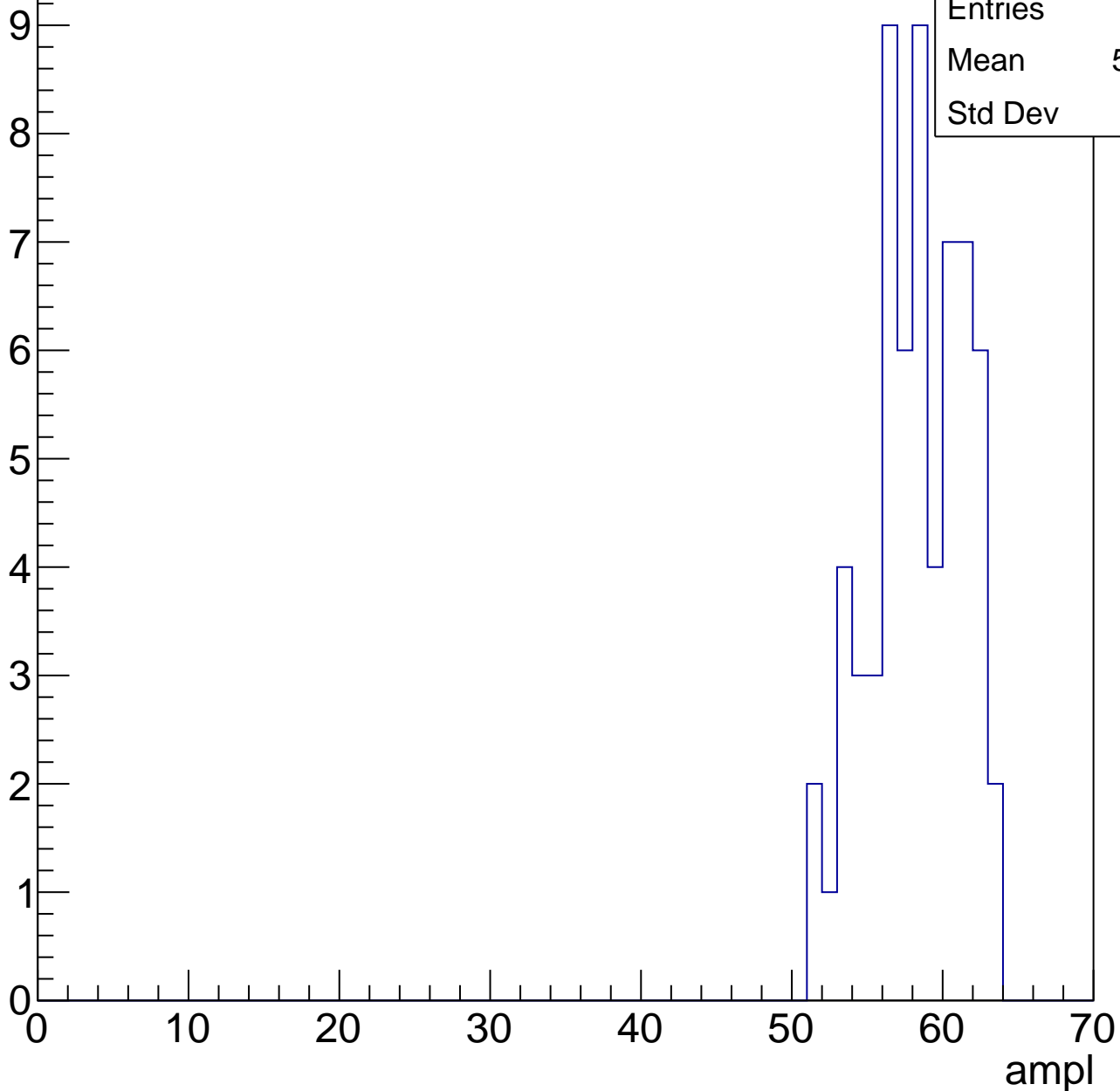


B0L001S, U23-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.81
Std Dev	3.06

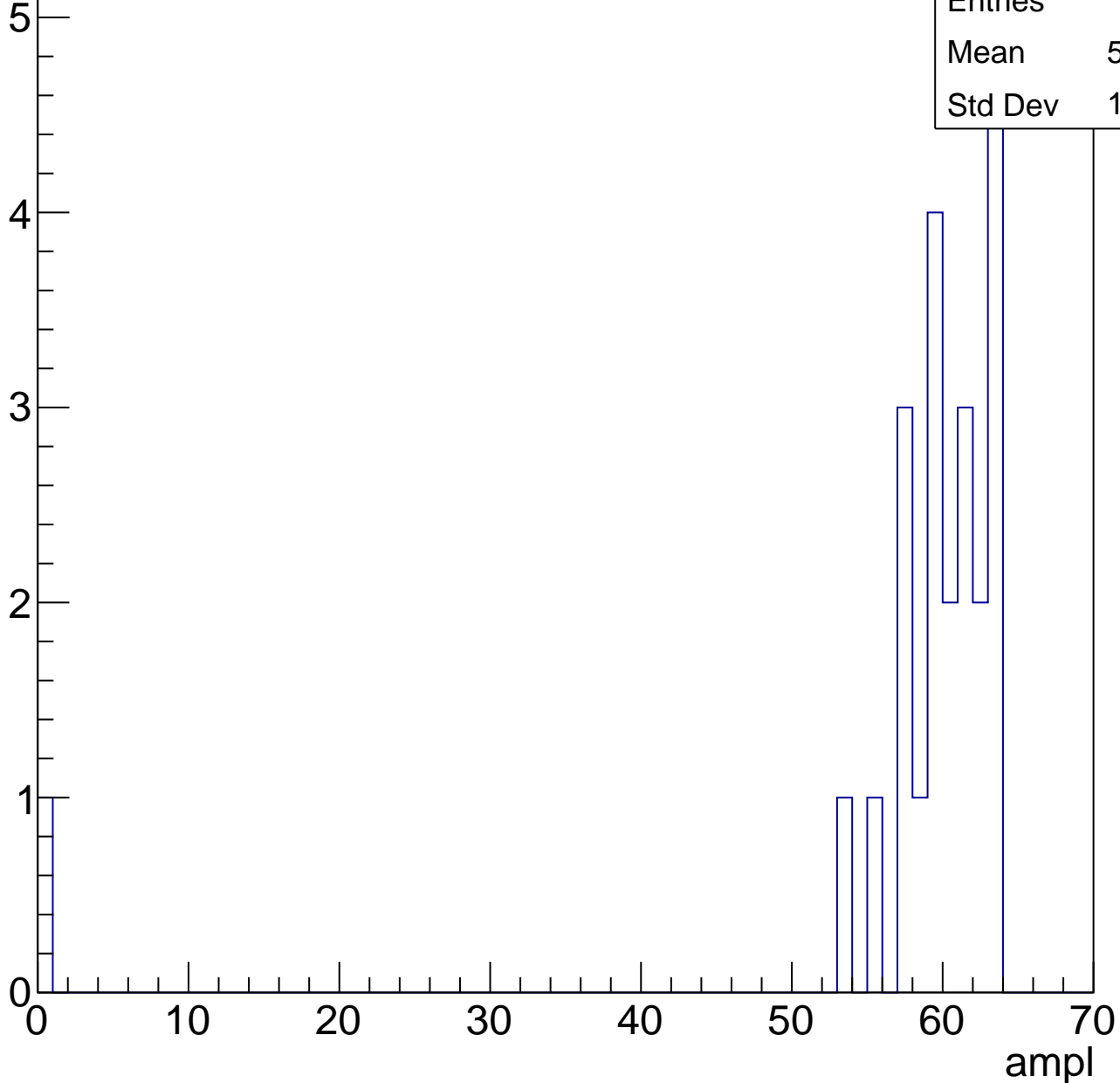


B0L001S, U23-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	57.17
Std Dev	12.48



B0L001S, U23-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U23-ch126, adc0

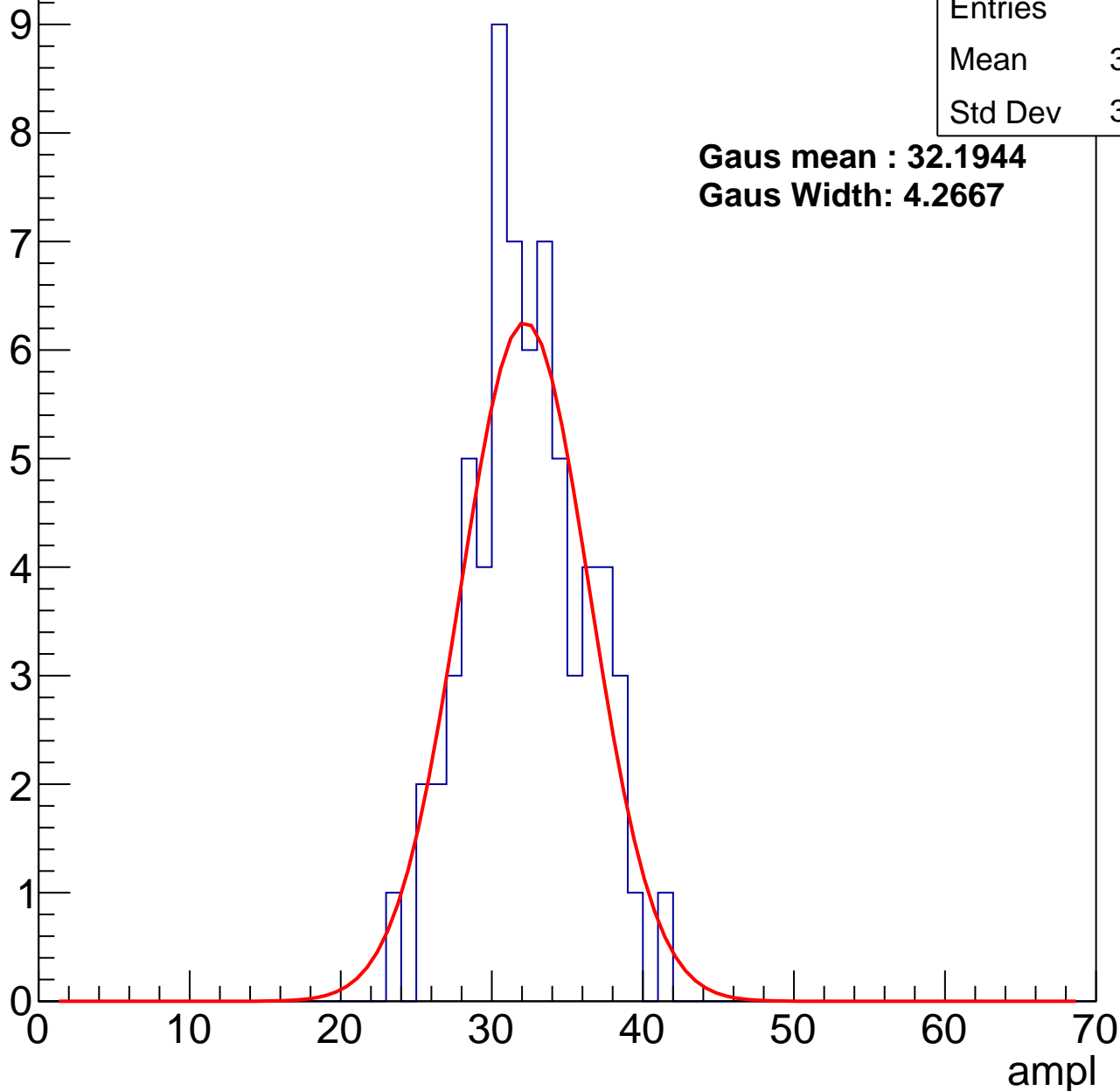
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	31.84
Std Dev	3.744

Gaus mean : 32.1944

Gaus Width: 4.2667



B0L001S, U23-ch126, adc1

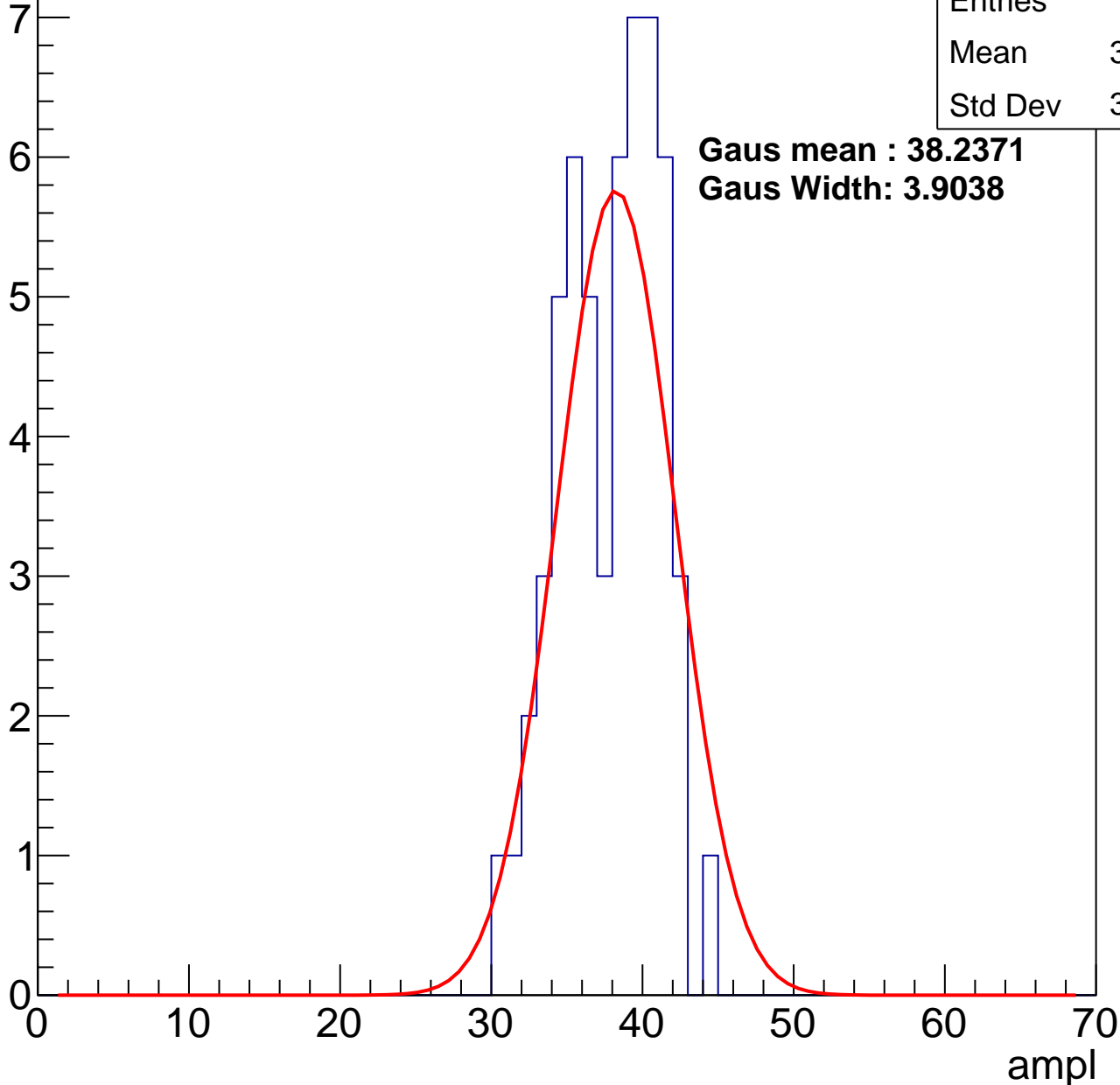
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	37.36
Std Dev	3.182

Gaus mean : 38.2371

Gaus Width: 3.9038



B0L001S, U23-ch126, adc2

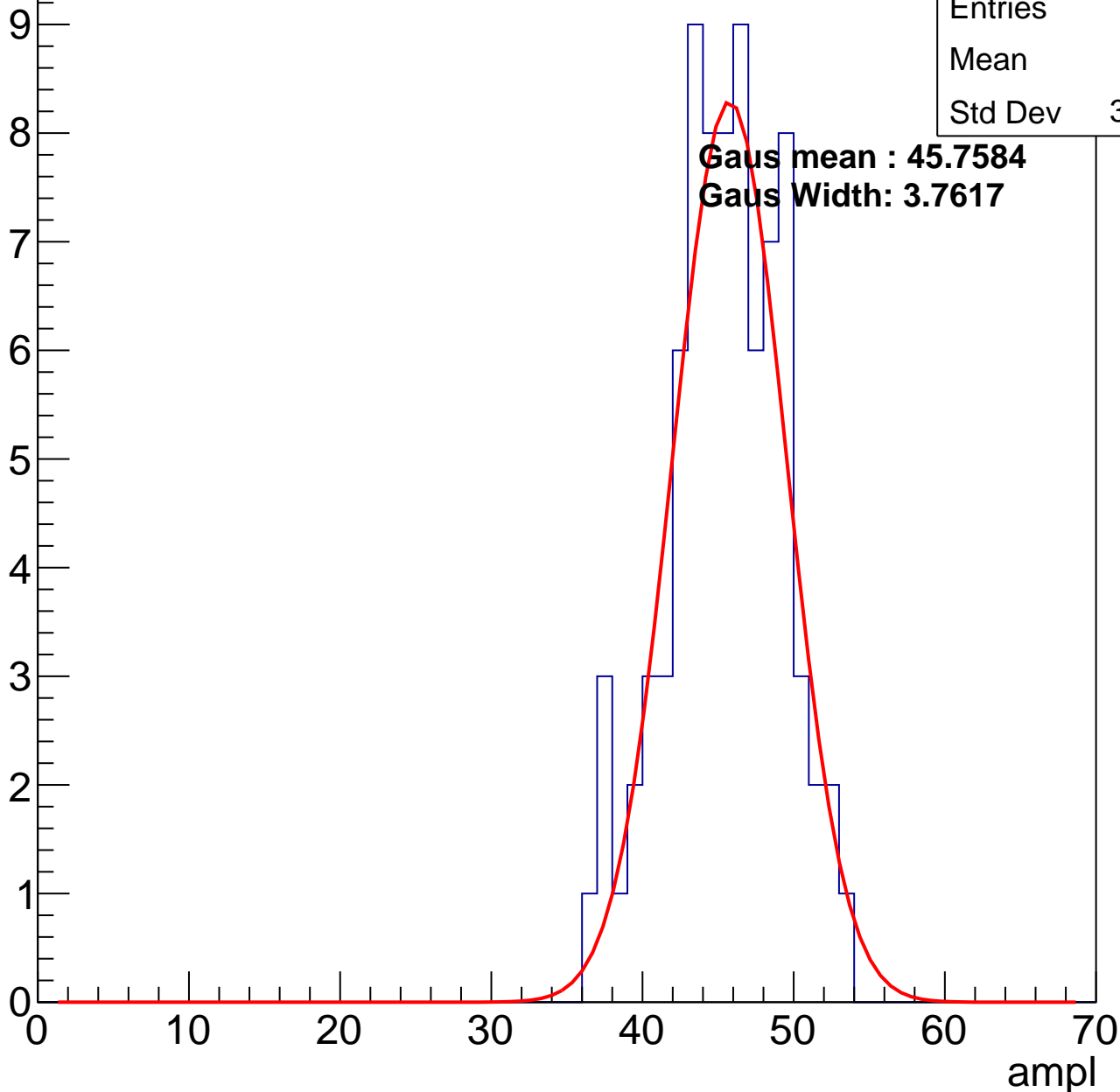
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	45
Std Dev	3.742

Gaus mean : 45.7584

Gaus Width: 3.7617

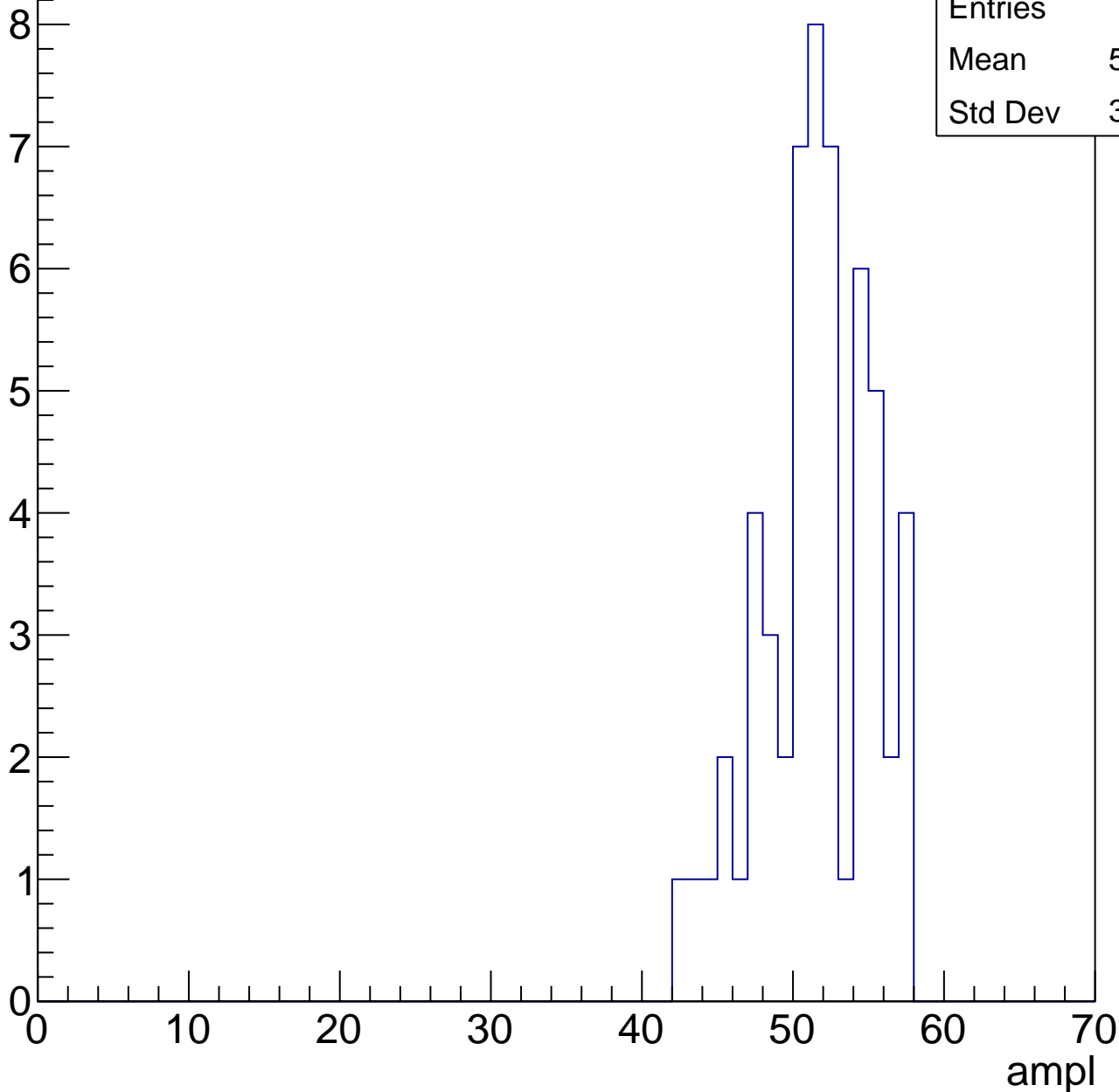


B0L001S, U23-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.07
Std Dev	3.657

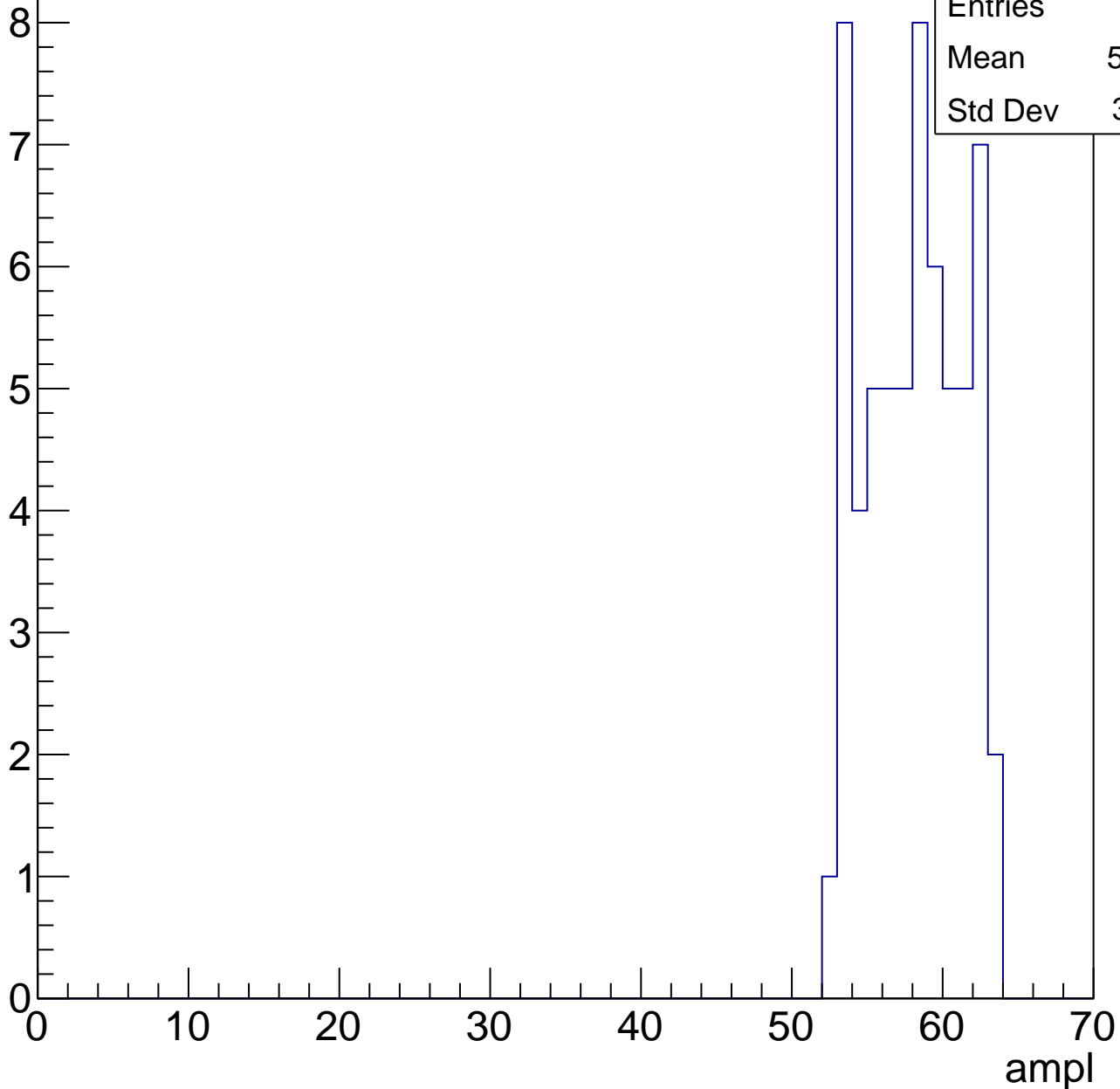


B0L001S, U23-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	57.62
Std Dev	3.121

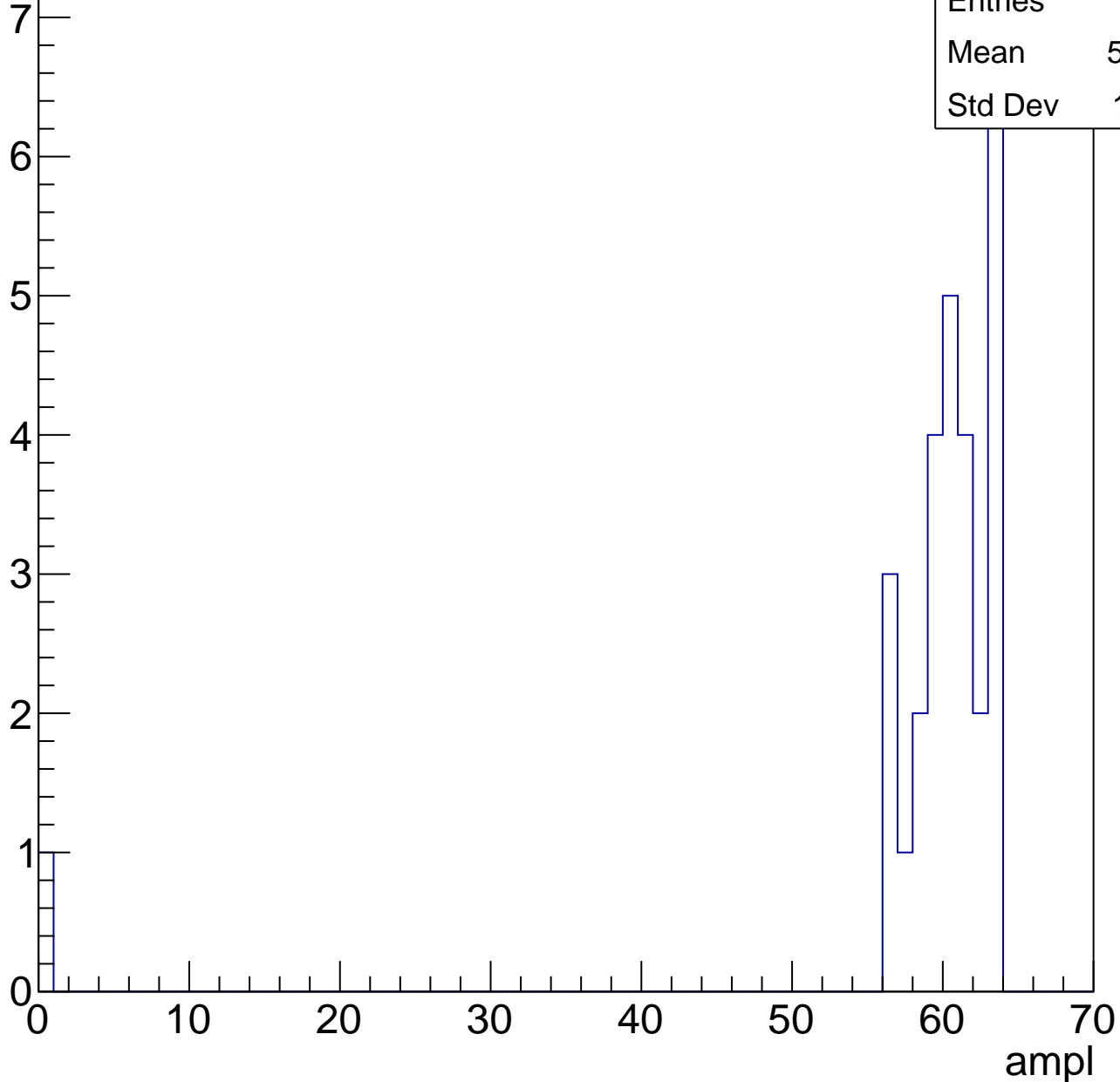


B0L001S, U23-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.14
Std Dev	11.21



B0L001S, U23-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U23-ch127, adc0

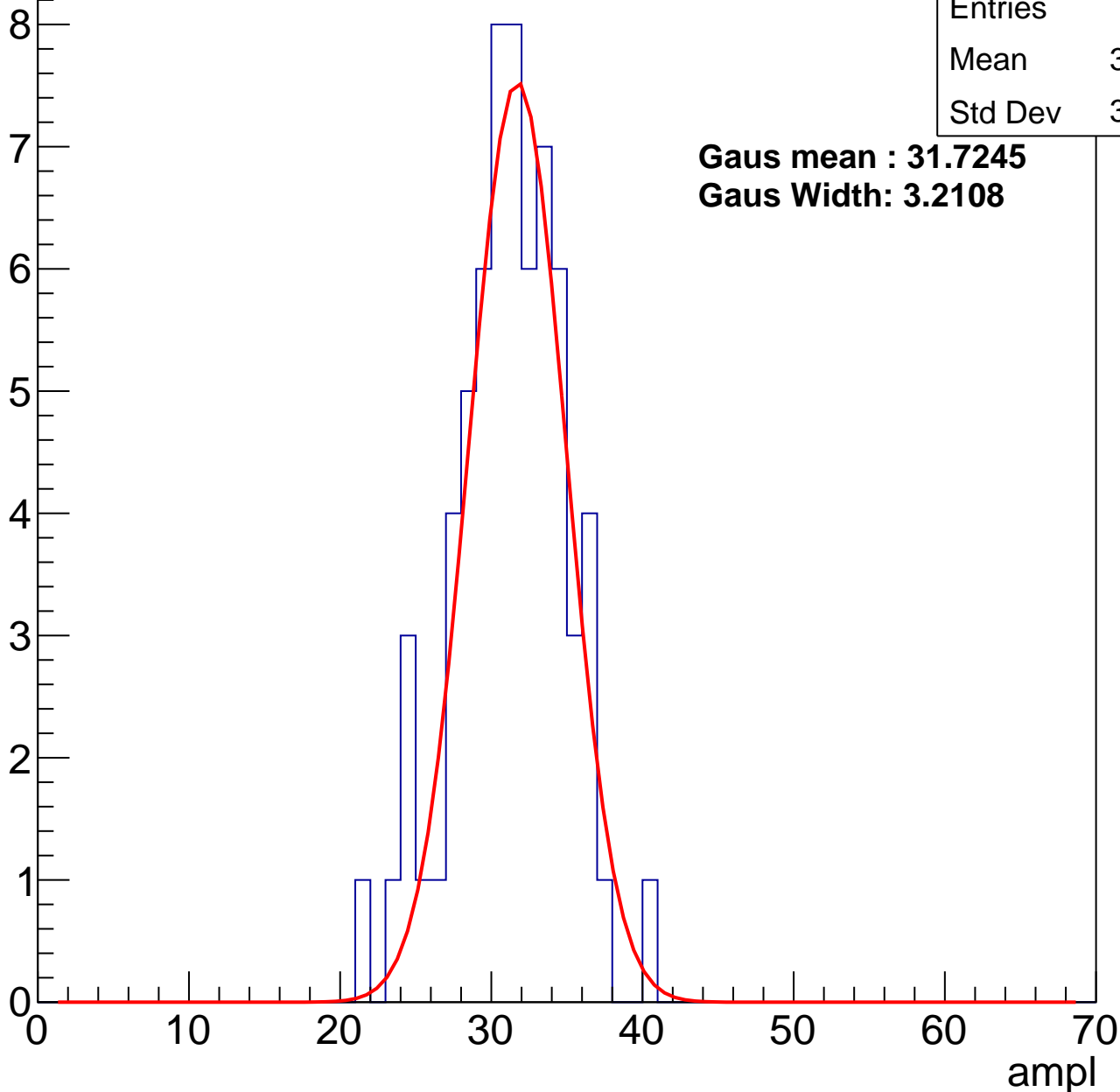
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.76
Std Dev	3.614

Gaus mean : 31.7245

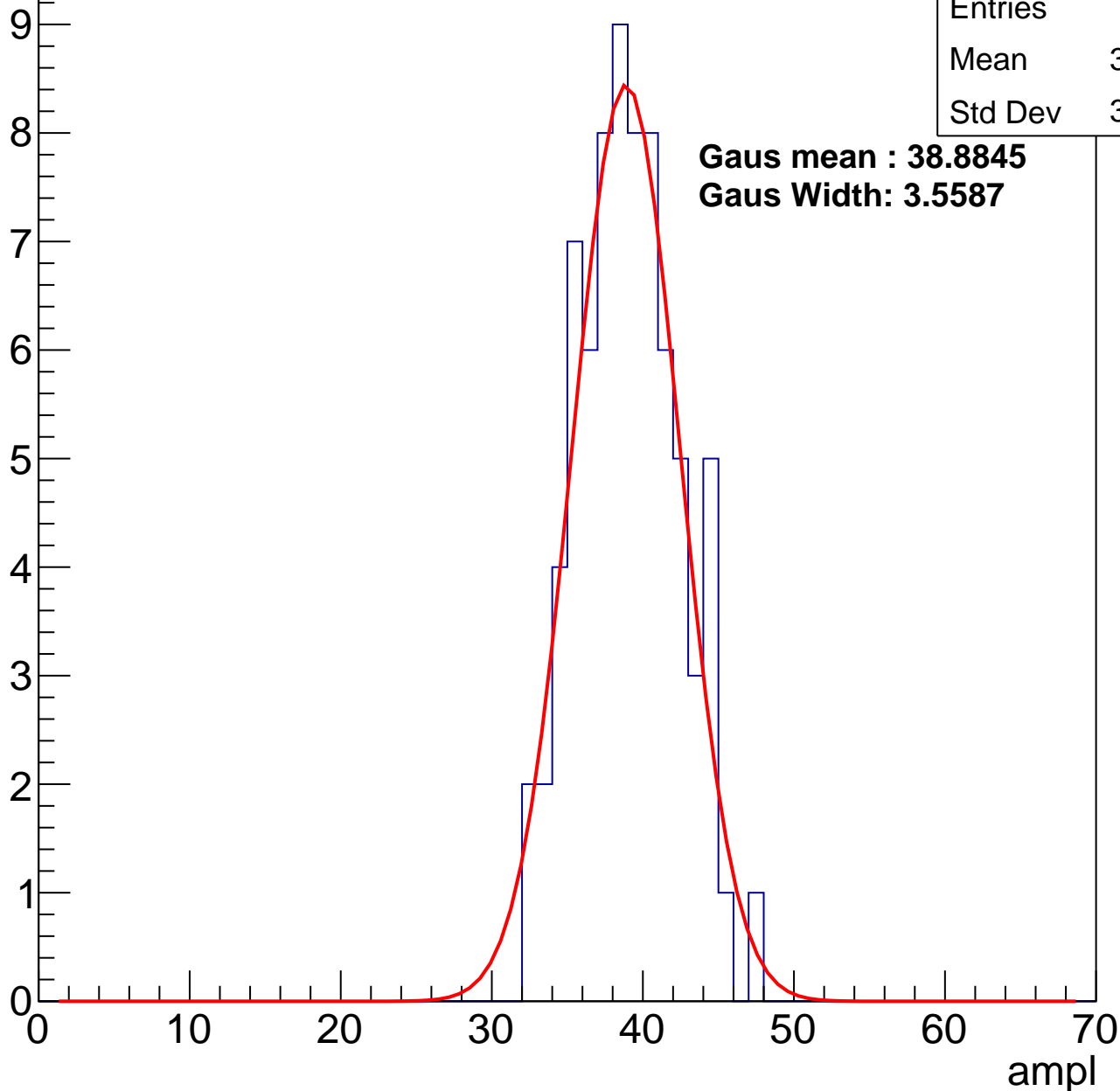
Gaus Width: 3.2108



B0L001S, U23-ch127, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch127, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	45.41
Std Dev	3.33

Gaus mean : 46.3302

Gaus Width: 3.5546

Entry

10

8

6

4

2

0

0

10

20

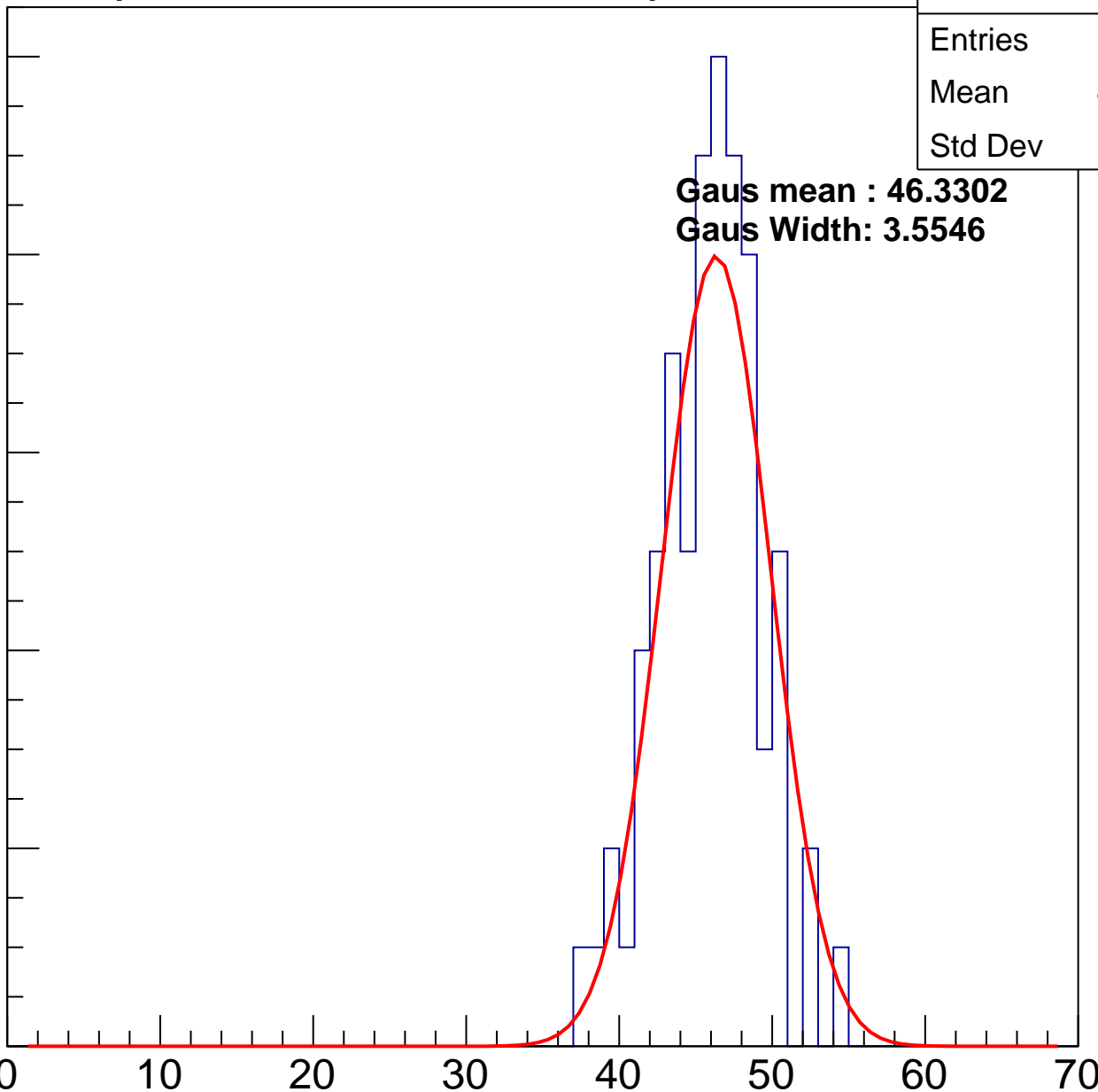
30

40

50

60

ampl



B0L001S, U23-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	53.39
Std Dev	3.07

Entry

10

8

6

4

2

0

0

10

20

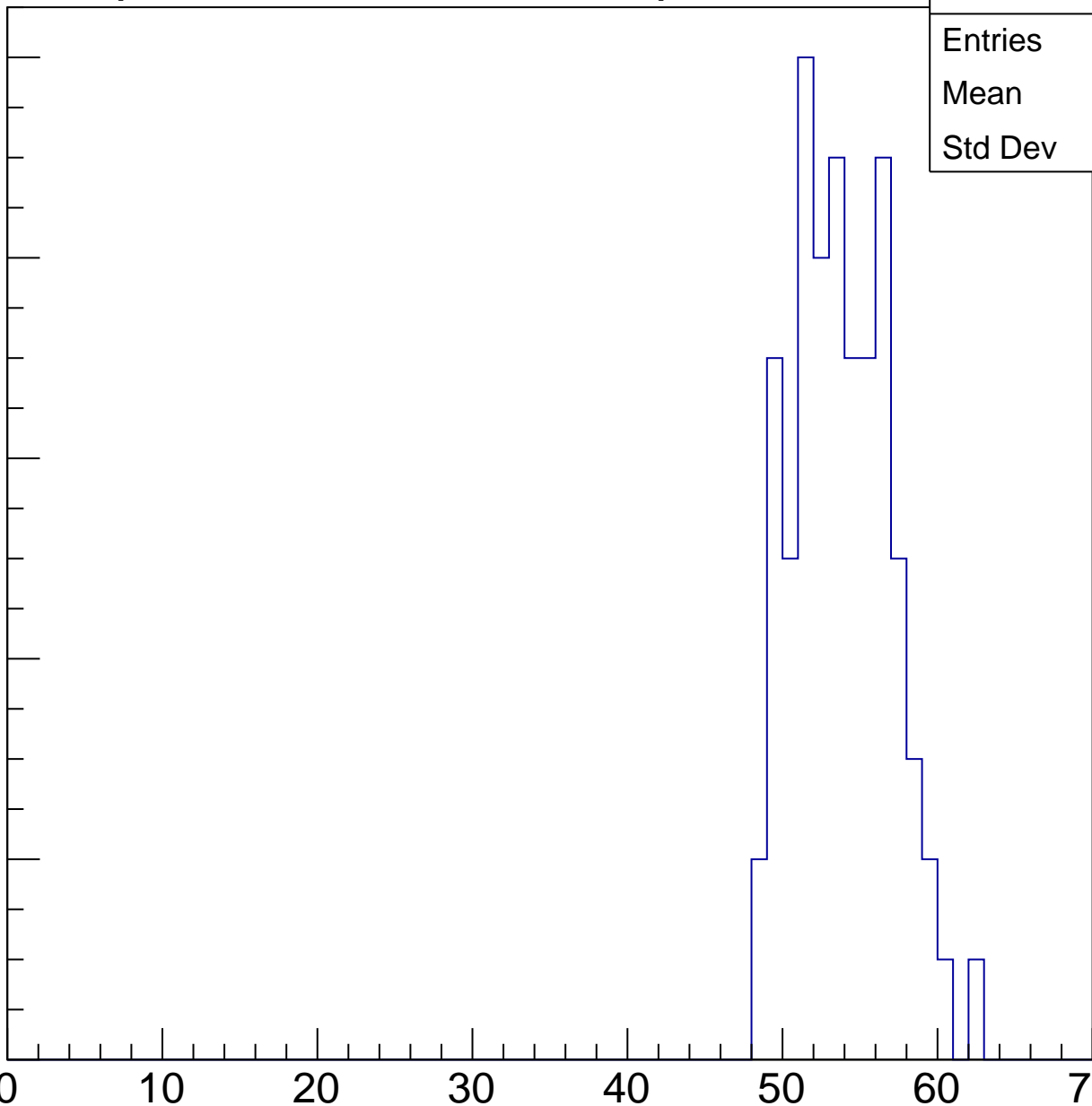
30

40

50

60

ampl

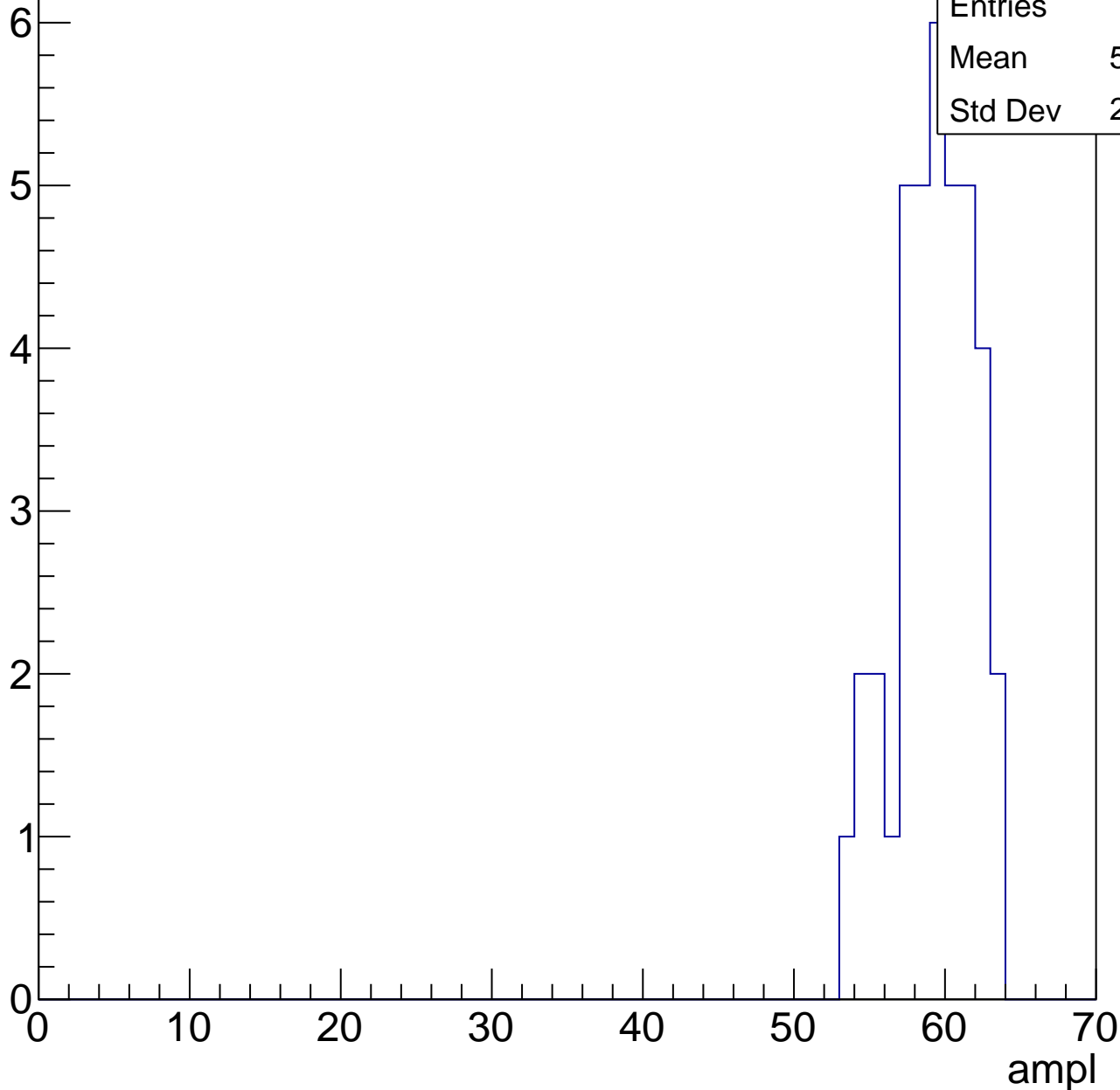


B0L001S, U23-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.82
Std Dev	2.532

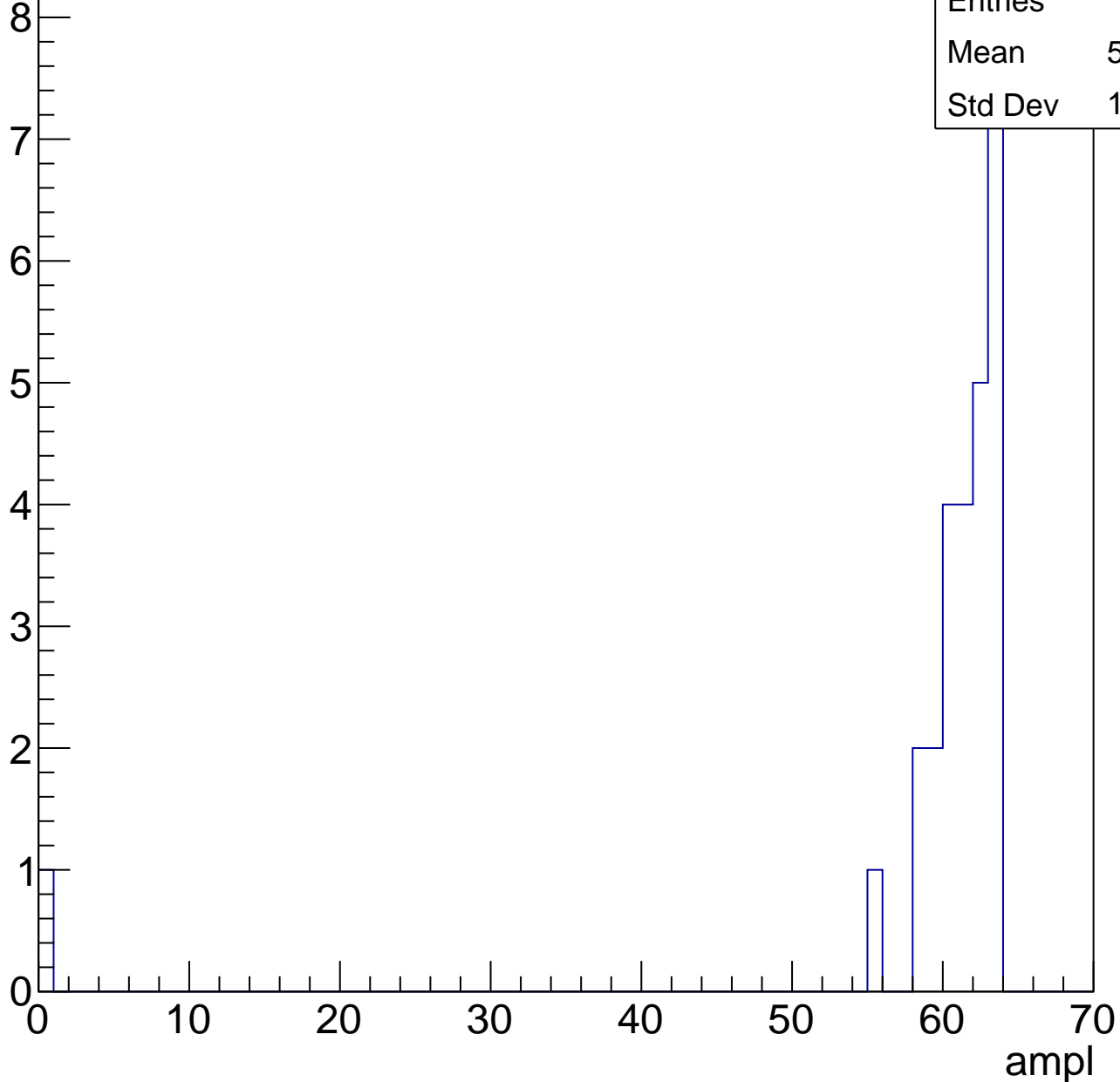


B0L001S, U23-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.78
Std Dev	11.69



B0L001S, U23-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

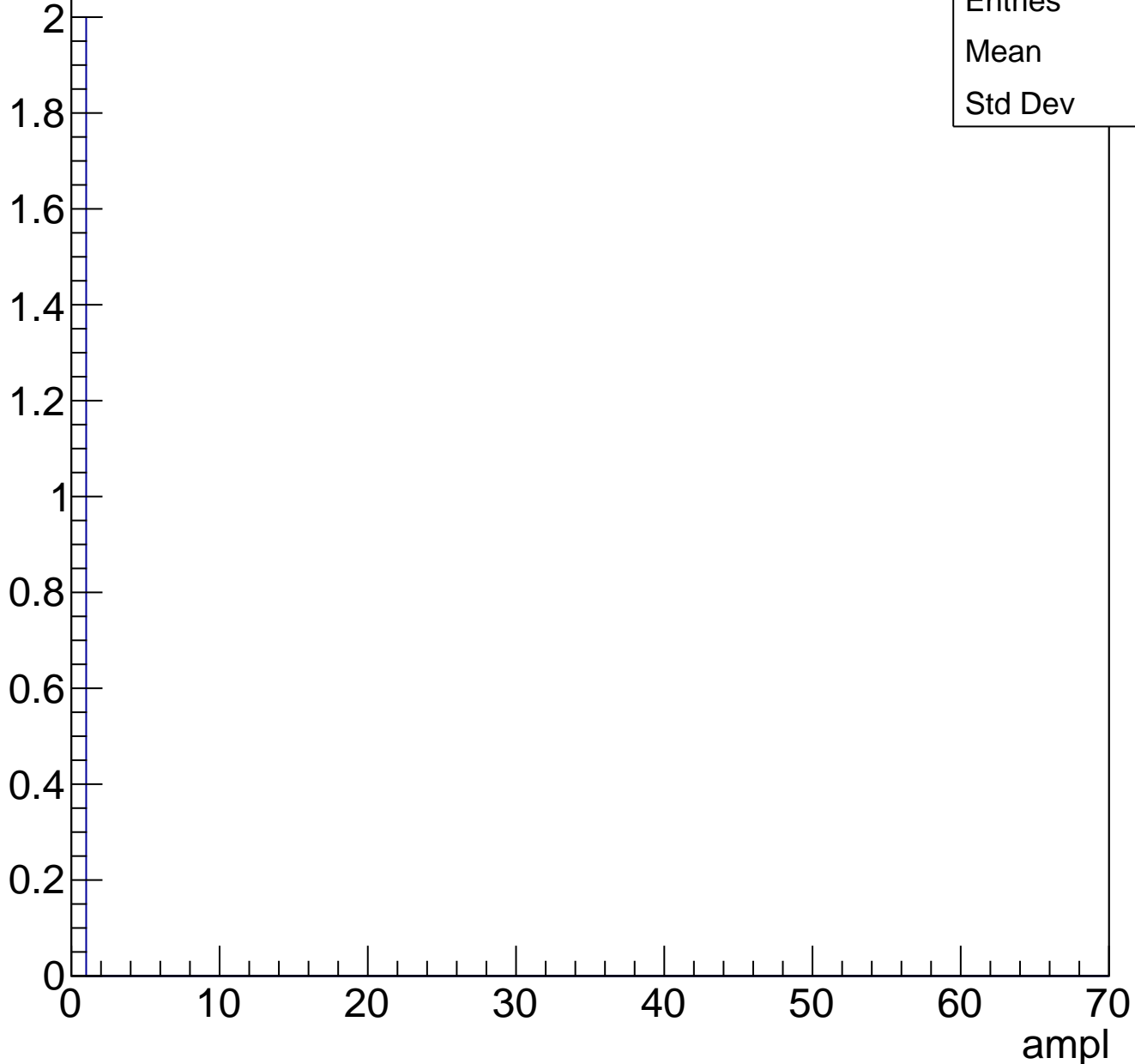
Entry



B0L001S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0