

B1L103S, U20-ch0, adc0

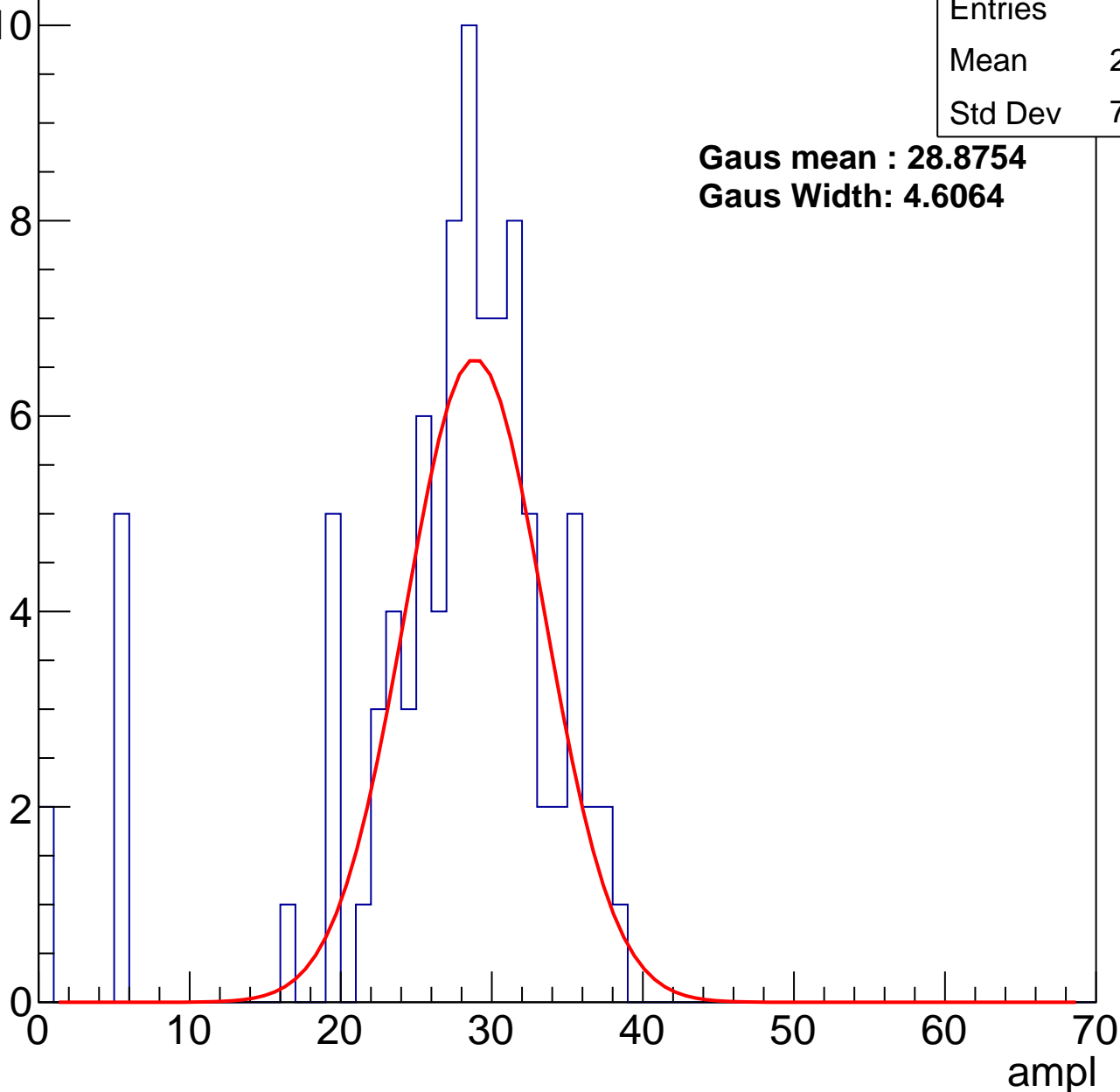
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	93
Mean	26.35
Std Dev	7.923

Gaus mean : 28.8754

Gaus Width: 4.6064



B1L103S, U20-ch0, adc1

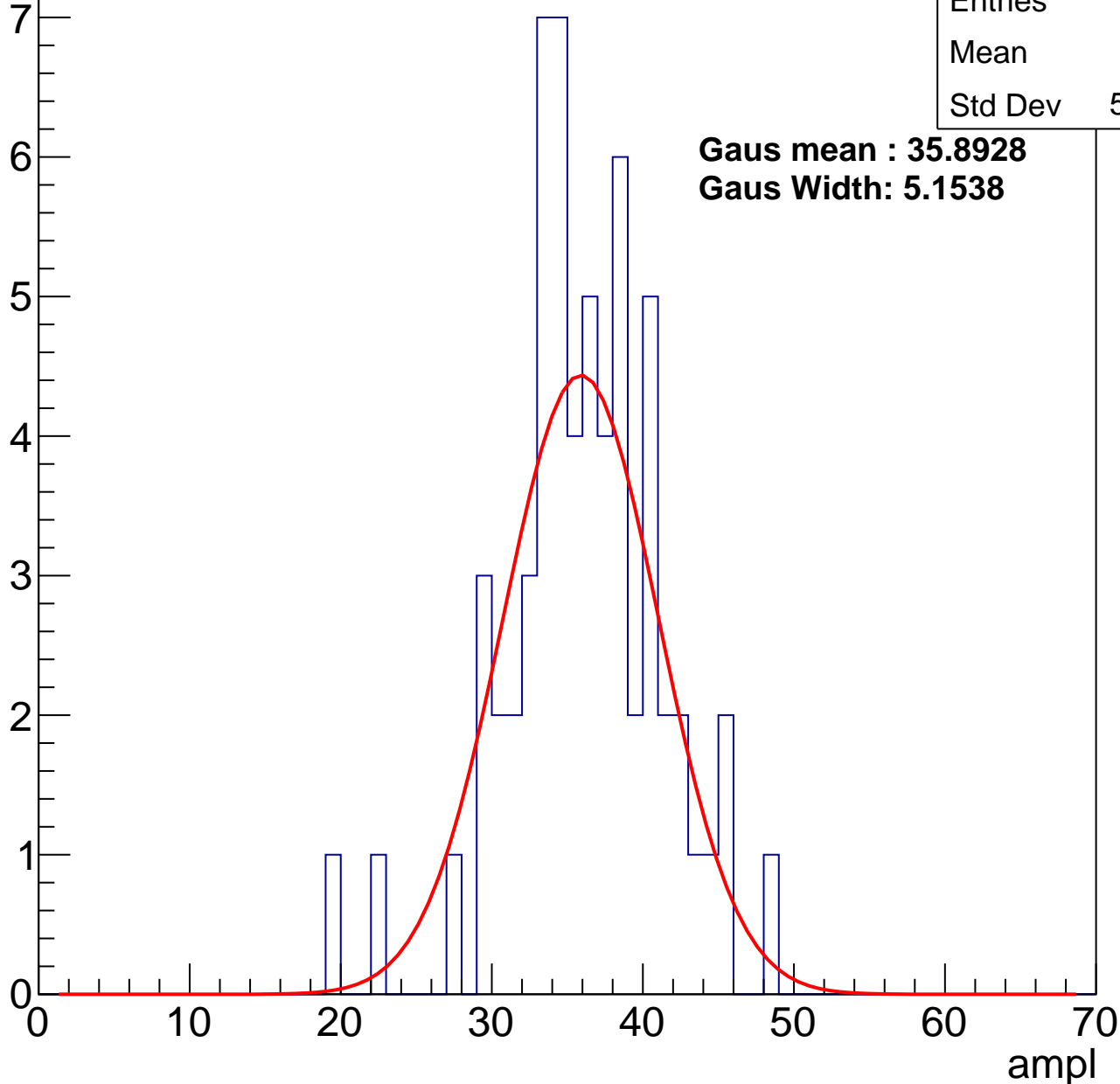
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.6
Std Dev	5.135

Gaus mean : 35.8928

Gaus Width: 5.1538



B1L103S, U20-ch0, adc2

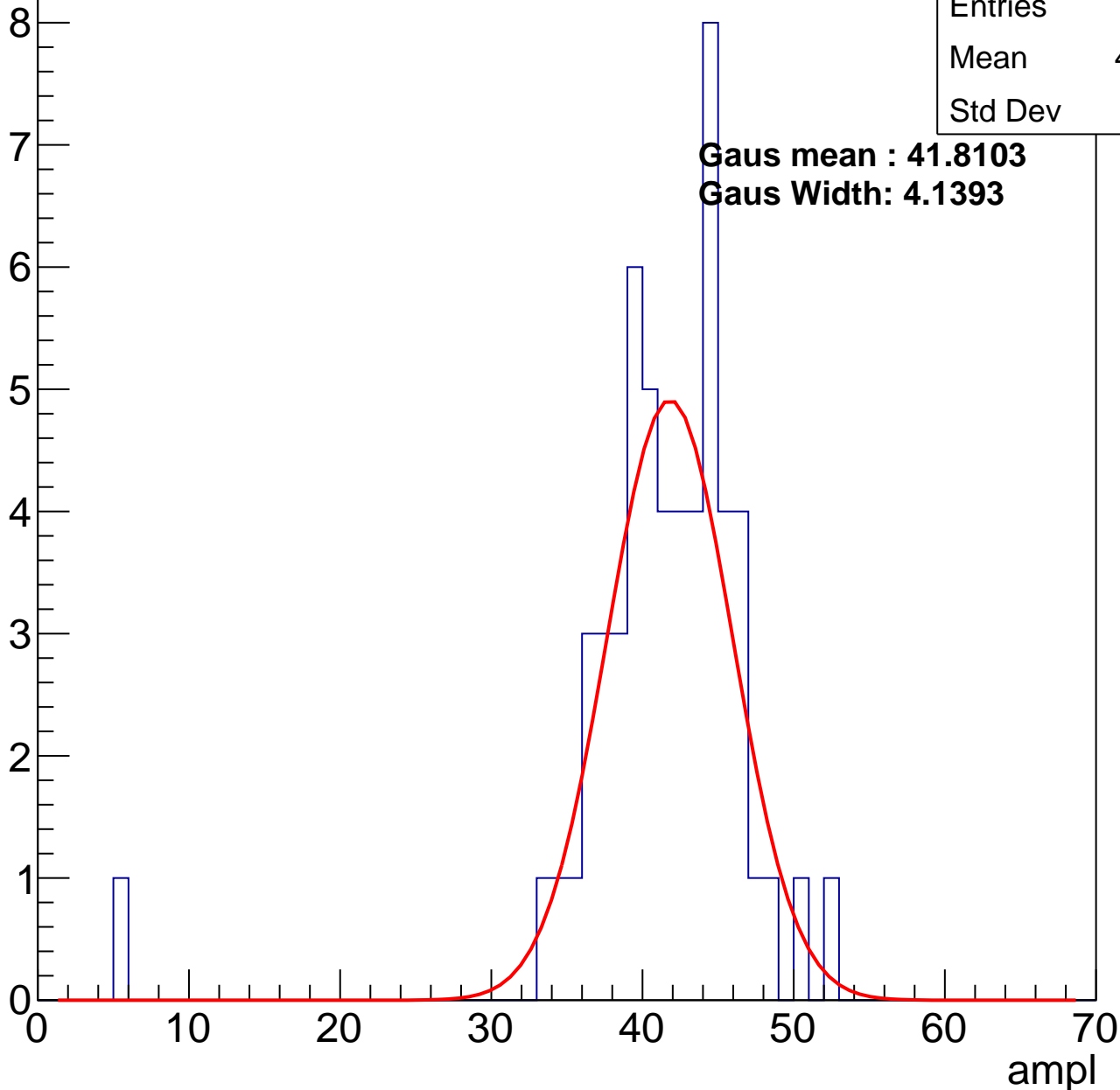
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	40.91
Std Dev	6.22

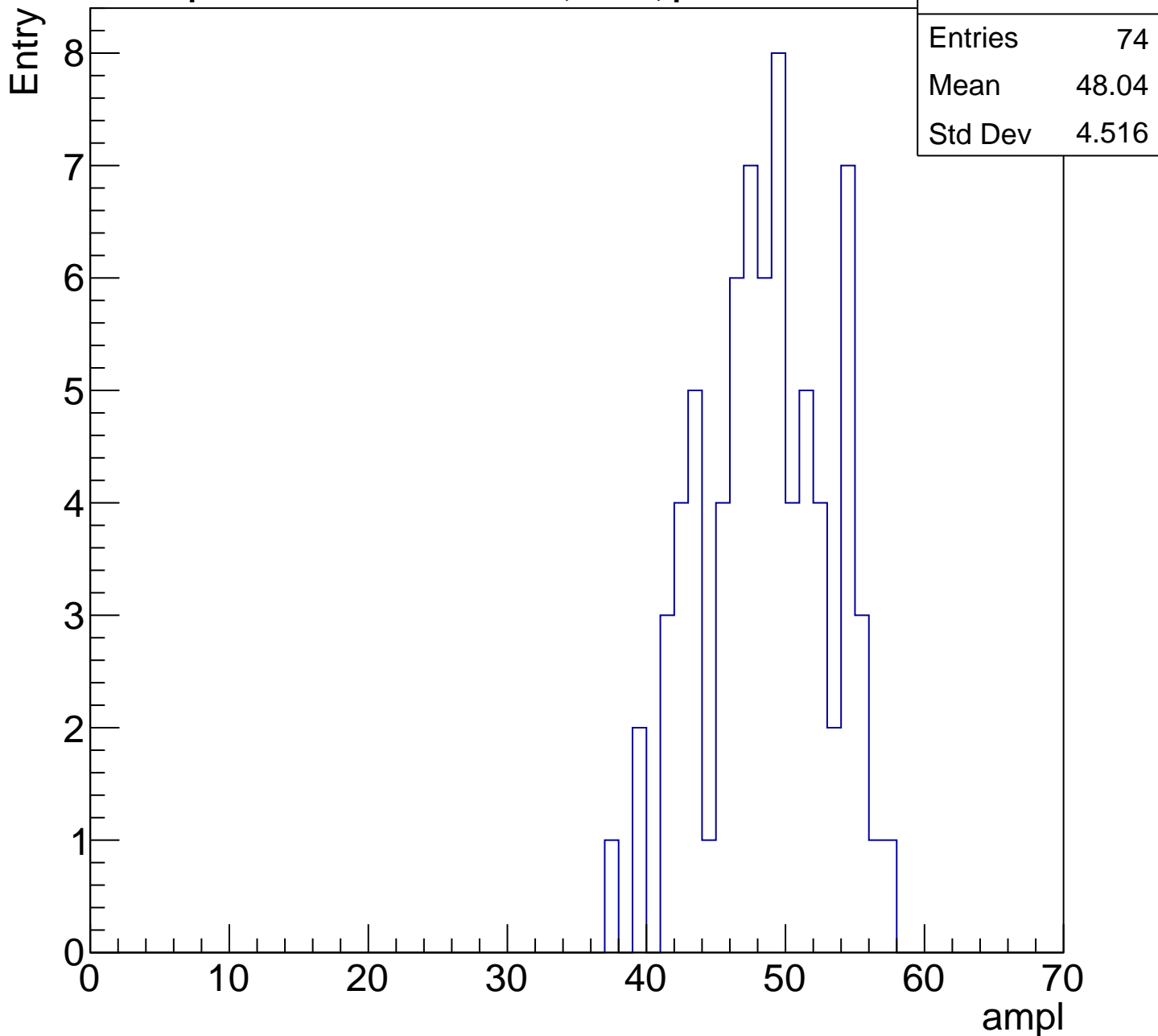
Gaus mean : 41.8103

Gaus Width: 4.1393



B1L103S, U20-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

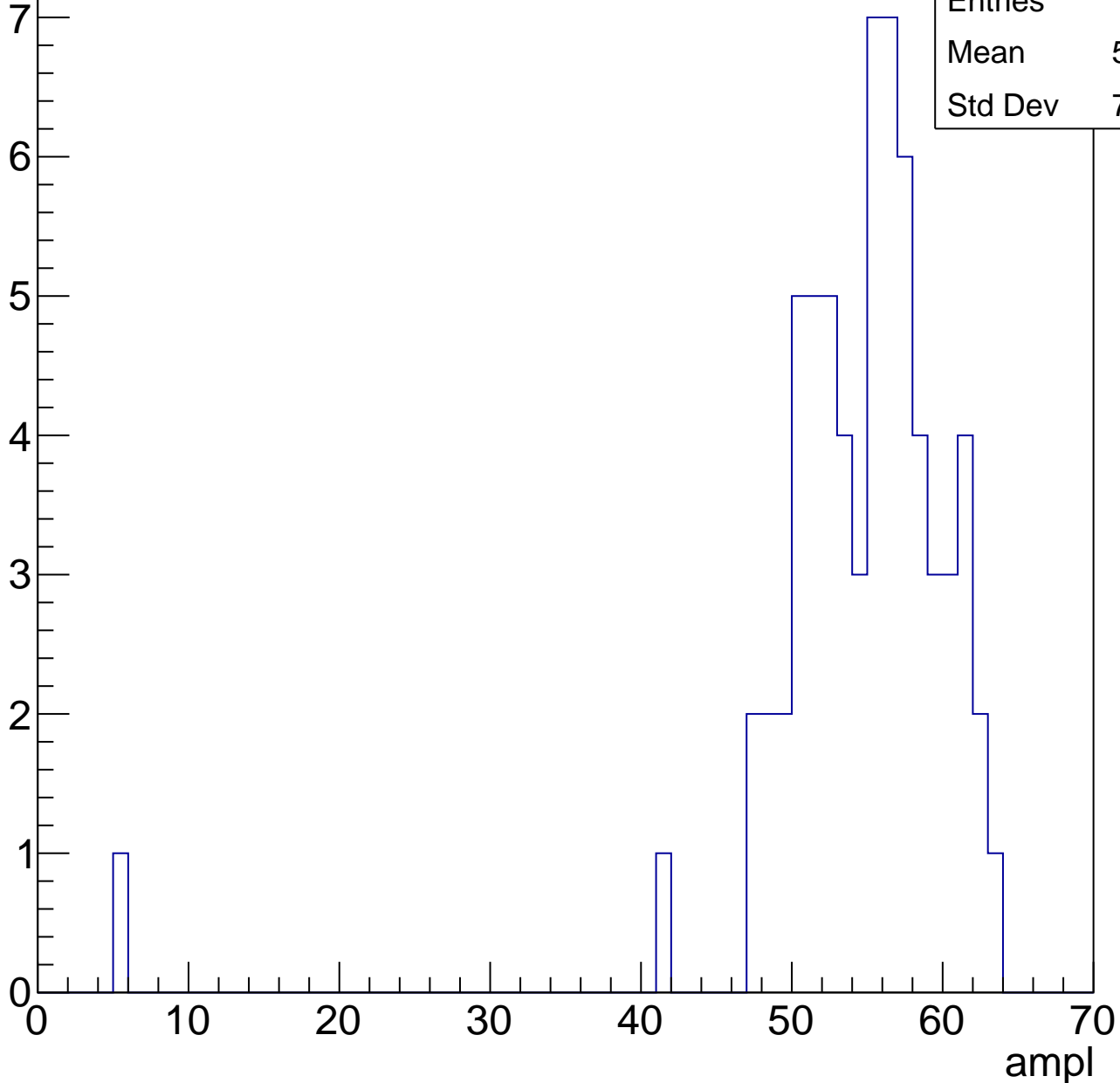


B1L103S, U20-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	53.91
Std Dev	7.411

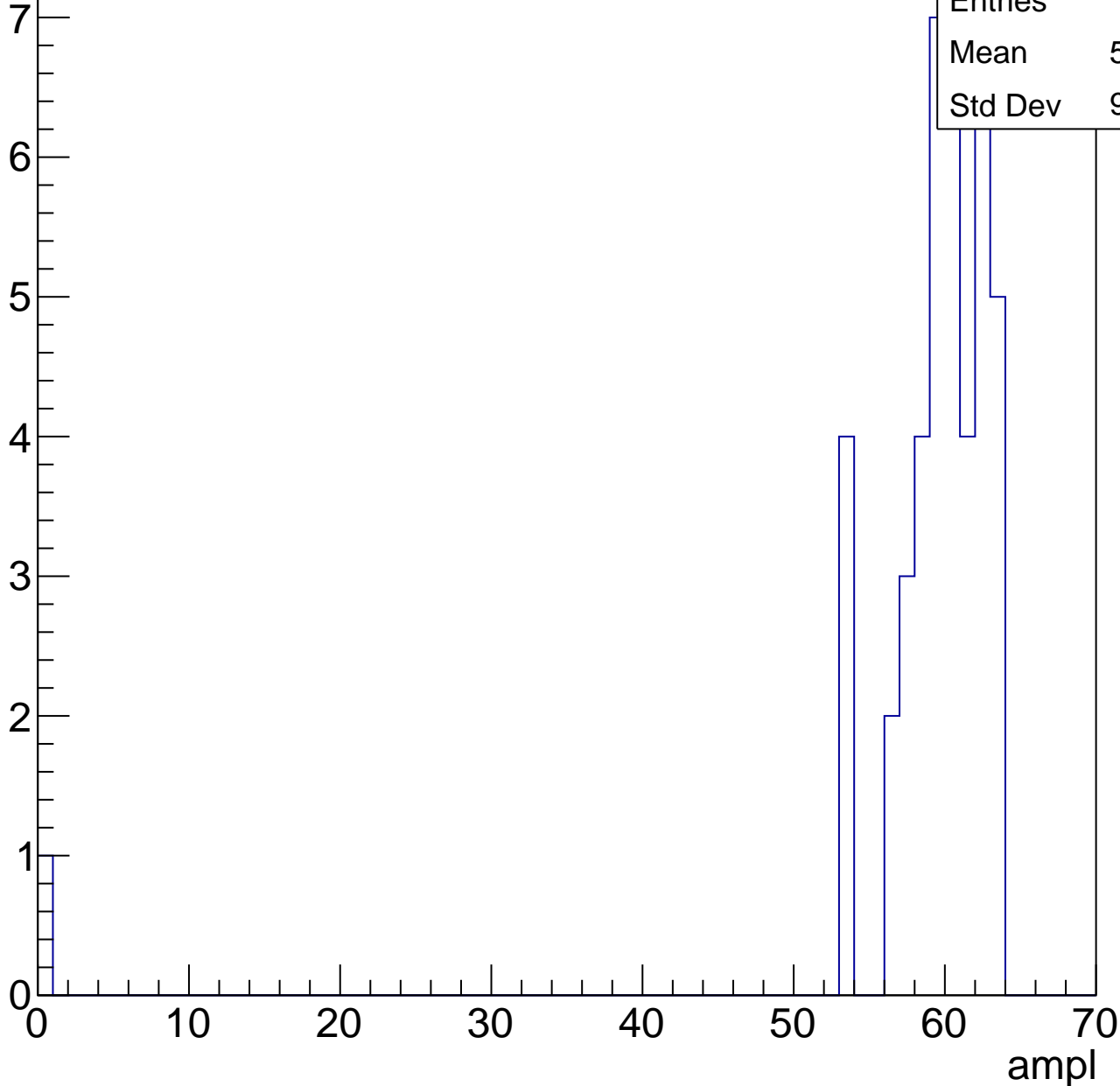


B1L103S, U20-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

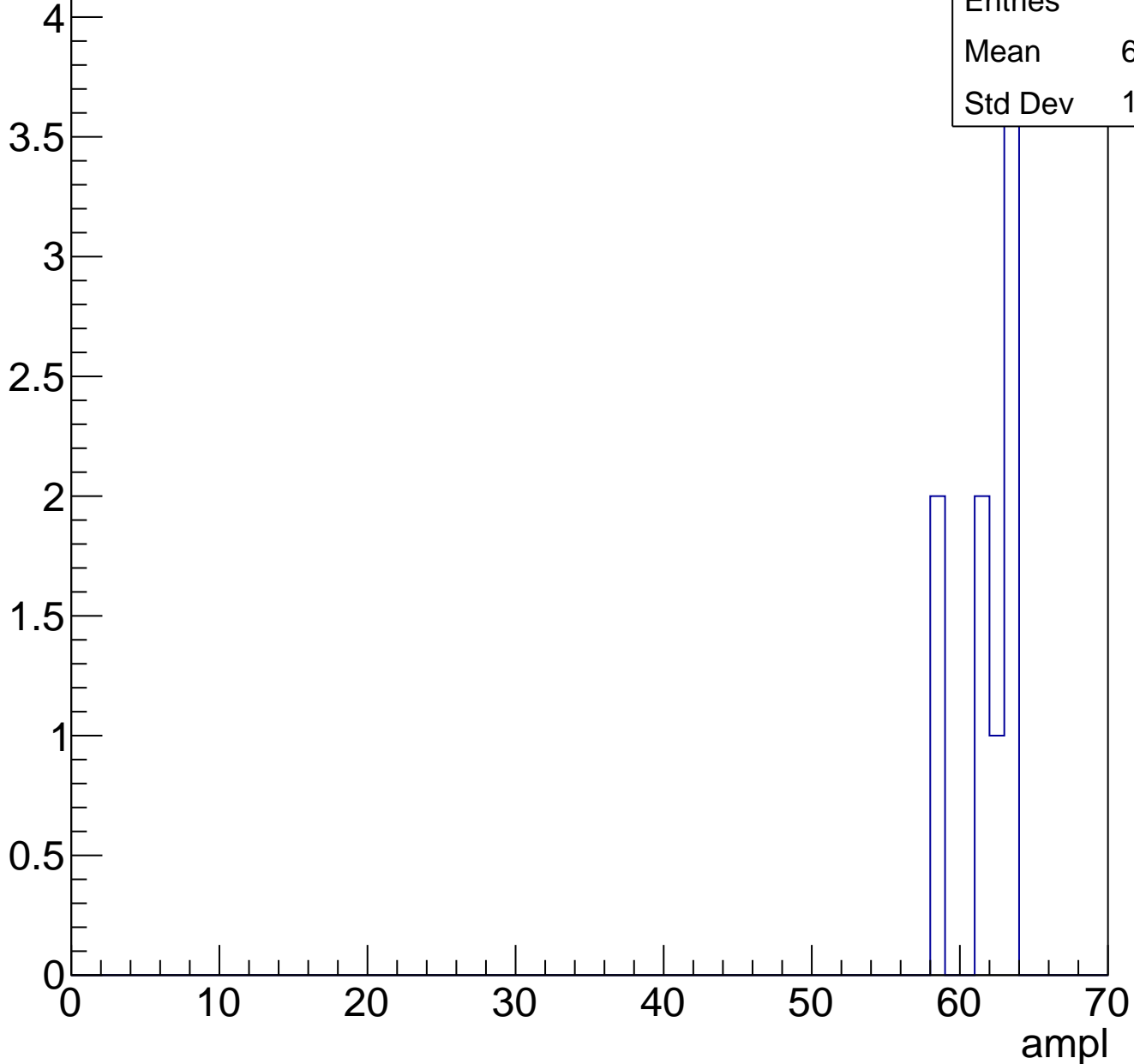
Entries	44
Mean	58.02
Std Dev	9.272



B1L103S, U20-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch1, adc0

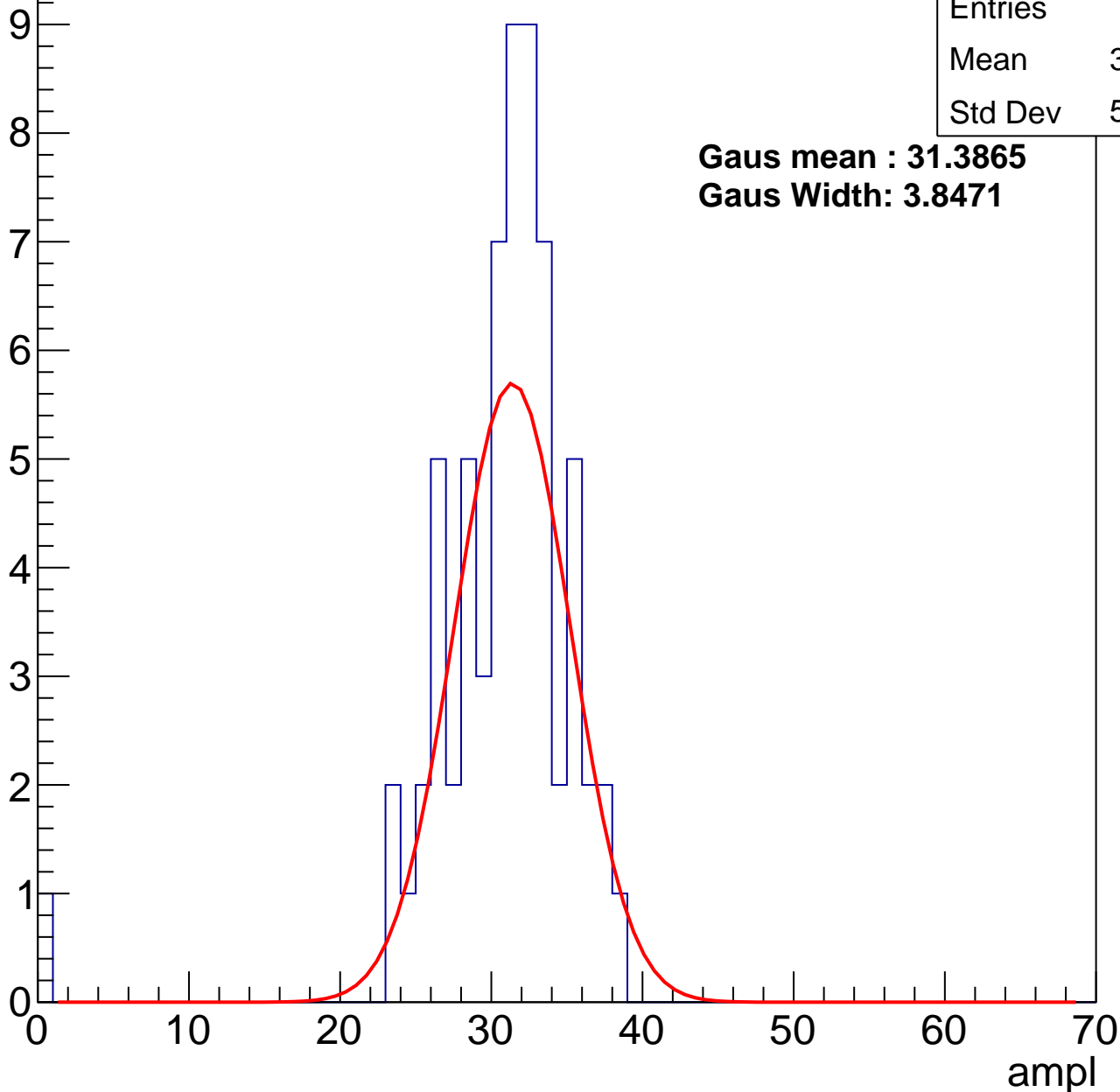
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.25
Std Dev	5.114

Gaus mean : 31.3865

Gaus Width: 3.8471



B1L103S, U20-ch1, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	86
Mean	38.44
Std Dev	3.955

Gaus mean : 38.9936

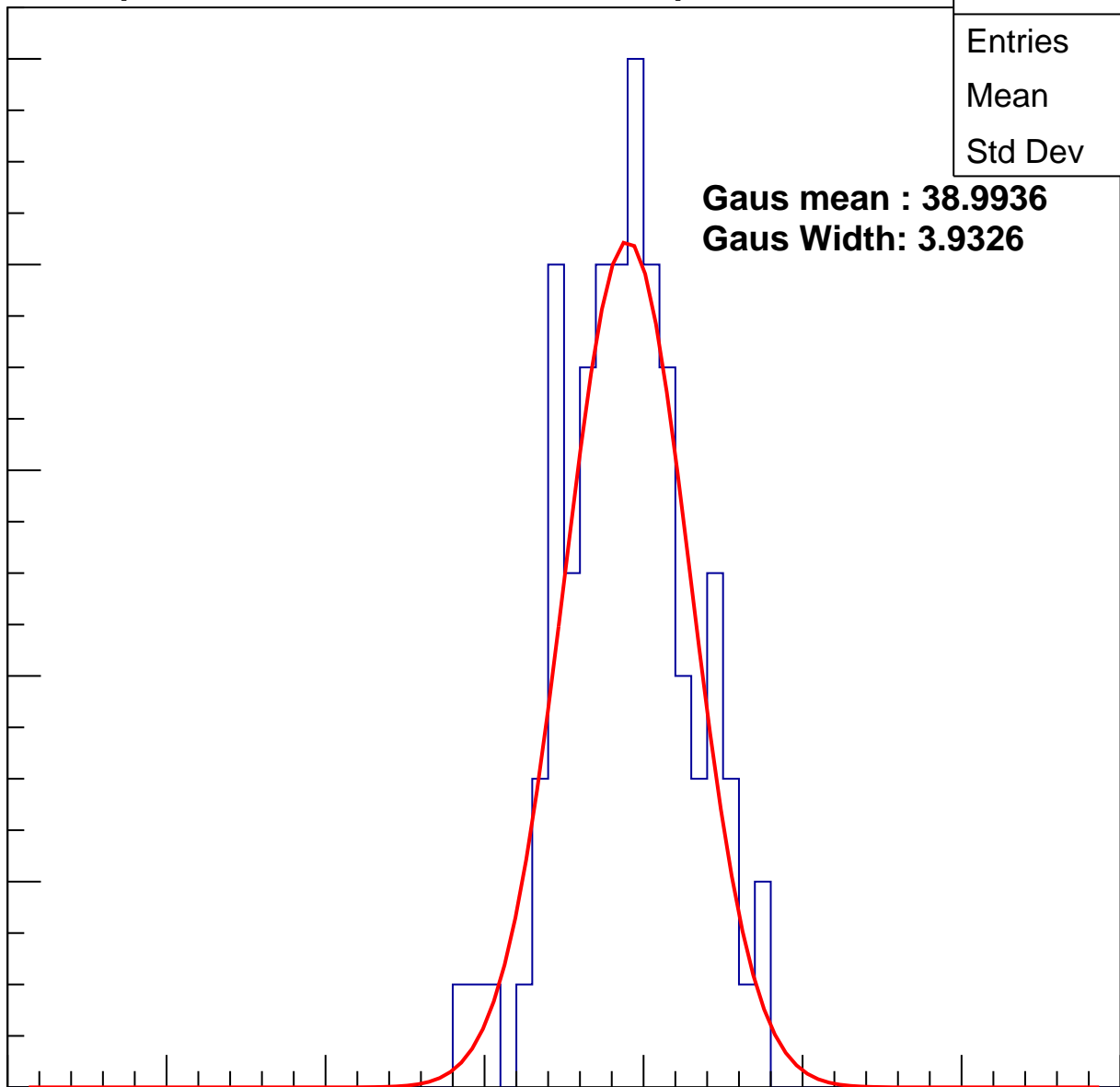
Gaus Width: 3.9326

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch1, adc2

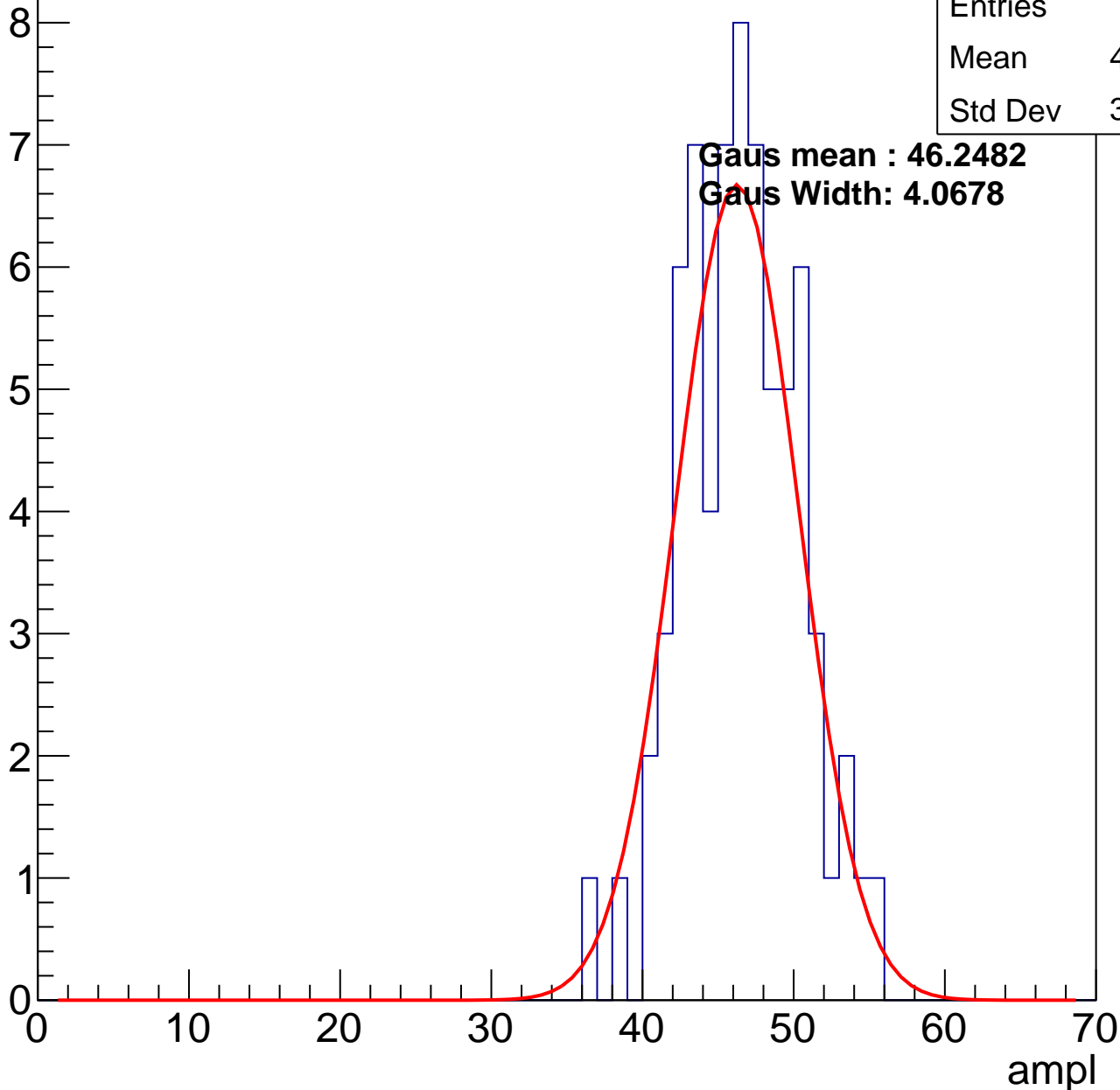
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	46.04
Std Dev	3.808

Gaus mean : 46.2482

Gaus Width: 4.0678

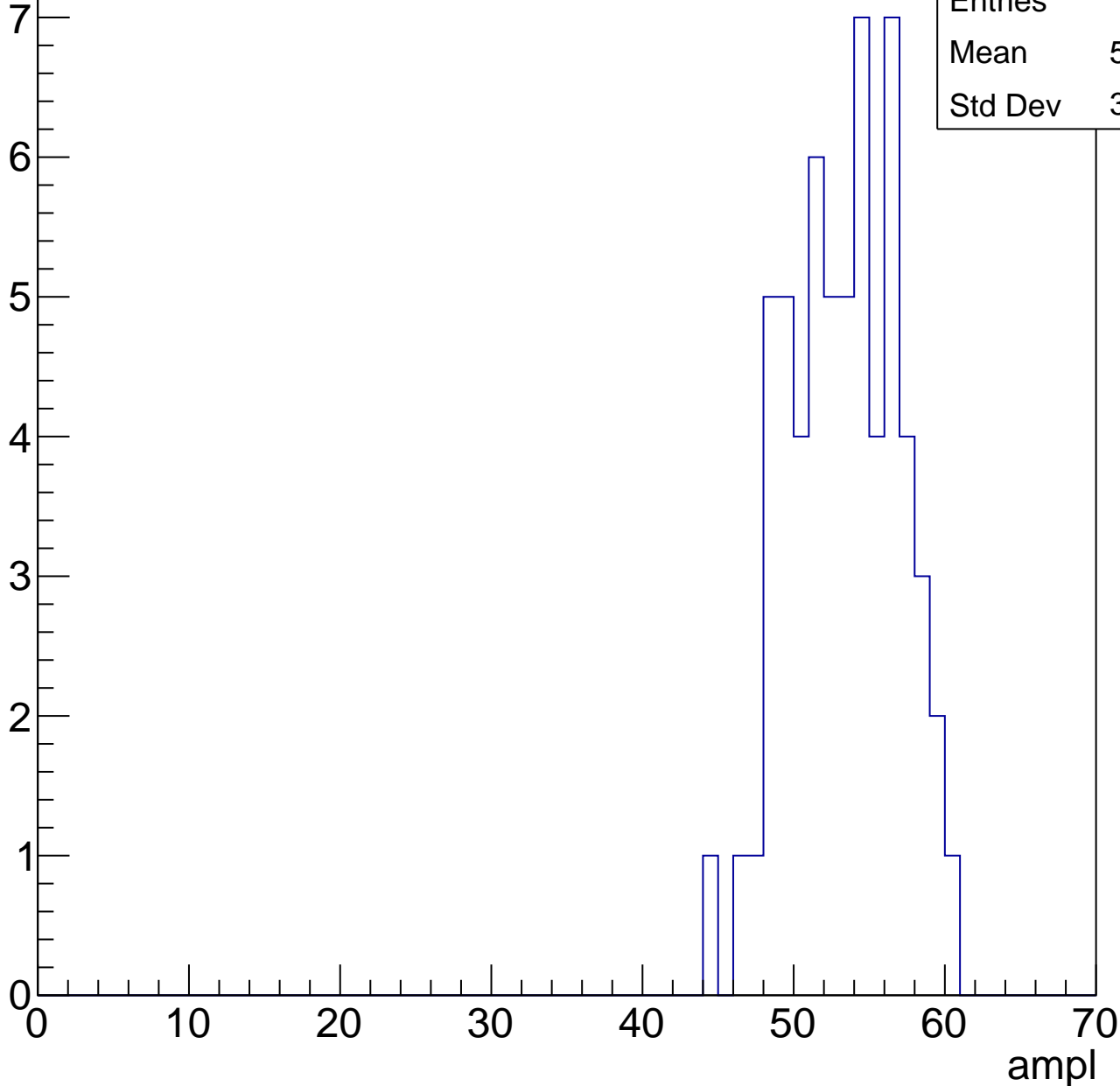


B1L103S, U20-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	52.84
Std Dev	3.577

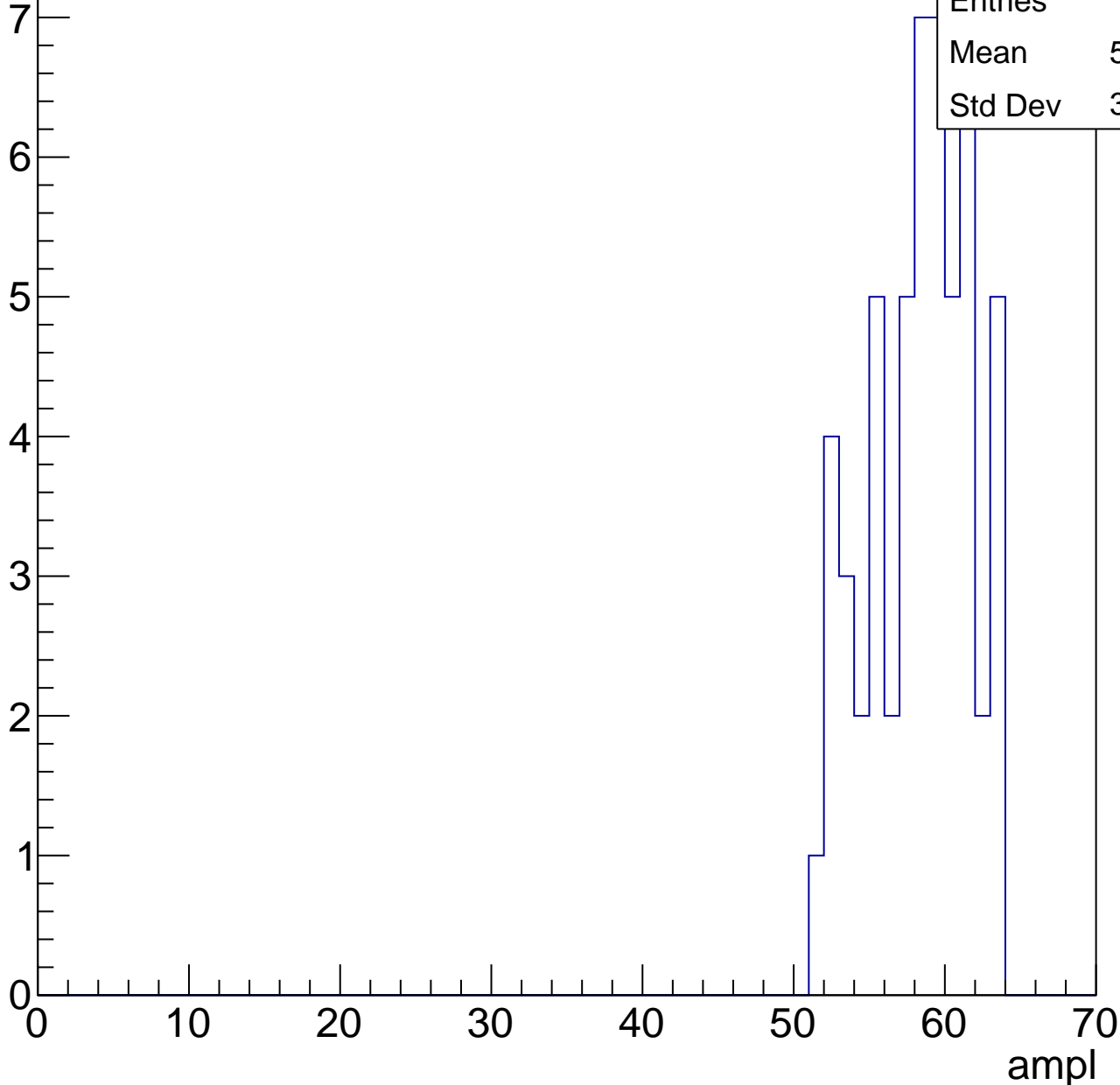


B1L103S, U20-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.87
Std Dev	3.314

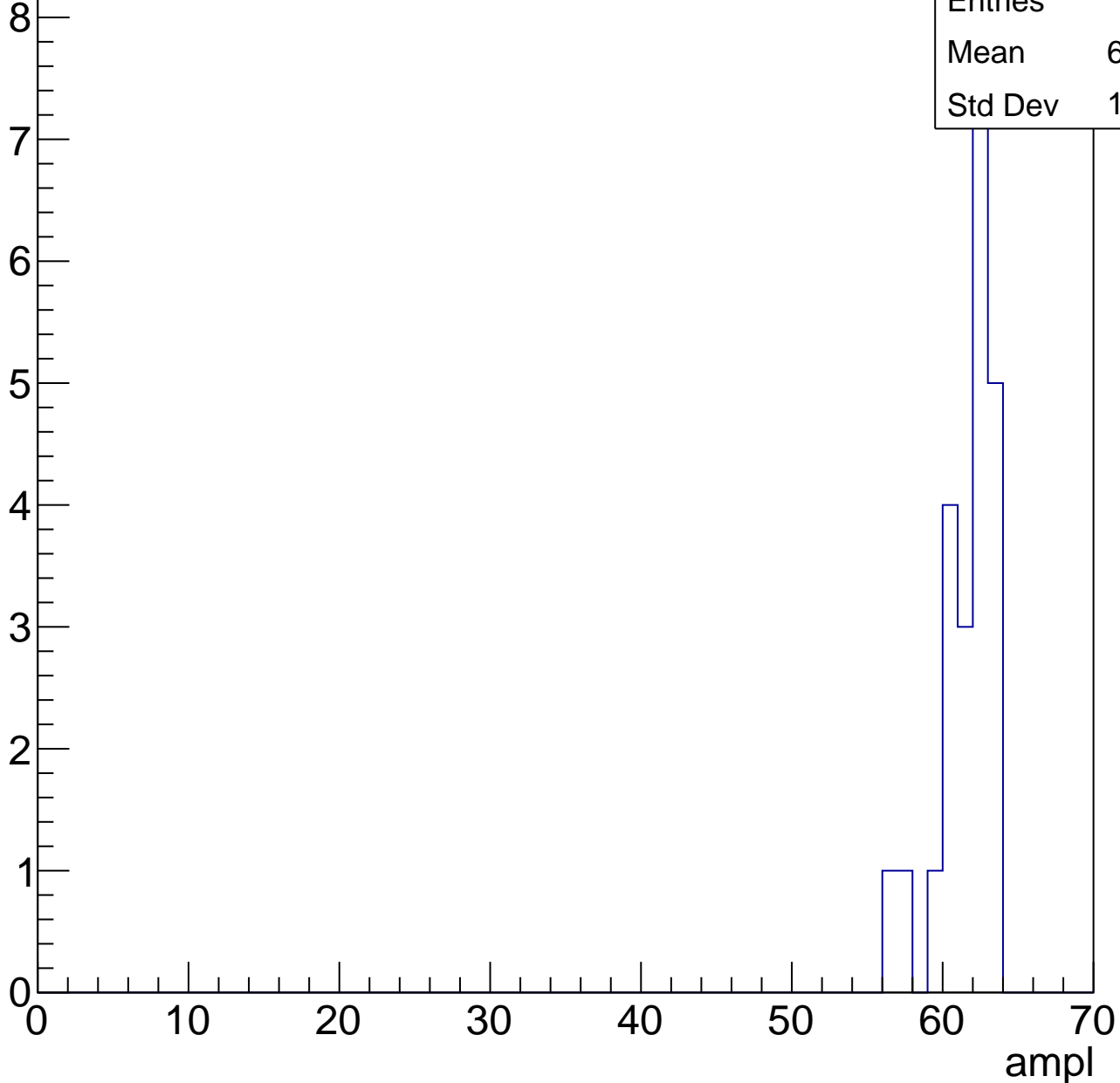


B1L103S, U20-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	61.13
Std Dev	1.825



B1L103S, U20-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch2, adc0

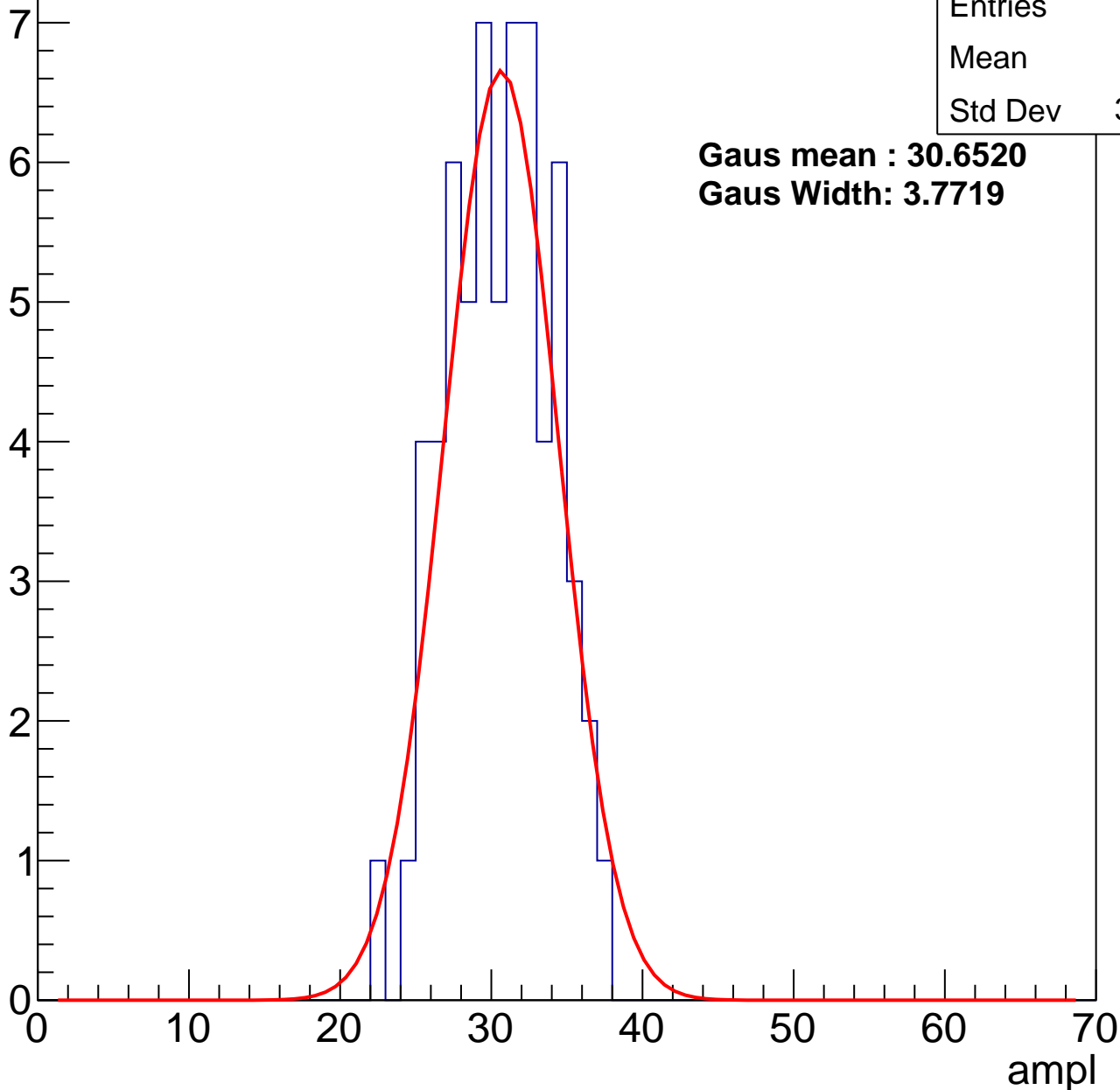
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	30.1
Std Dev	3.351

Gaus mean : 30.6520

Gaus Width: 3.7719



B1L103S, U20-ch2, adc1

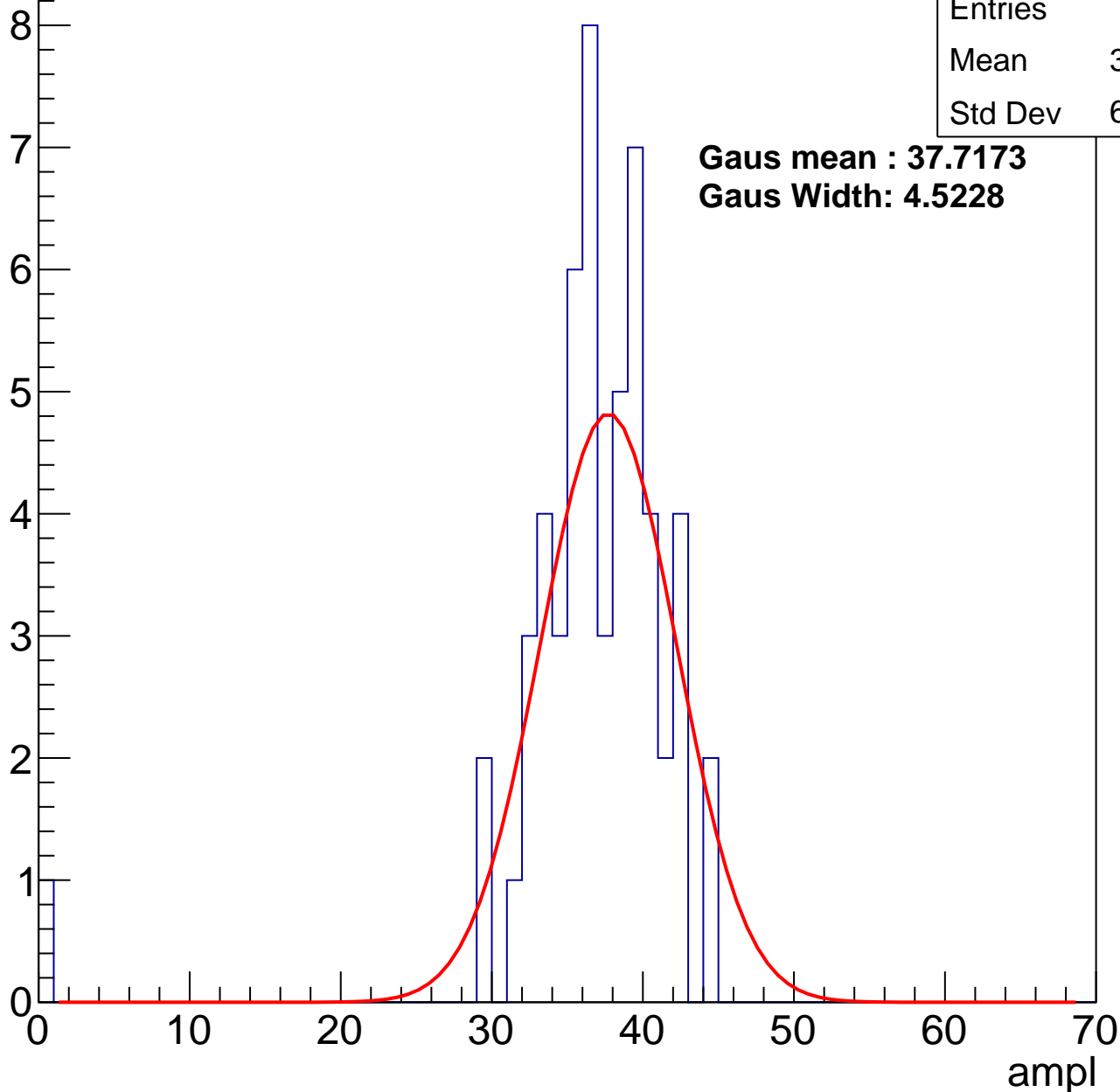
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	36.16
Std Dev	6.017

Gaus mean : 37.7173

Gaus Width: 4.5228



B1L103S, U20-ch2, adc2

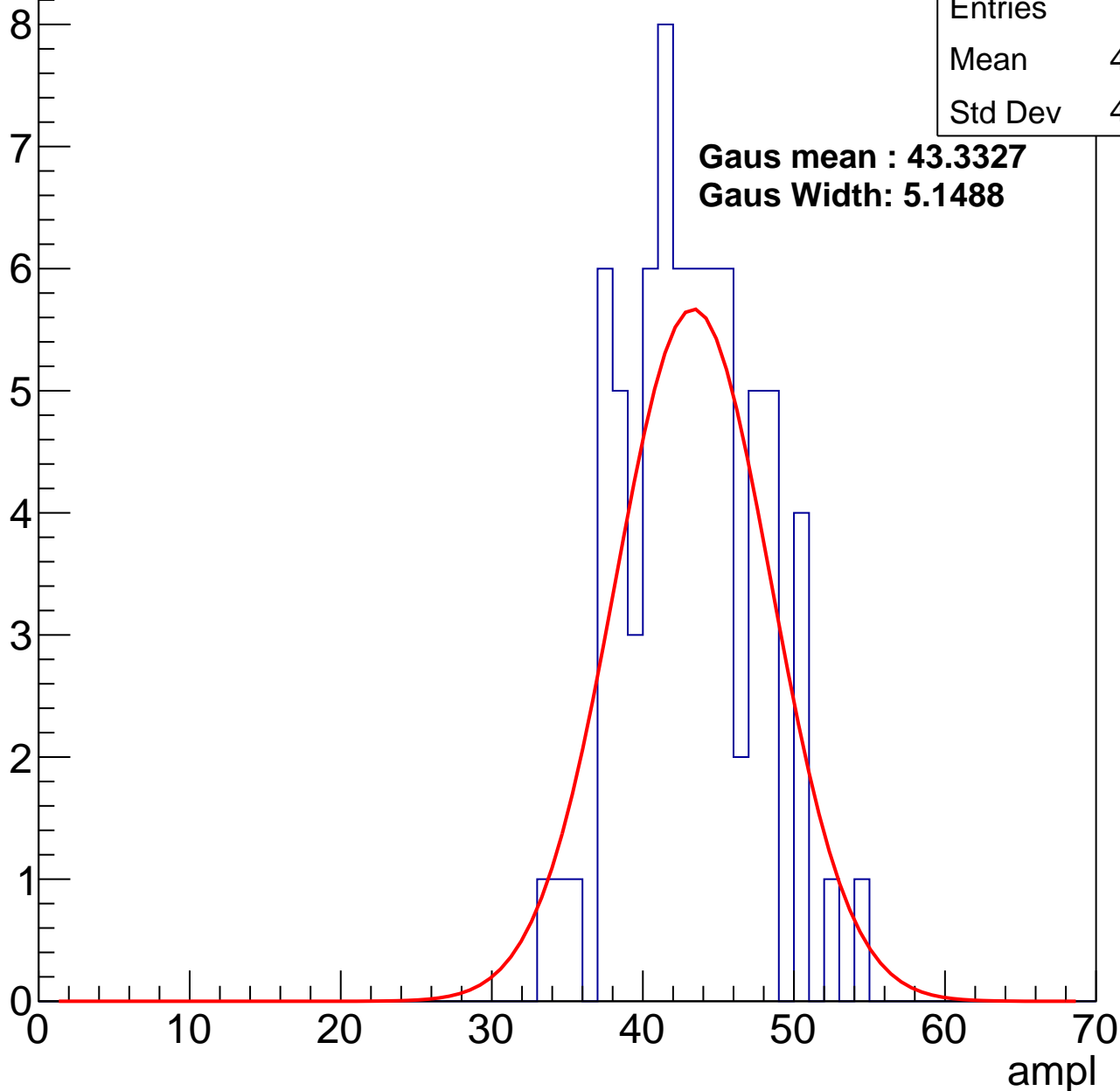
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.68
Std Dev	4.332

Gaus mean : 43.3327

Gaus Width: 5.1488

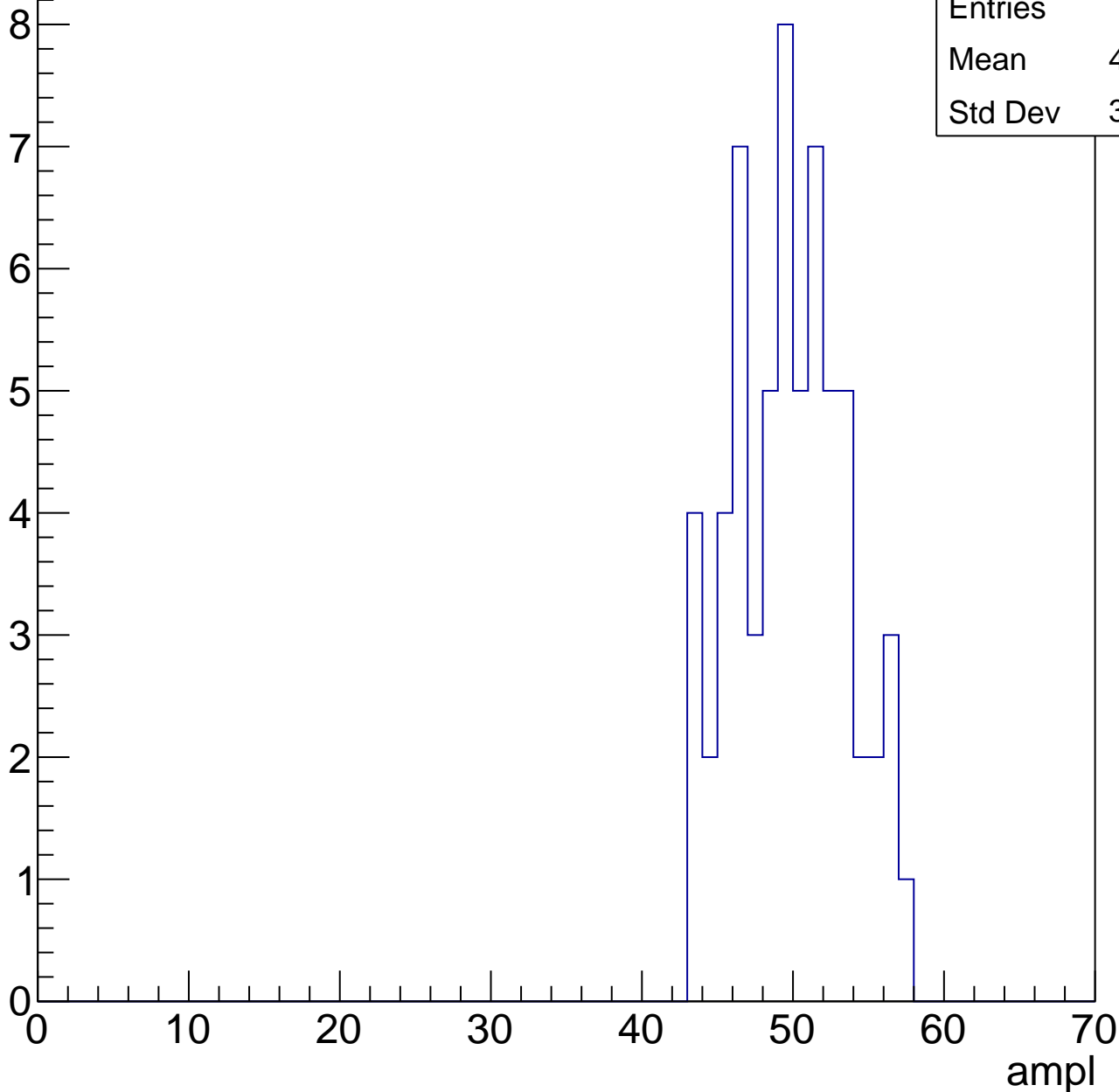


B1L103S, U20-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

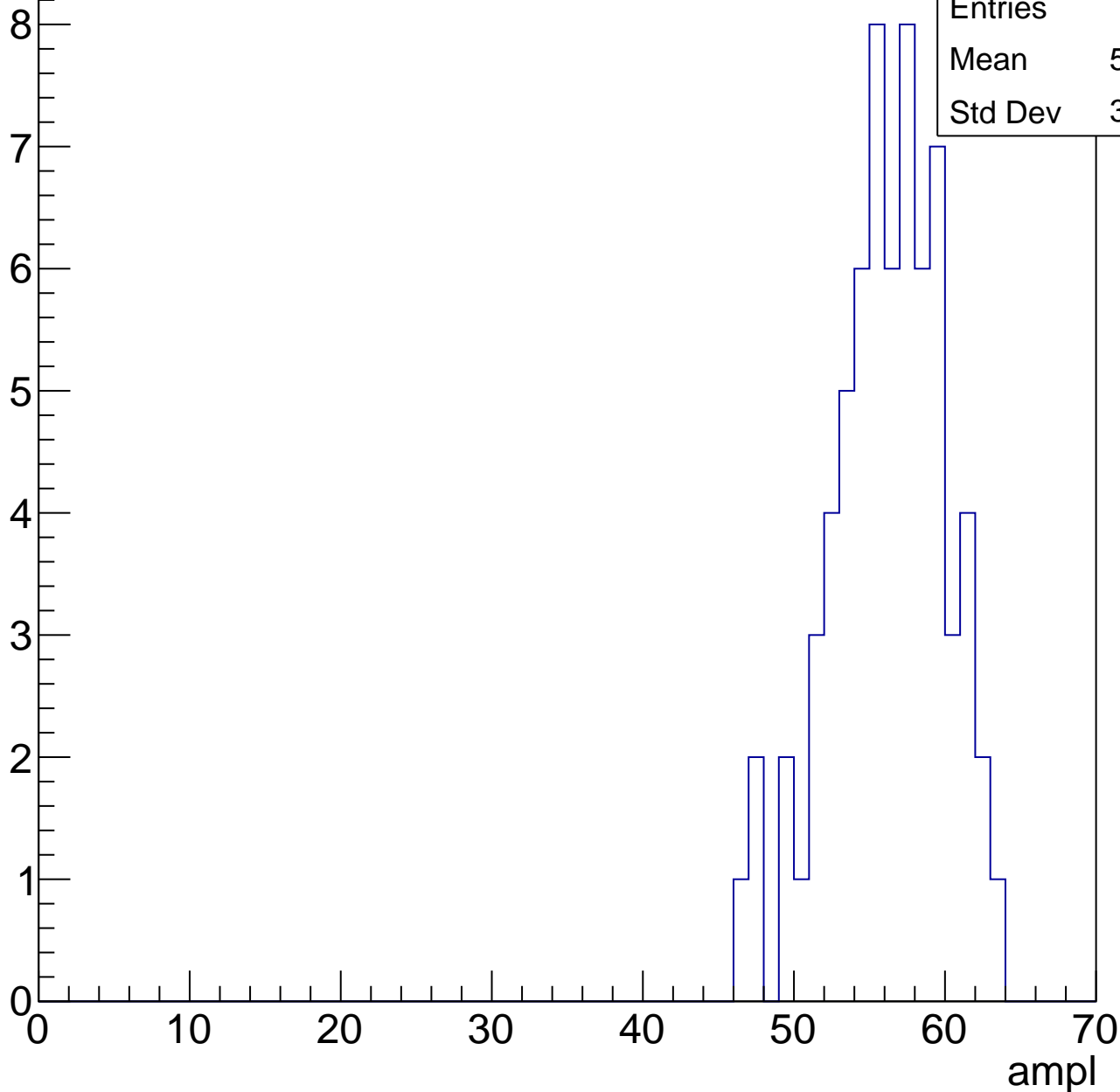
Entries	63
Mean	49.37
Std Dev	3.609



B1L103S, U20-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

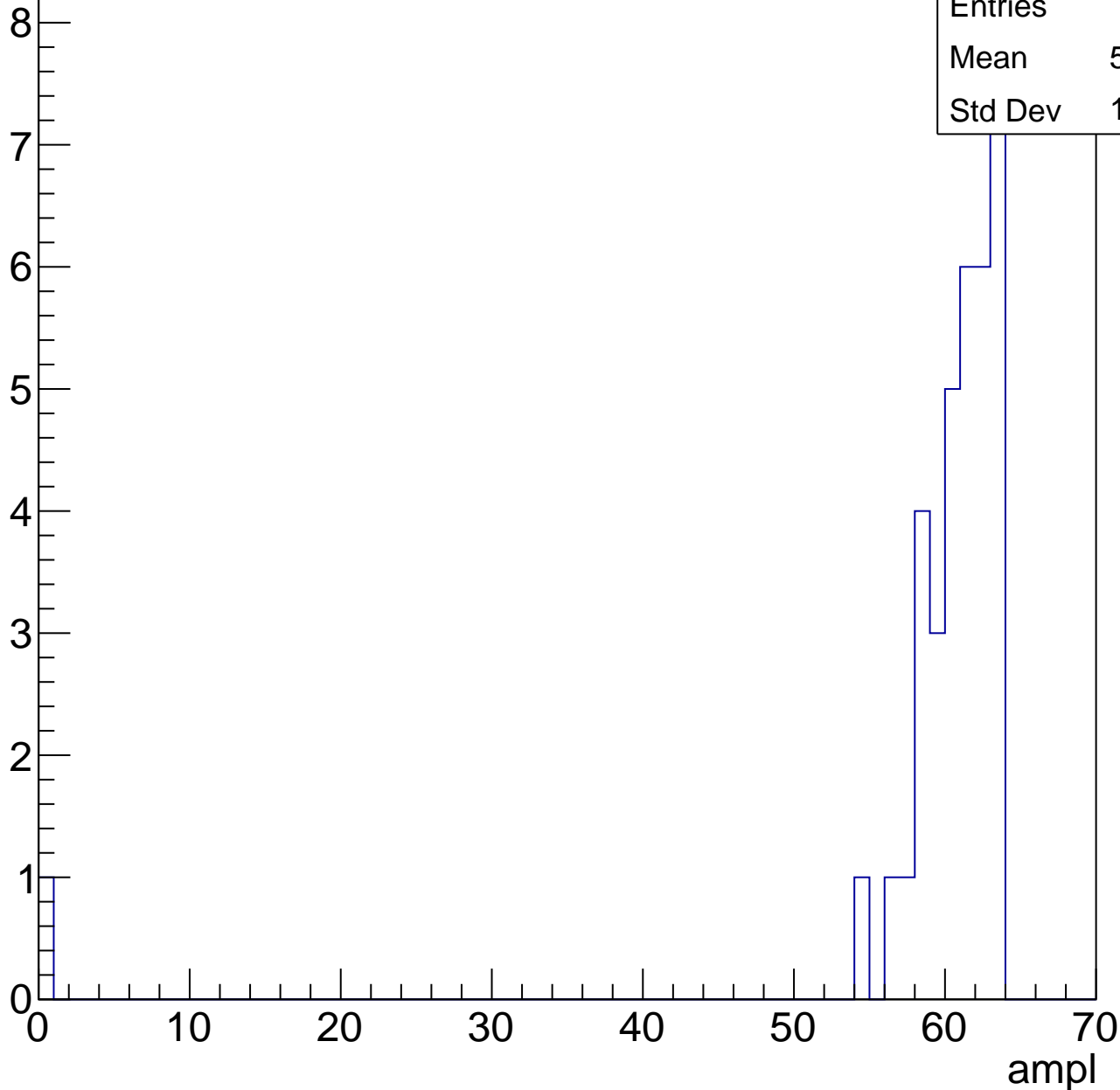


B1L103S, U20-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.83
Std Dev	10.18



B1L103S, U20-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

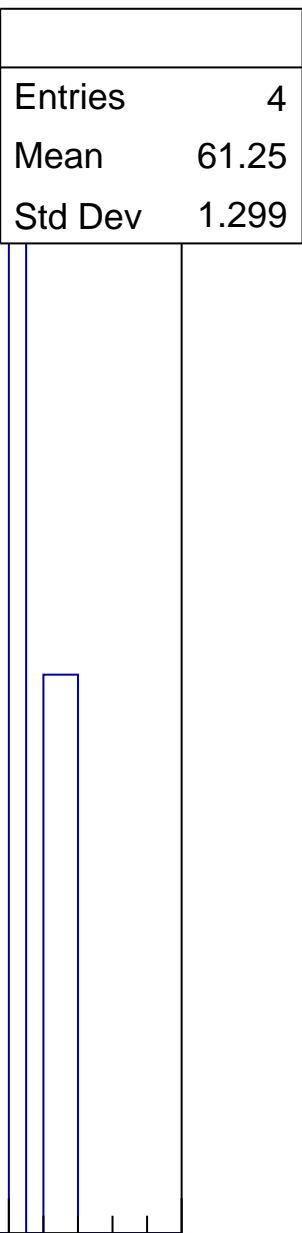
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.299

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch3, adc0

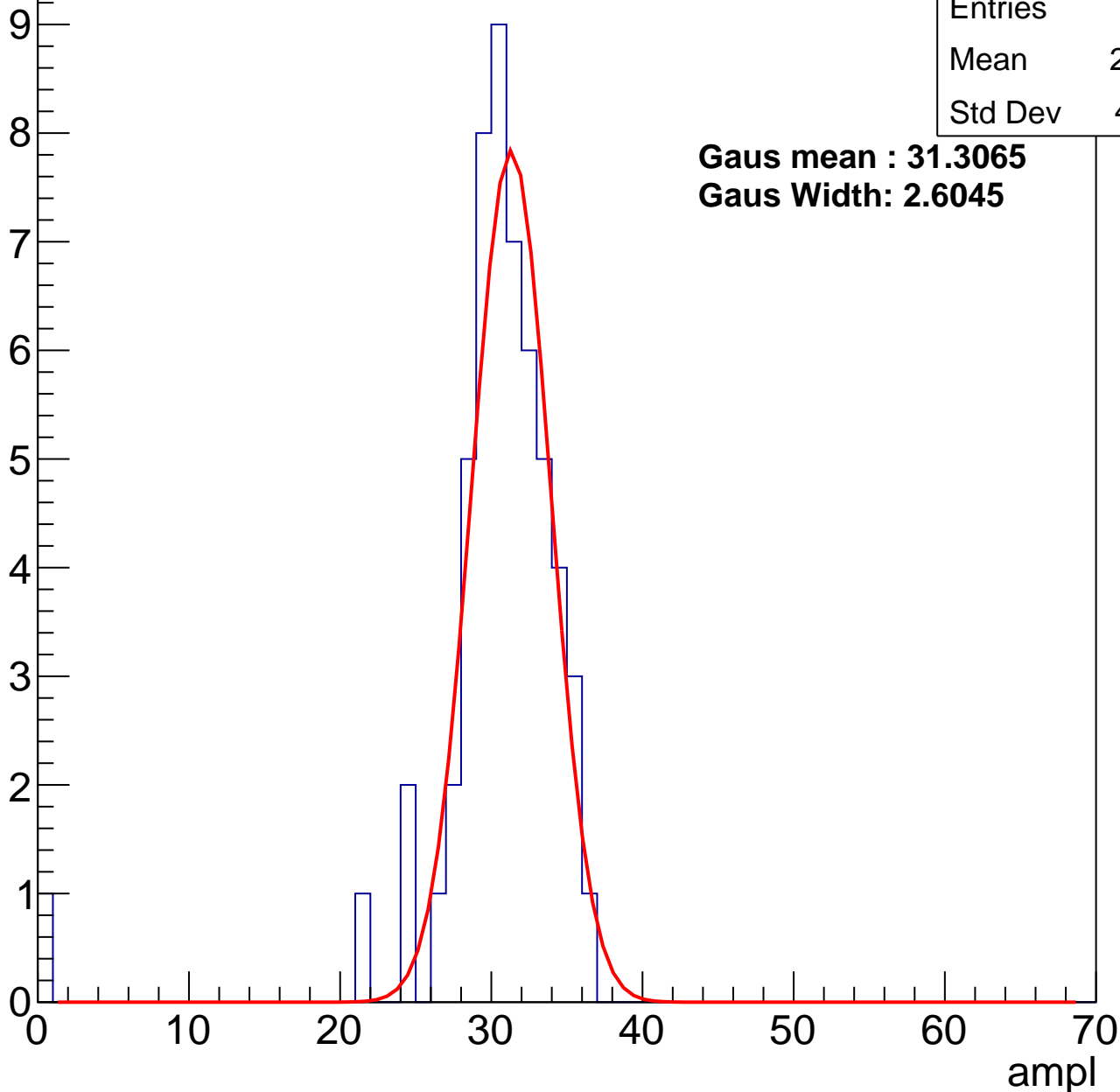
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	29.85
Std Dev	4.981

Gaus mean : 31.3065

Gaus Width: 2.6045



B1L103S, U20-ch3, adc1

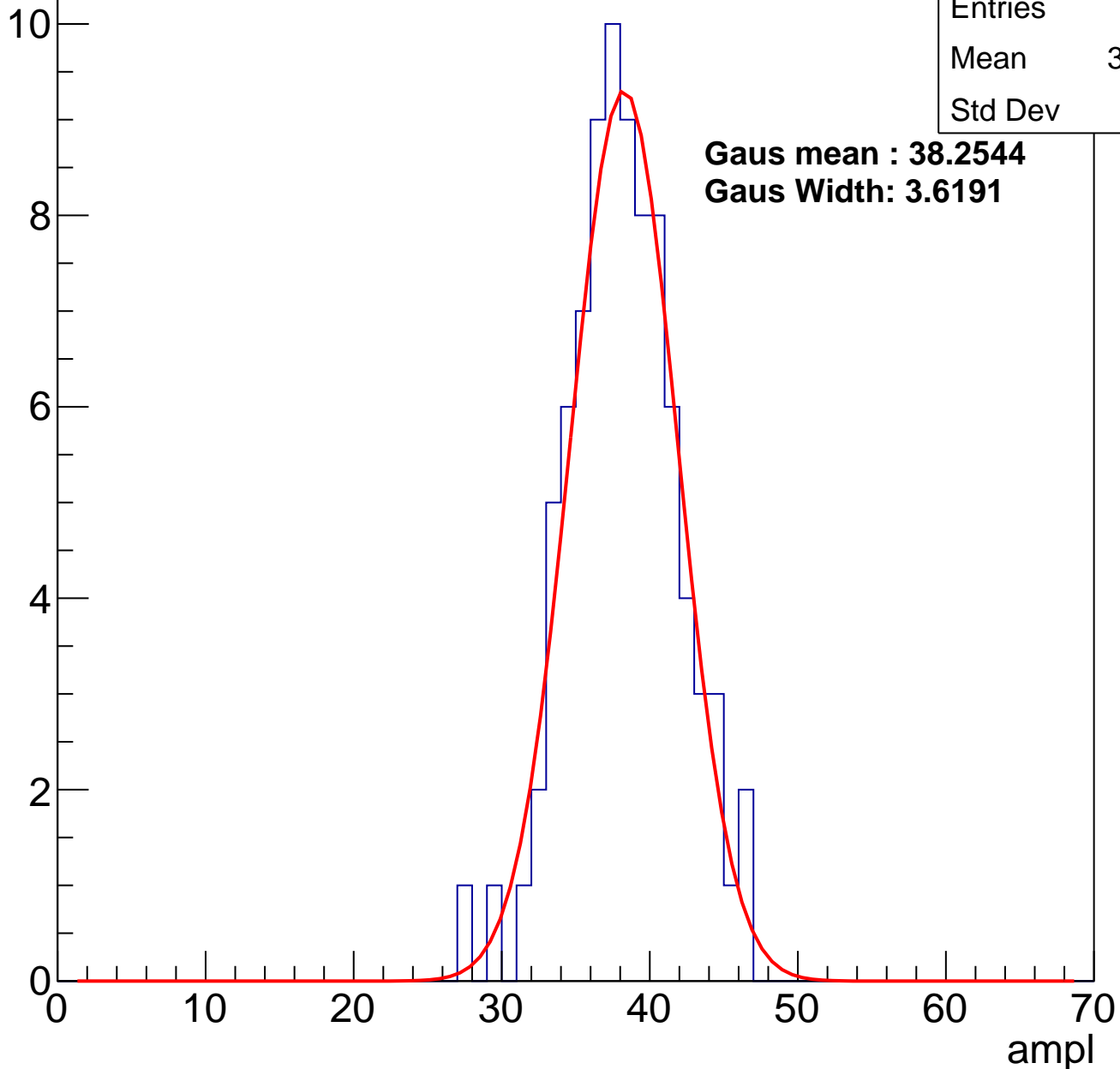
calib_packv5_042523_0143.root, FC#7, port C2

Entries	86
Mean	37.73
Std Dev	3.68

Gaus mean : 38.2544

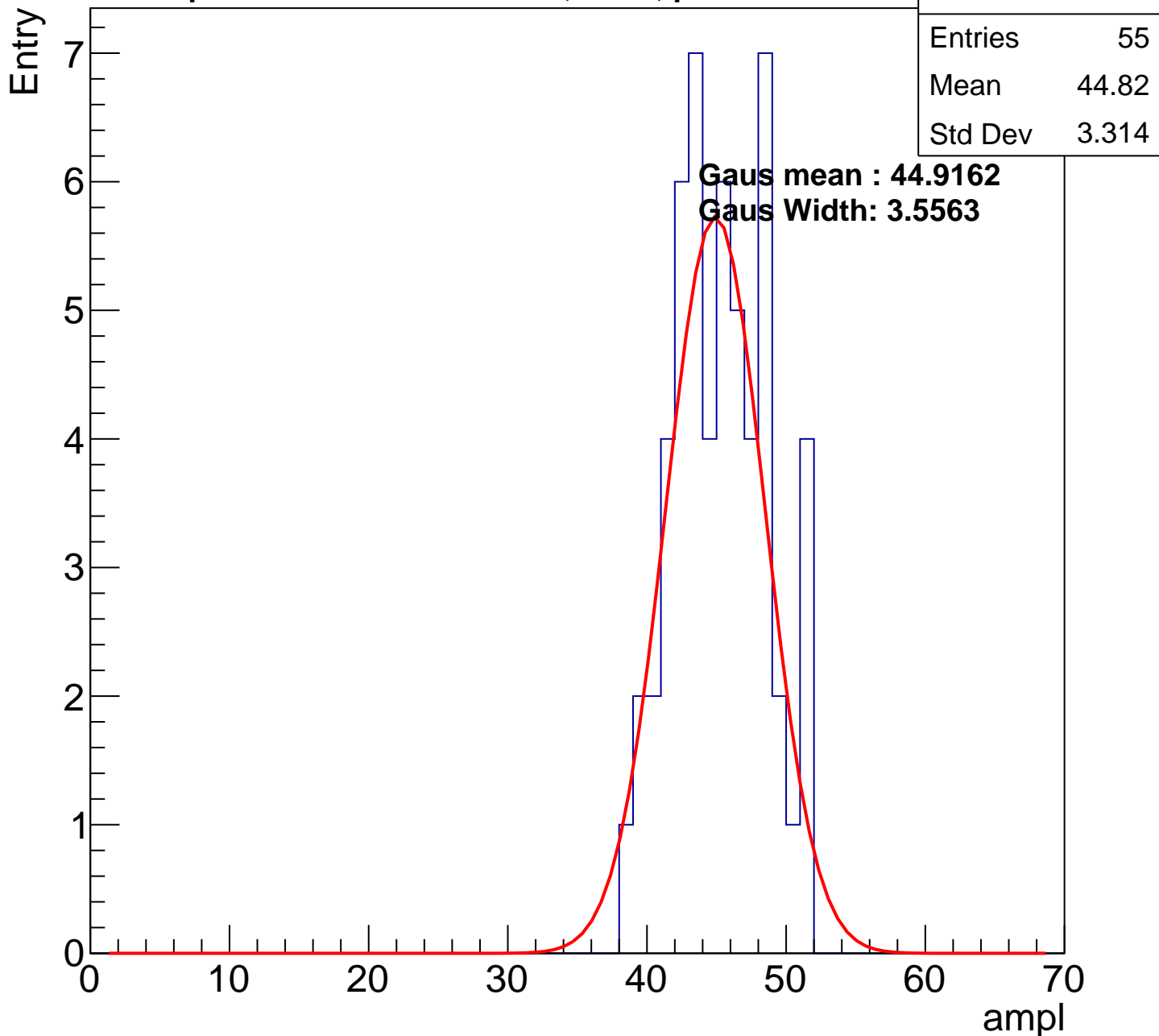
Gaus Width: 3.6191

Entry



B1L103S, U20-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

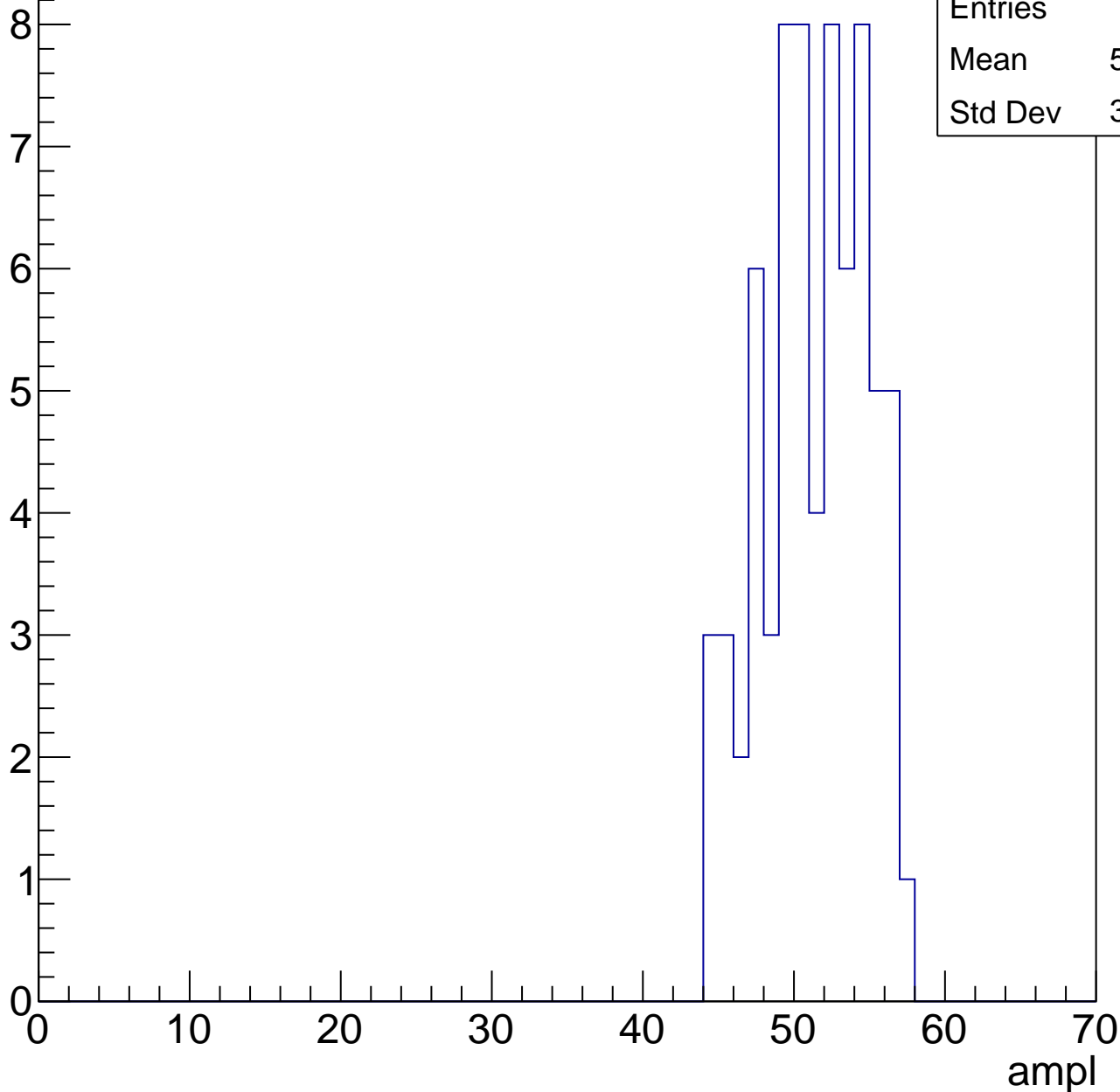


B1L103S, U20-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

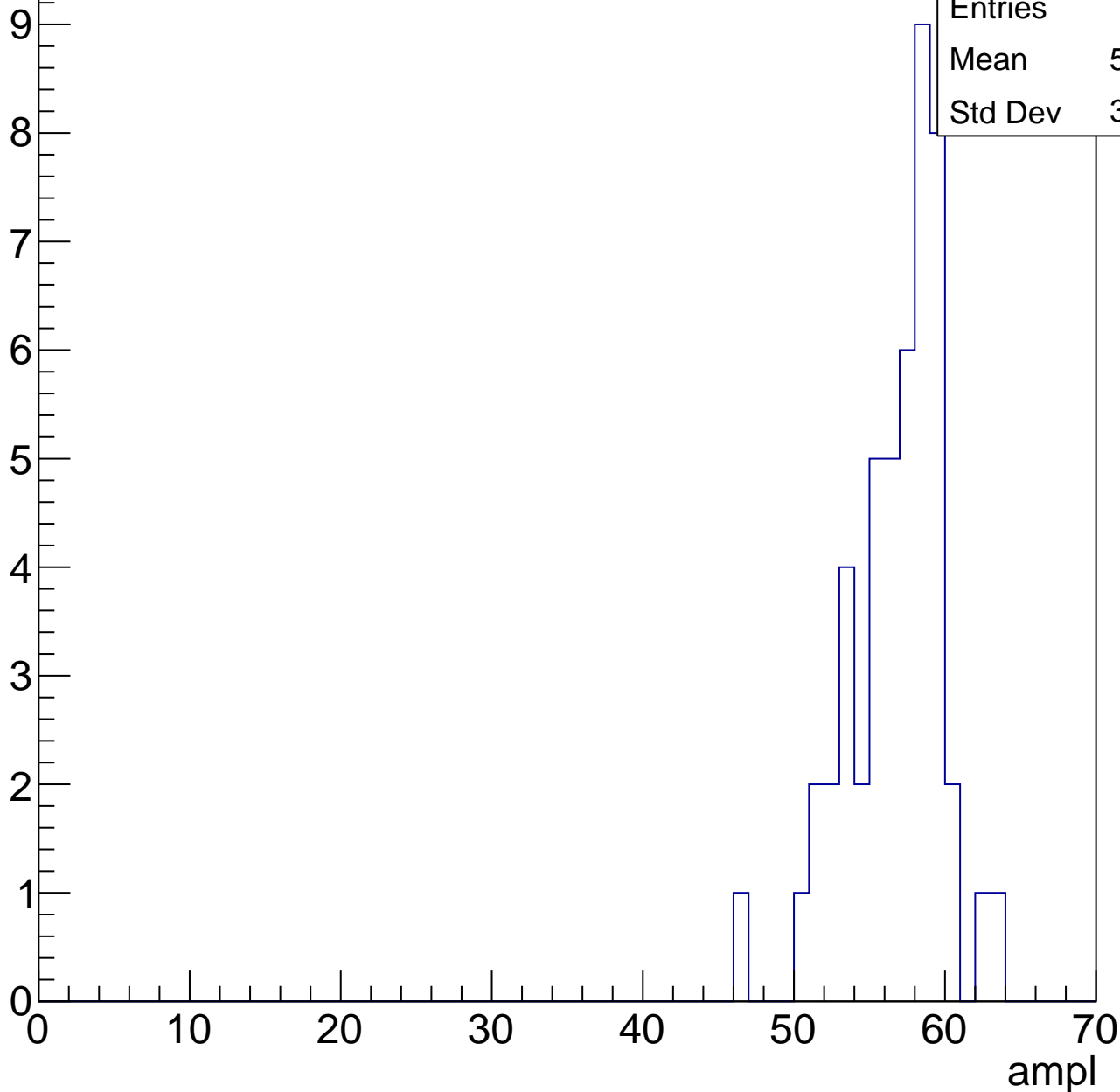
Entries	70
Mean	50.84
Std Dev	3.404



B1L103S, U20-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

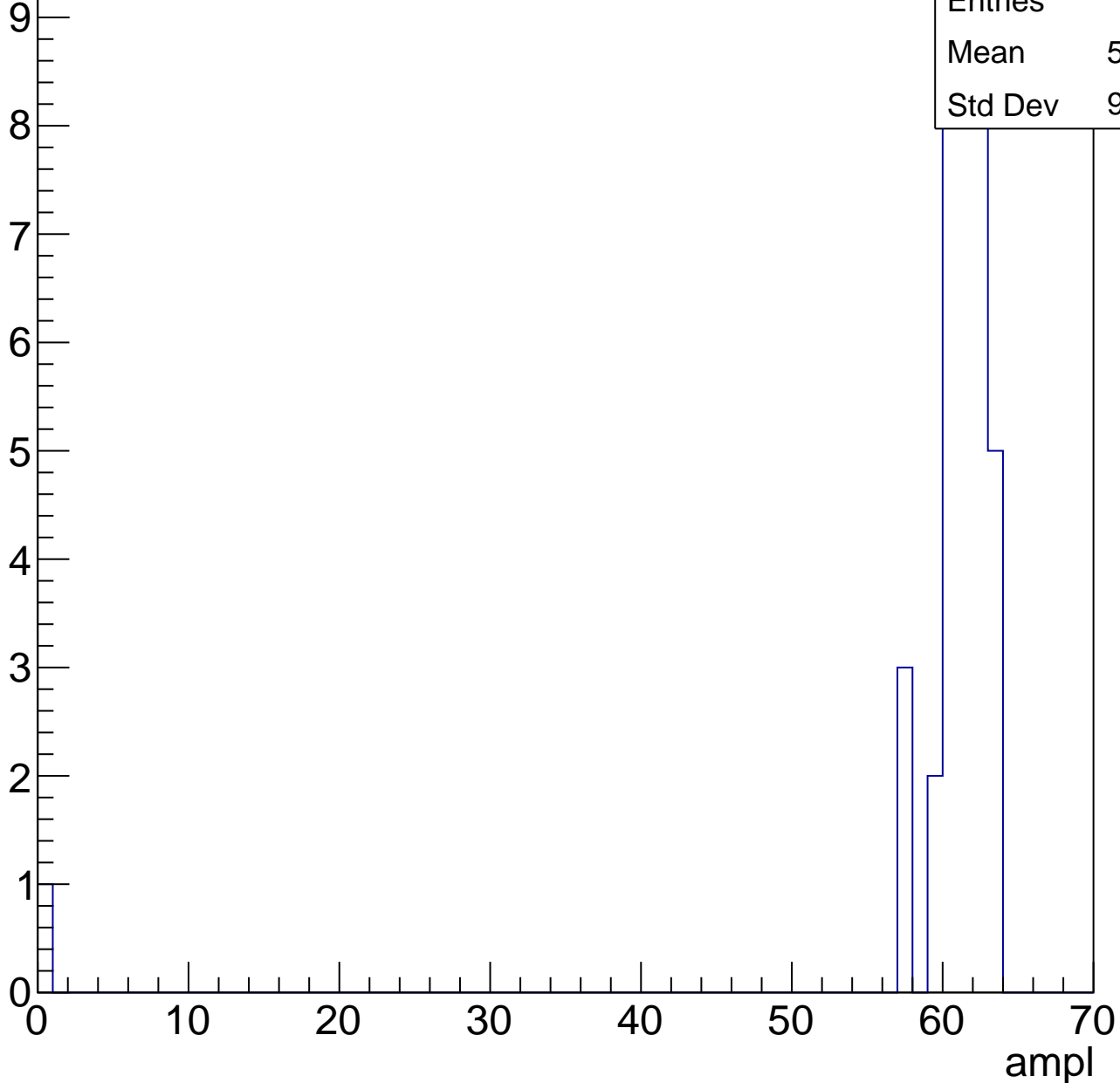


B1L103S, U20-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

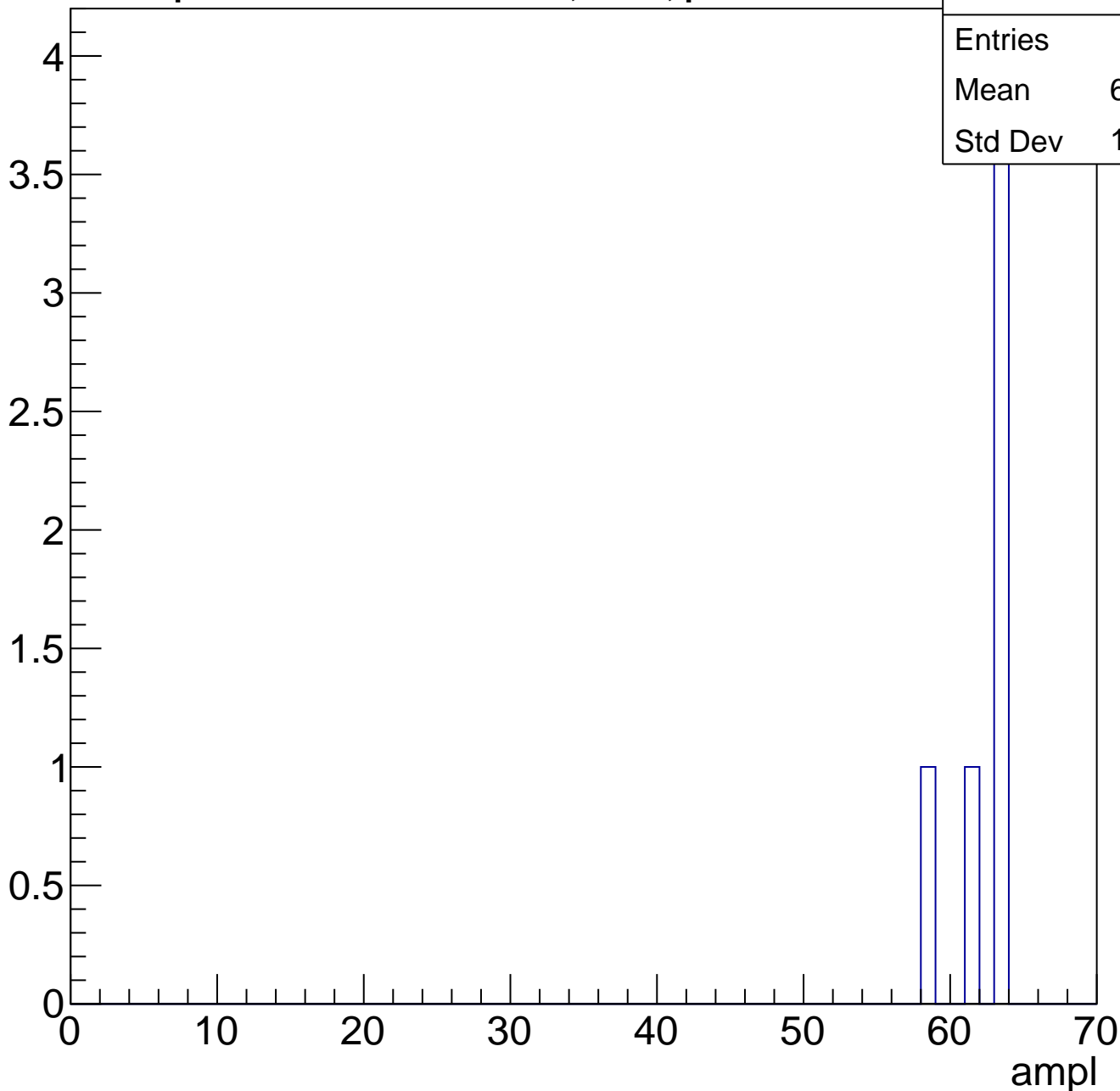
Entries	37
Mean	59.22
Std Dev	9.995



B1L103S, U20-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch4, adc0

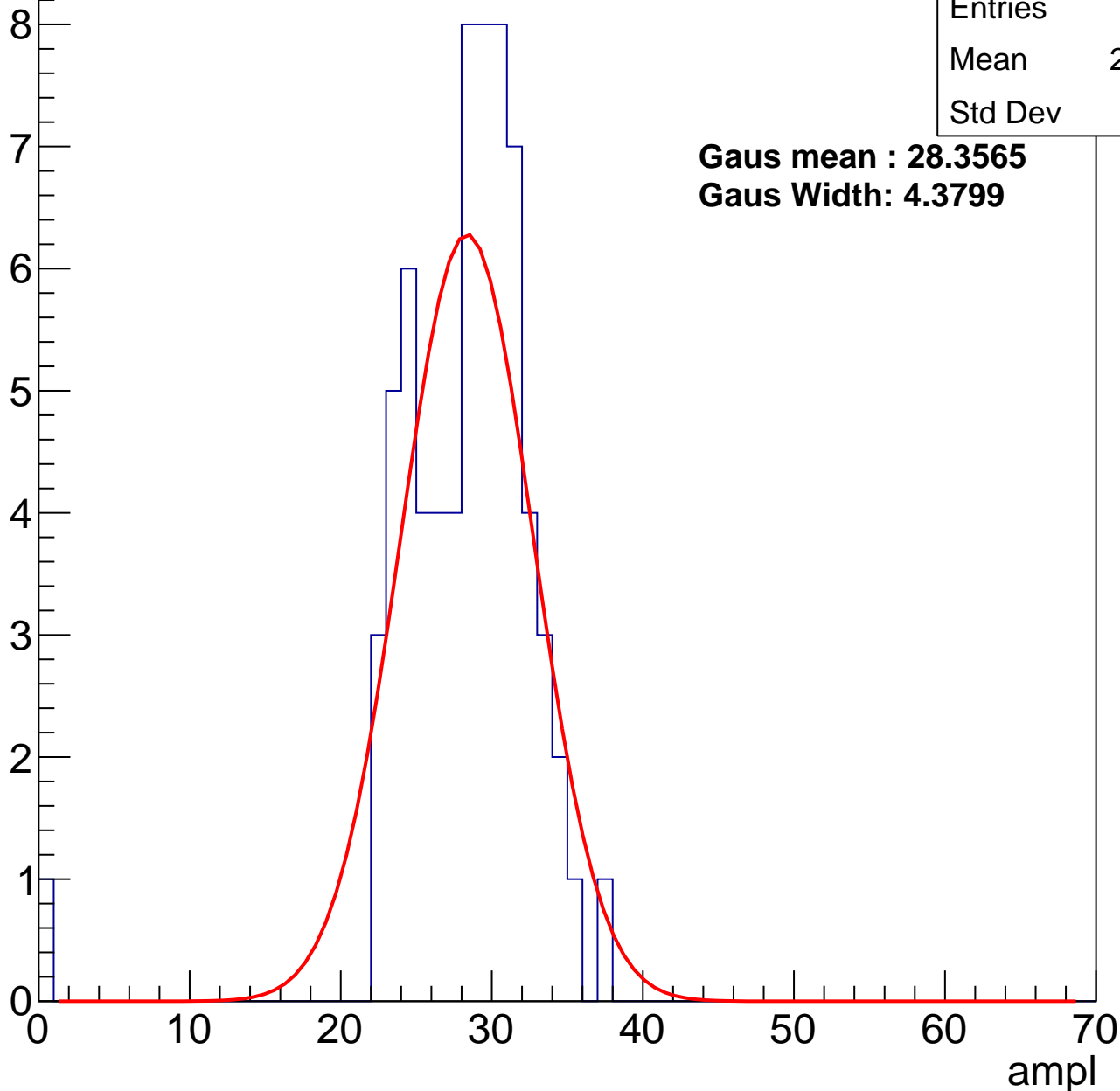
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.78
Std Dev	4.83

Gaus mean : 28.3565

Gaus Width: 4.3799



B1L103S, U20-ch4, adc1

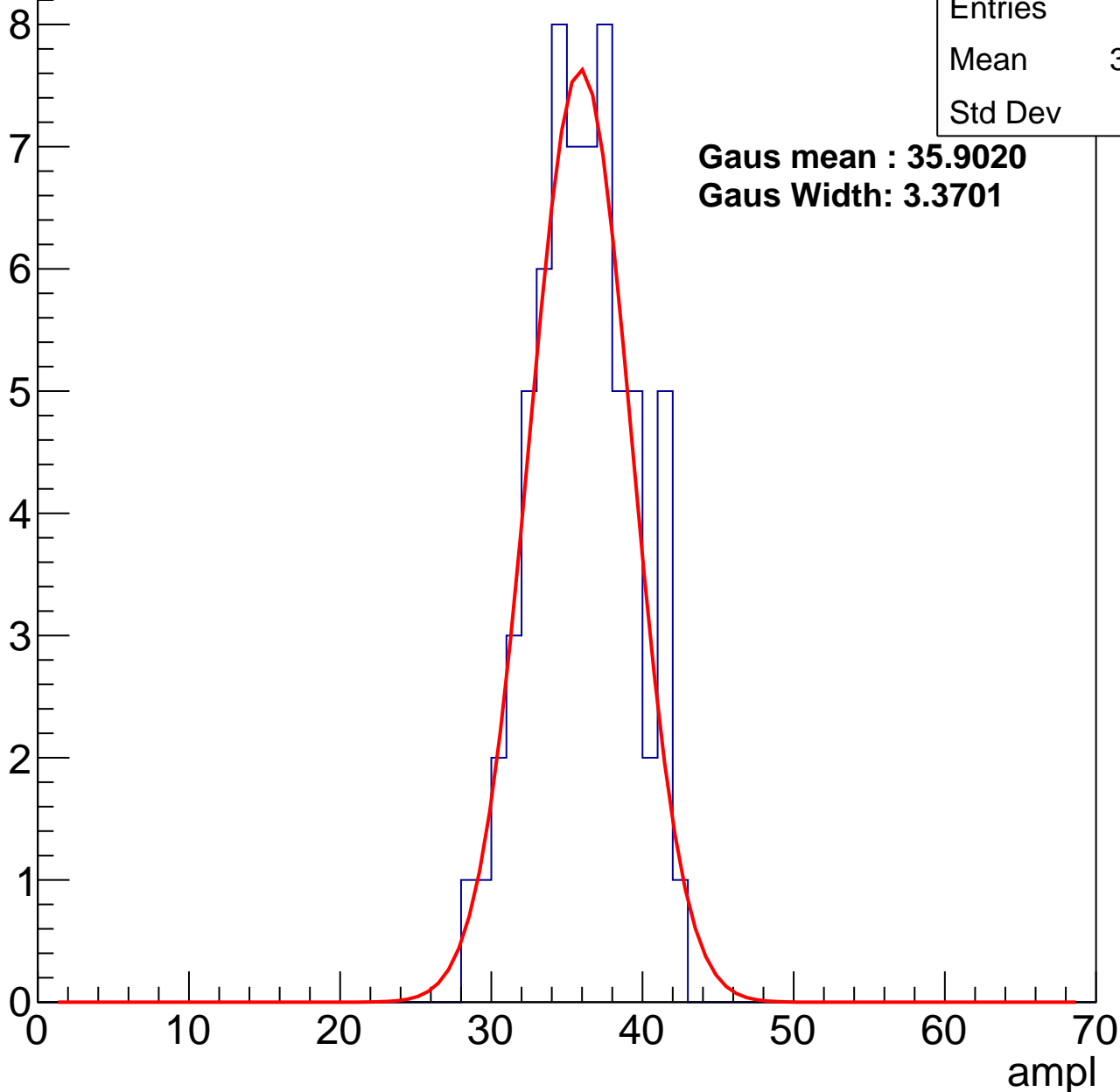
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.53
Std Dev	3.22

Gaus mean : 35.9020

Gaus Width: 3.3701



B1L103S, U20-ch4, adc2

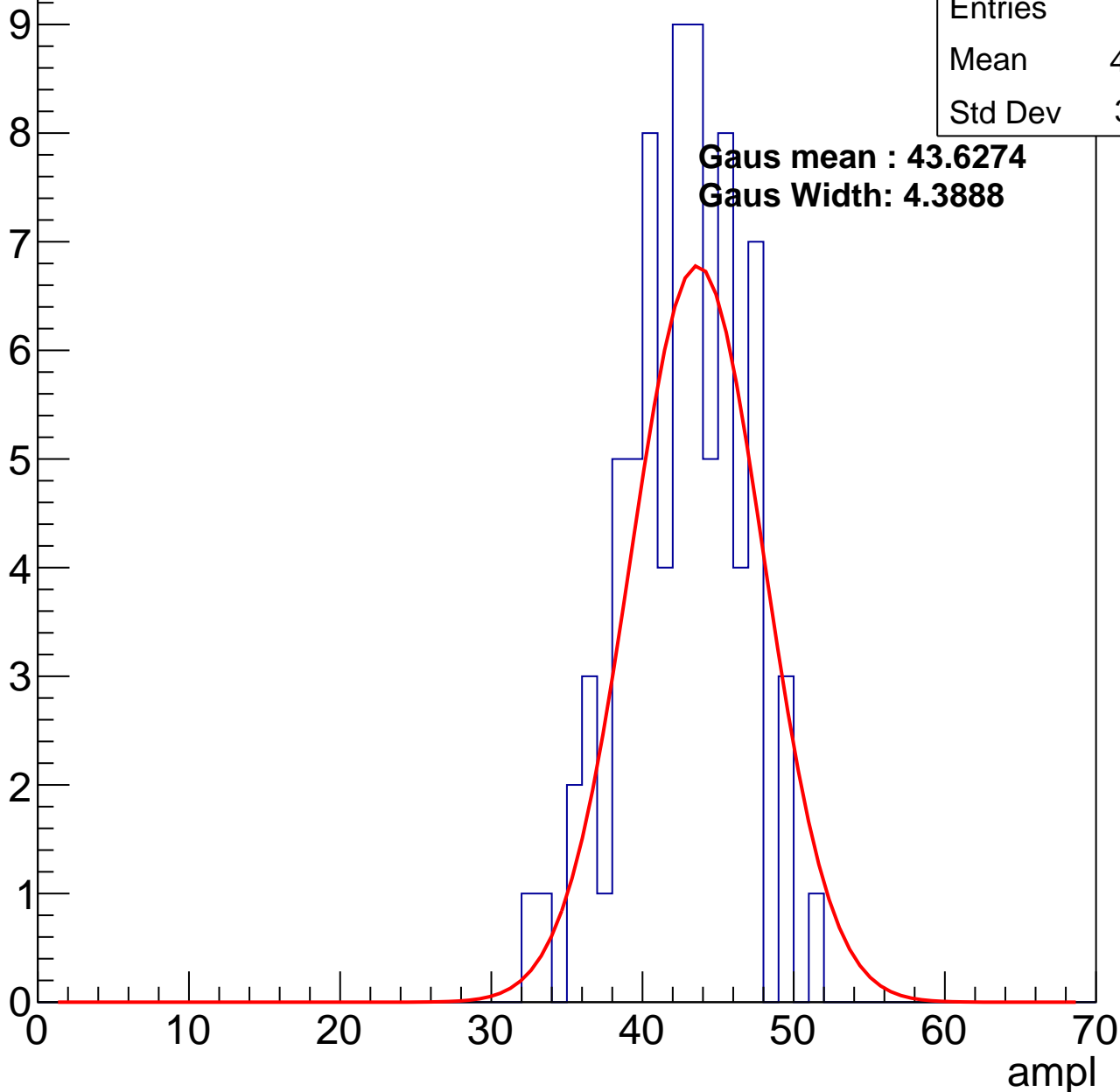
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	42.17
Std Dev	3.871

Gaus mean : 43.6274

Gaus Width: 4.3888



B1L103S, U20-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	57
Mean	48.95
Std Dev	3.337

Entry

10

8

6

4

2

0

0

10

20

30

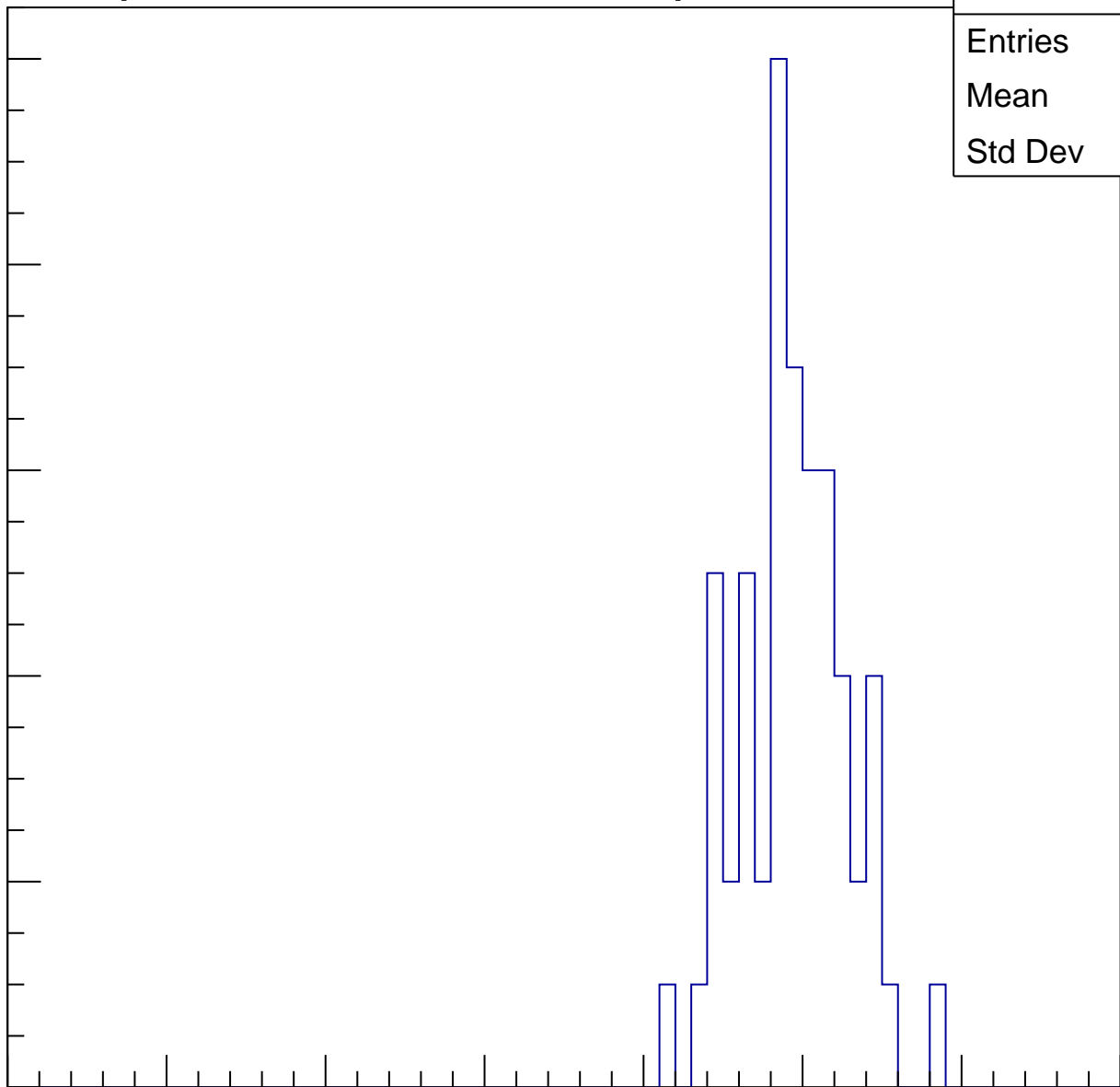
40

50

60

70

ampl

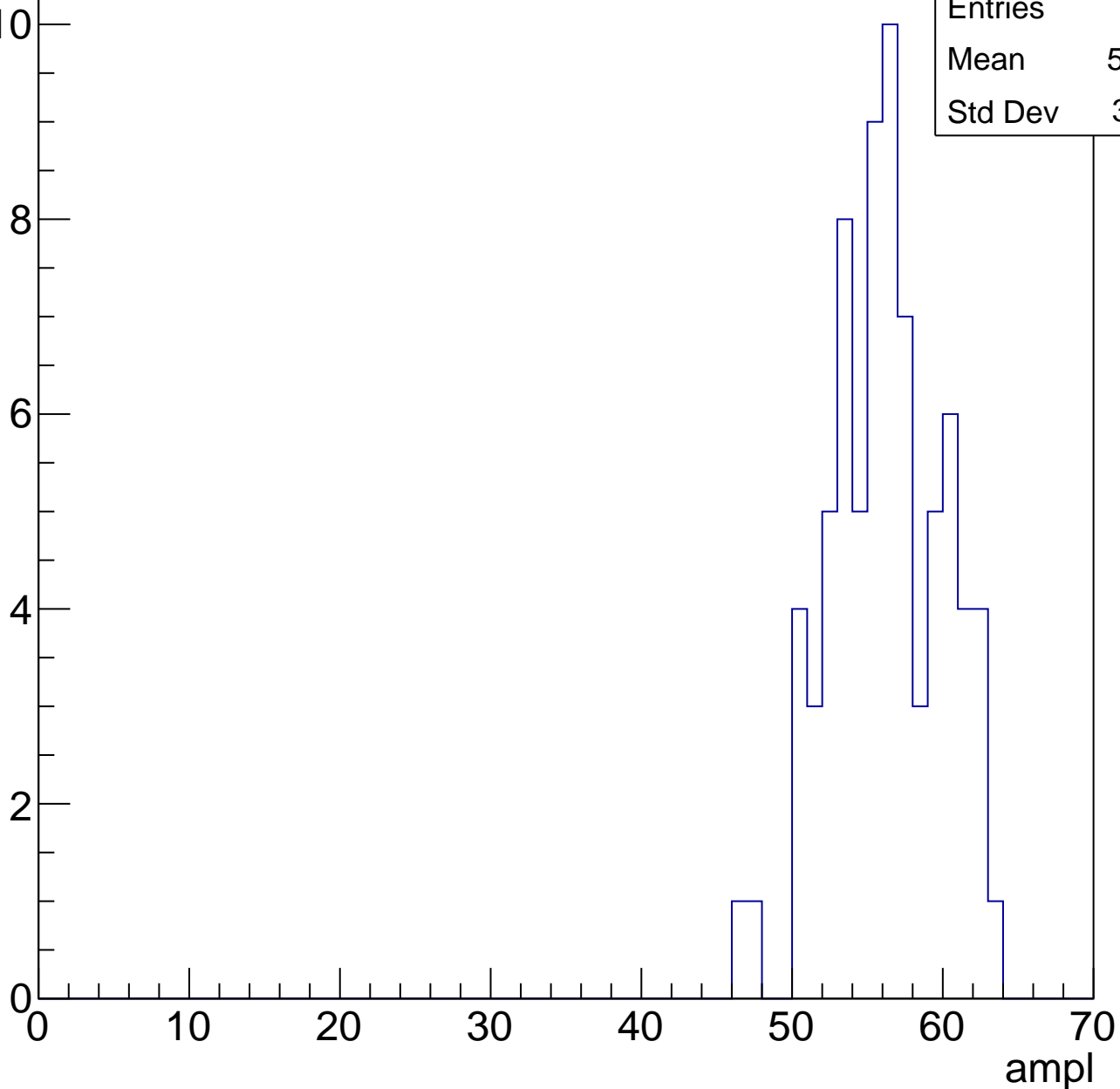


B1L103S, U20-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	55.76
Std Dev	3.681

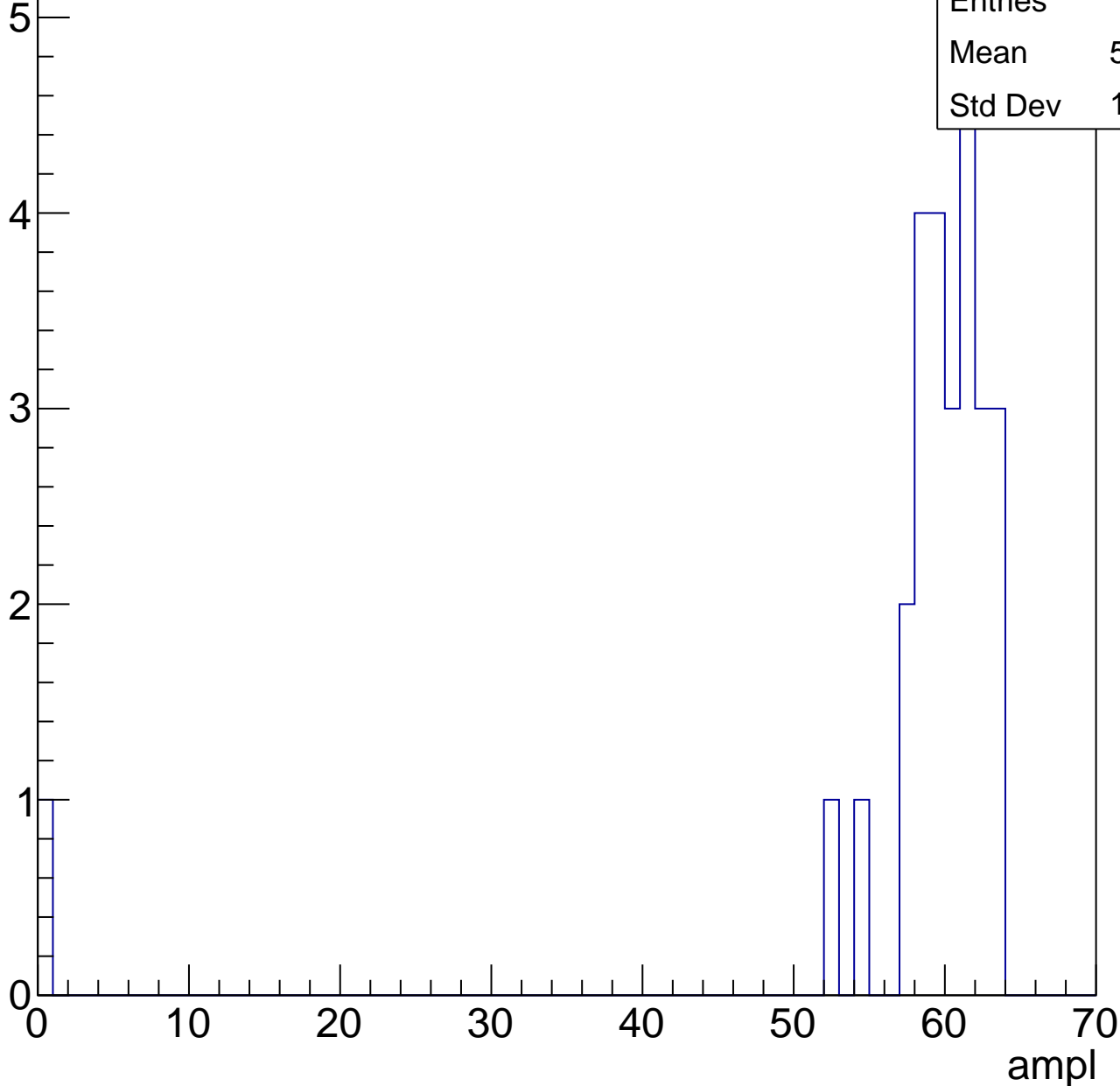


B1L103S, U20-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	57.33
Std Dev	11.53

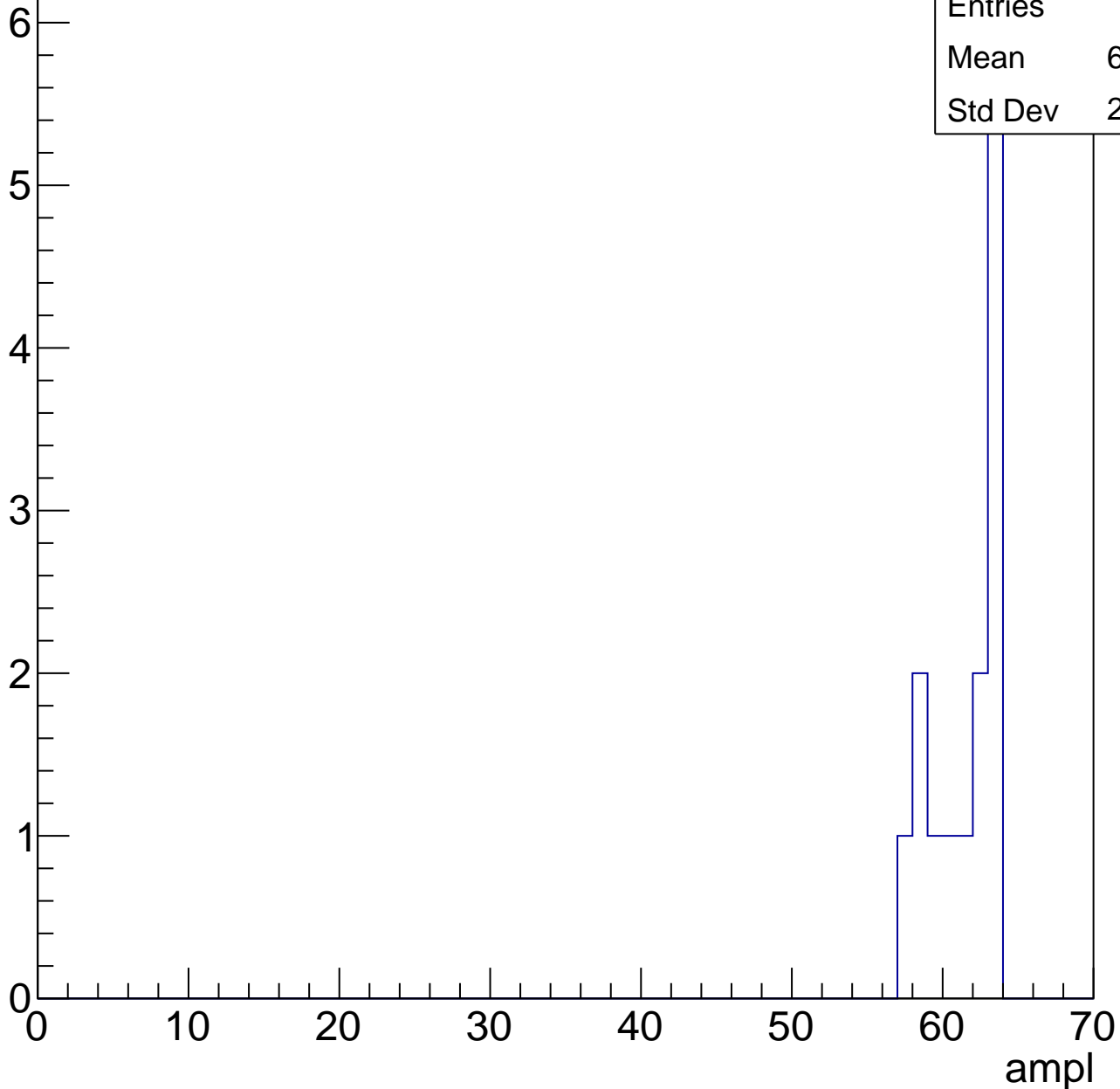


B1L103S, U20-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.07
Std Dev	2.154

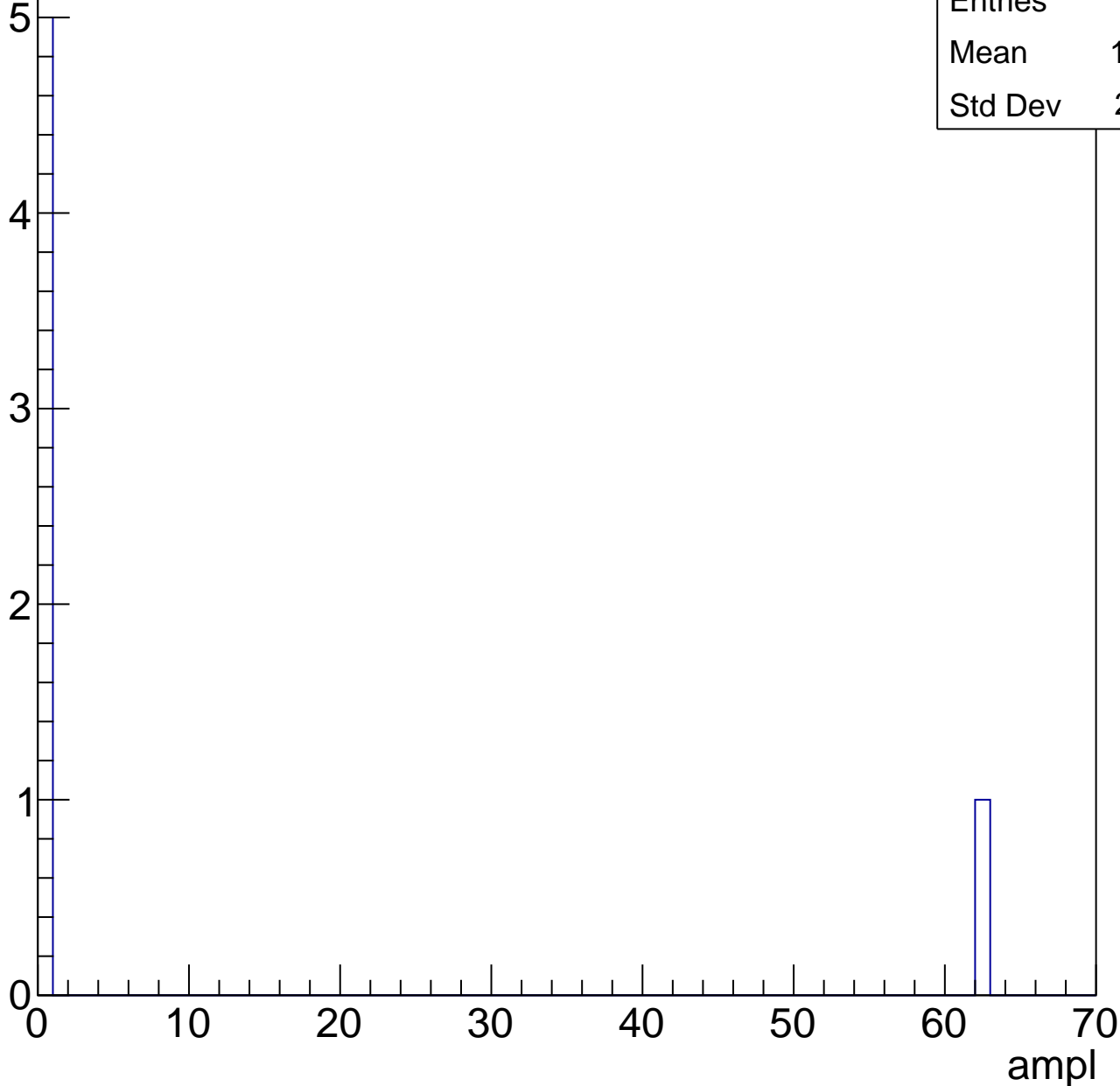


B1L103S, U20-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	6
Mean	10.33
Std Dev	23.11



B1L103S, U20-ch5, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	29.66
Std Dev	3.47

Gaus mean : 29.9806

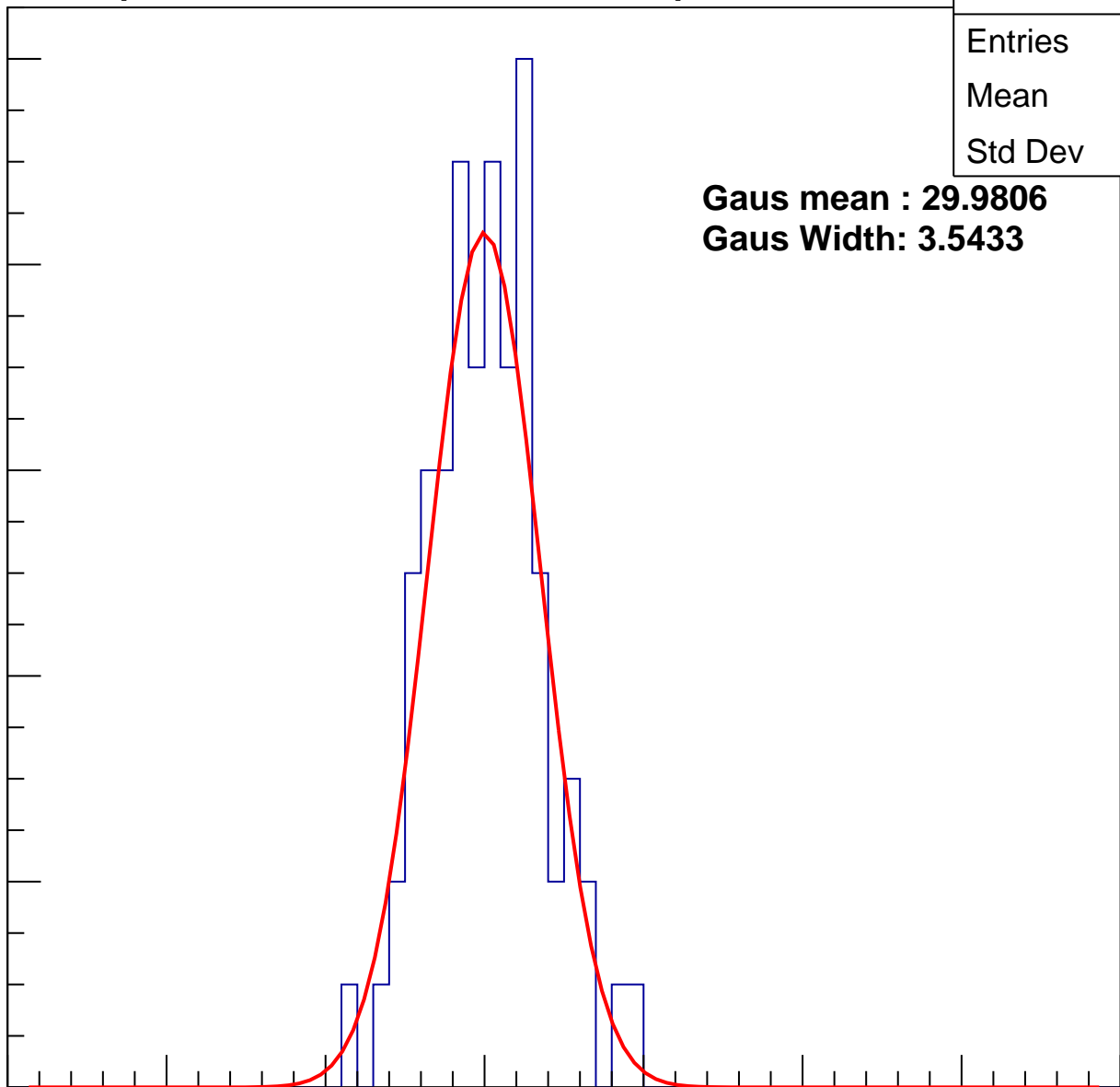
Gaus Width: 3.5433

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch5, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	37.66
Std Dev	3.546

Gaus mean : 37.7862

Gaus Width: 3.7772

10

8

6

4

2

0

0

10

20

30

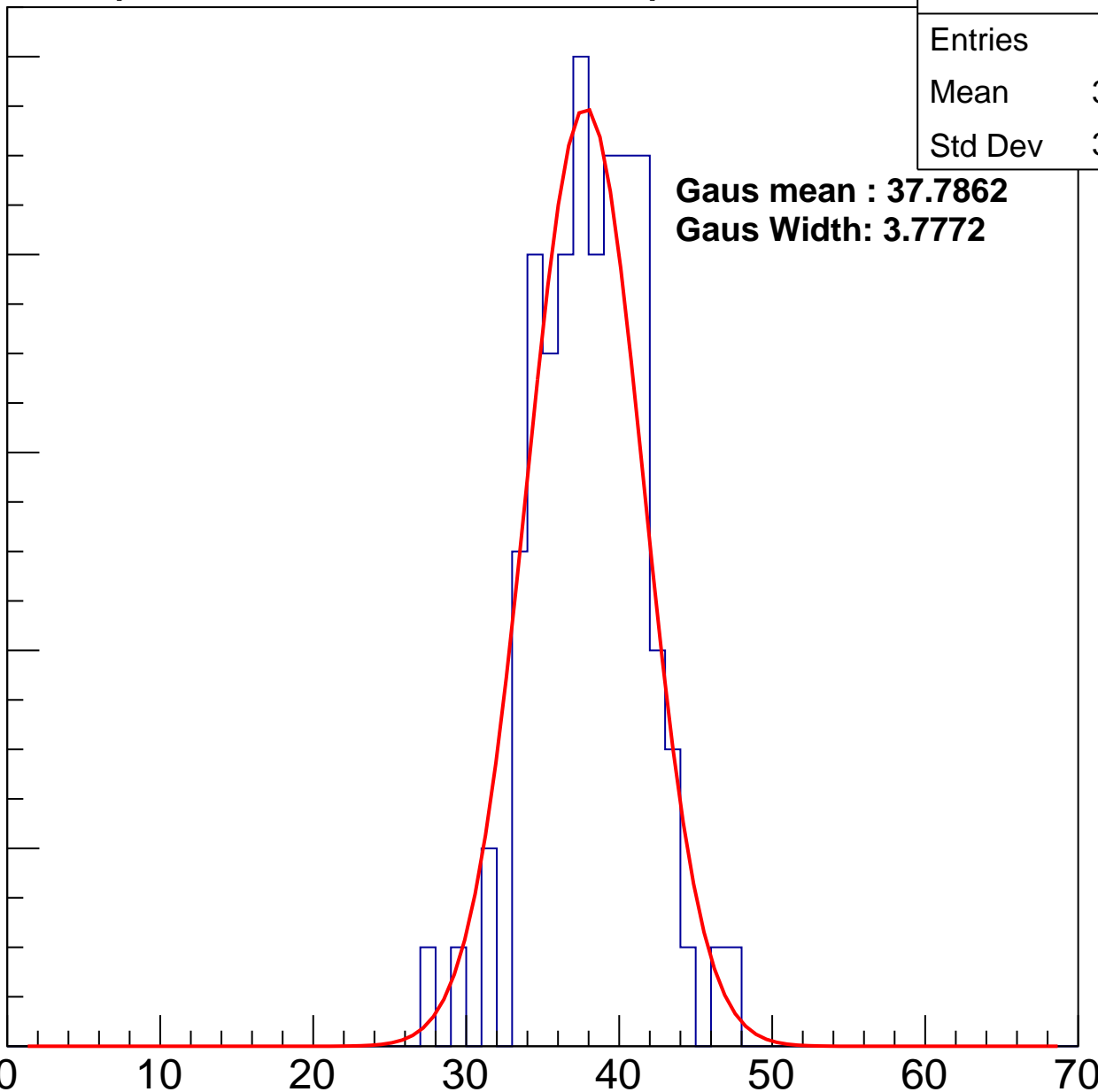
40

50

60

70

ampl



B1L103S, U20-ch5, adc2

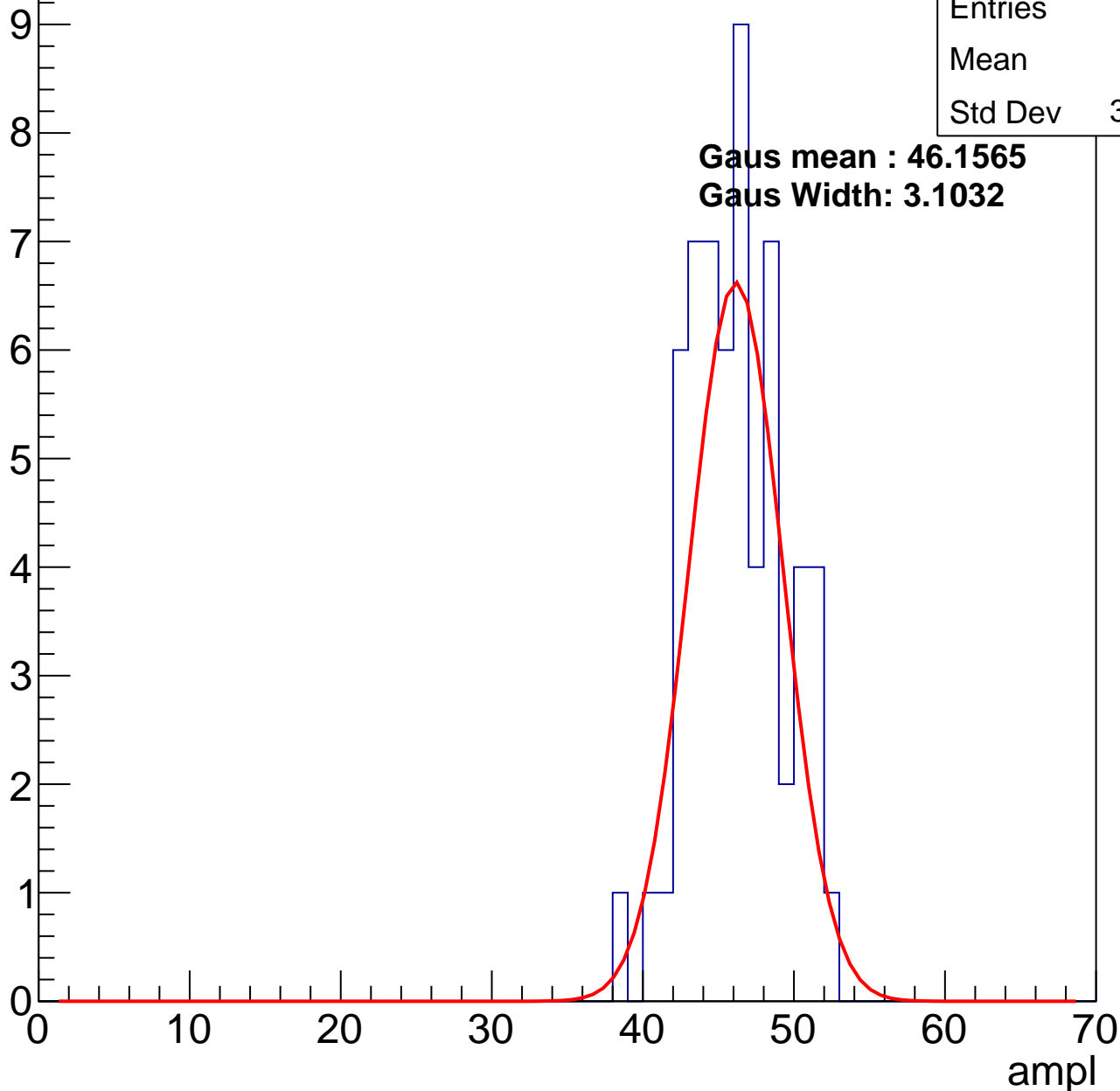
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	45.7
Std Dev	3.062

Gaus mean : 46.1565

Gaus Width: 3.1032

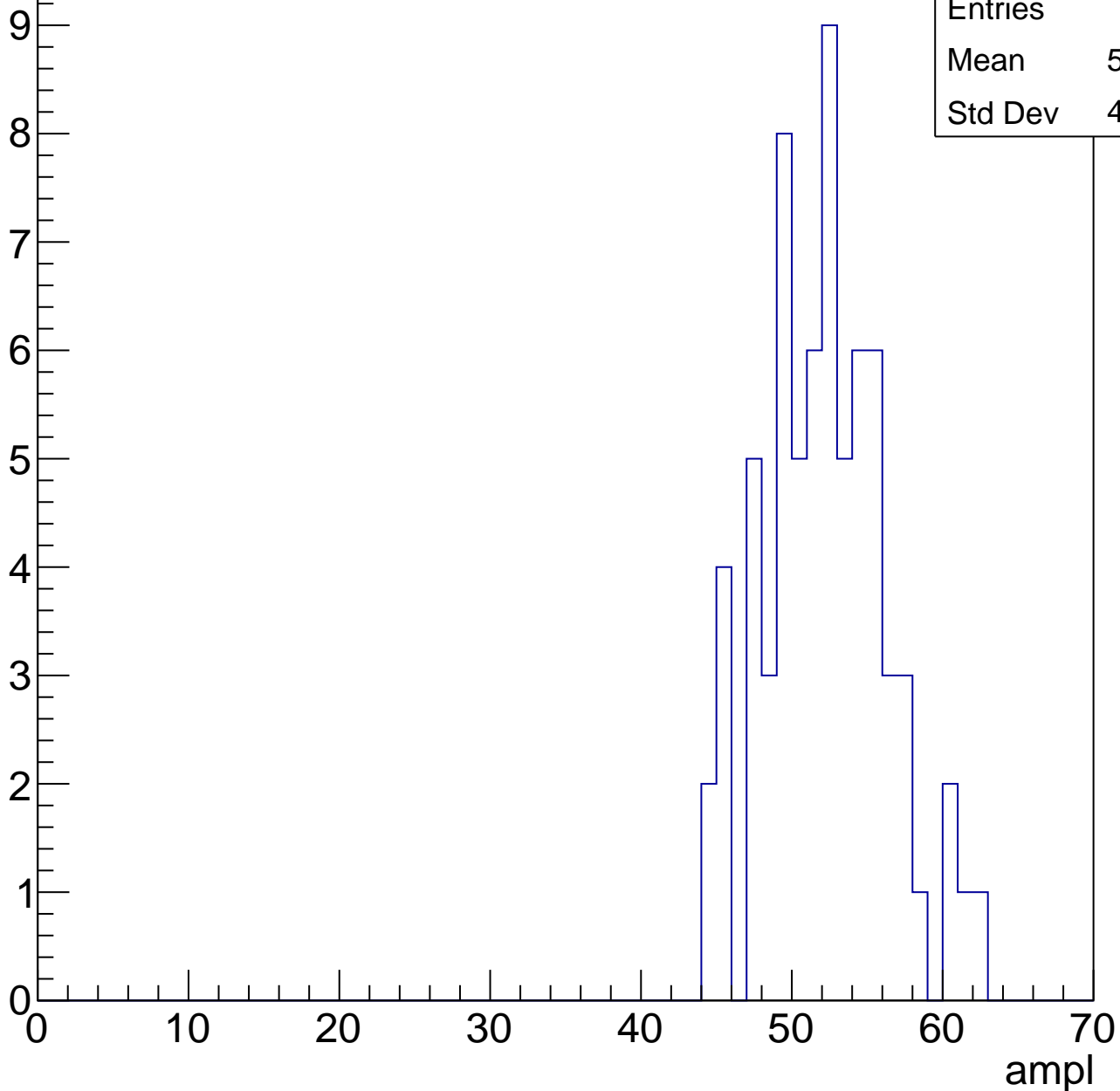


B1L103S, U20-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	51.74
Std Dev	4.038

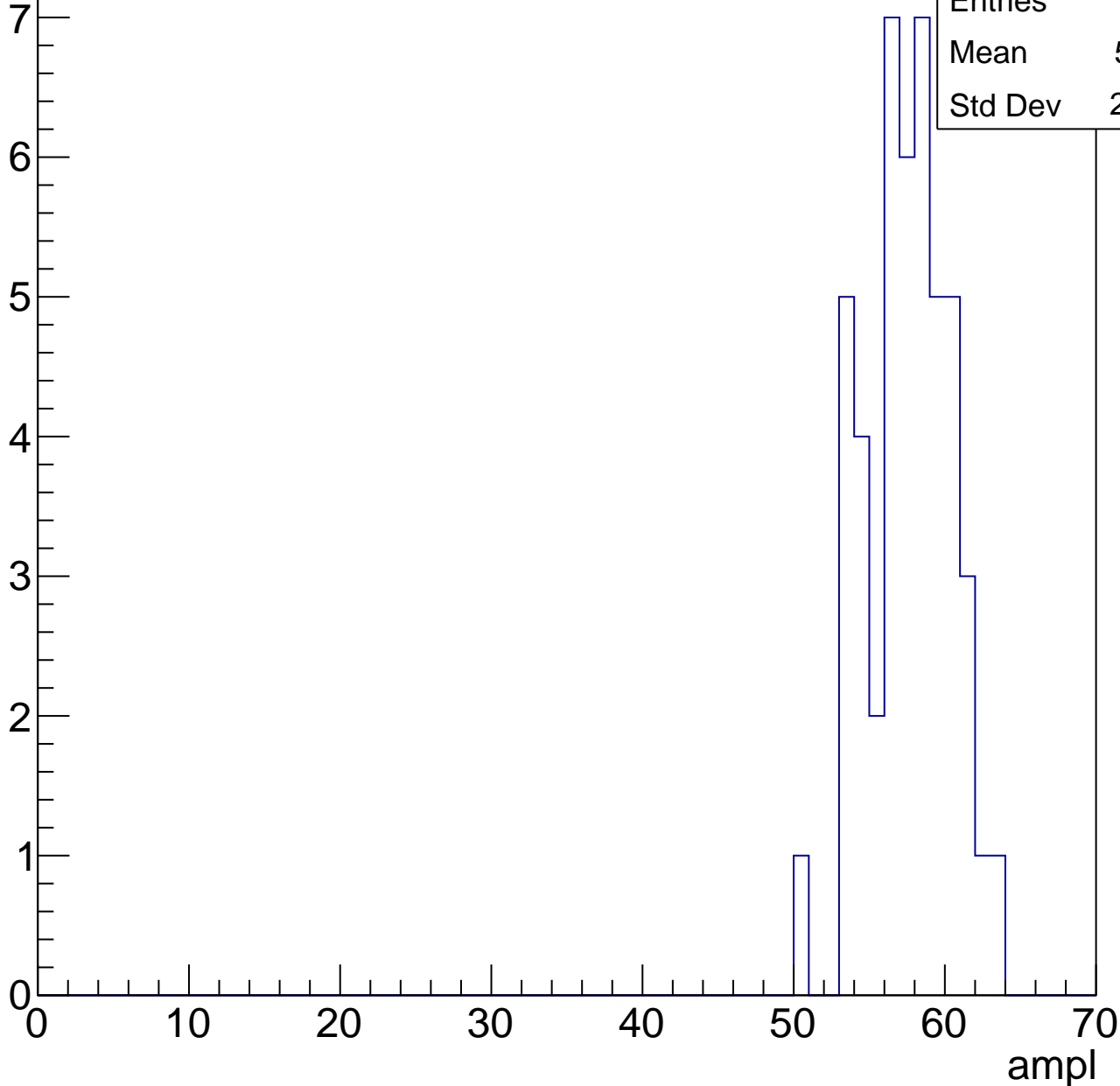


B1L103S, U20-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	57.11
Std Dev	2.769

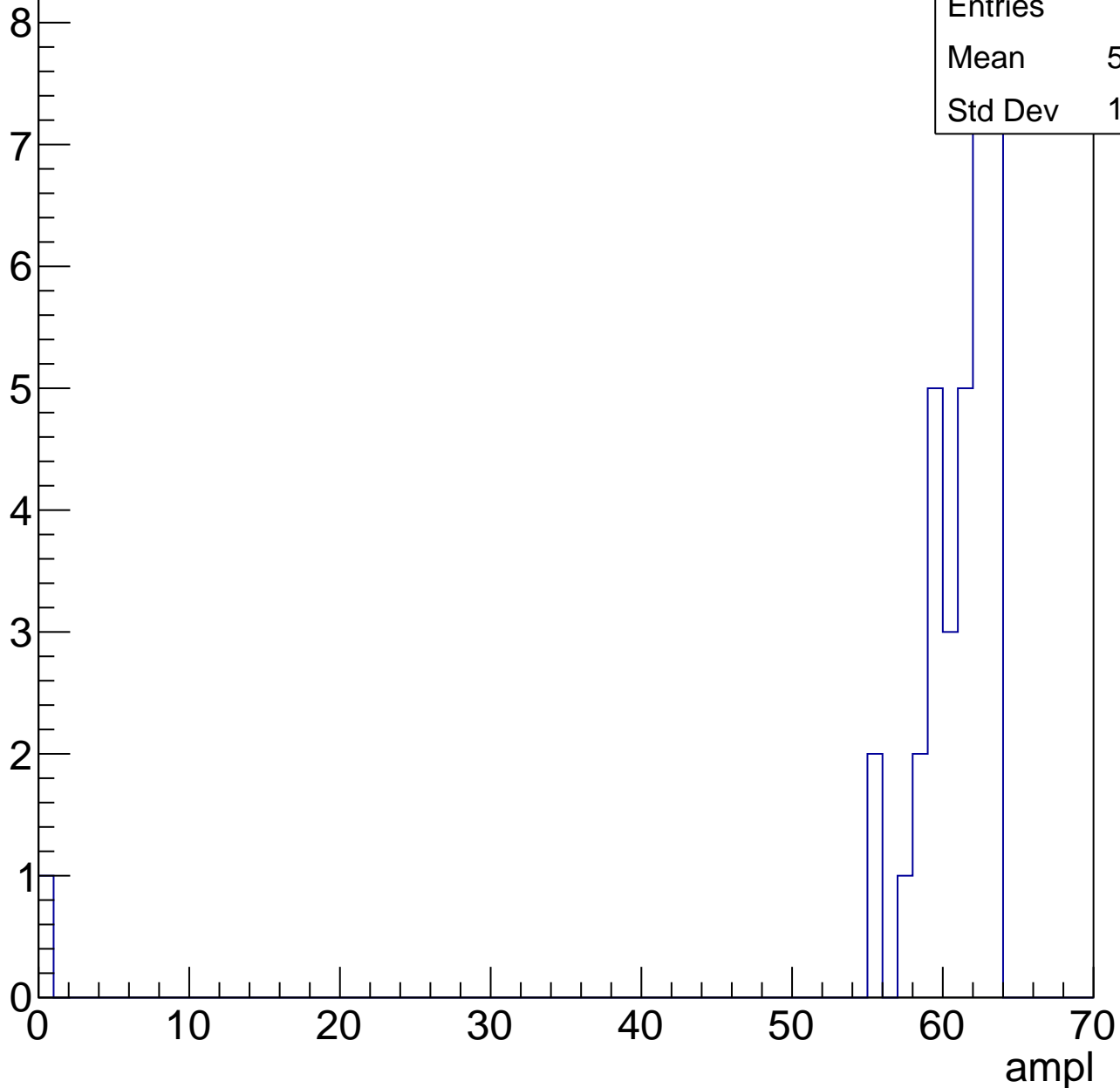


B1L103S, U20-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.94
Std Dev	10.34



B1L103S, U20-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch6, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	61
Mean	29.52
Std Dev	3.39

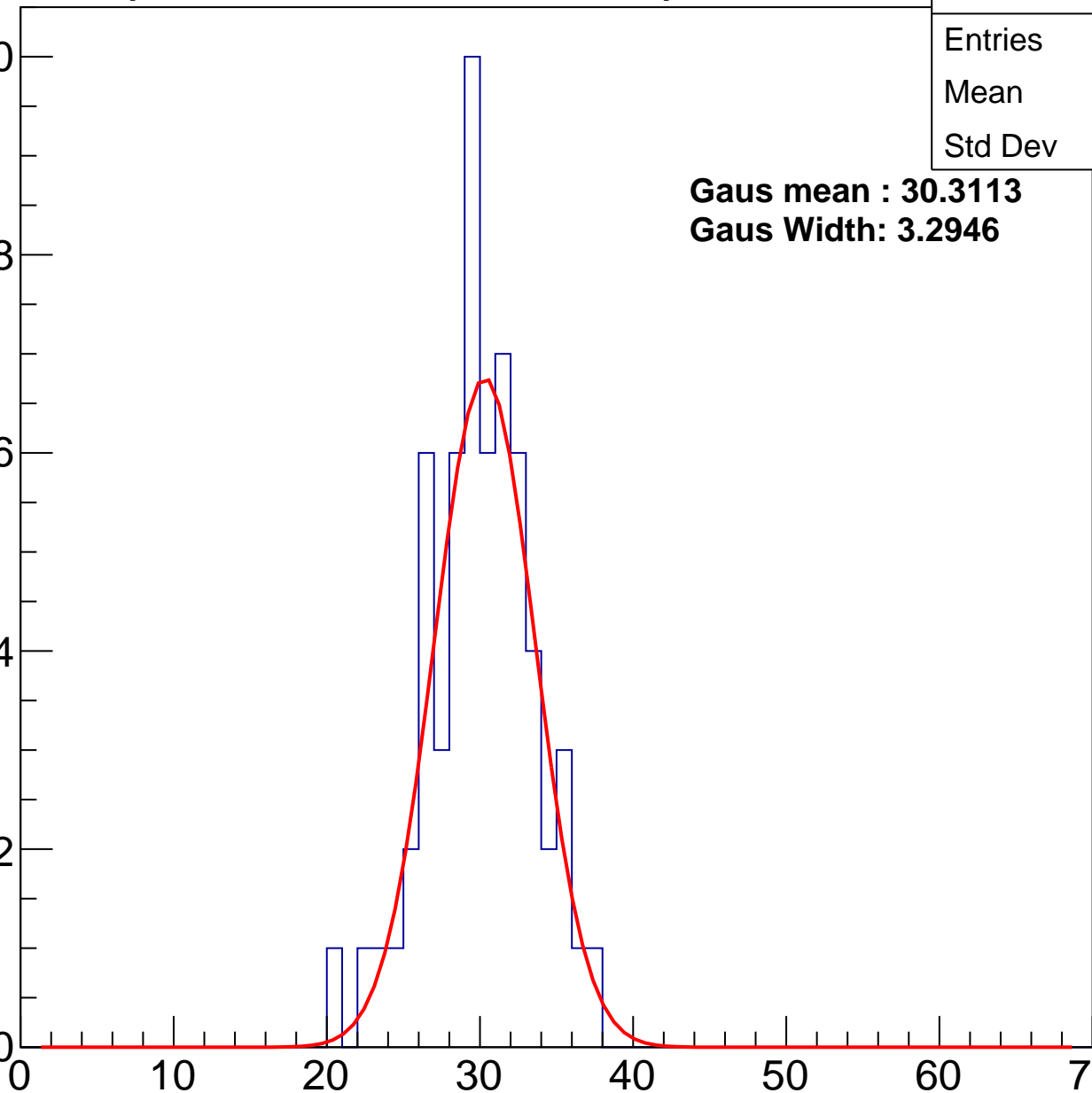
Gaus mean : 30.3113

Gaus Width: 3.2946

Entry

10
8
6
4
2
0

ampl



B1L103S, U20-ch6, adc1

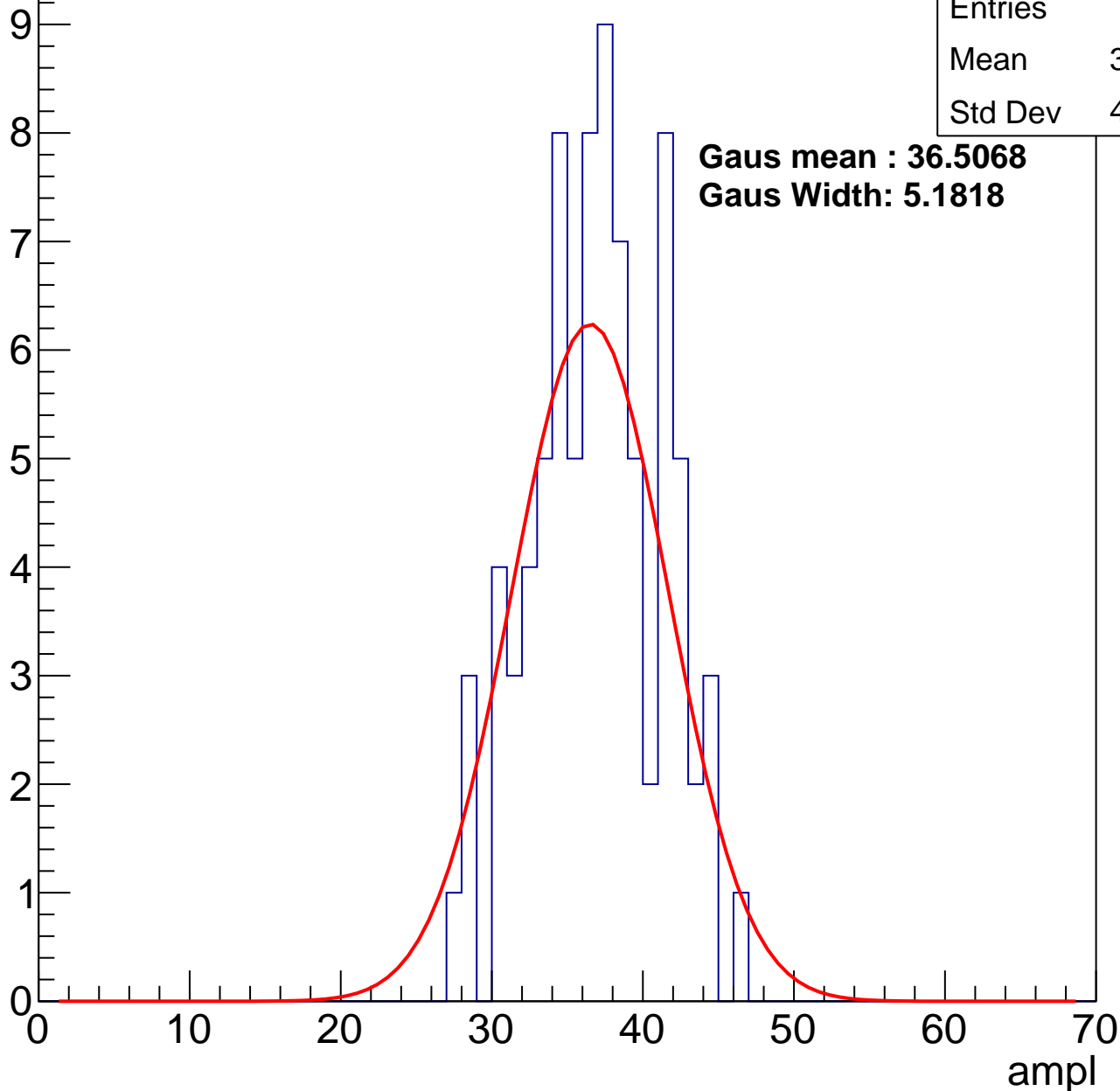
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	36.48
Std Dev	4.255

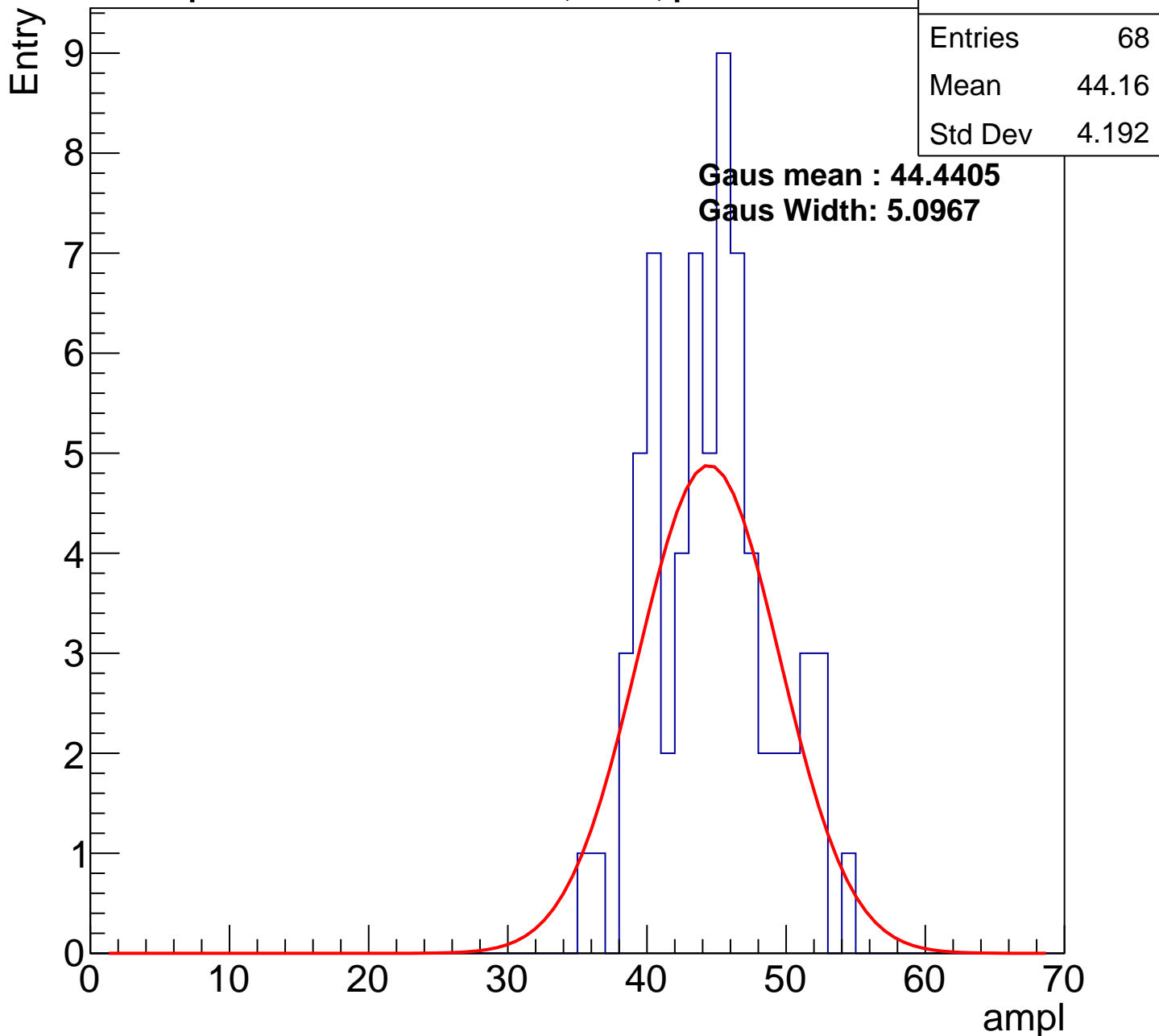
Gaus mean : 36.5068

Gaus Width: 5.1818



B1L103S, U20-ch6, adc2

calib_packv5_042523_0143.root, FC#7, port C2

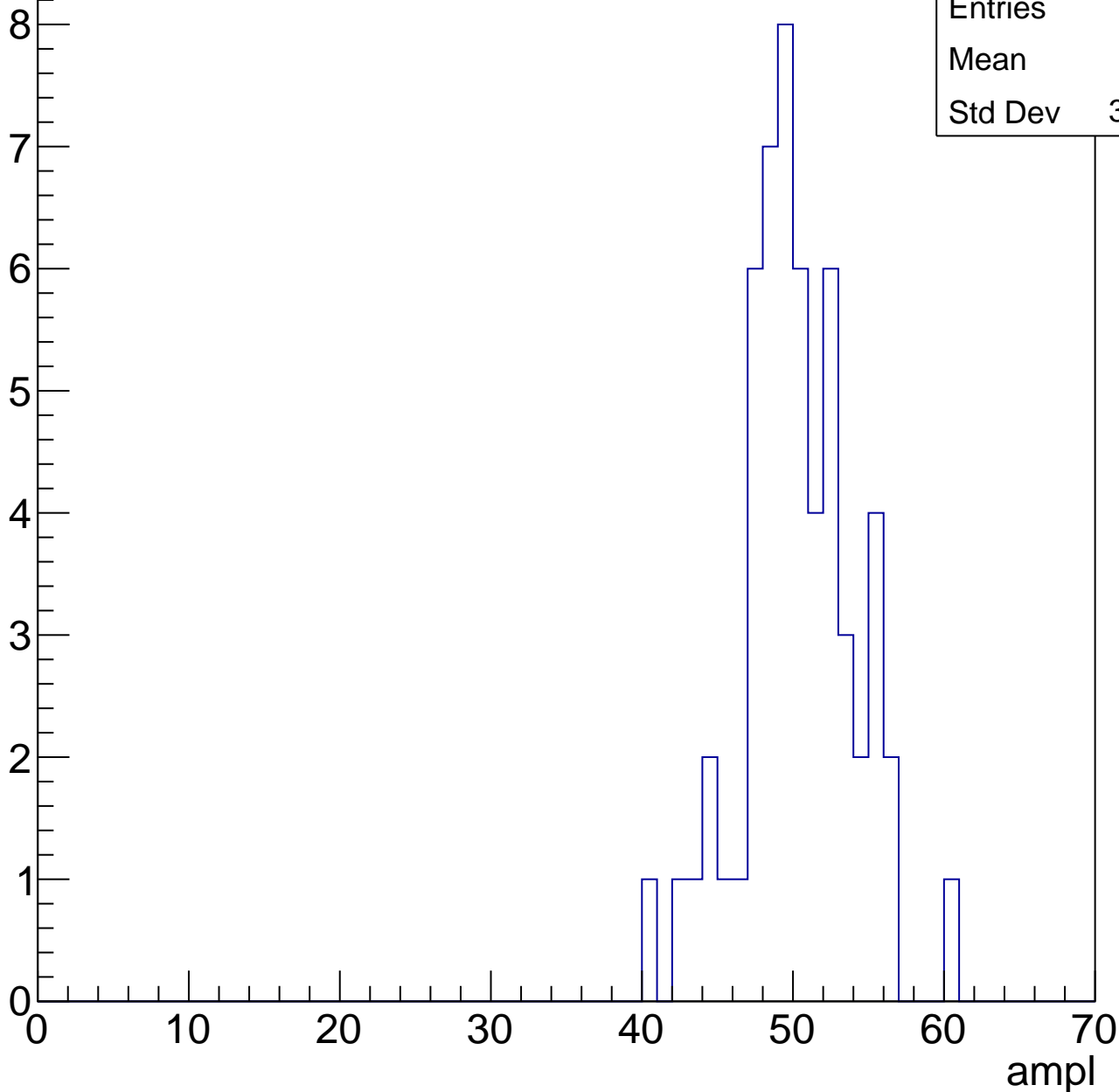


B1L103S, U20-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.8
Std Dev	3.715

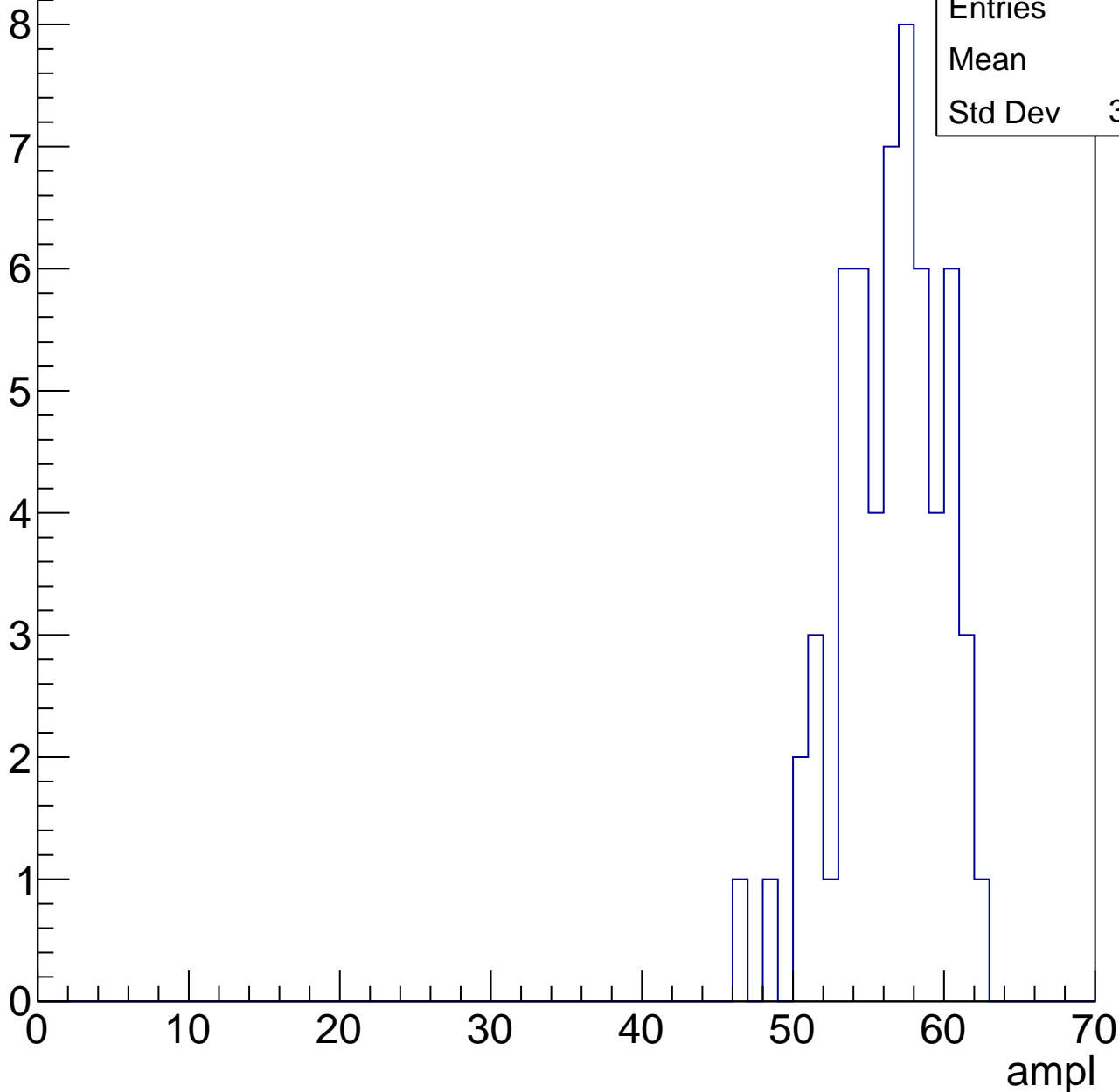


B1L103S, U20-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.9
Std Dev	3.403

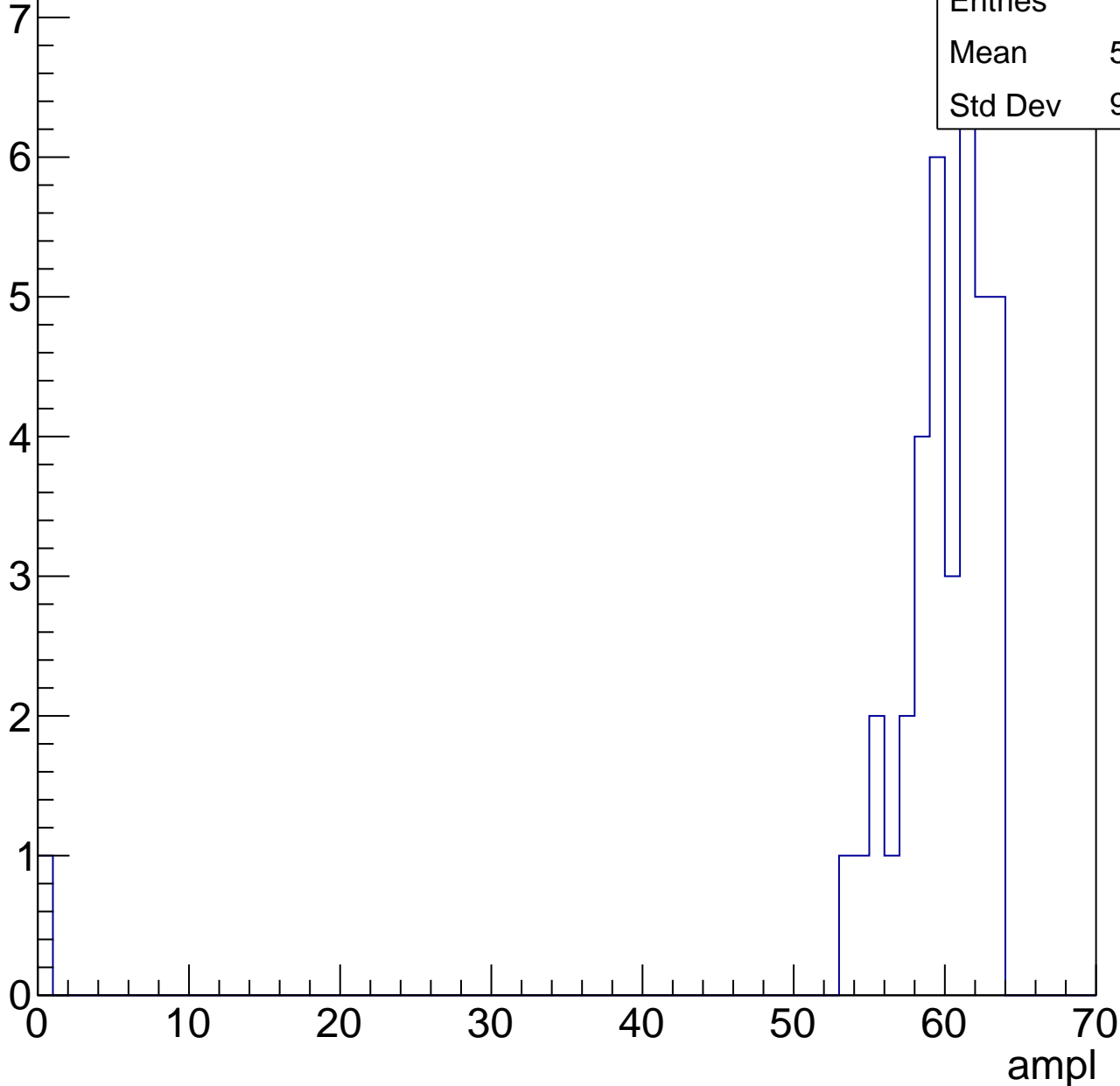


B1L103S, U20-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.03
Std Dev	9.887

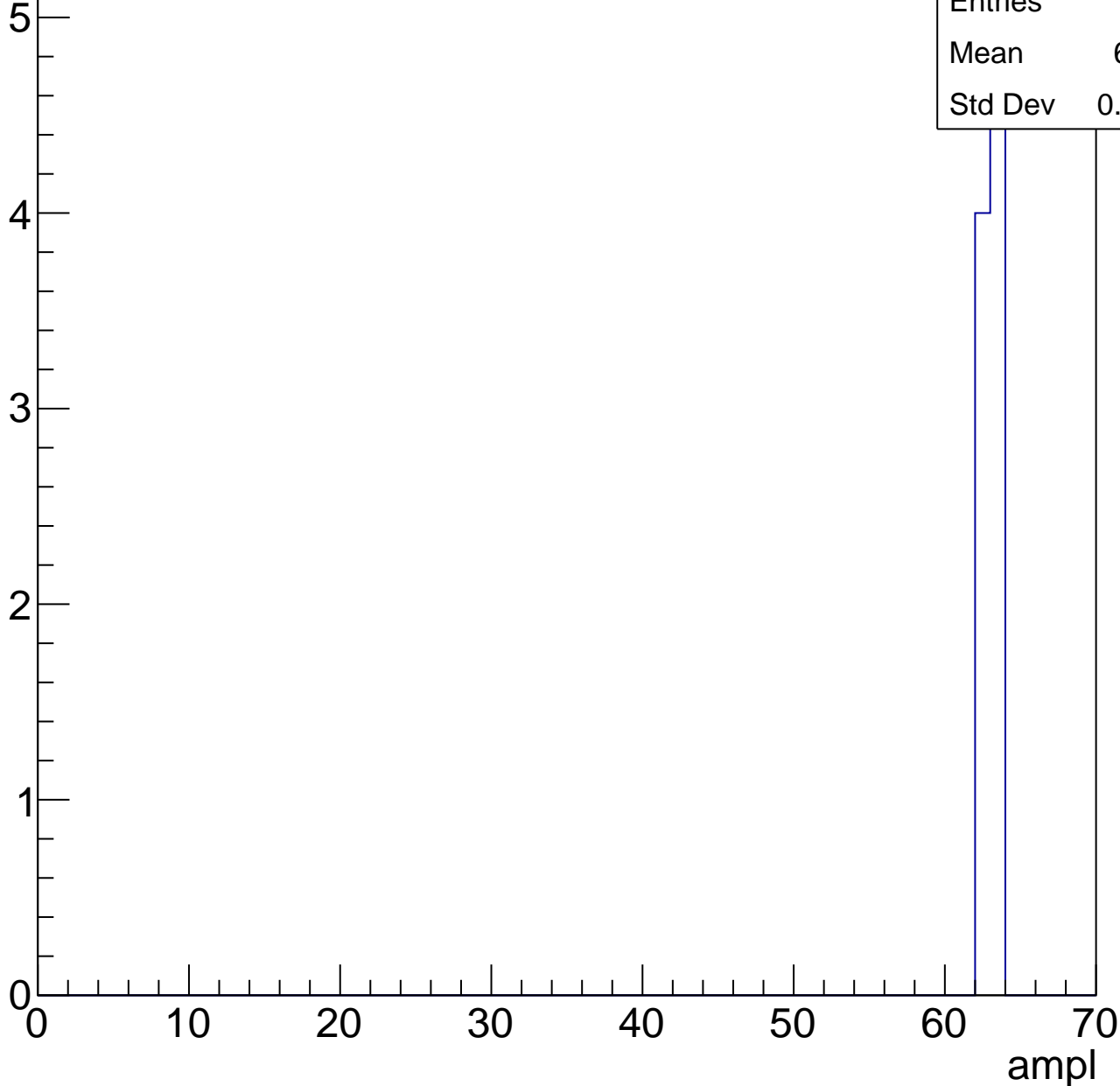


B1L103S, U20-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	62.56
Std Dev	0.4969



B1L103S, U20-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch7, adc0

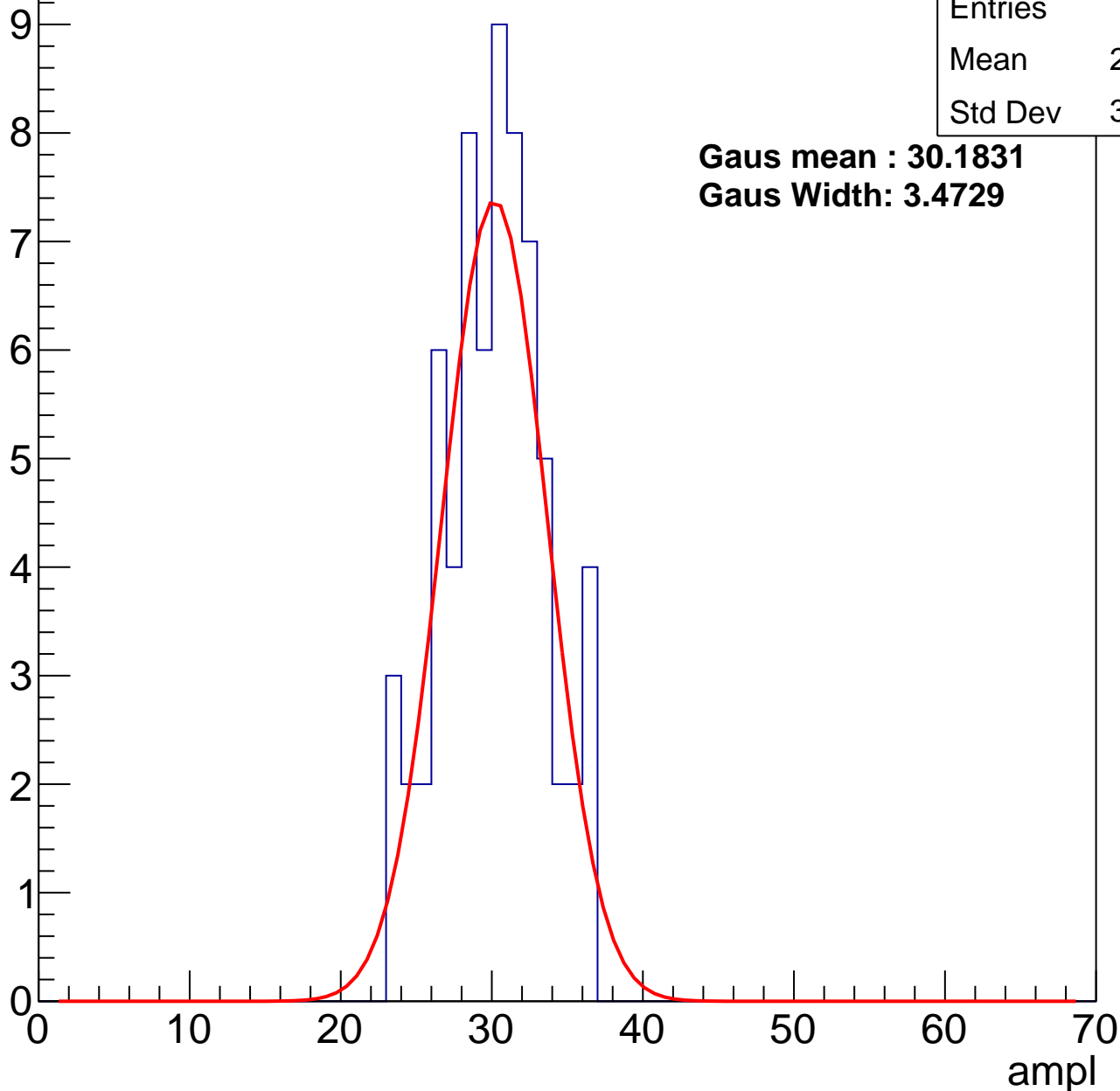
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.68
Std Dev	3.296

Gaus mean : 30.1831

Gaus Width: 3.4729



B1L103S, U20-ch7, adc1

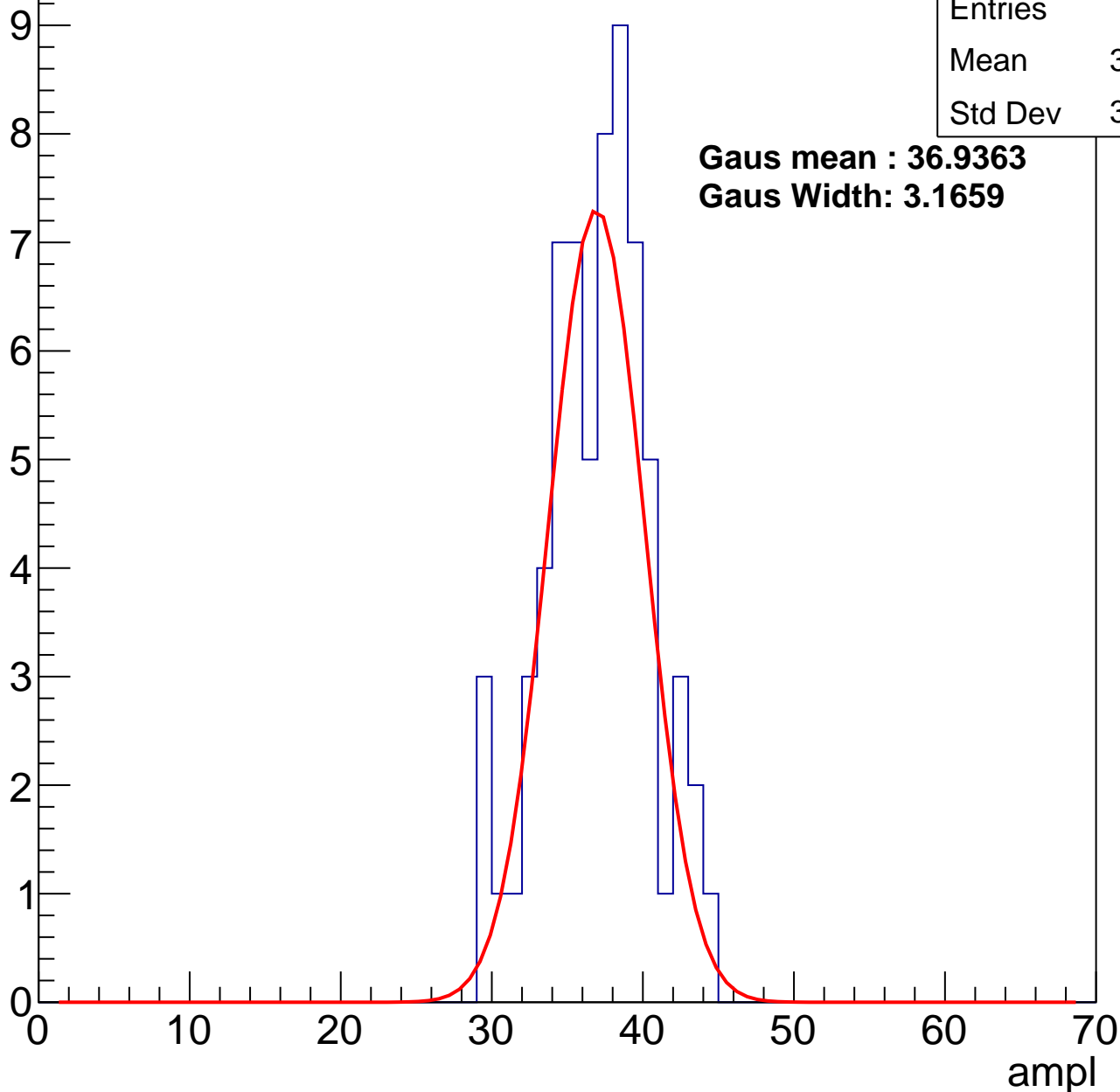
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.52
Std Dev	3.435

Gaus mean : 36.9363

Gaus Width: 3.1659



B1L103S, U20-ch7, adc2

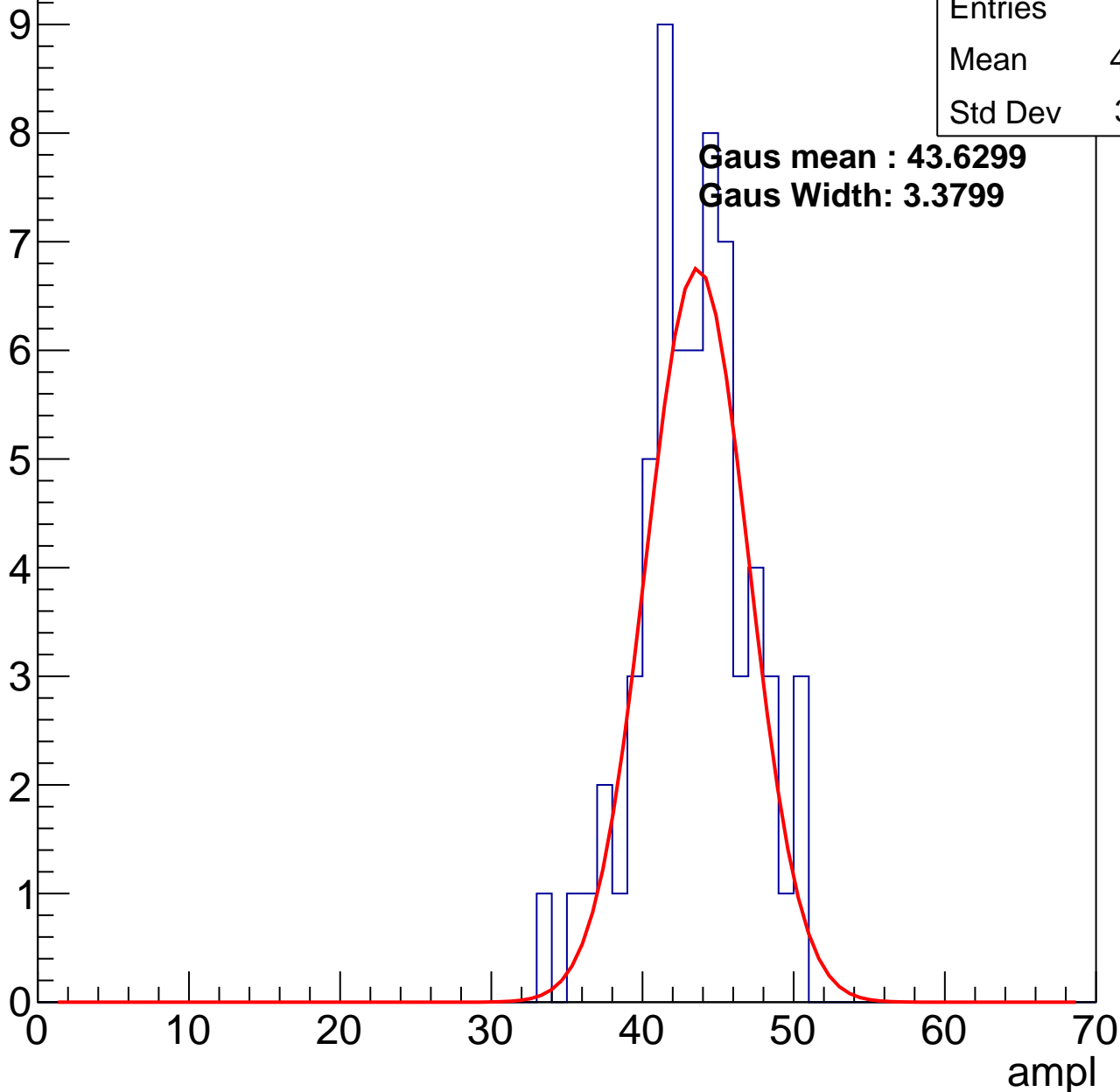
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.94
Std Dev	3.601

Gaus mean : 43.6299

Gaus Width: 3.3799

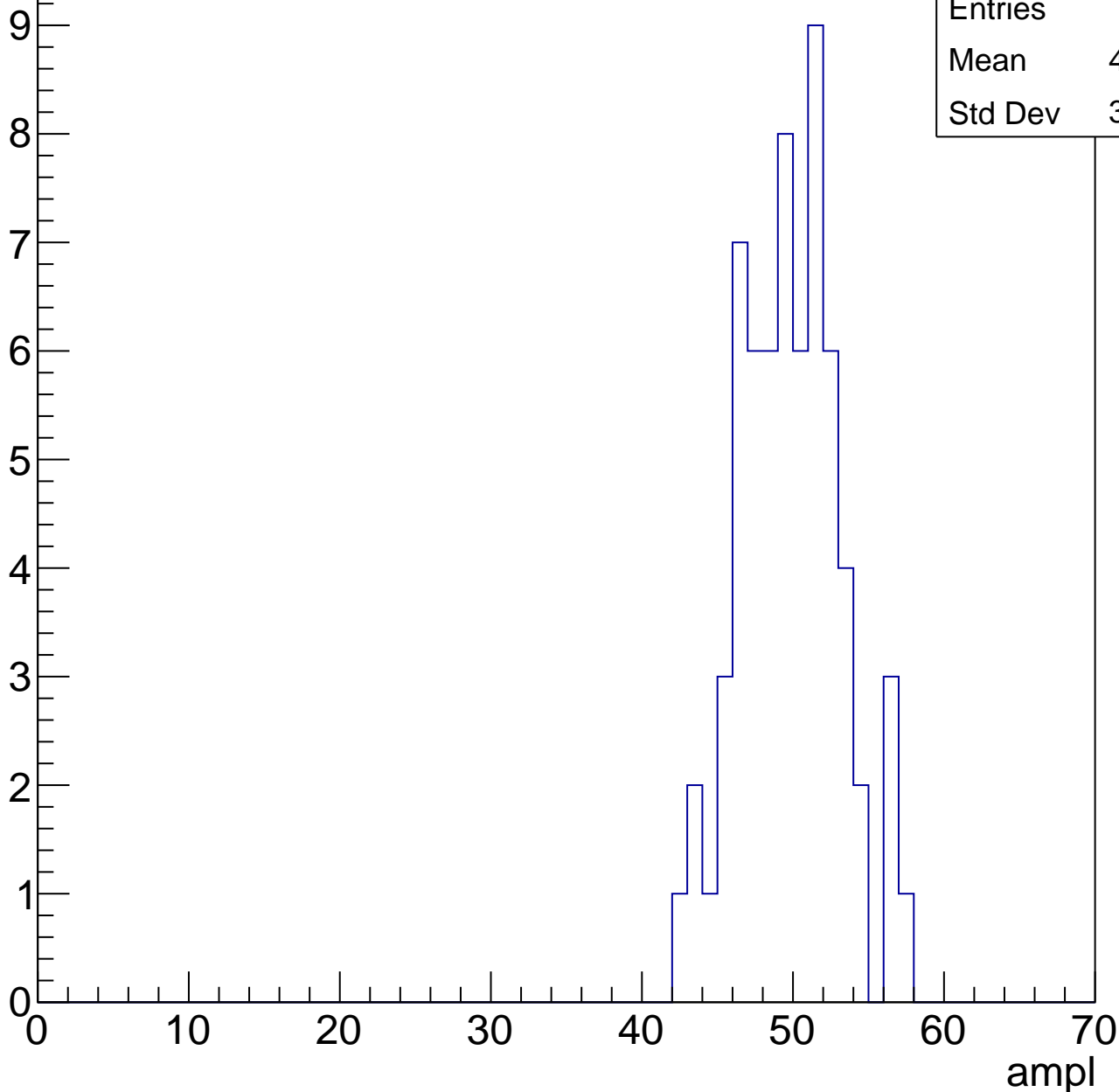


B1L103S, U20-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

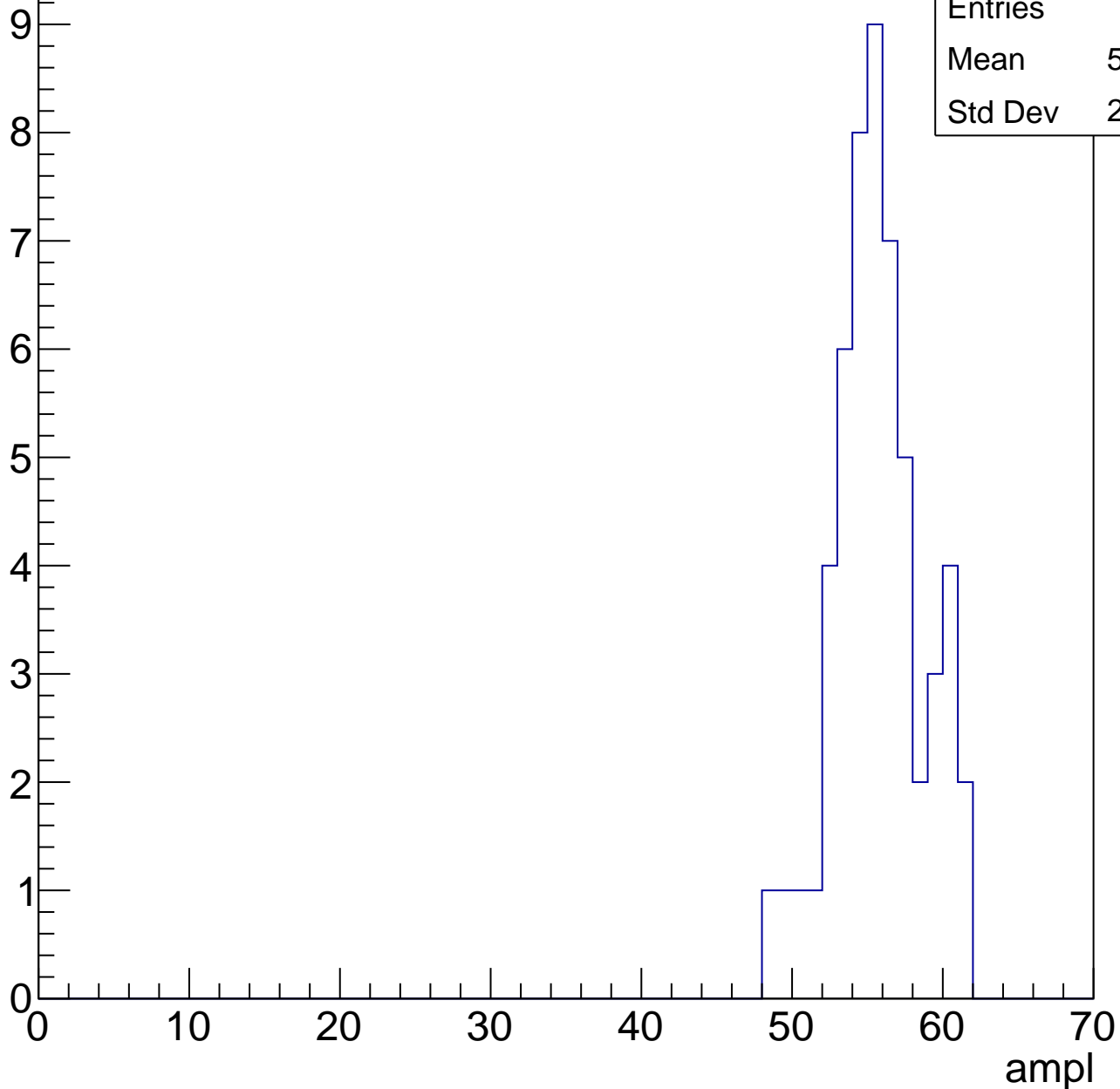
Entries	65
Mean	49.34
Std Dev	3.283



B1L103S, U20-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

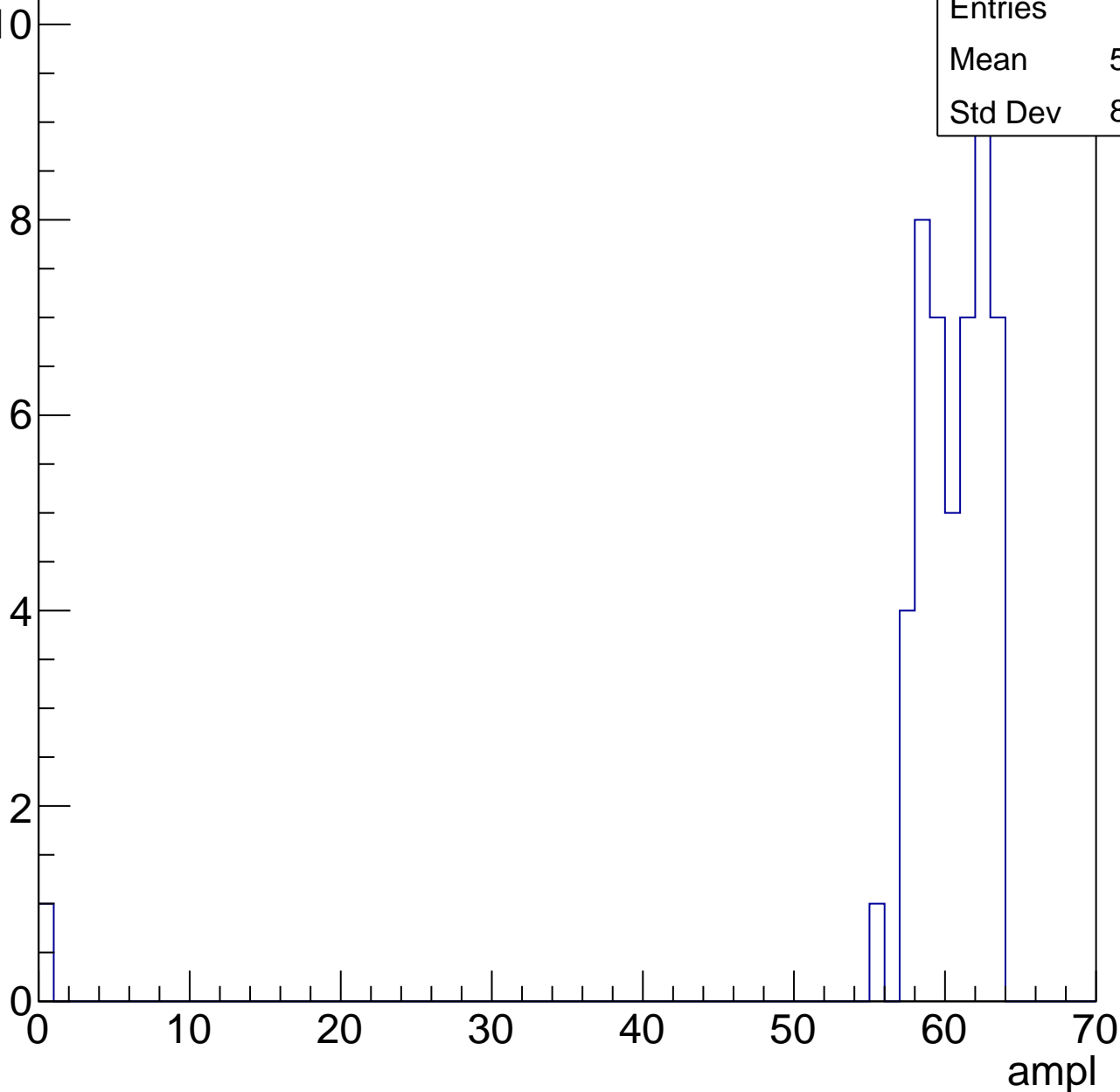


B1L103S, U20-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.96
Std Dev	8.667



B1L103S, U20-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch8, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	29.19
Std Dev	4.076

Gaus mean : 30.0625

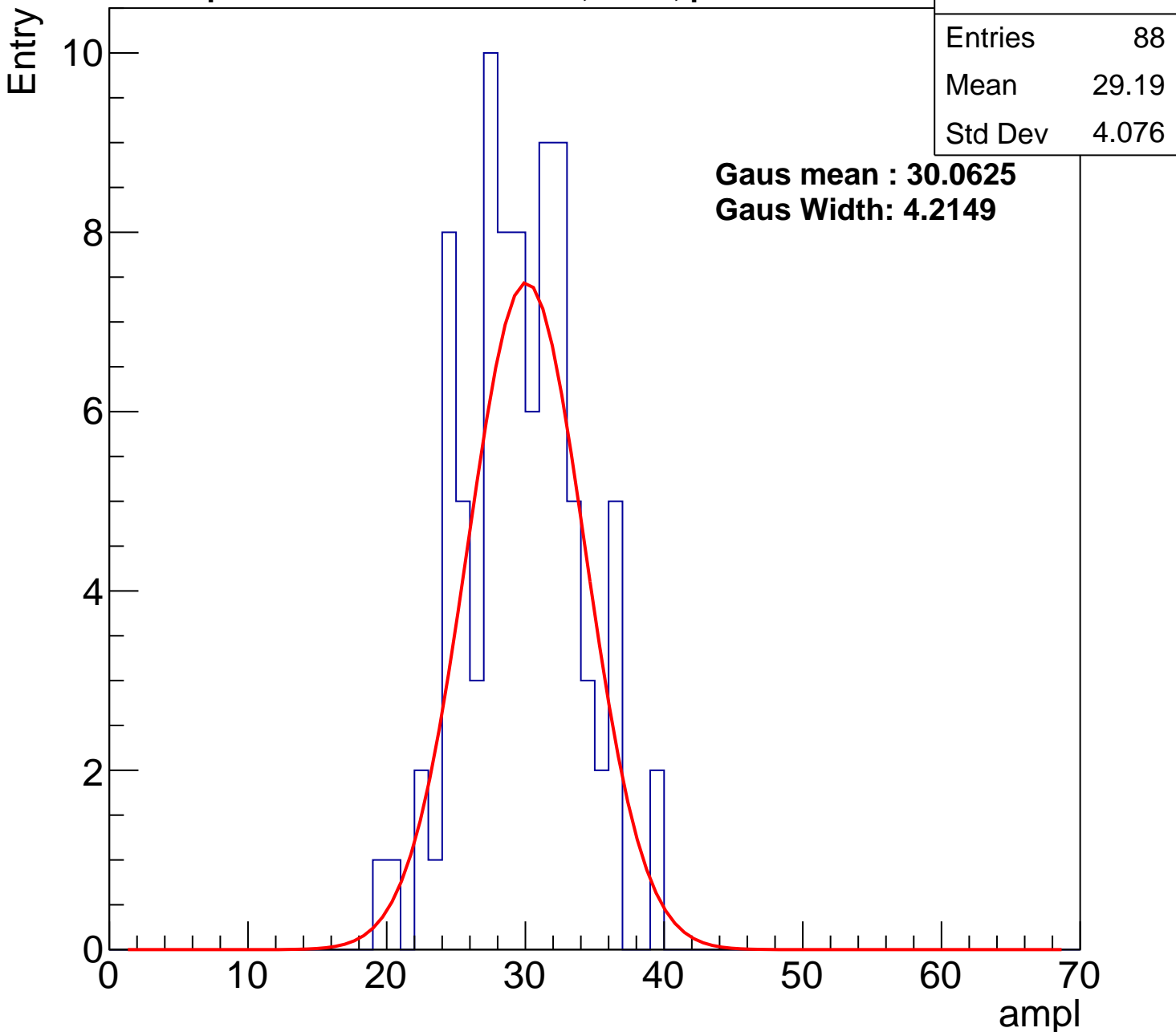
Gaus Width: 4.2149

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch8, adc1

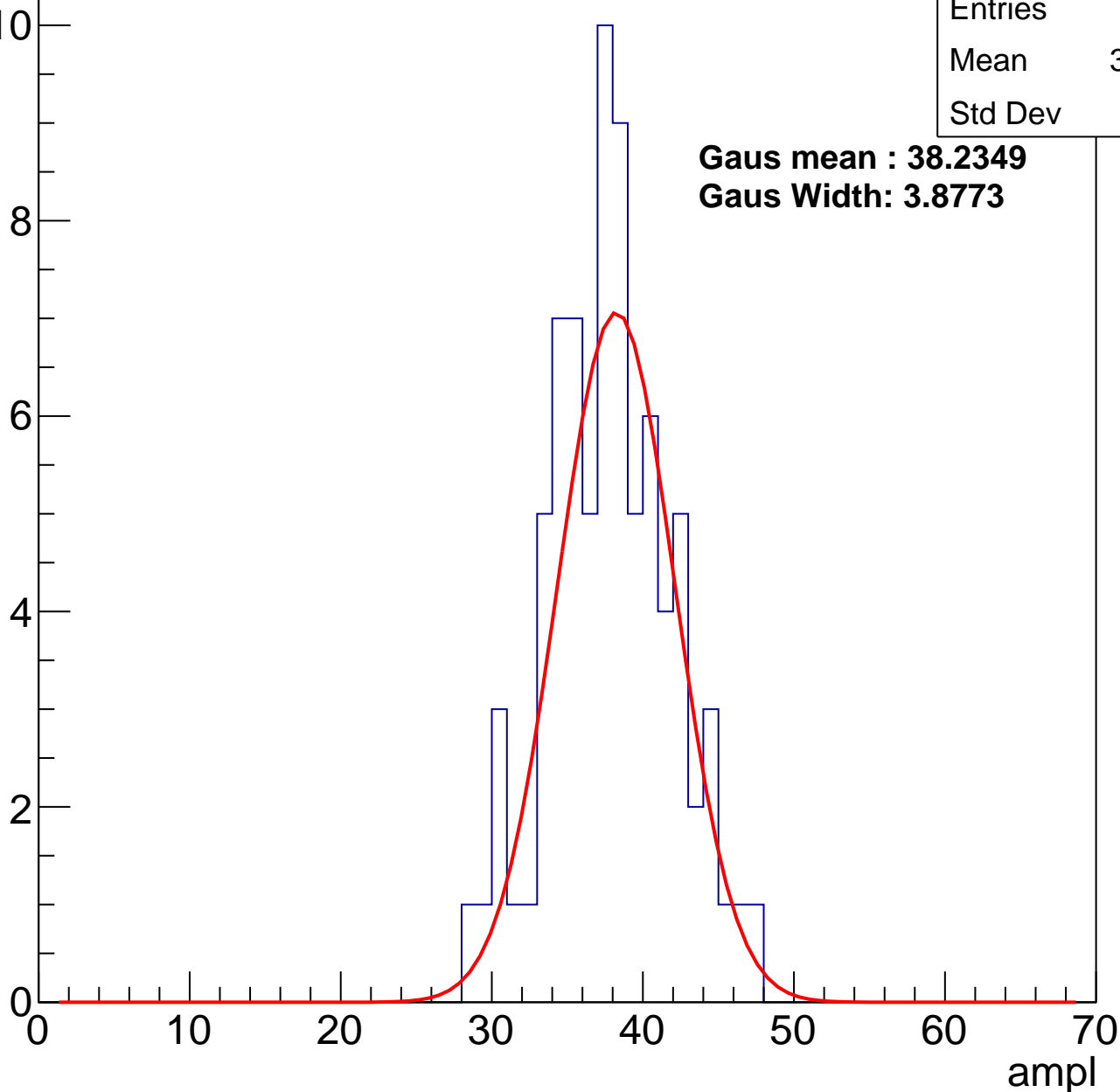
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	37.37
Std Dev	4.01

Gaus mean : 38.2349

Gaus Width: 3.8773



B1L103S, U20-ch8, adc2

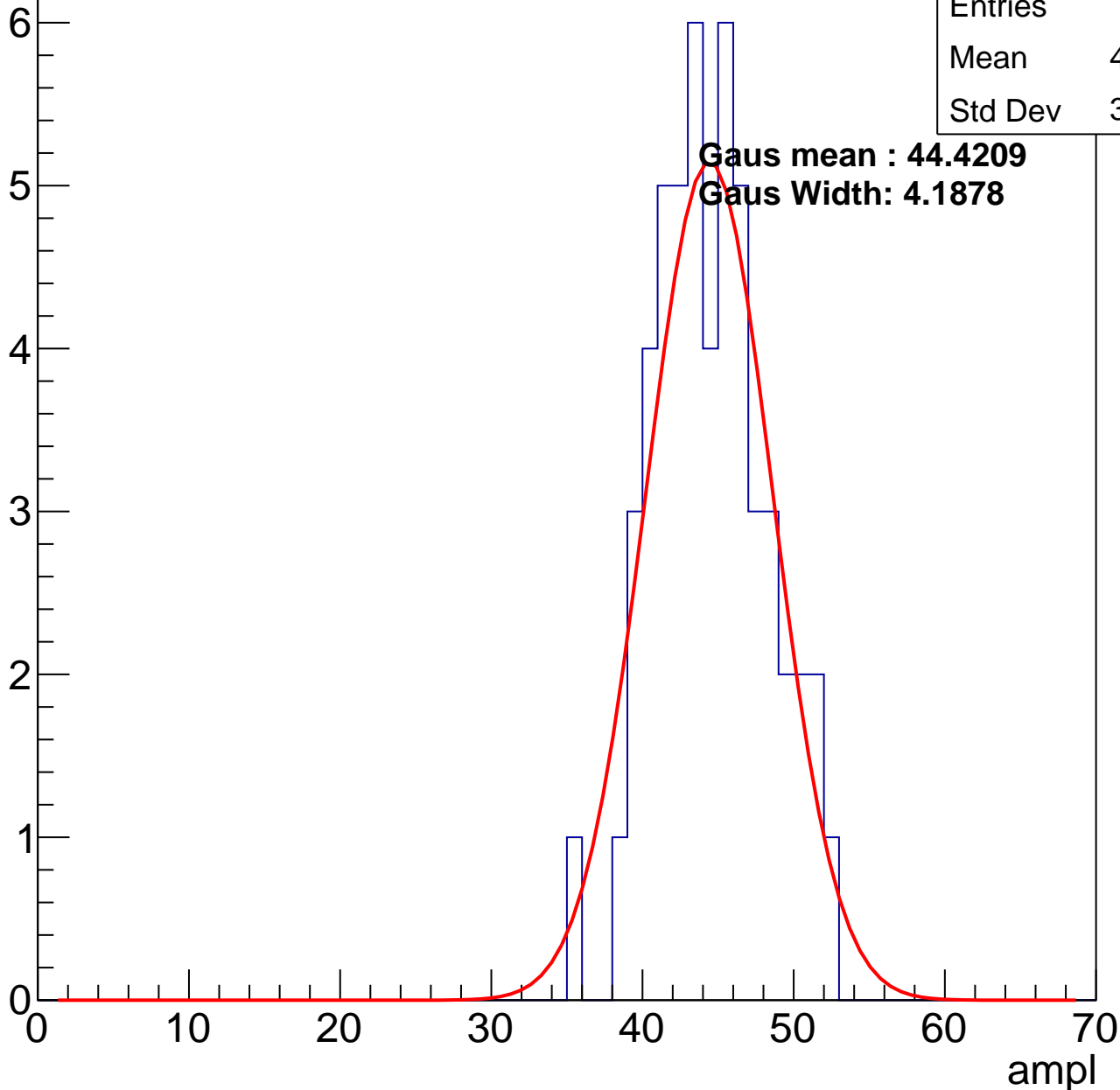
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.08
Std Dev	3.664

Gaus mean : 44.4209

Gaus Width: 4.1878

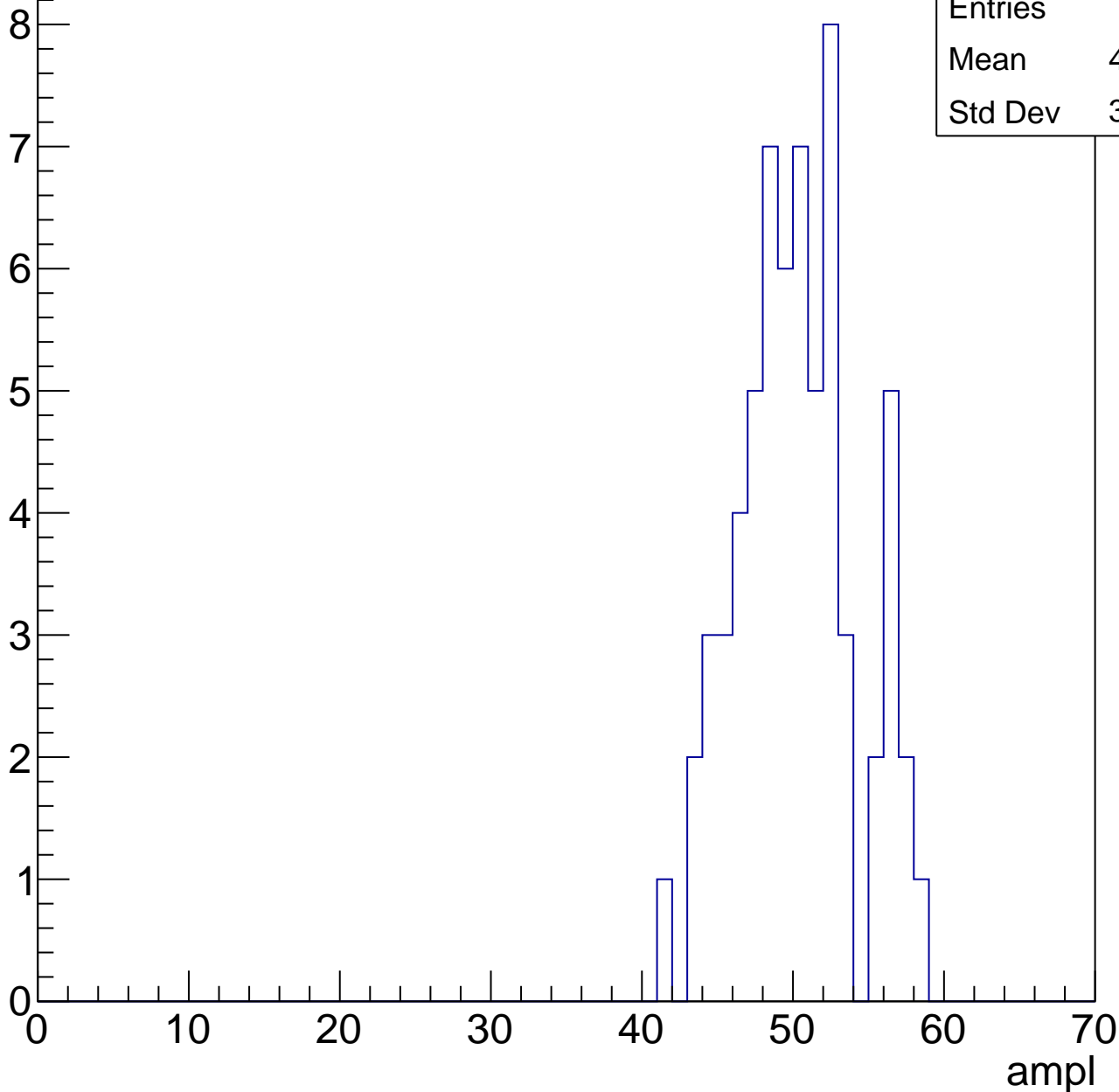


B1L103S, U20-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.77
Std Dev	3.868

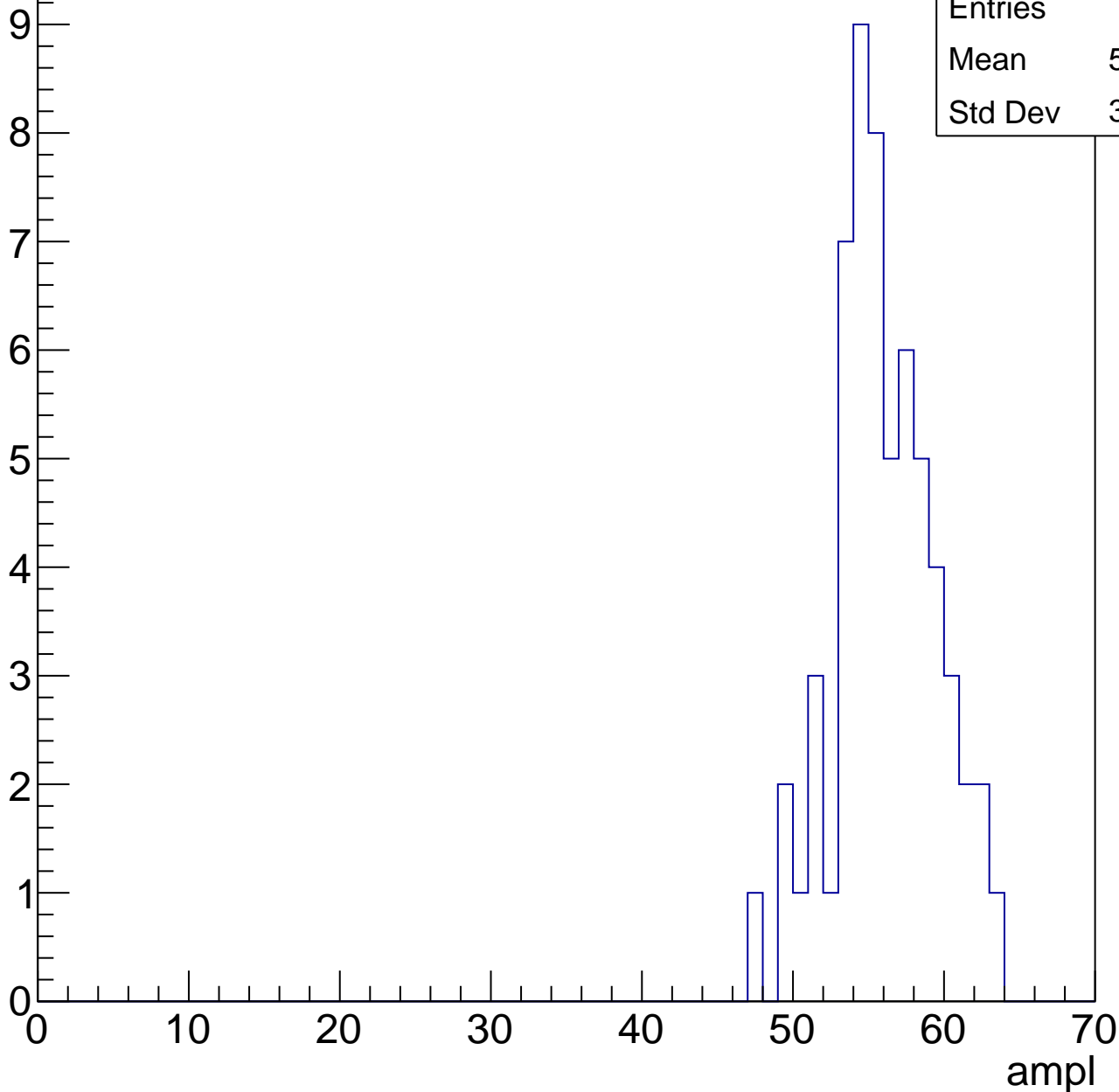


B1L103S, U20-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.57
Std Dev	3.373

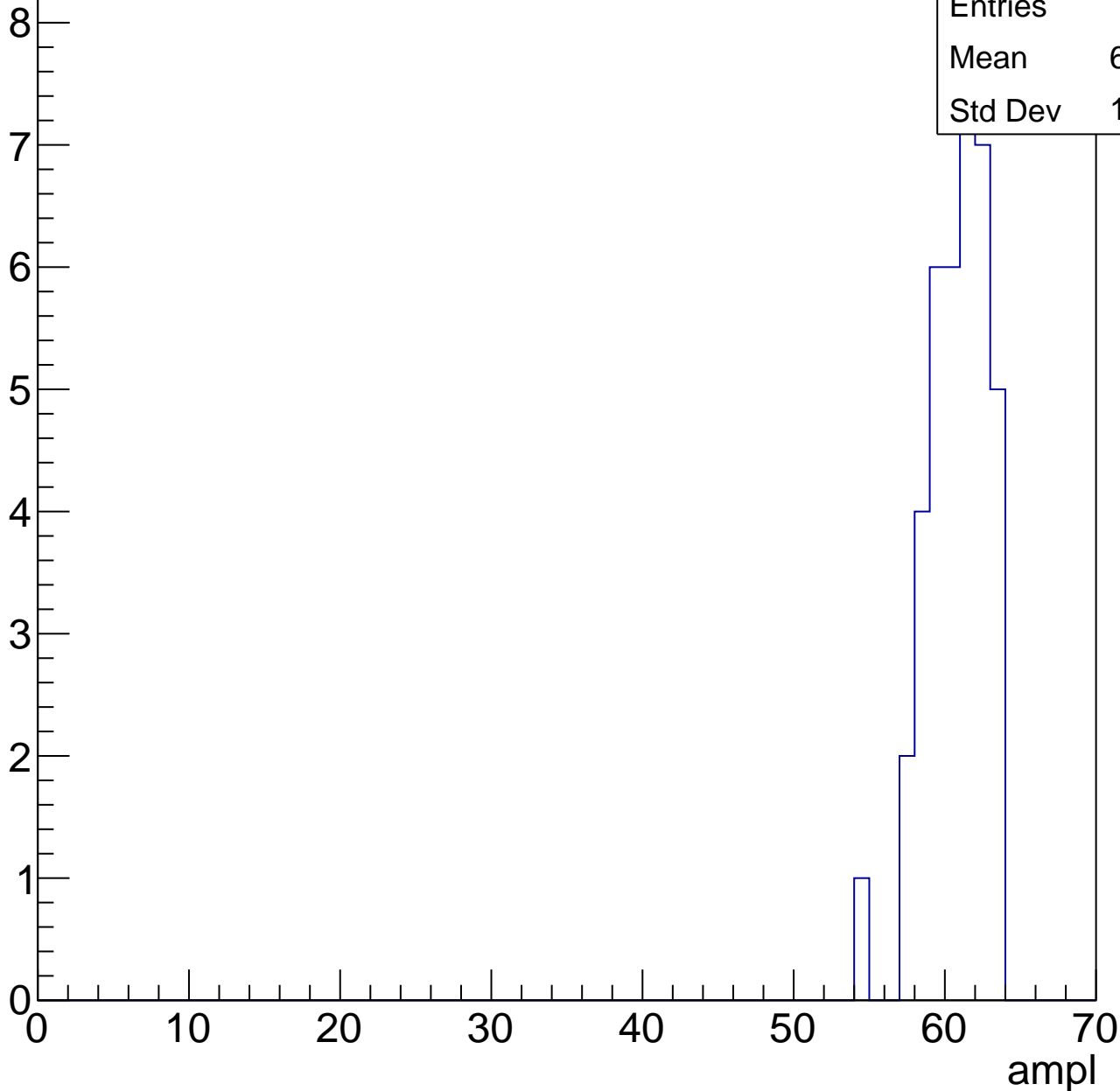


B1L103S, U20-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

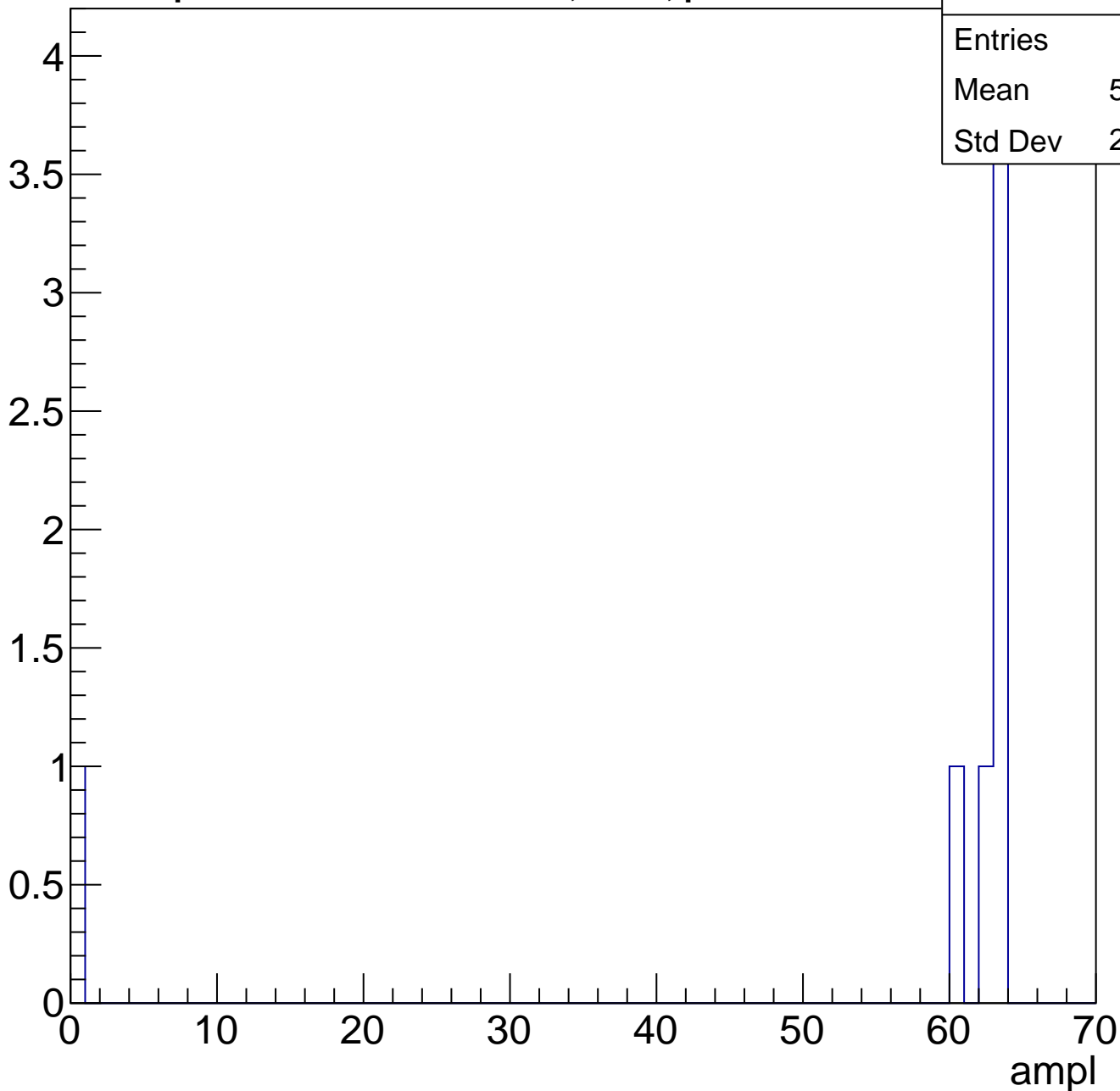
Entries	39
Mean	60.28
Std Dev	1.986



B1L103S, U20-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch9, adc0

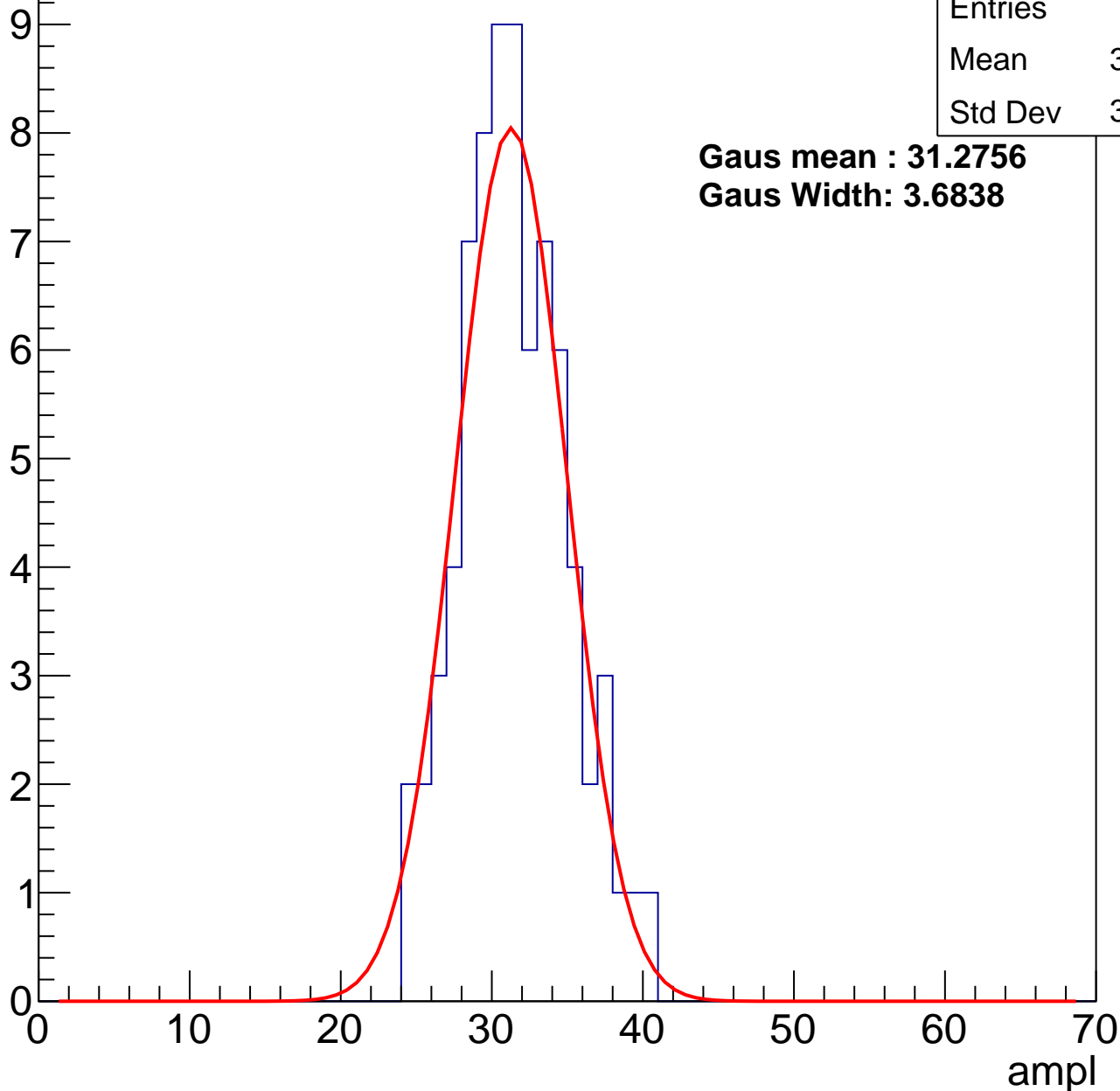
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	31.04
Std Dev	3.496

Gaus mean : 31.2756

Gaus Width: 3.6838



B1L103S, U20-ch9, adc1

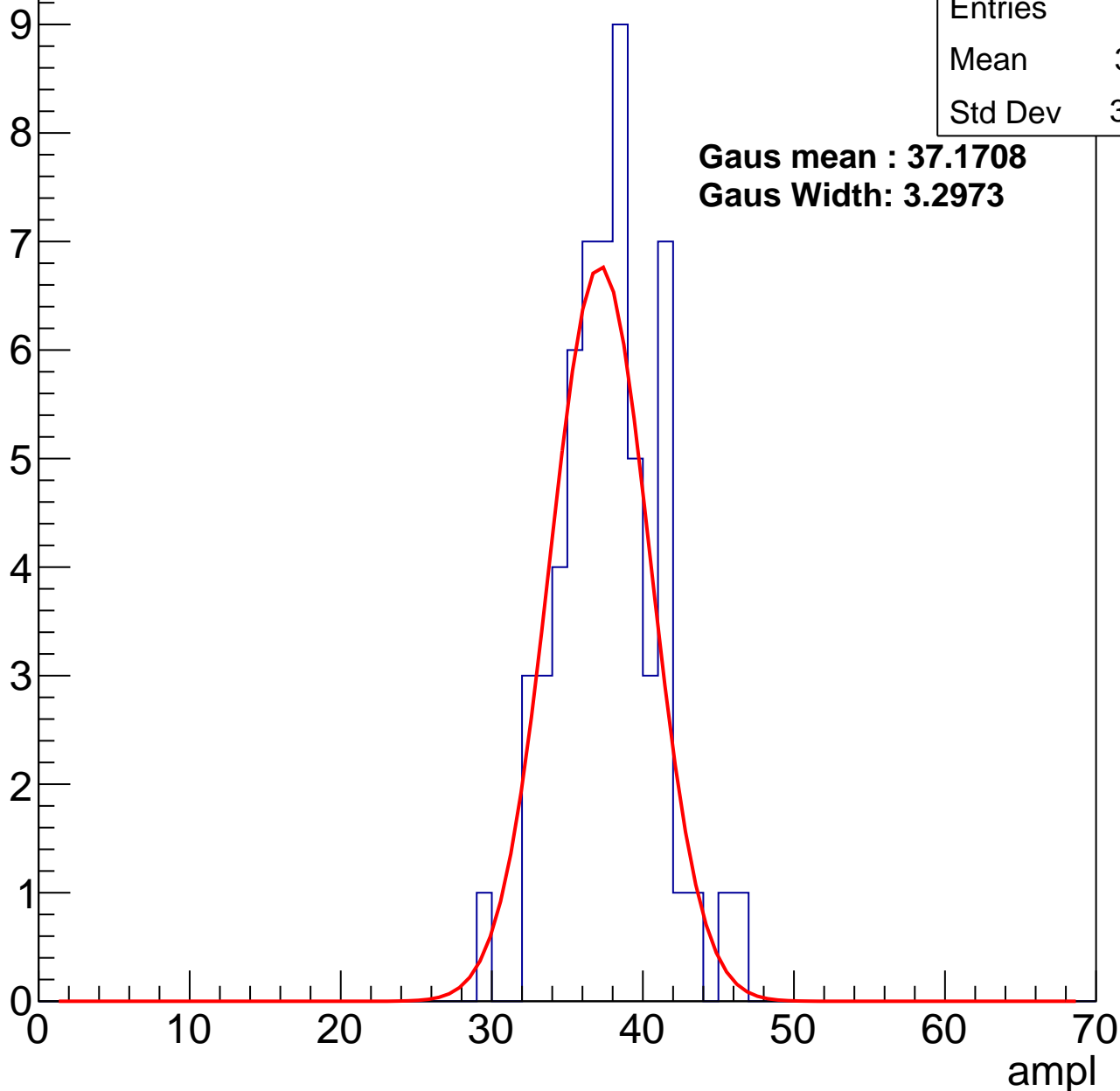
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	37.31
Std Dev	3.248

Gaus mean : 37.1708

Gaus Width: 3.2973



B1L103S, U20-ch9, adc2

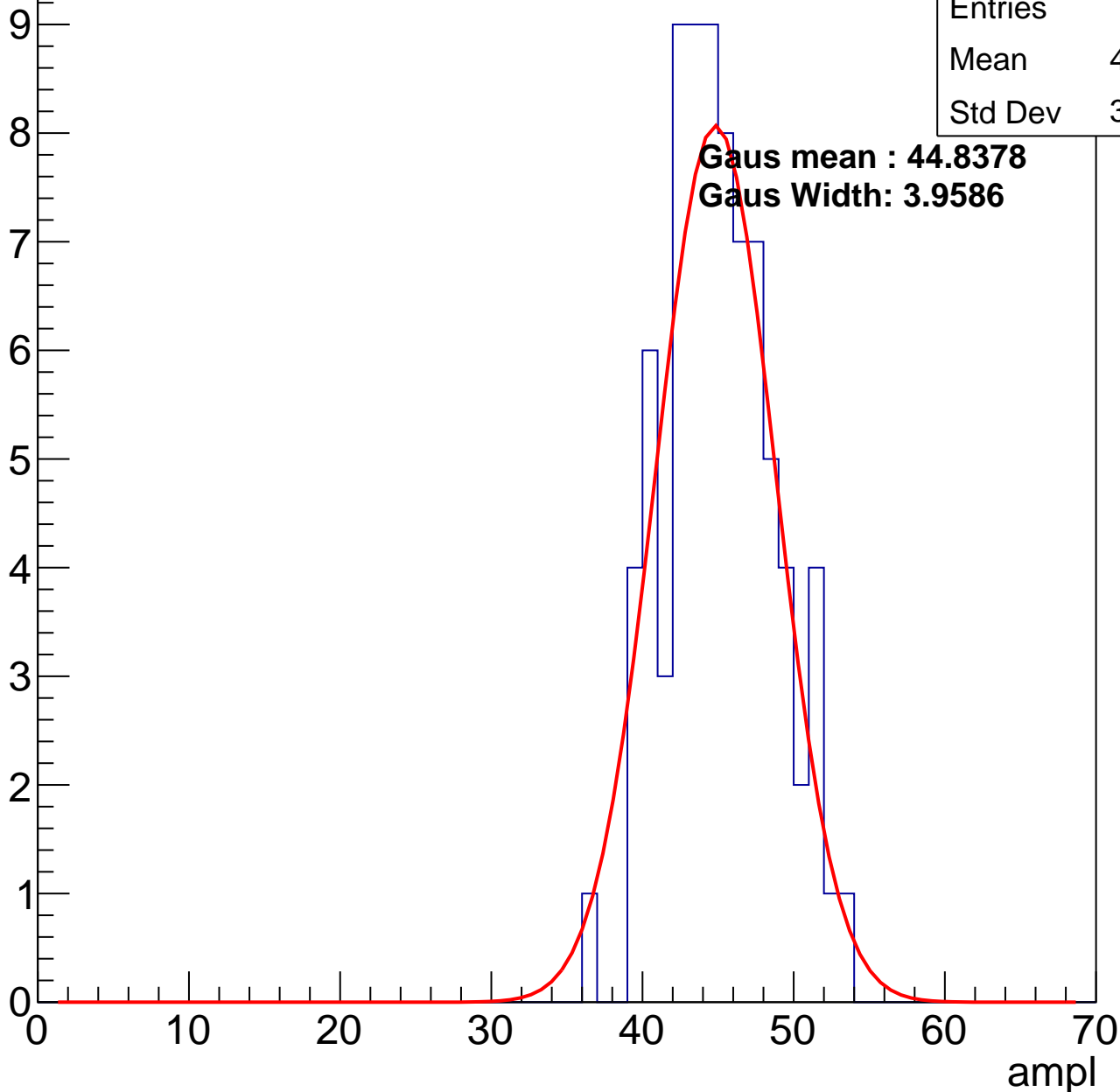
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	44.65
Std Dev	3.518

Gaus mean : 44.8378

Gaus Width: 3.9586

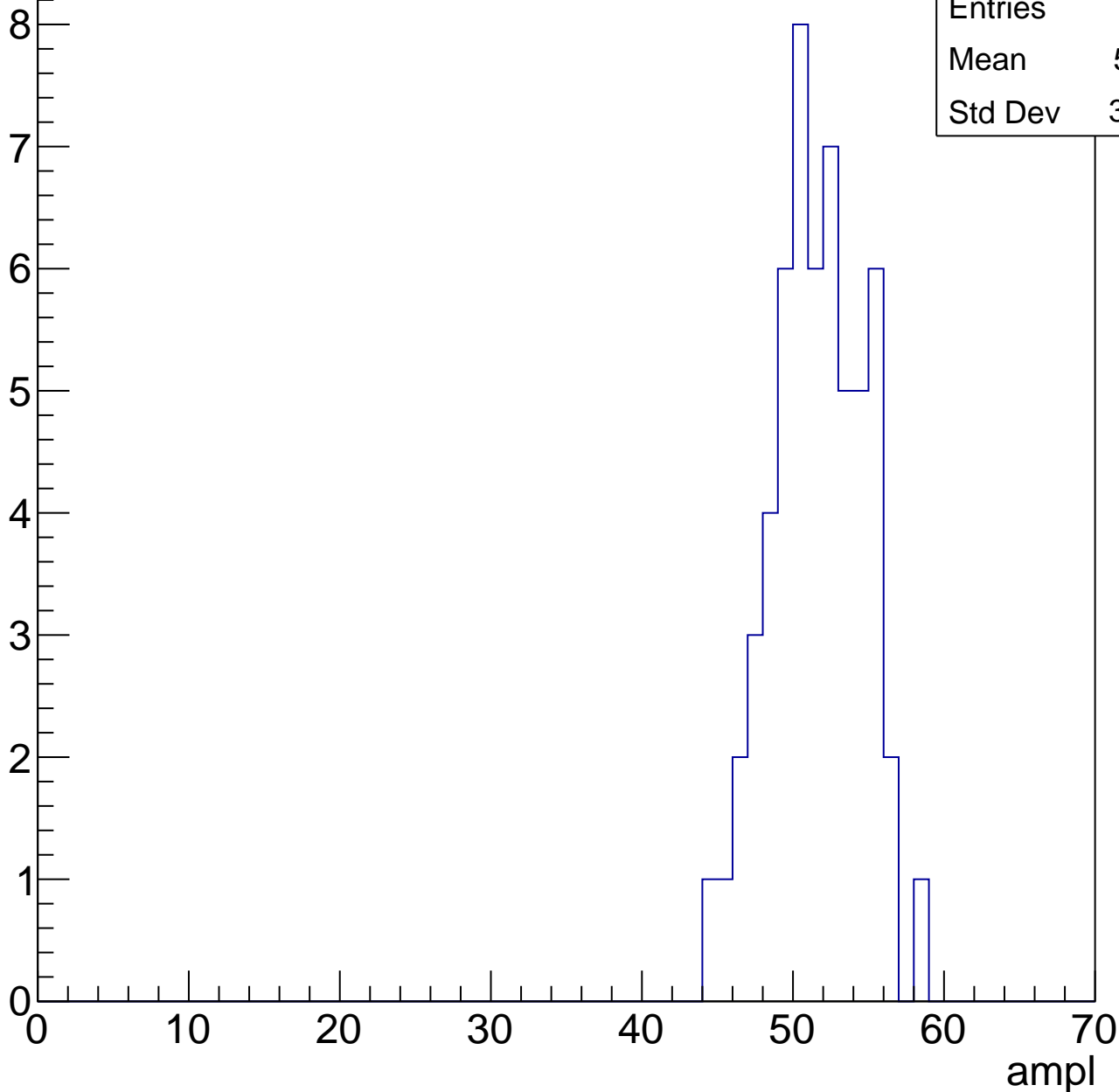


B1L103S, U20-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	51.11
Std Dev	3.013

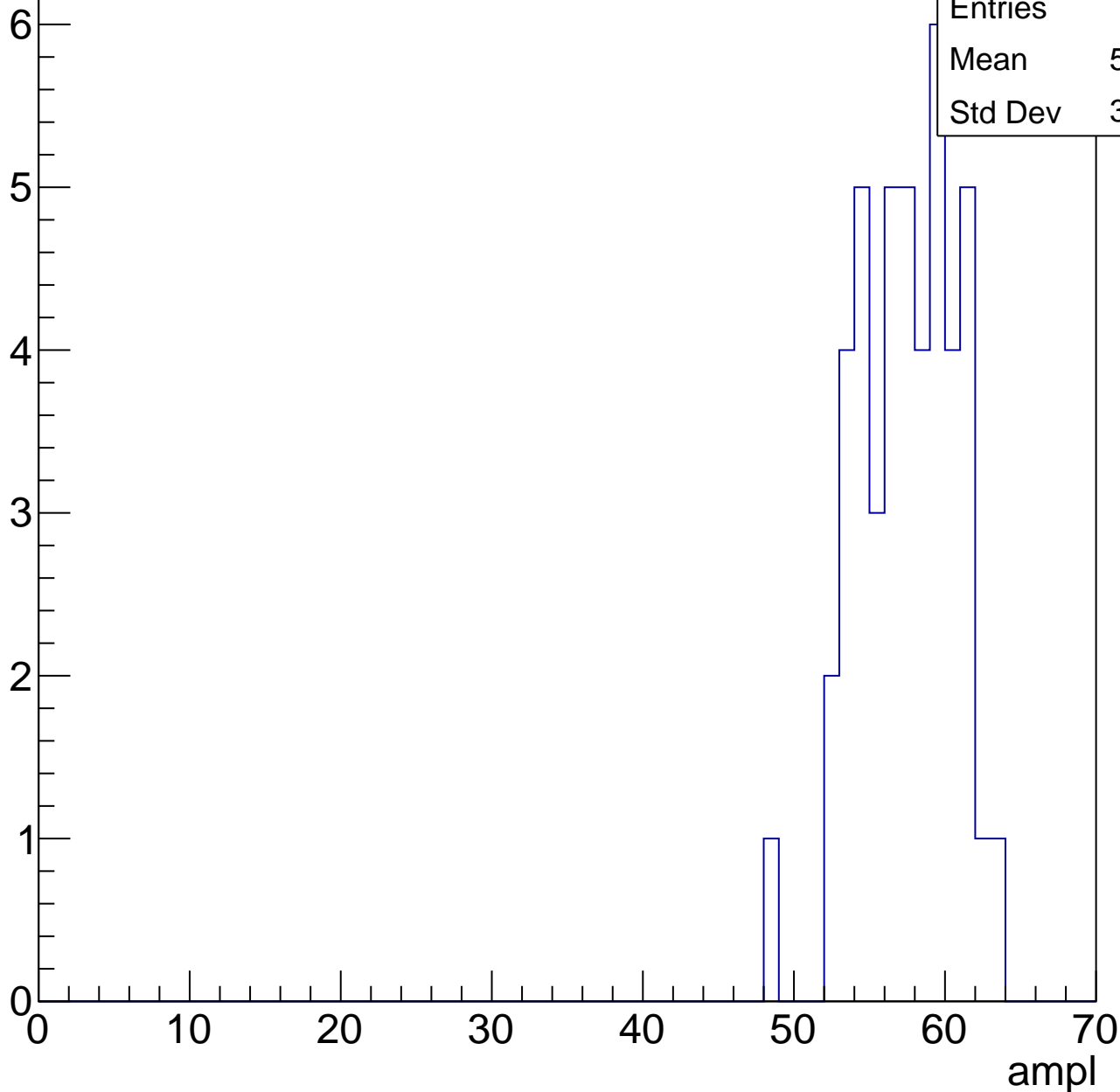


B1L103S, U20-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	56.96
Std Dev	3.169

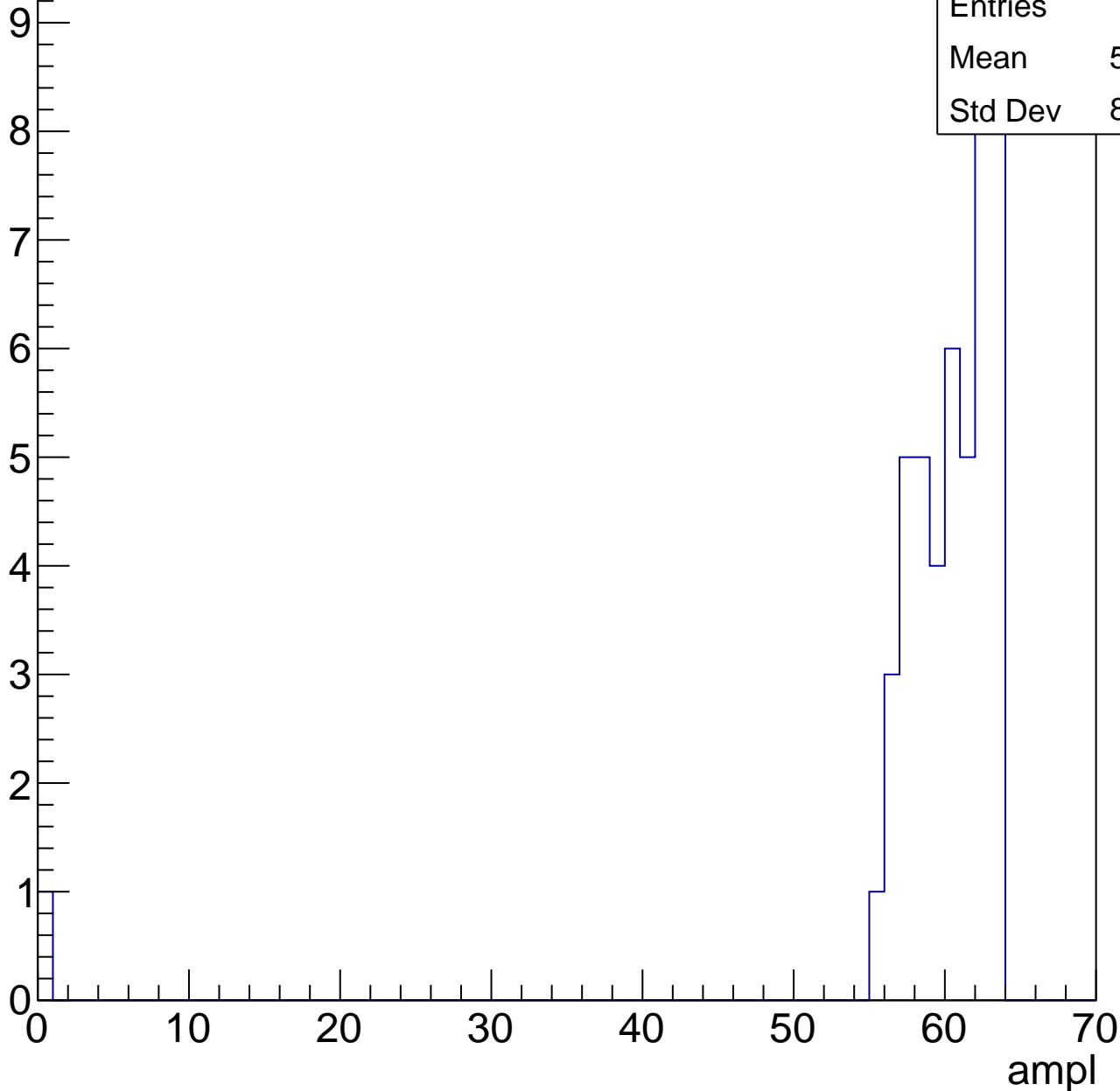


B1L103S, U20-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.83
Std Dev	8.896



B1L103S, U20-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

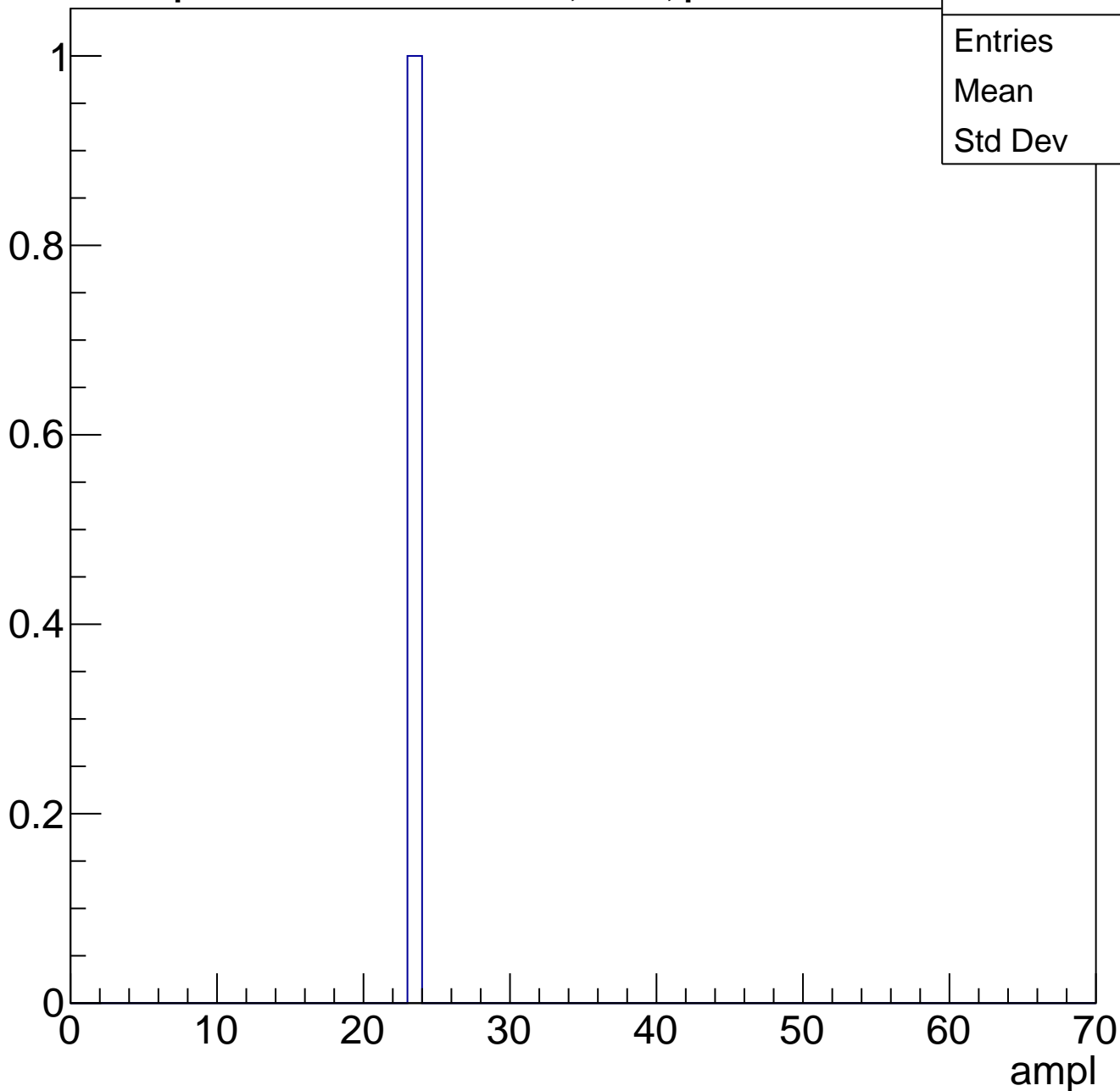


Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch10, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	30.06
Std Dev	4.123

Gaus mean : 30.8124

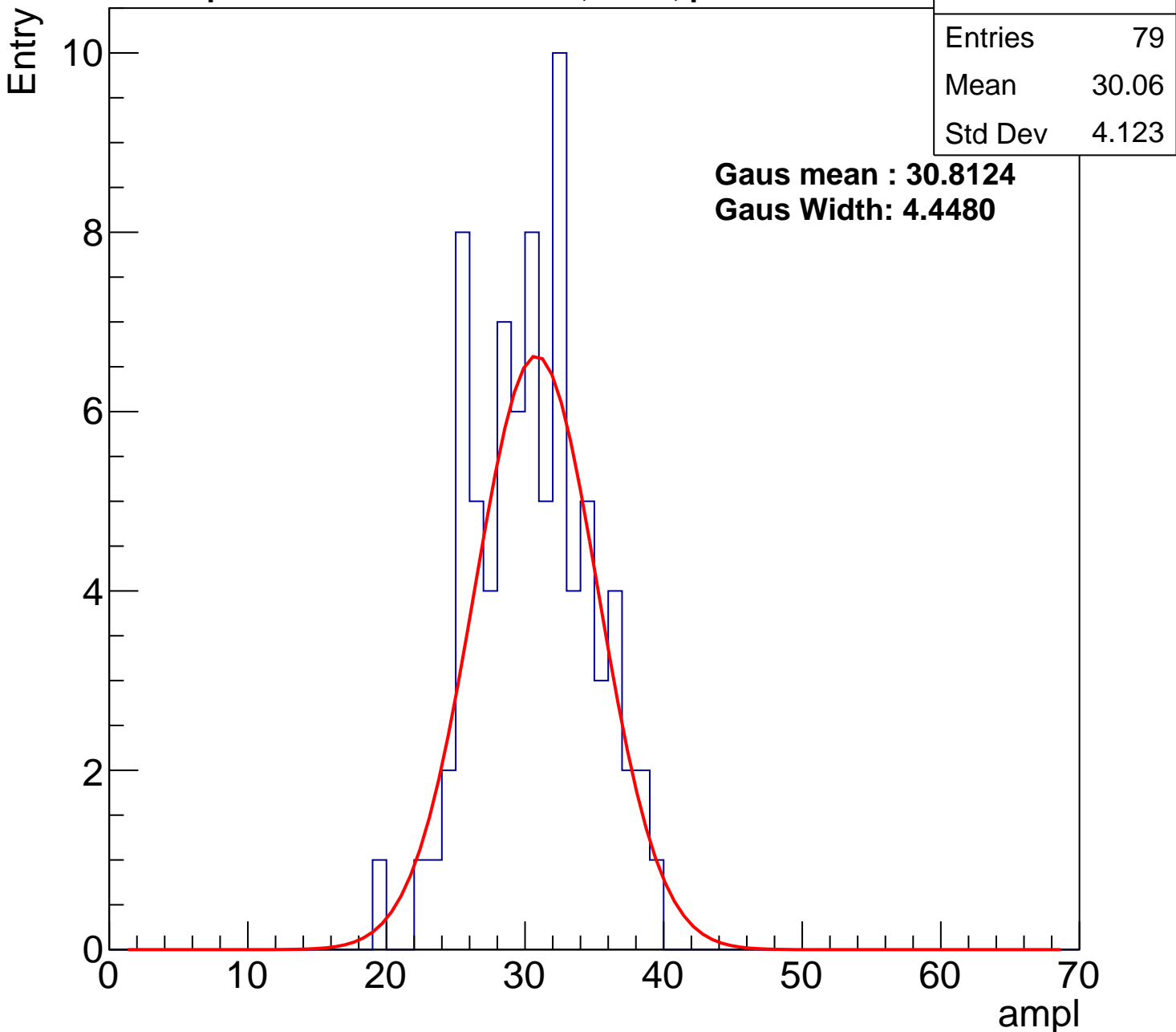
Gaus Width: 4.4480

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch10, adc1

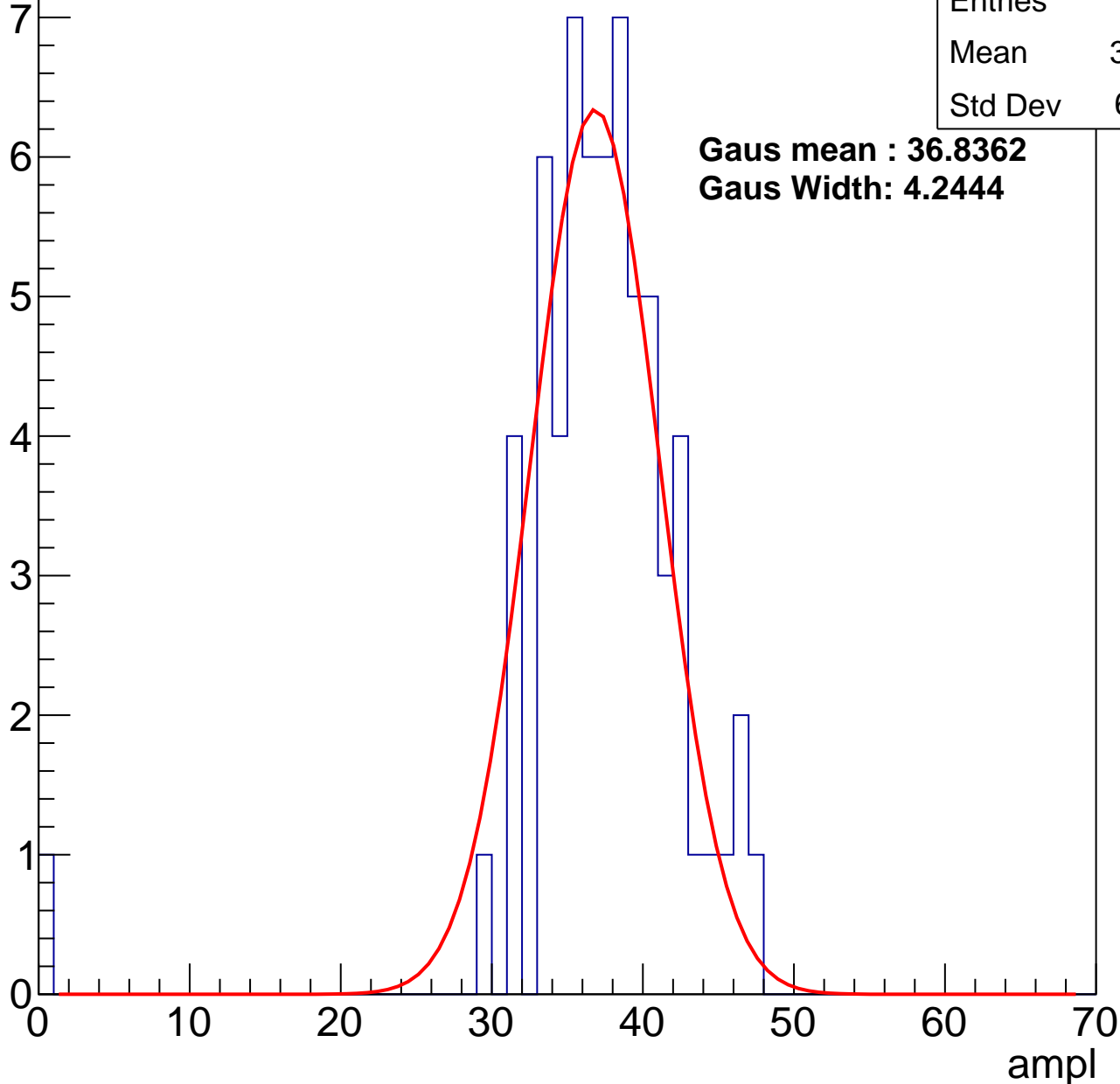
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.82
Std Dev	6.041

Gaus mean : 36.8362

Gaus Width: 4.2444



B1L103S, U20-ch10, adc2

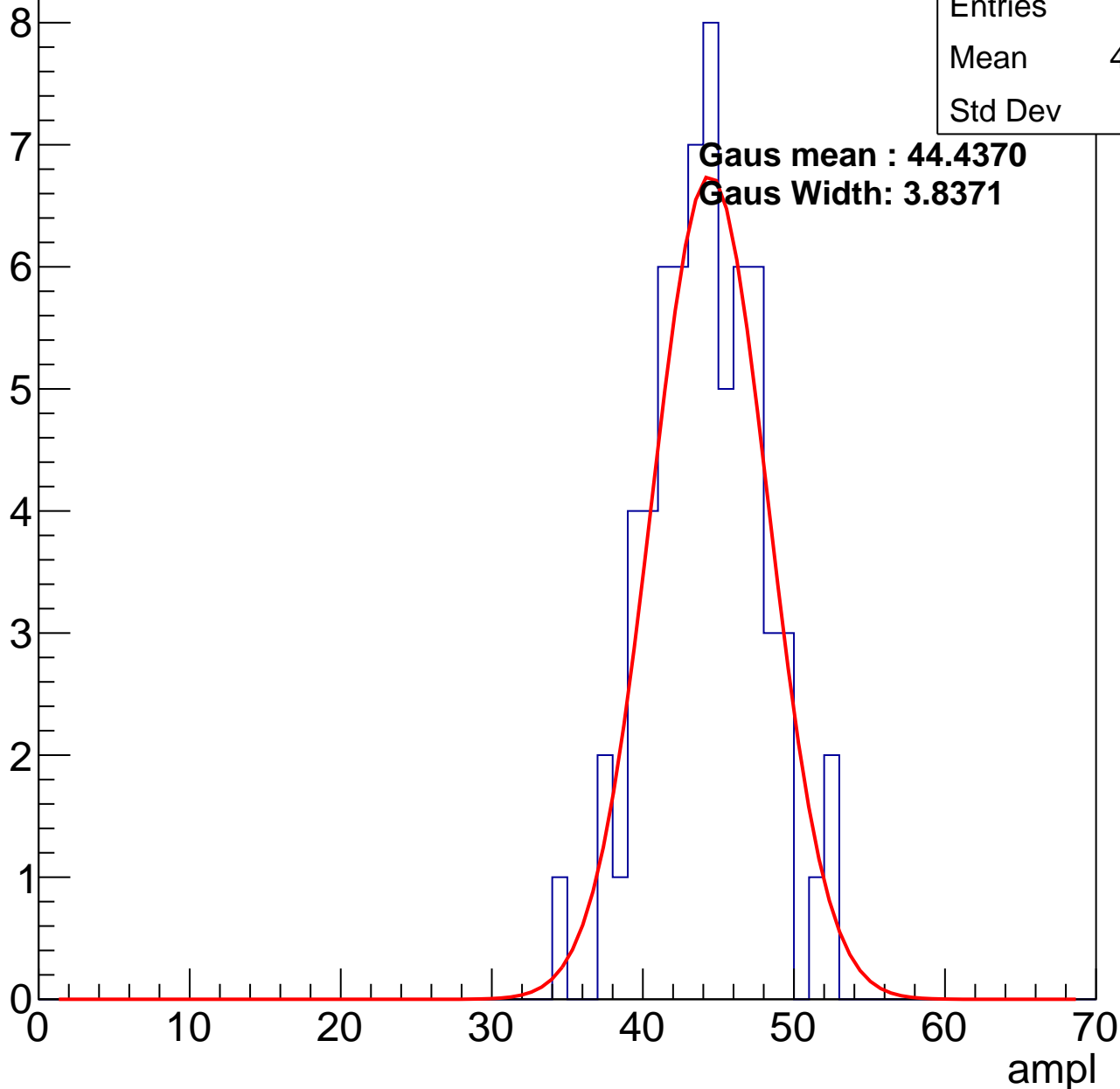
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.72
Std Dev	3.64

Gaus mean : 44.4370

Gaus Width: 3.8371

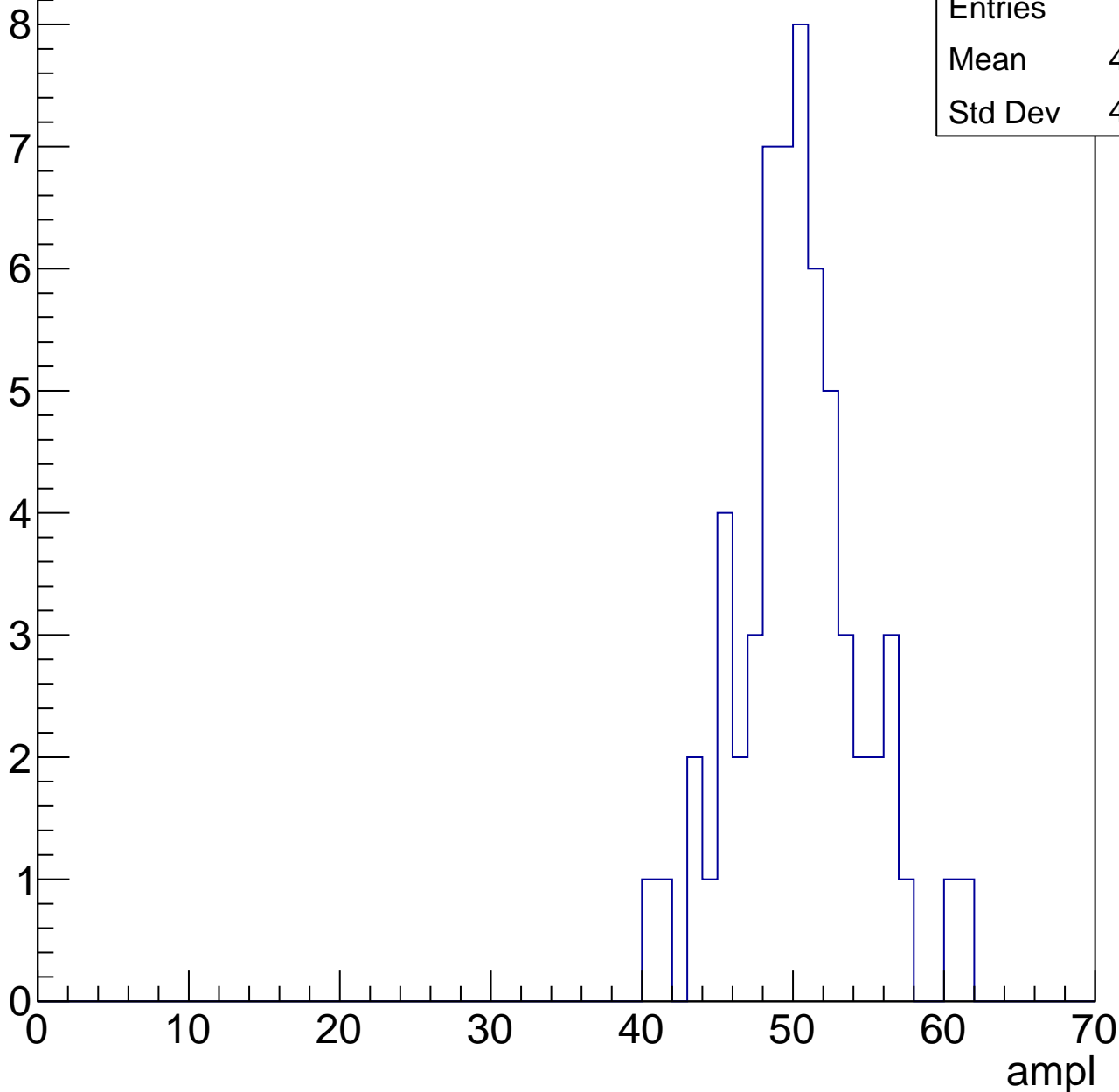


B1L103S, U20-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.87
Std Dev	4.125

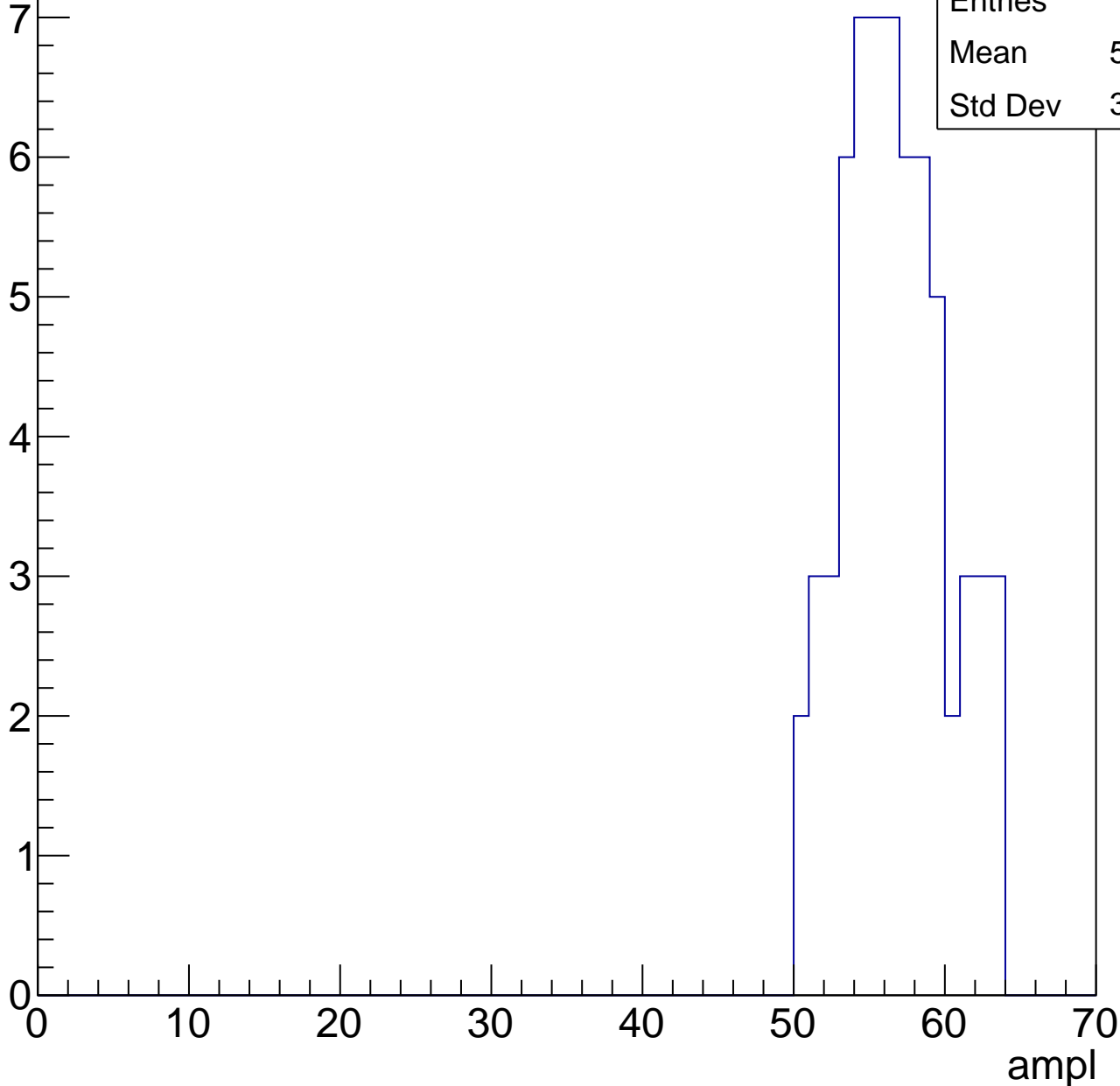


B1L103S, U20-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.27
Std Dev	3.372



B1L103S, U20-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	60.05
Std Dev	2.489

ampl

0

10

20

30

40

50

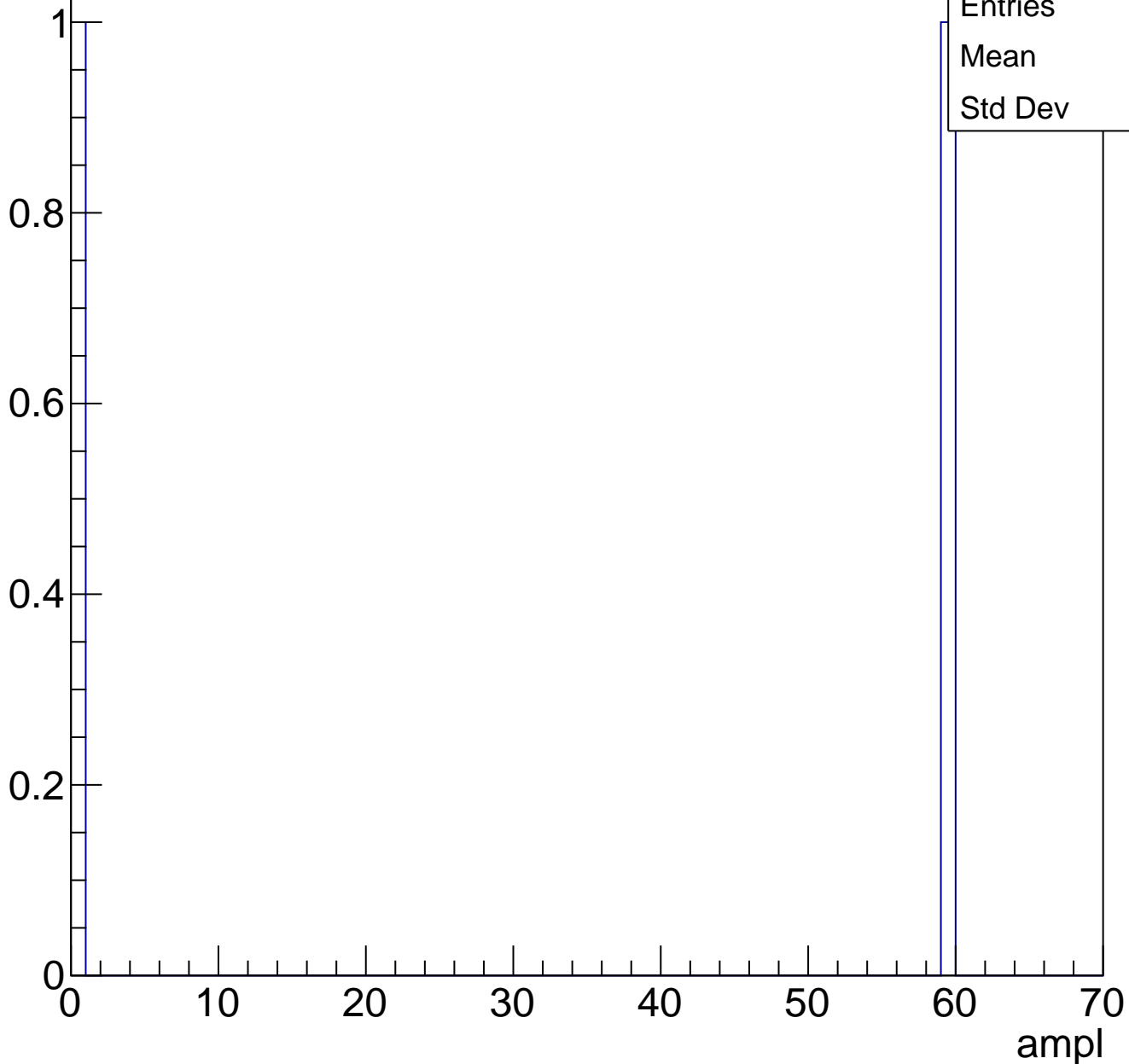
60

70

B1L103S, U20-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch11, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	30.96
Std Dev	4.946

Gaus mean : 31.4996

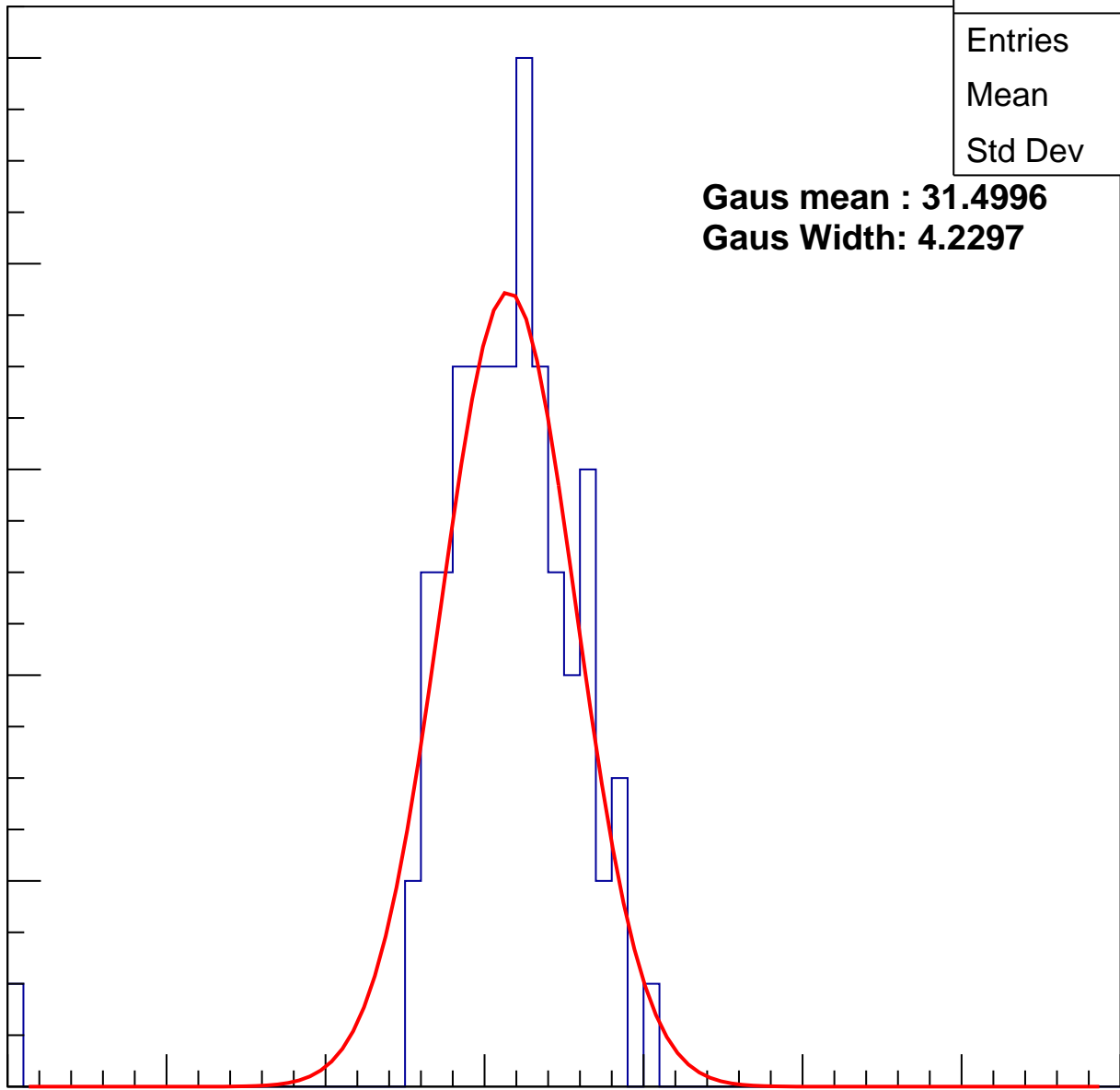
Gaus Width: 4.2297

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch11, adc1

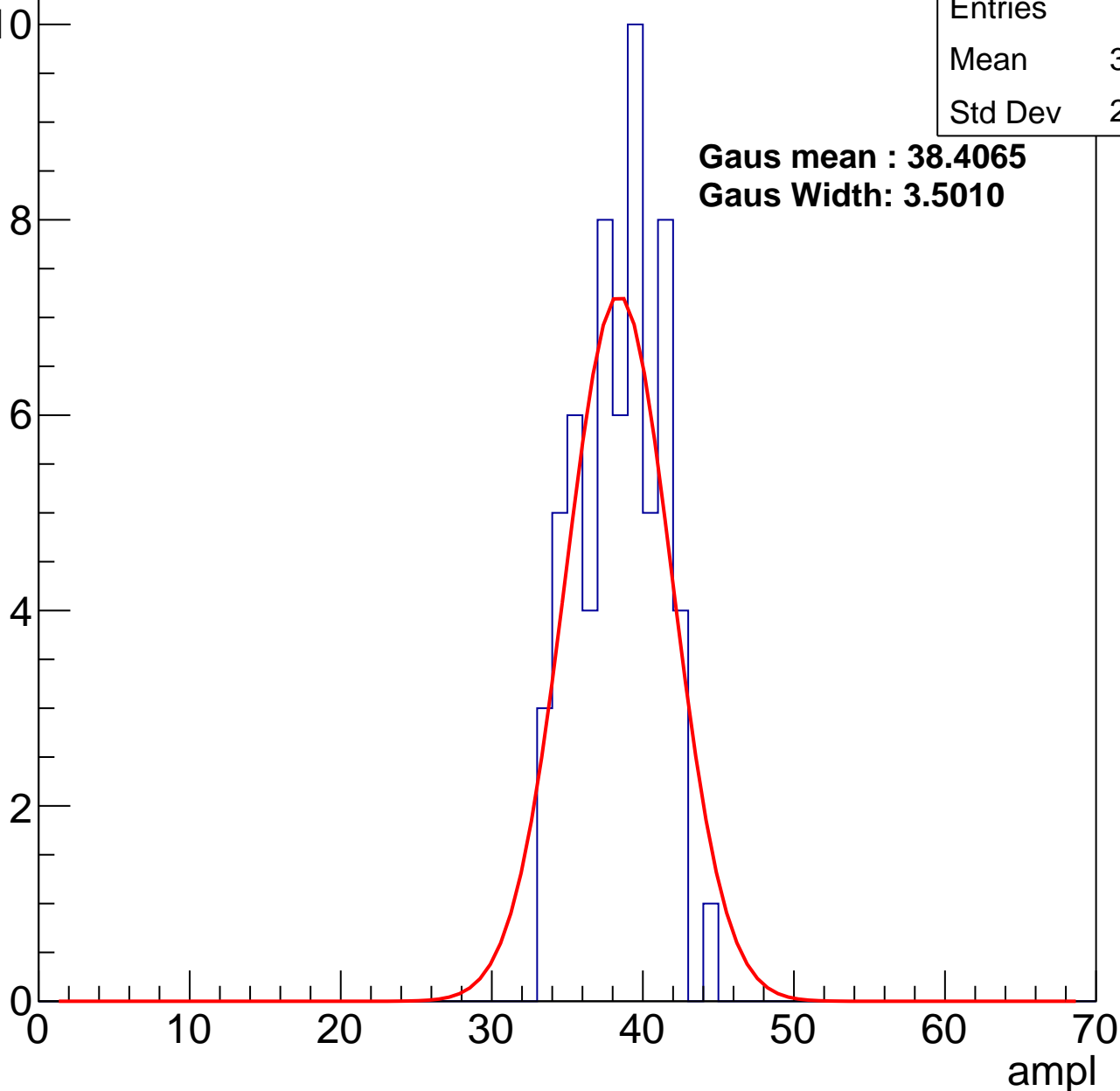
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37.95
Std Dev	2.692

Gaus mean : 38.4065

Gaus Width: 3.5010



B1L103S, U20-ch11, adc2

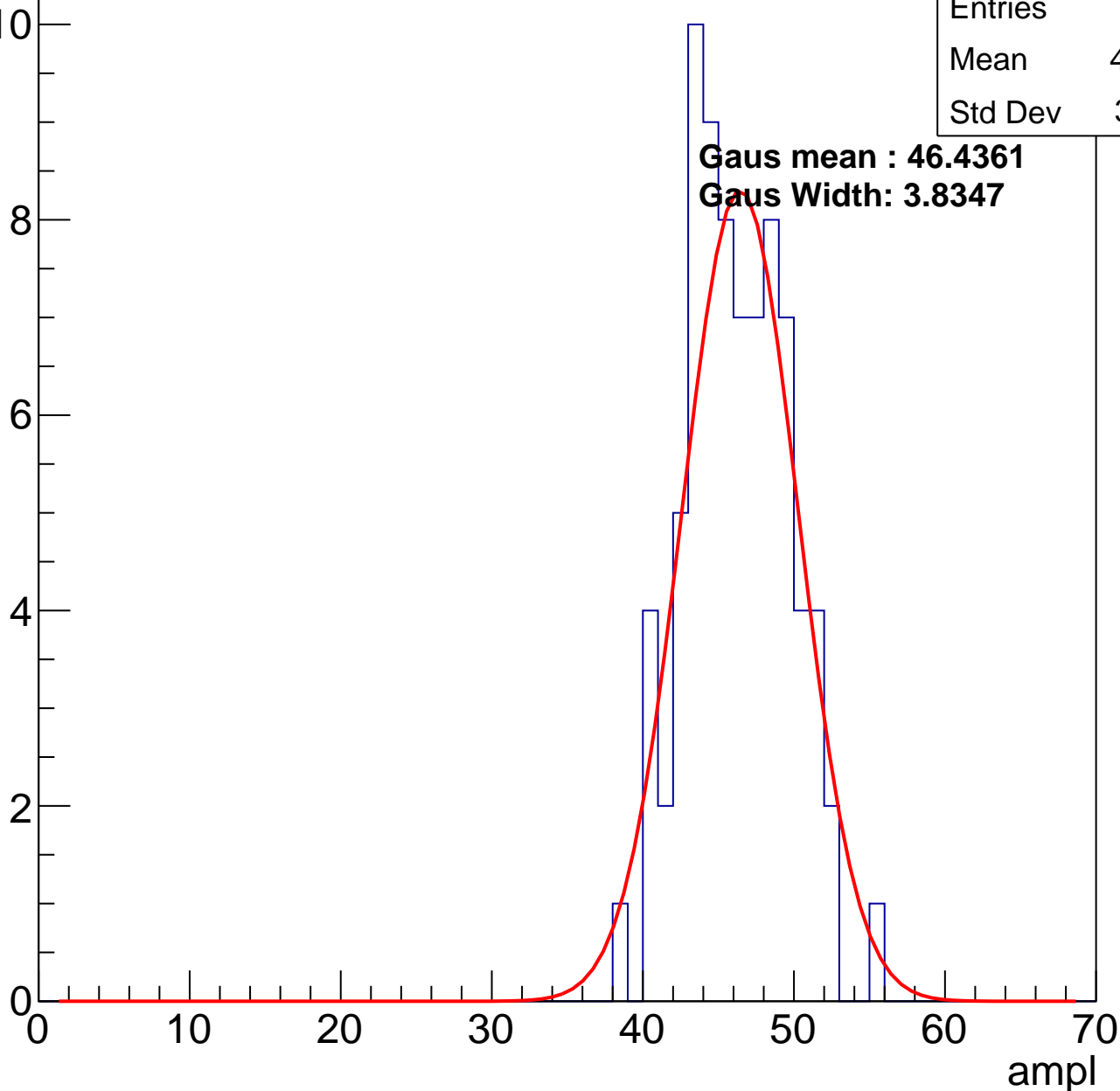
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	45.78
Std Dev	3.351

Gaus mean : 46.4361

Gaus Width: 3.8347

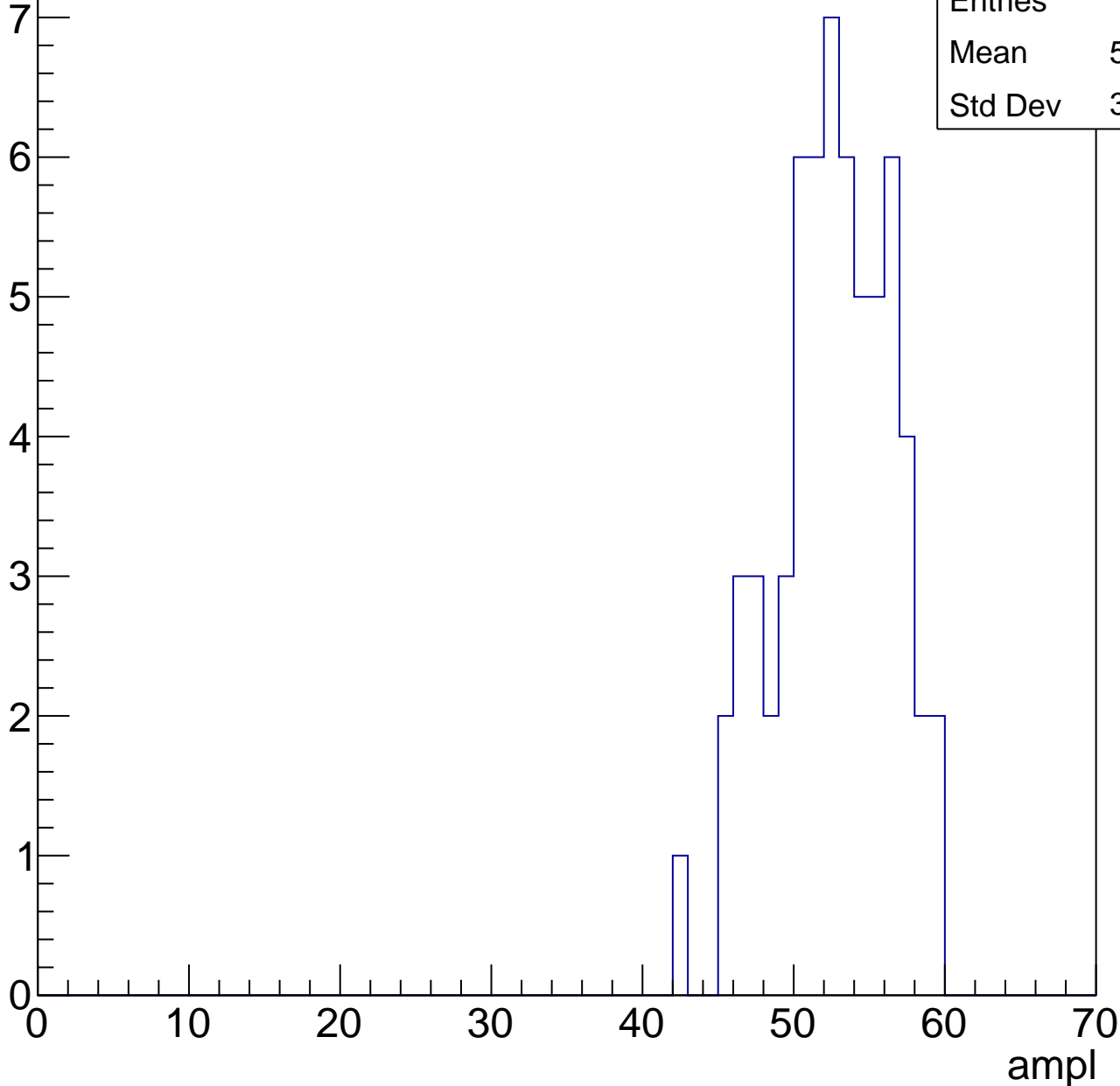


B1L103S, U20-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	52.14
Std Dev	3.792

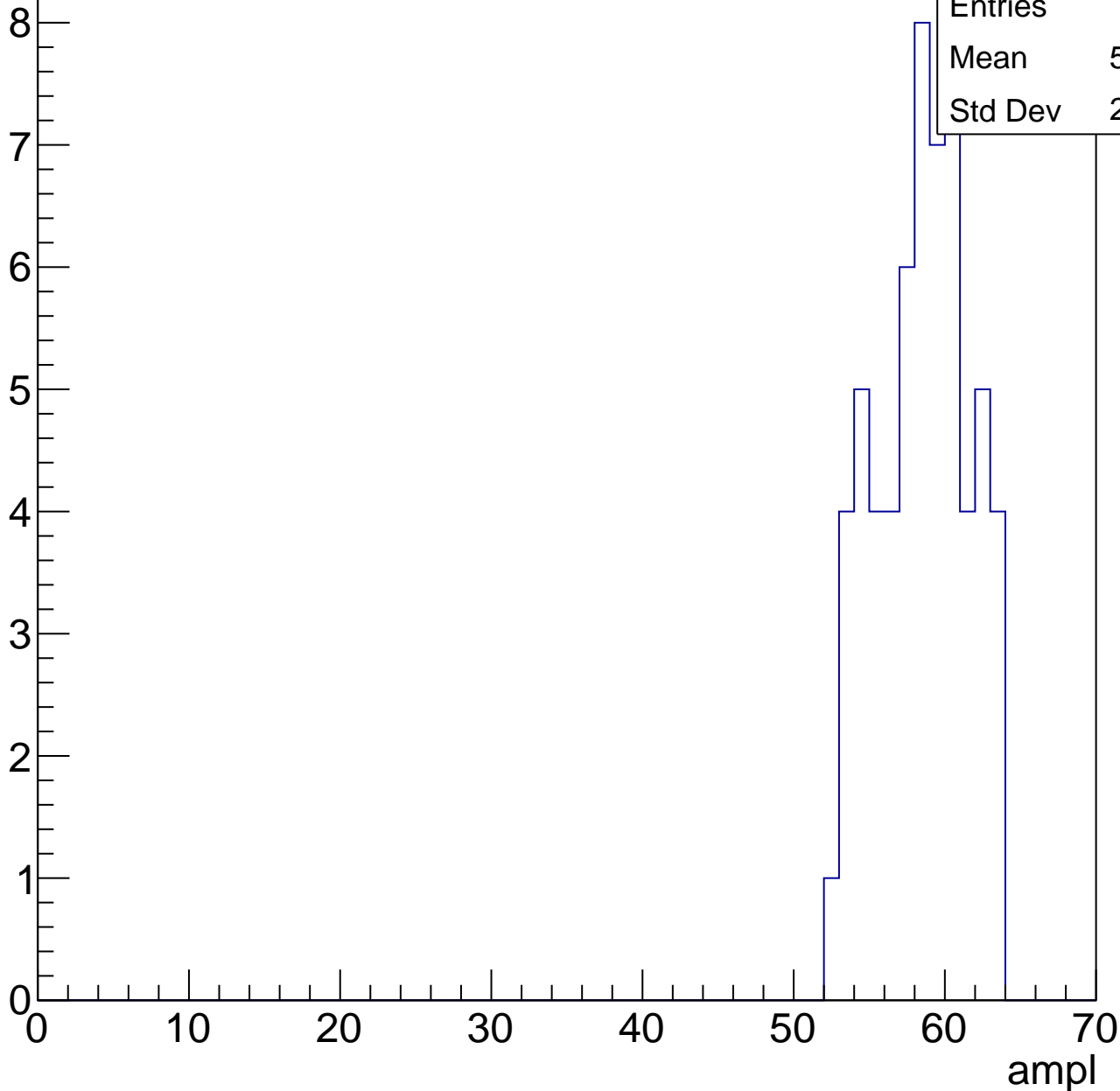


B1L103S, U20-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	58.05
Std Dev	2.969

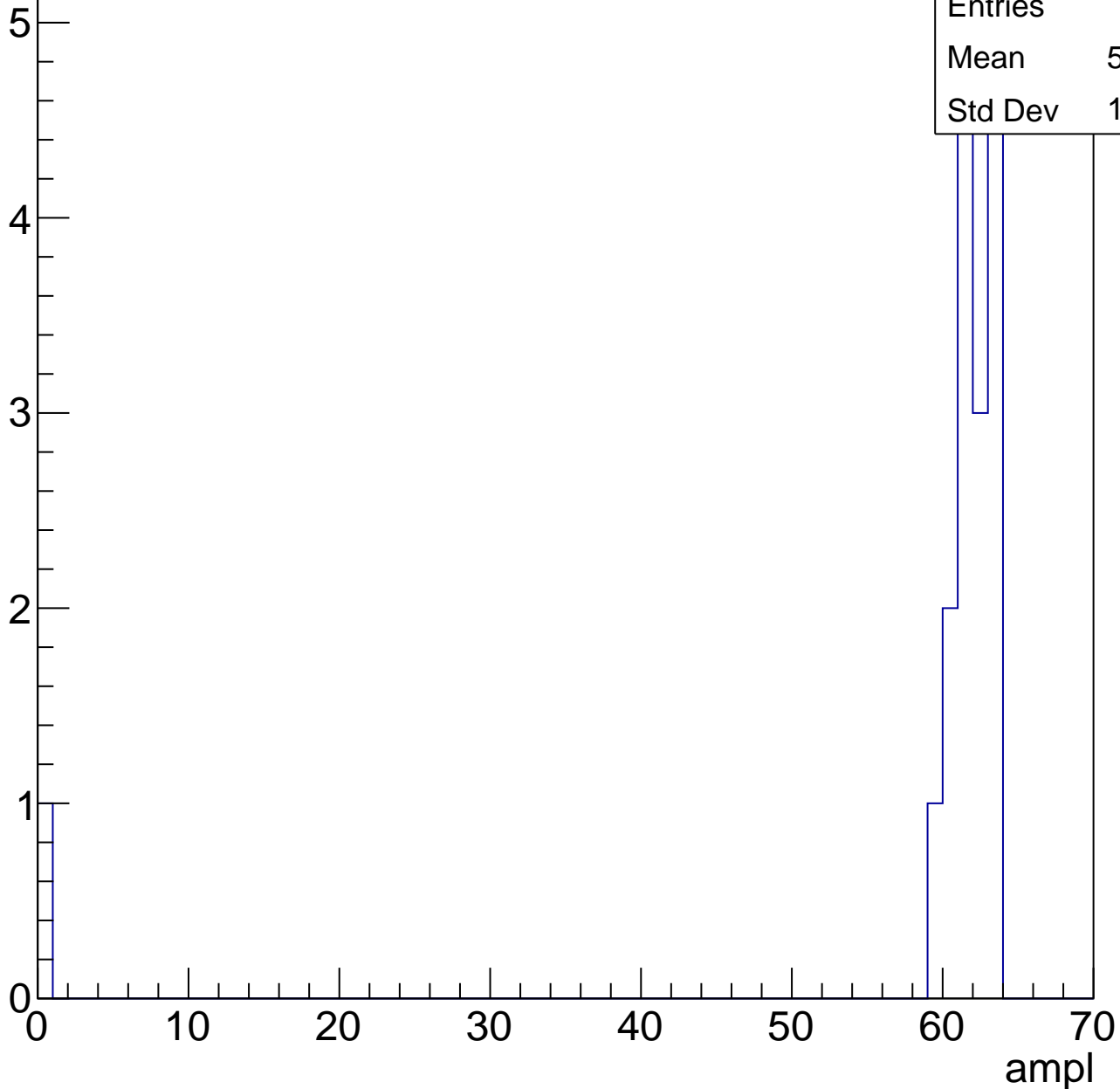


B1L103S, U20-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.94
Std Dev	14.53



B1L103S, U20-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch12, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	66
Mean	30.23
Std Dev	3.919

Gaus mean : 30.8809

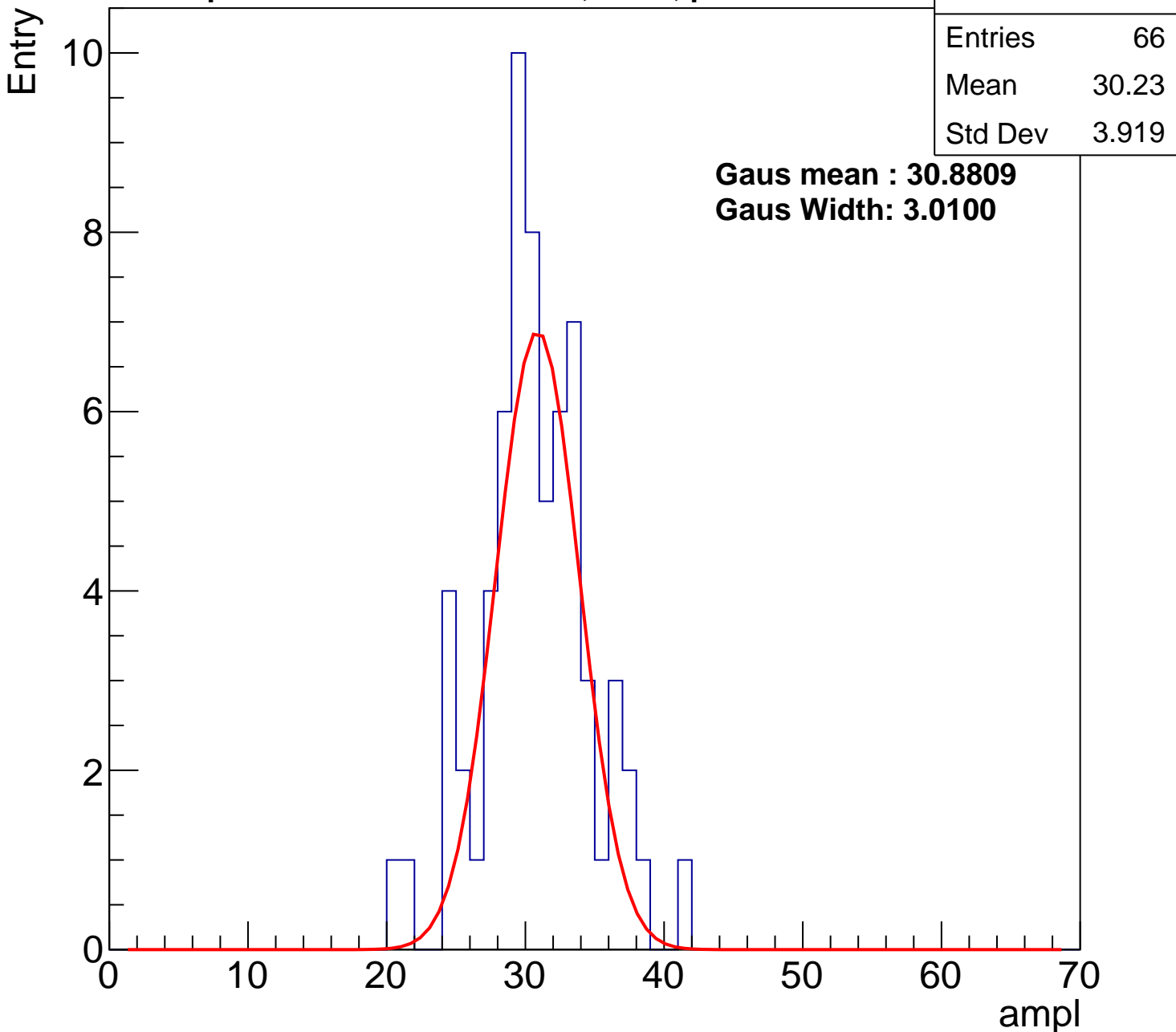
Gaus Width: 3.0100

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch12, adc1

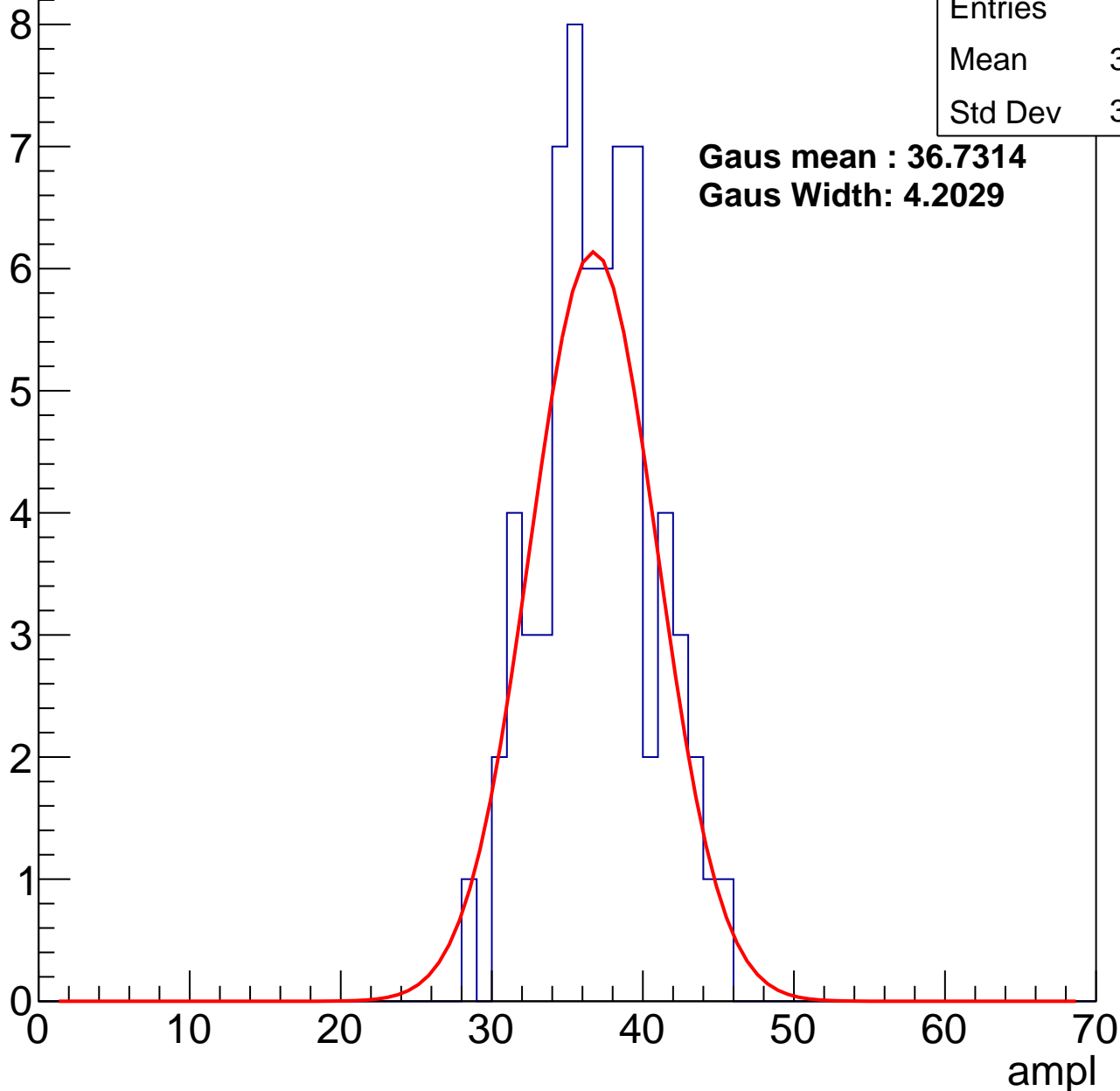
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.52
Std Dev	3.683

Gaus mean : 36.7314

Gaus Width: 4.2029



B1L103S, U20-ch12, adc2

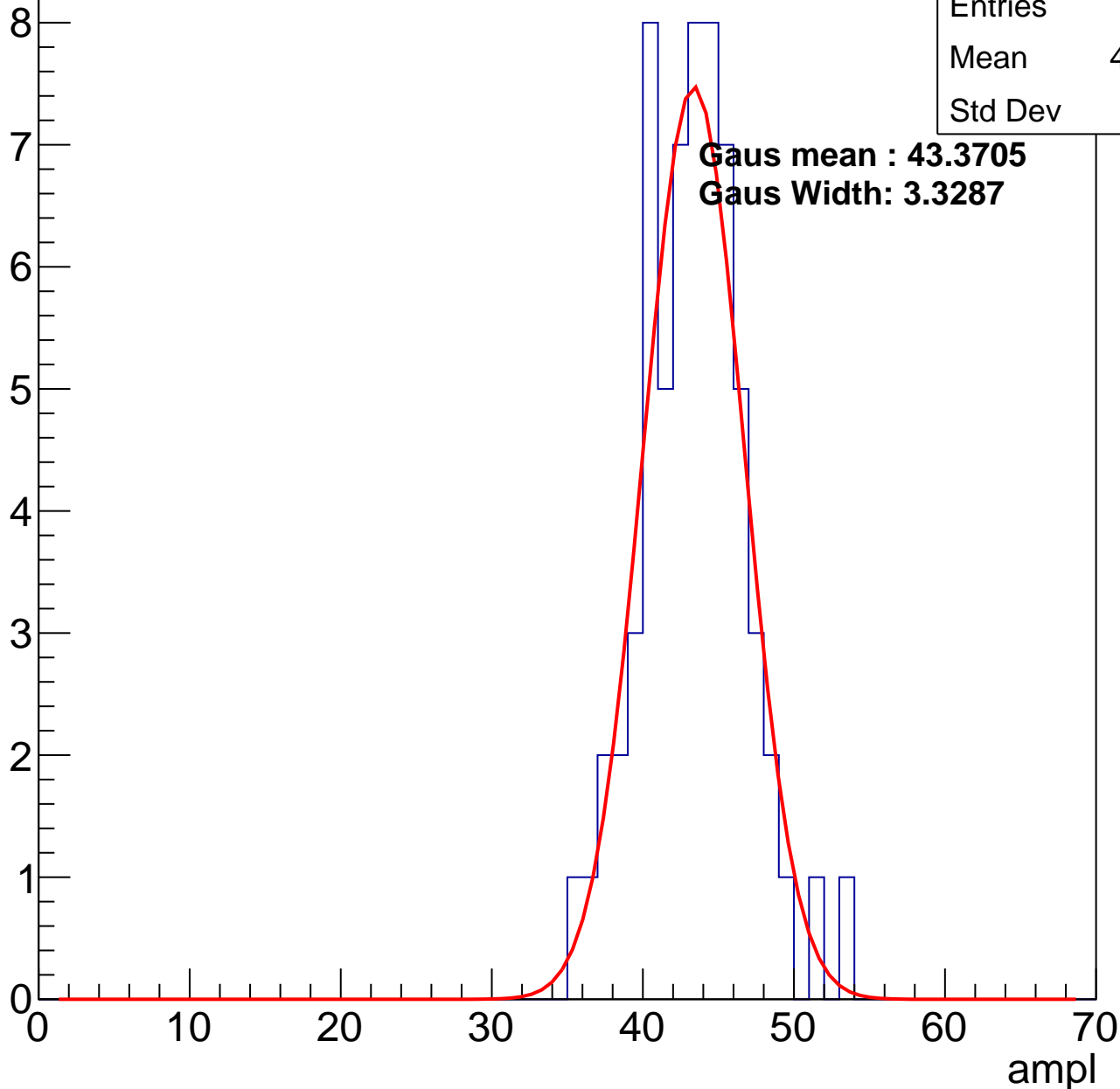
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.89
Std Dev	3.42

Gaus mean : 43.3705

Gaus Width: 3.3287

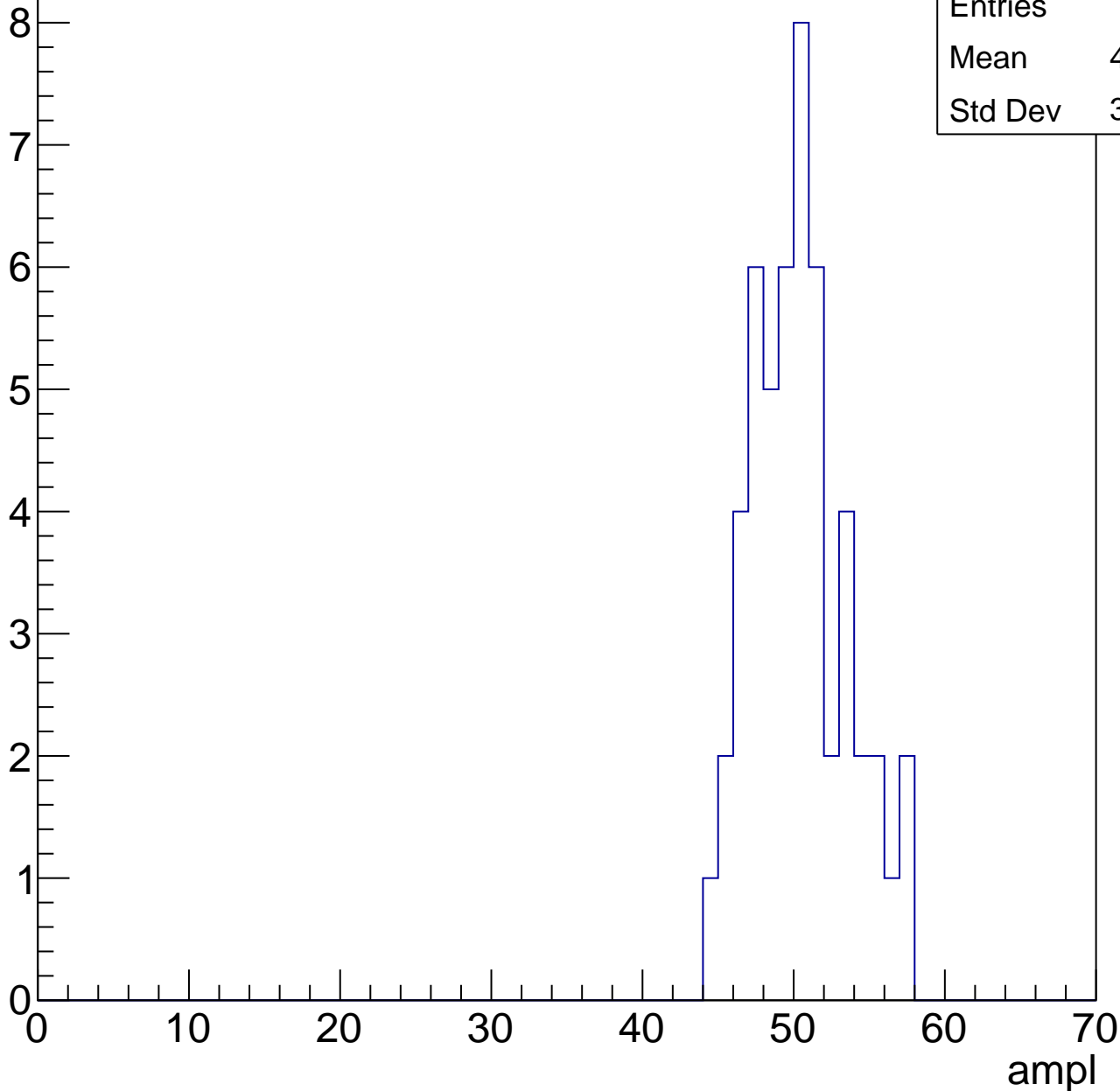


B1L103S, U20-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	49.88
Std Dev	3.116

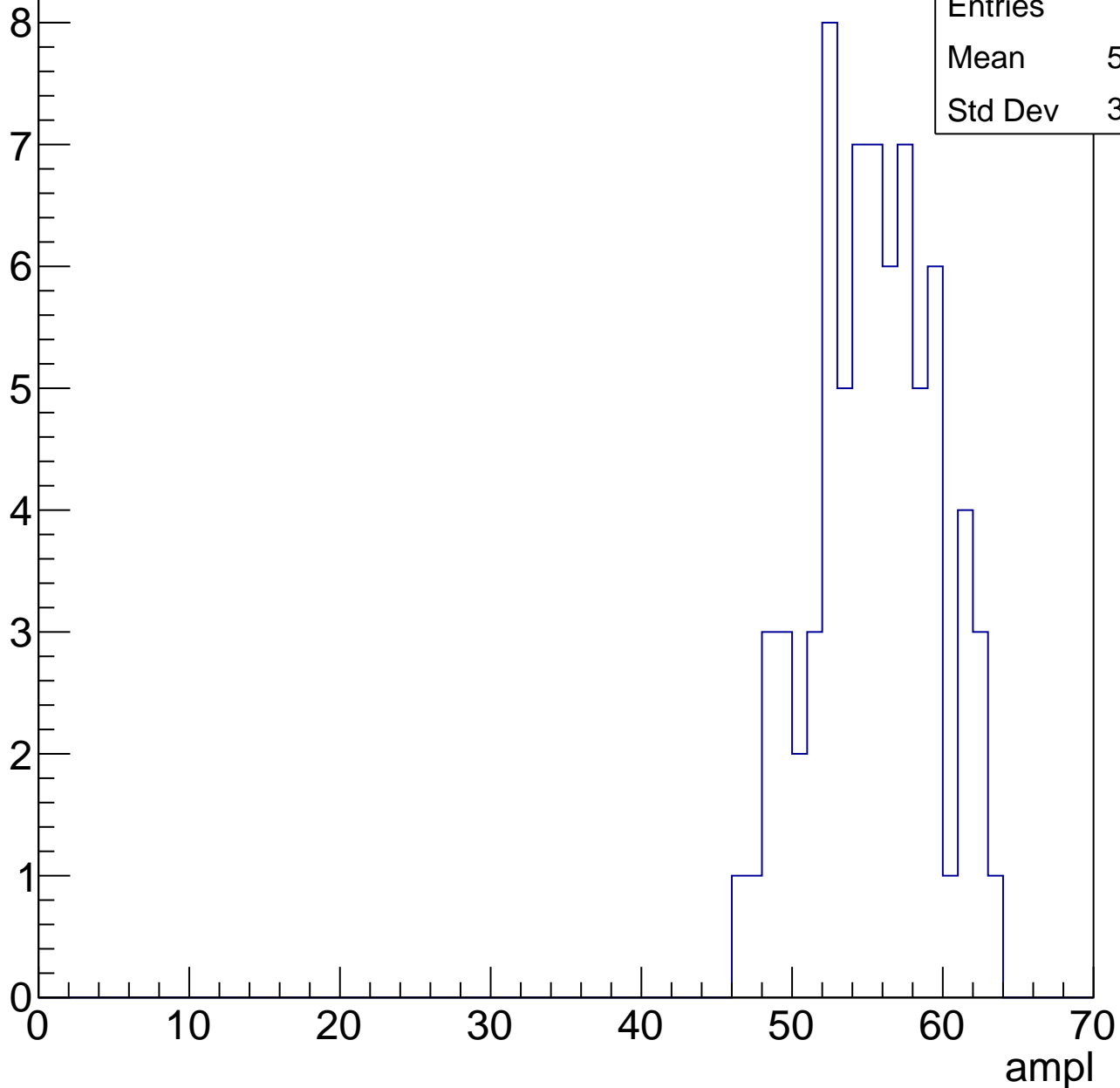


B1L103S, U20-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	54.97
Std Dev	3.972

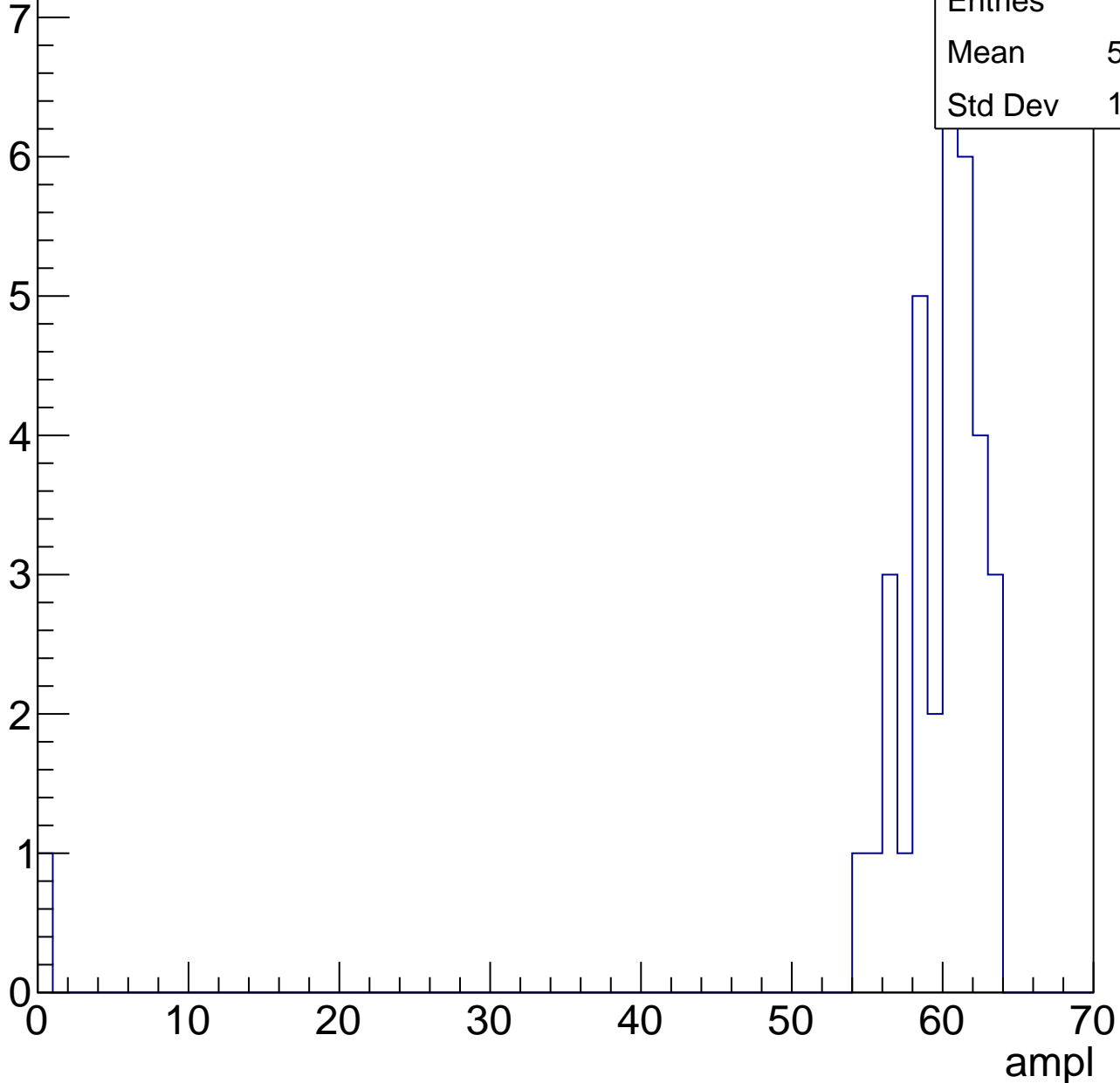


B1L103S, U20-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	57.79
Std Dev	10.32

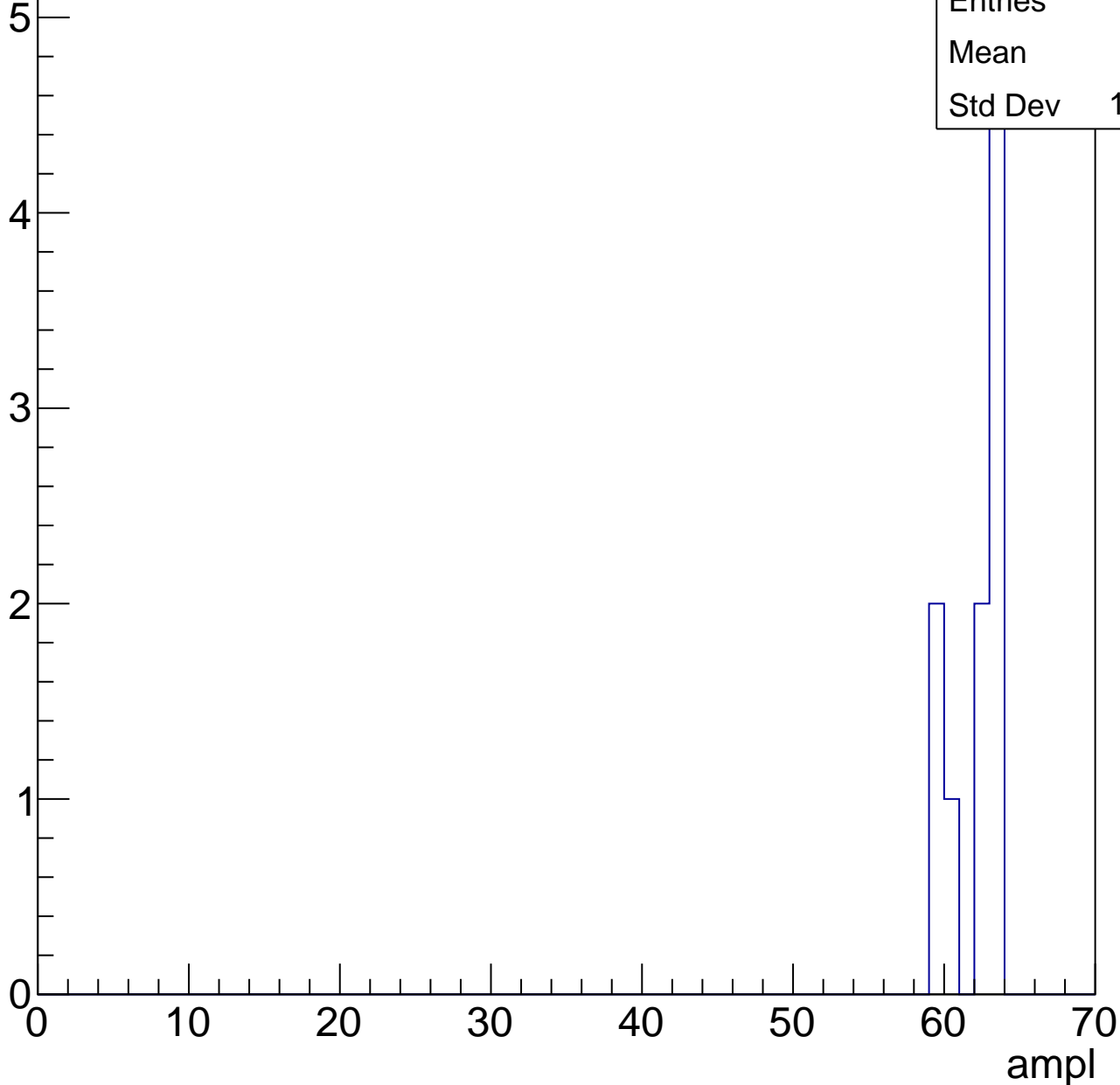


B1L103S, U20-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

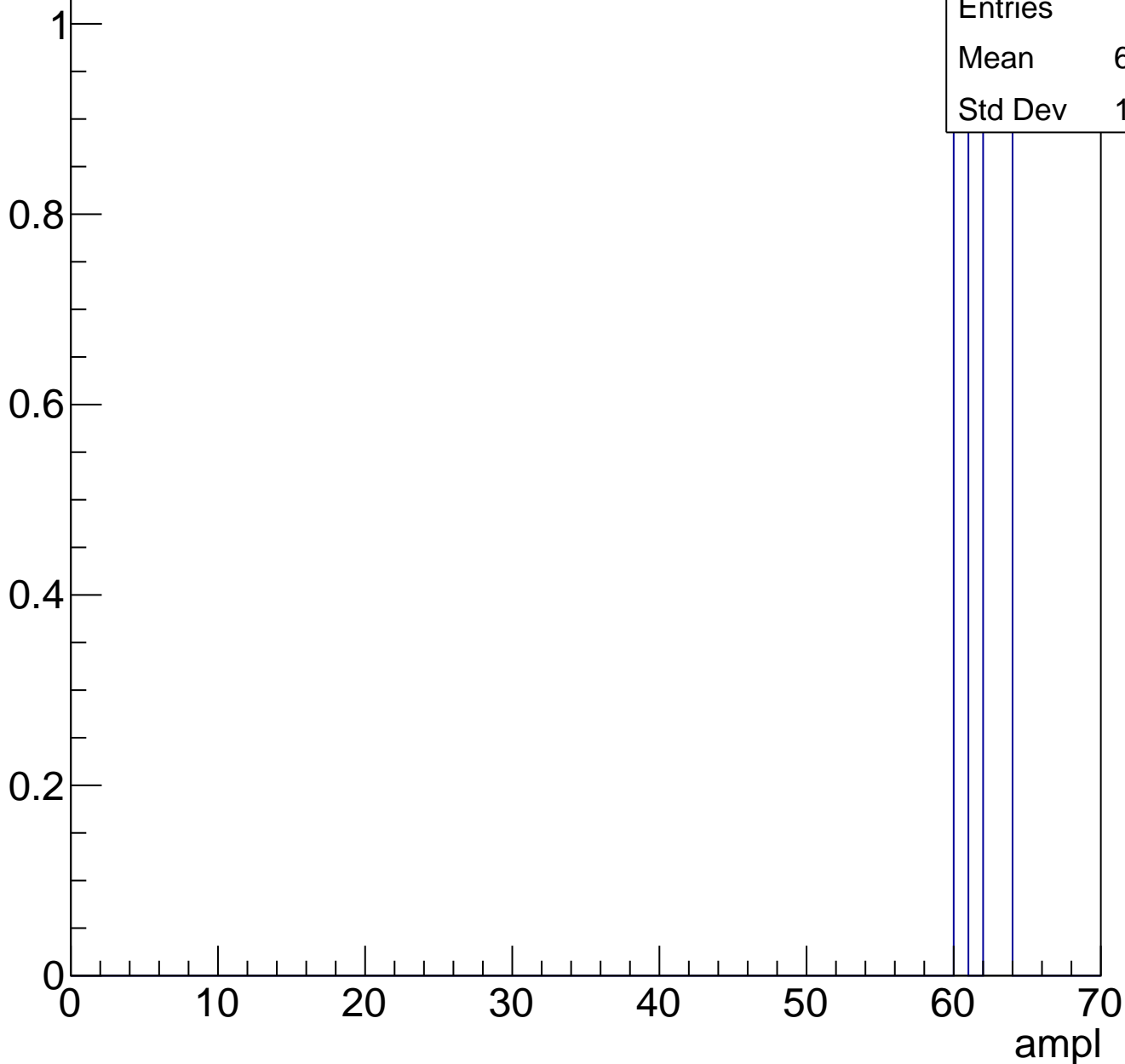
Entries	10
Mean	61.7
Std Dev	1.616



B1L103S, U20-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch13, adc0

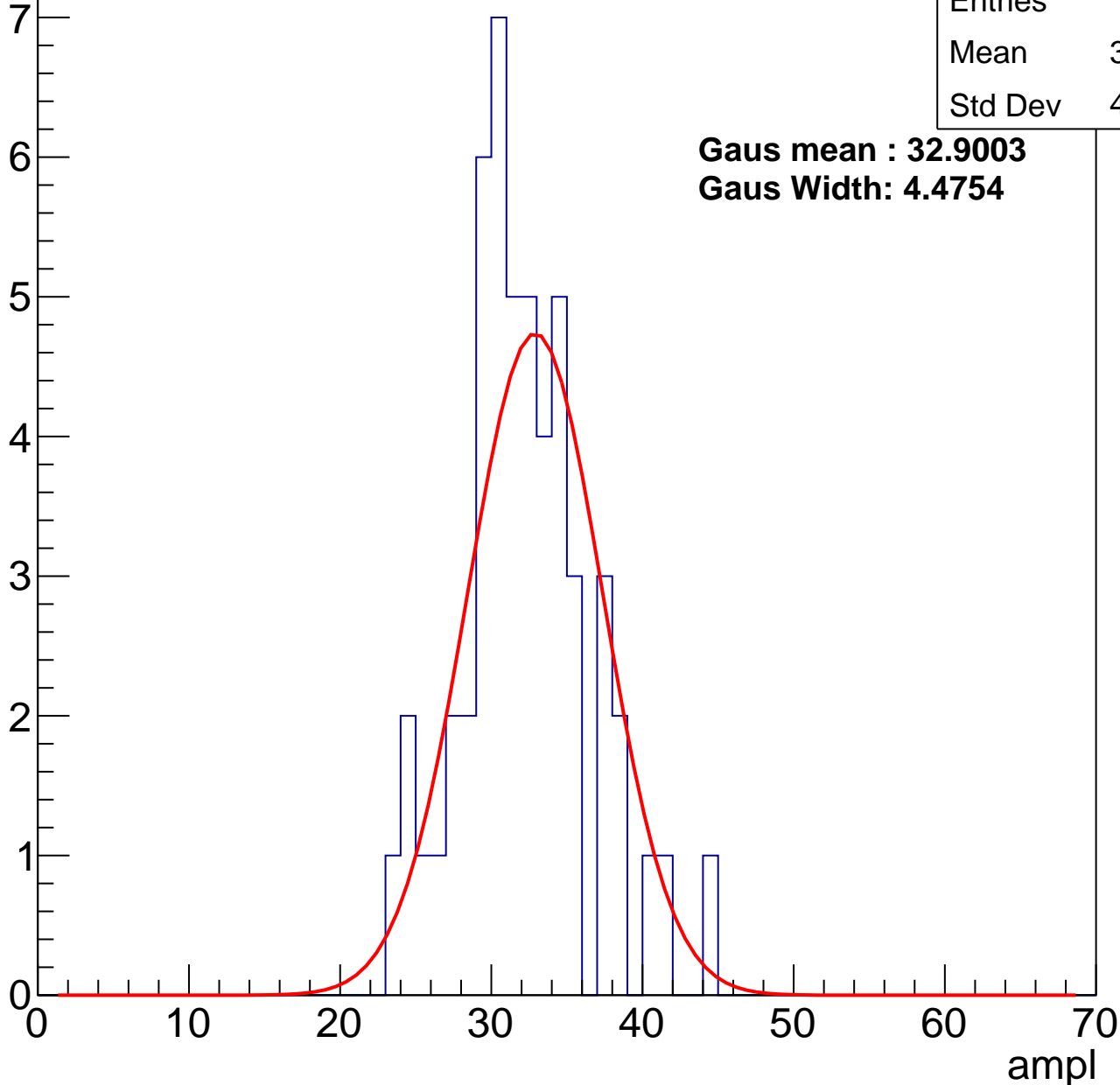
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	31.73
Std Dev	4.266

Gaus mean : 32.9003

Gaus Width: 4.4754



B1L103S, U20-ch13, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	90
Mean	37.54
Std Dev	4.113

Gaus mean : 38.1163

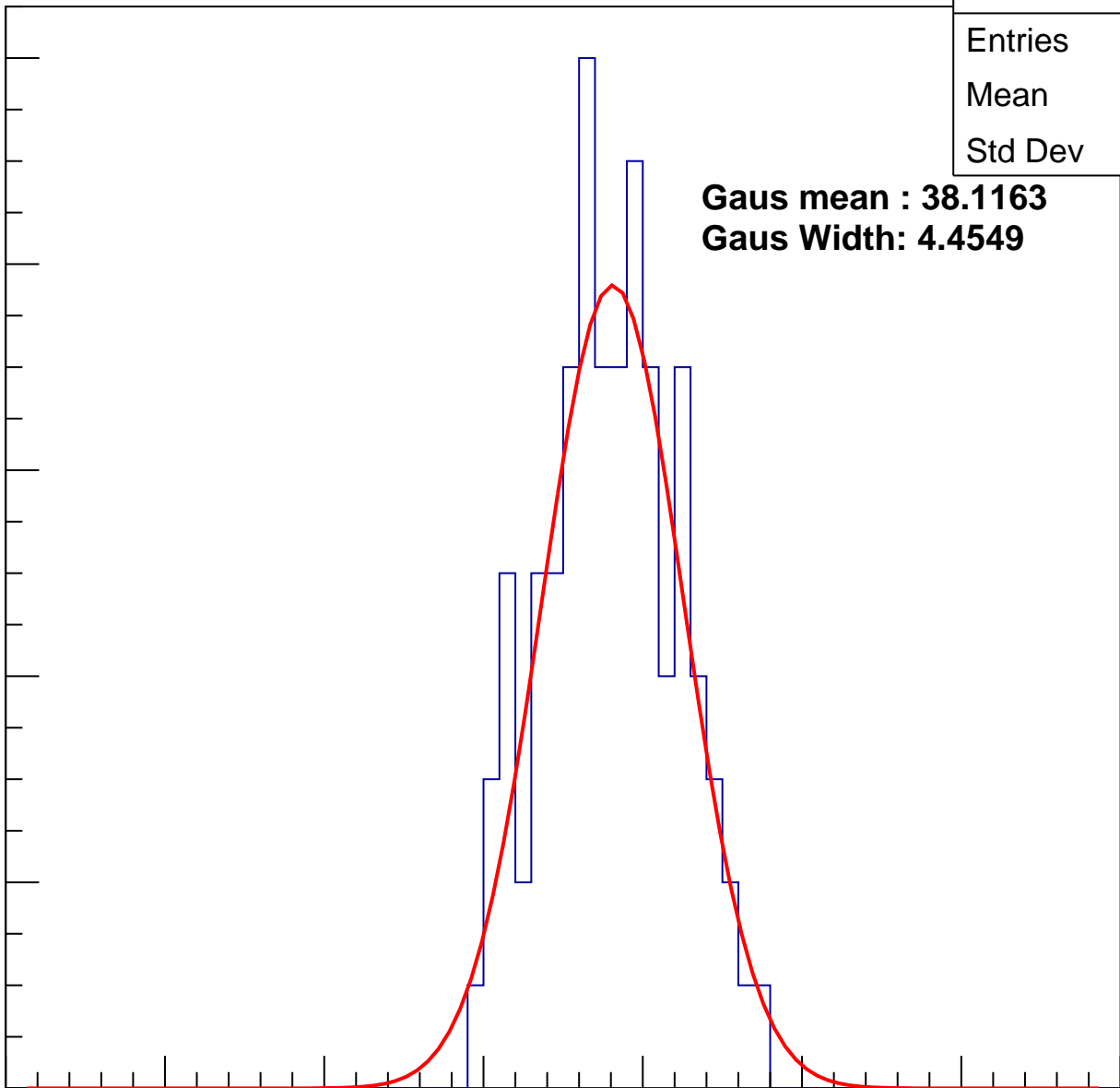
Gaus Width: 4.4549

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch13, adc2

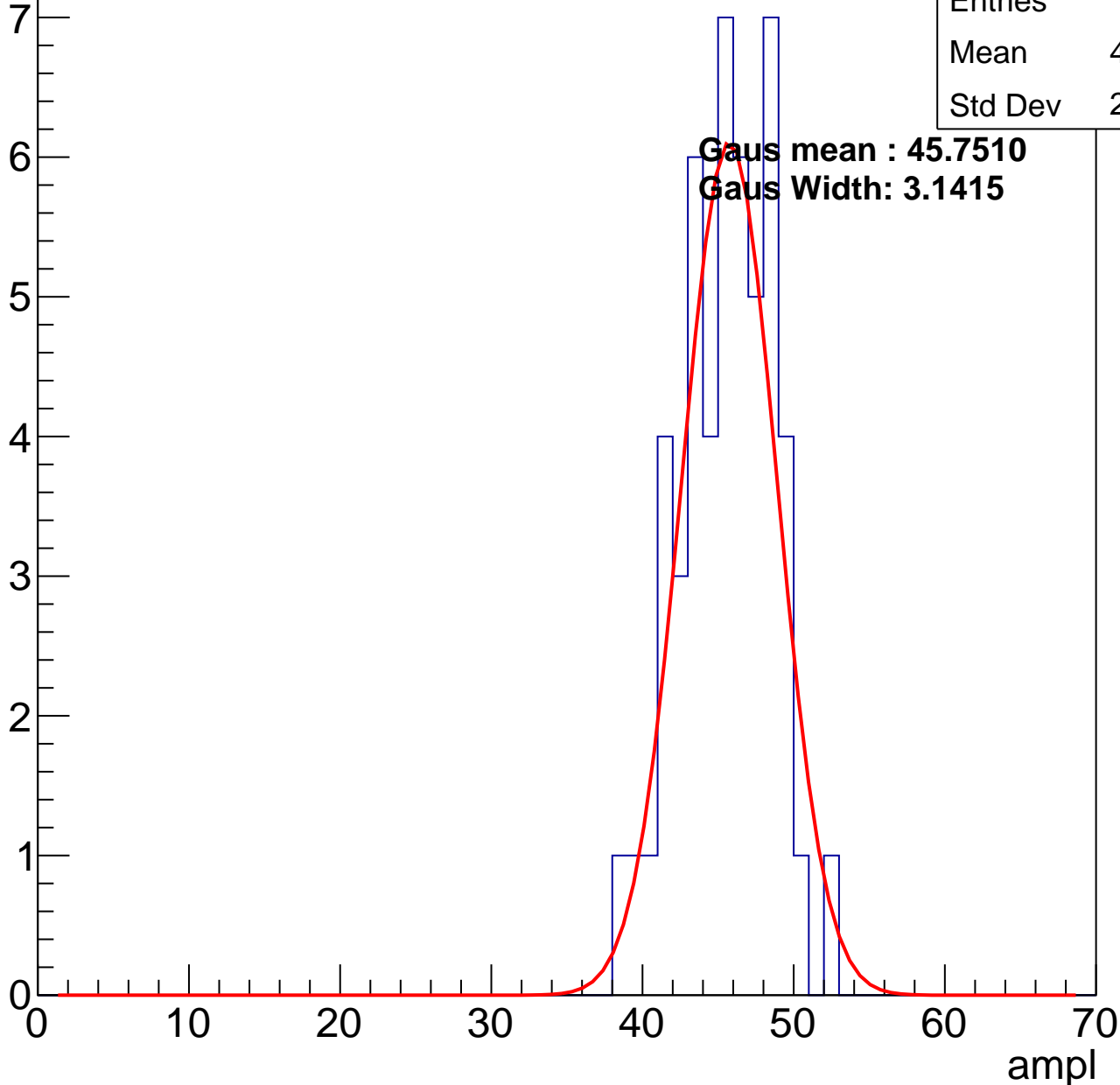
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	45.12
Std Dev	2.988

Gaus mean : 45.7510

Gaus Width: 3.1415

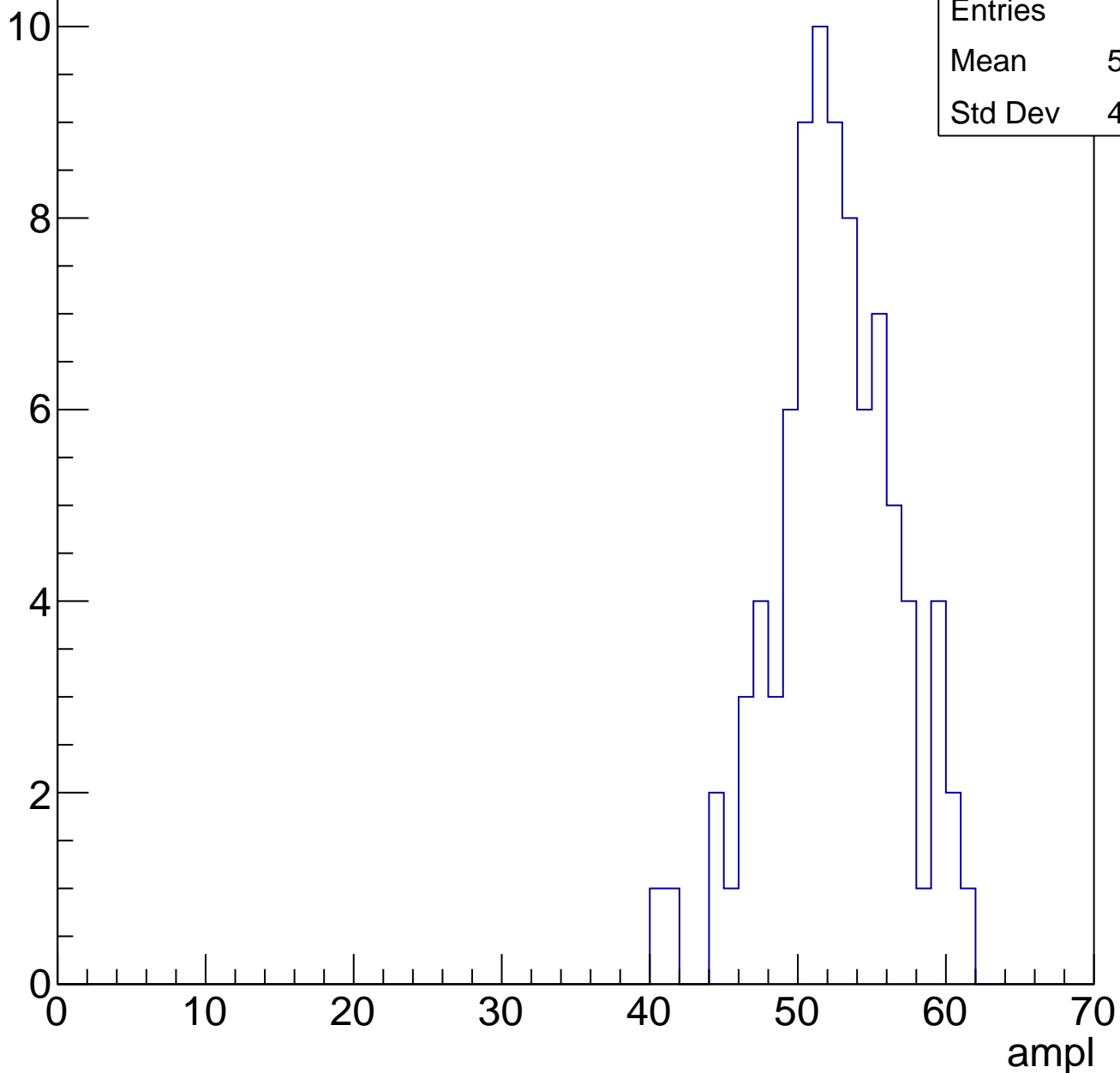


B1L103S, U20-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	51.98
Std Dev	4.172

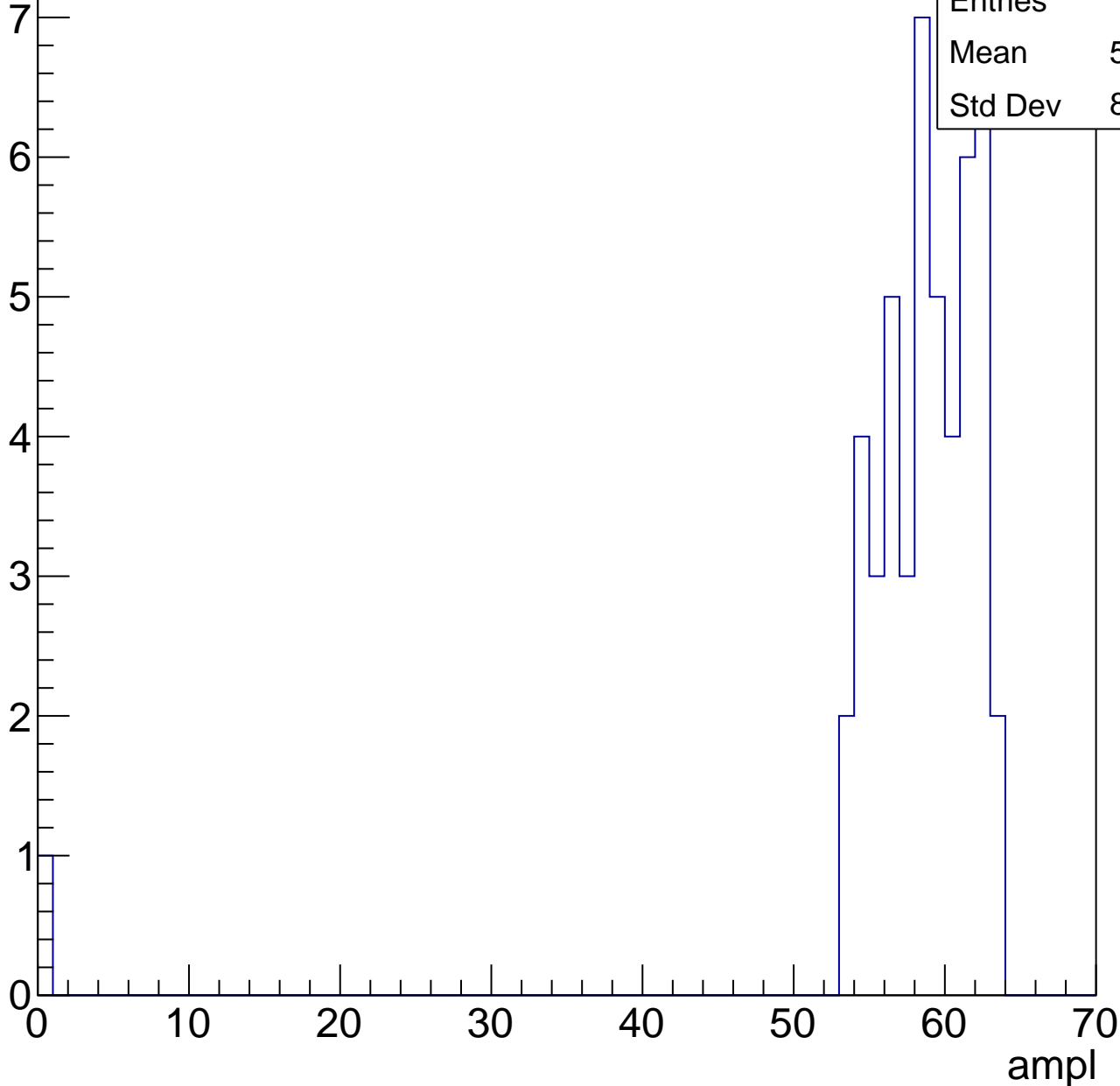


B1L103S, U20-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.24
Std Dev	8.733

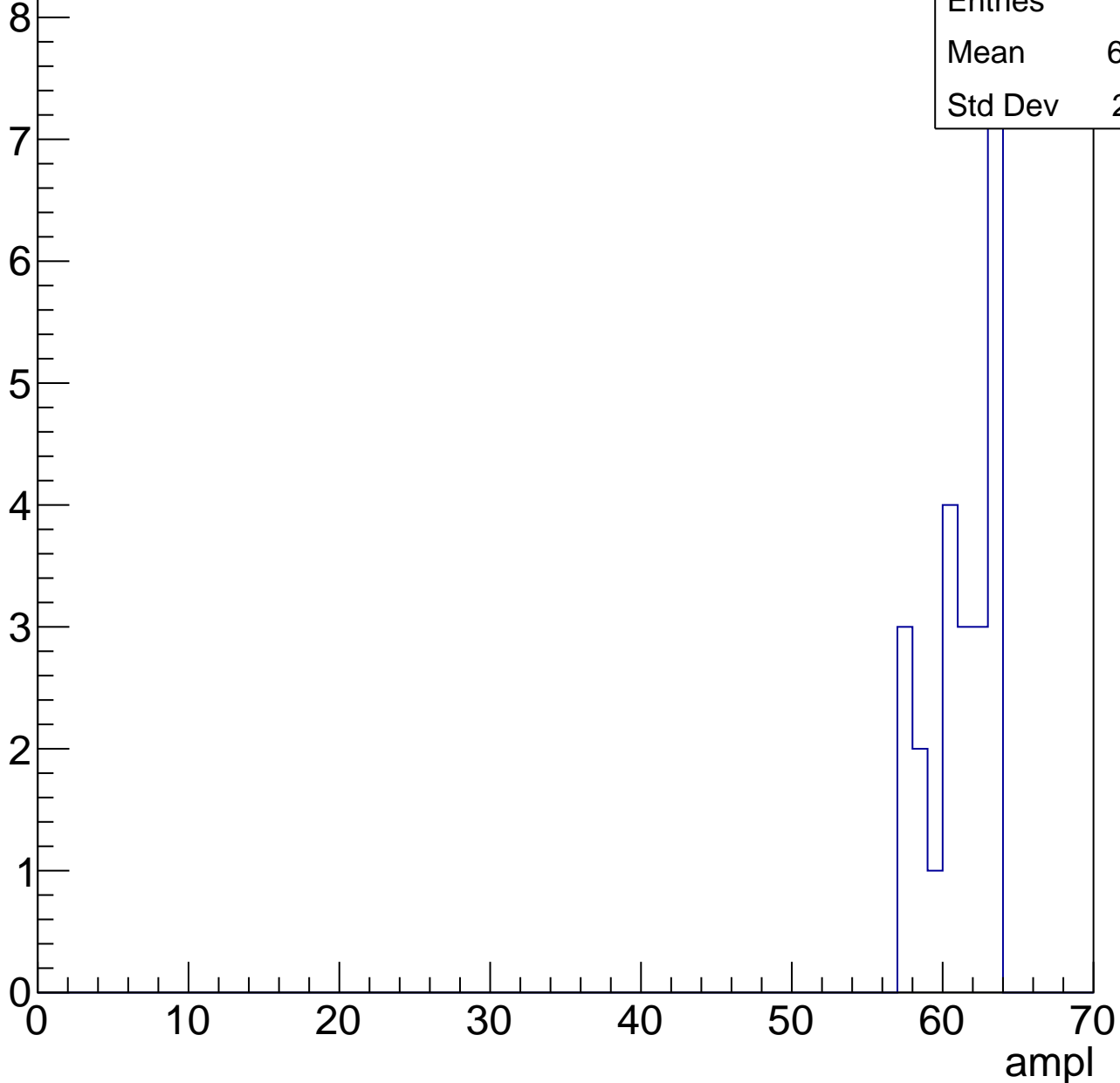


B1L103S, U20-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	60.79
Std Dev	2.121



B1L103S, U20-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10
Std Dev	10

B1L103S, U20-ch14, adc0

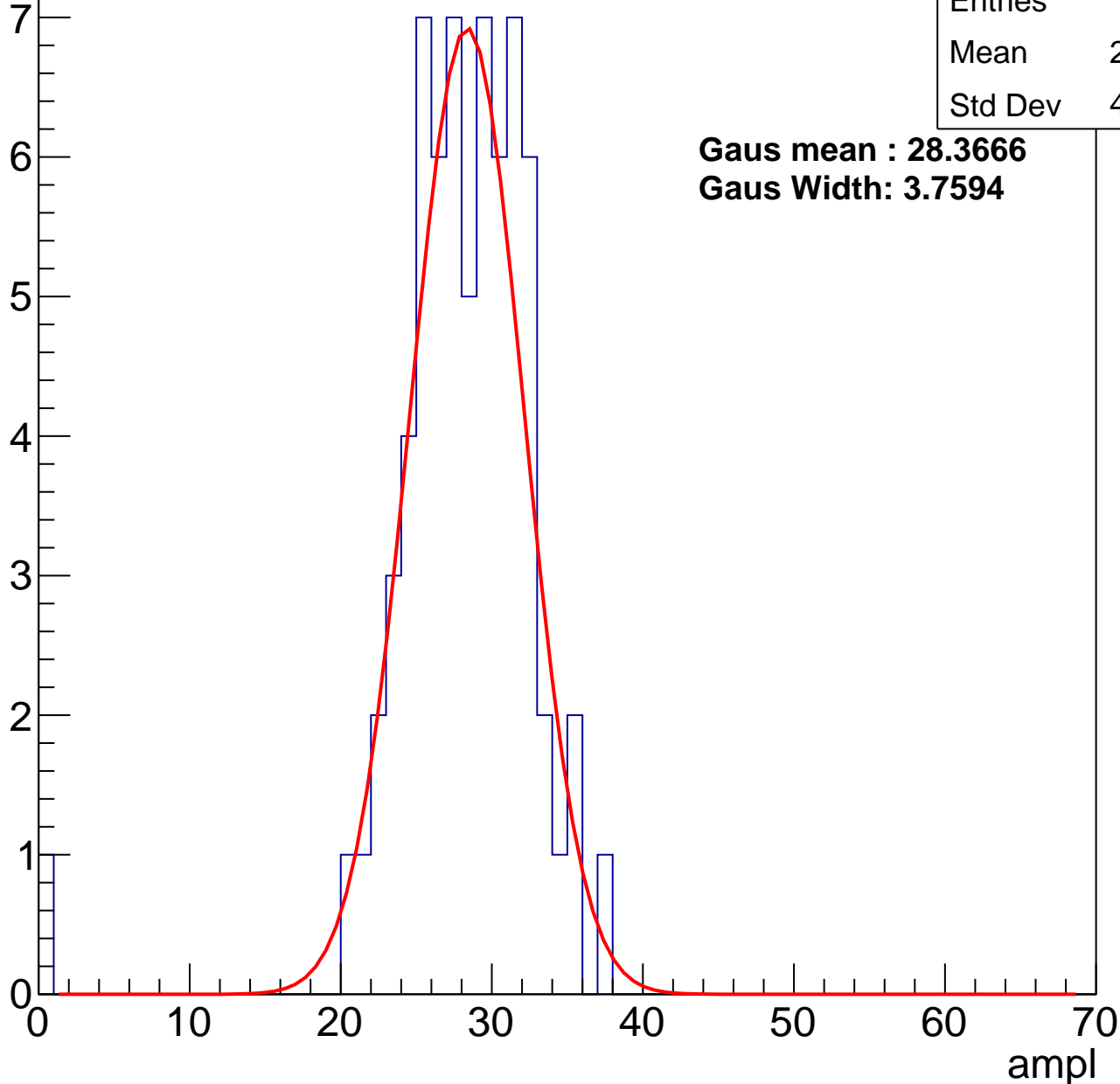
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.67
Std Dev	4.889

Gaus mean : 28.3666

Gaus Width: 3.7594



B1L103S, U20-ch14, adc1

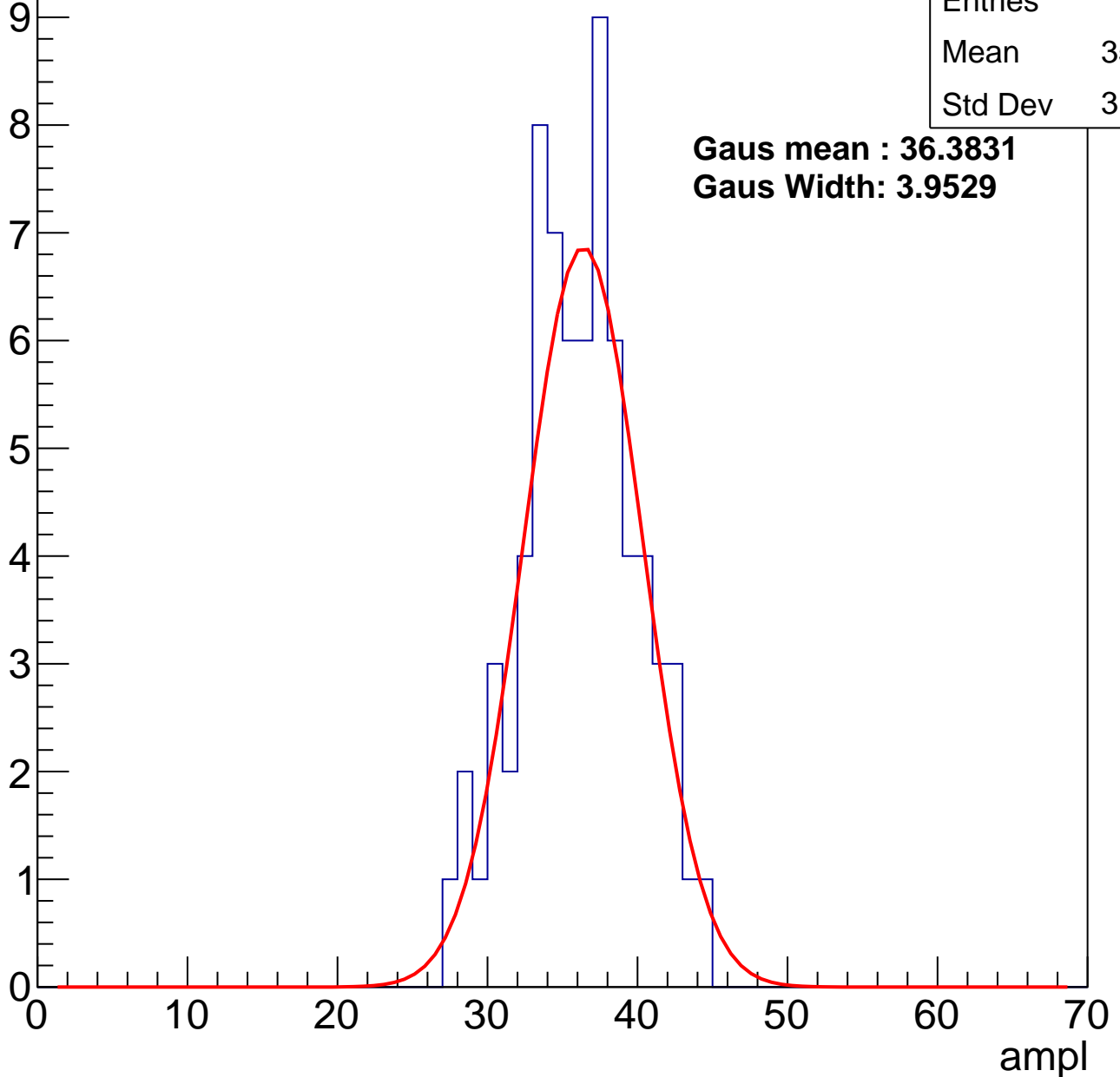
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.68
Std Dev	3.767

Gaus mean : 36.3831

Gaus Width: 3.9529



B1L103S, U20-ch14, adc2

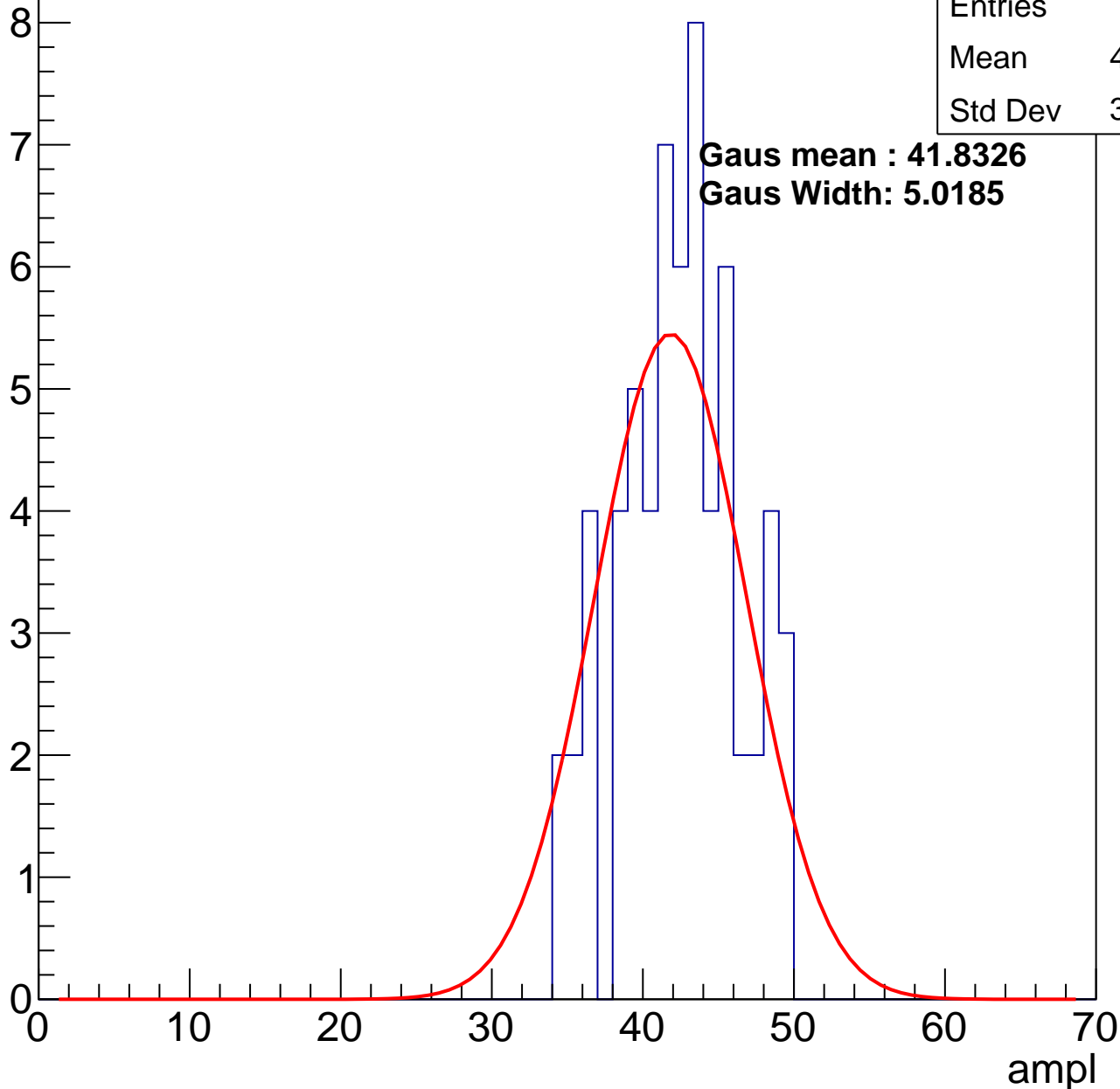
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.95
Std Dev	3.889

Gaus mean : 41.8326

Gaus Width: 5.0185

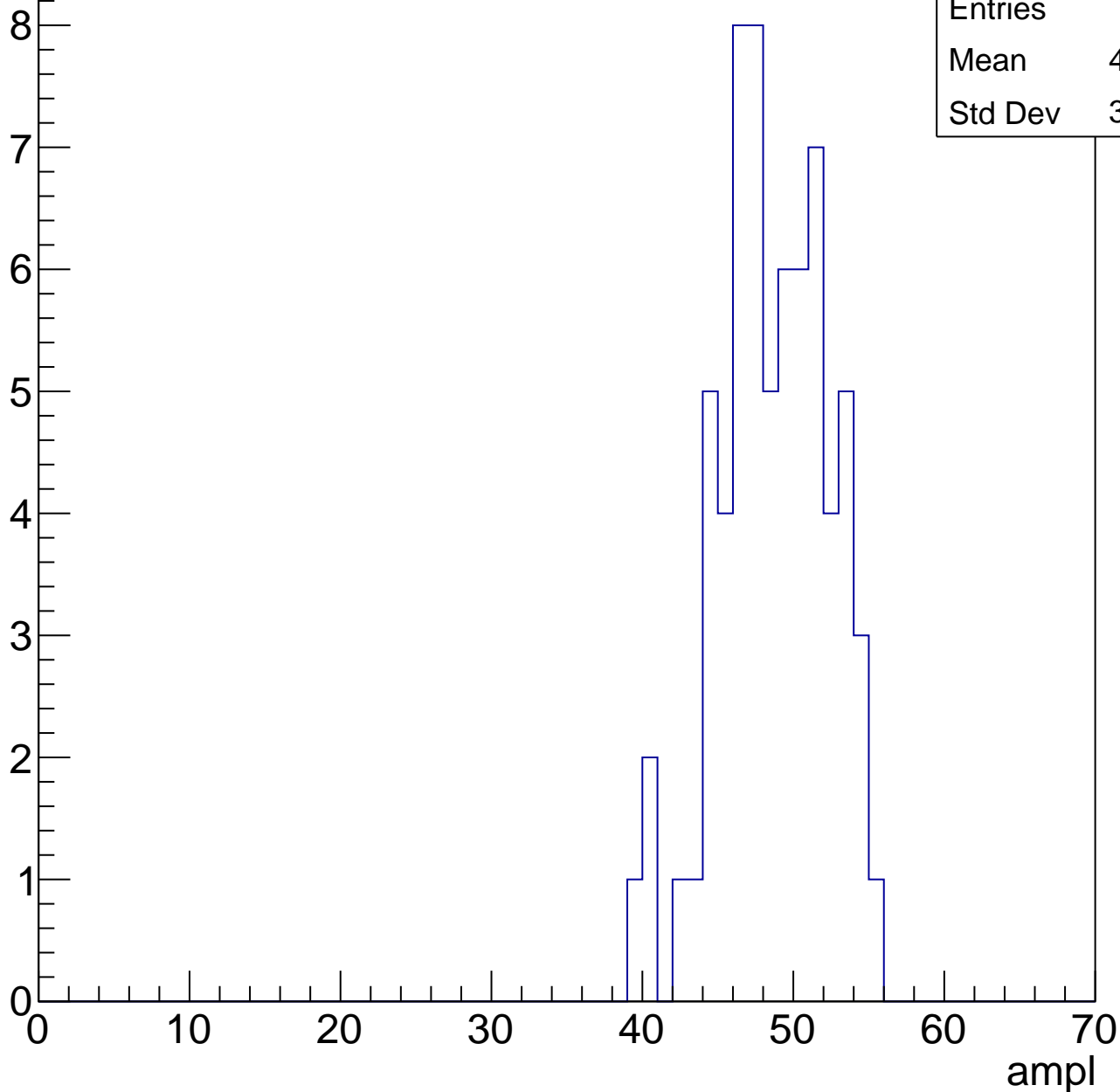


B1L103S, U20-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.19
Std Dev	3.588

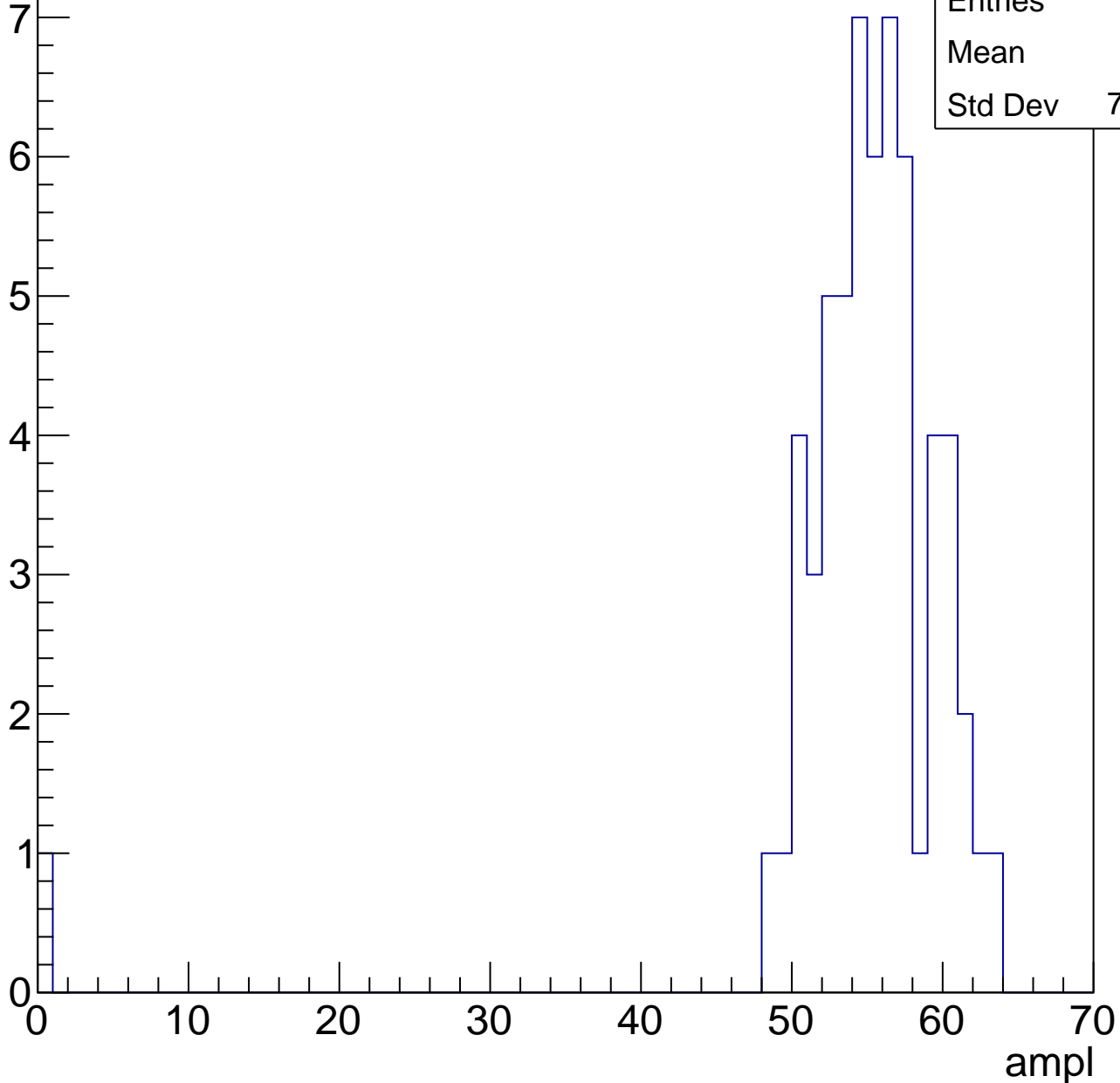


B1L103S, U20-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	54.2
Std Dev	7.899

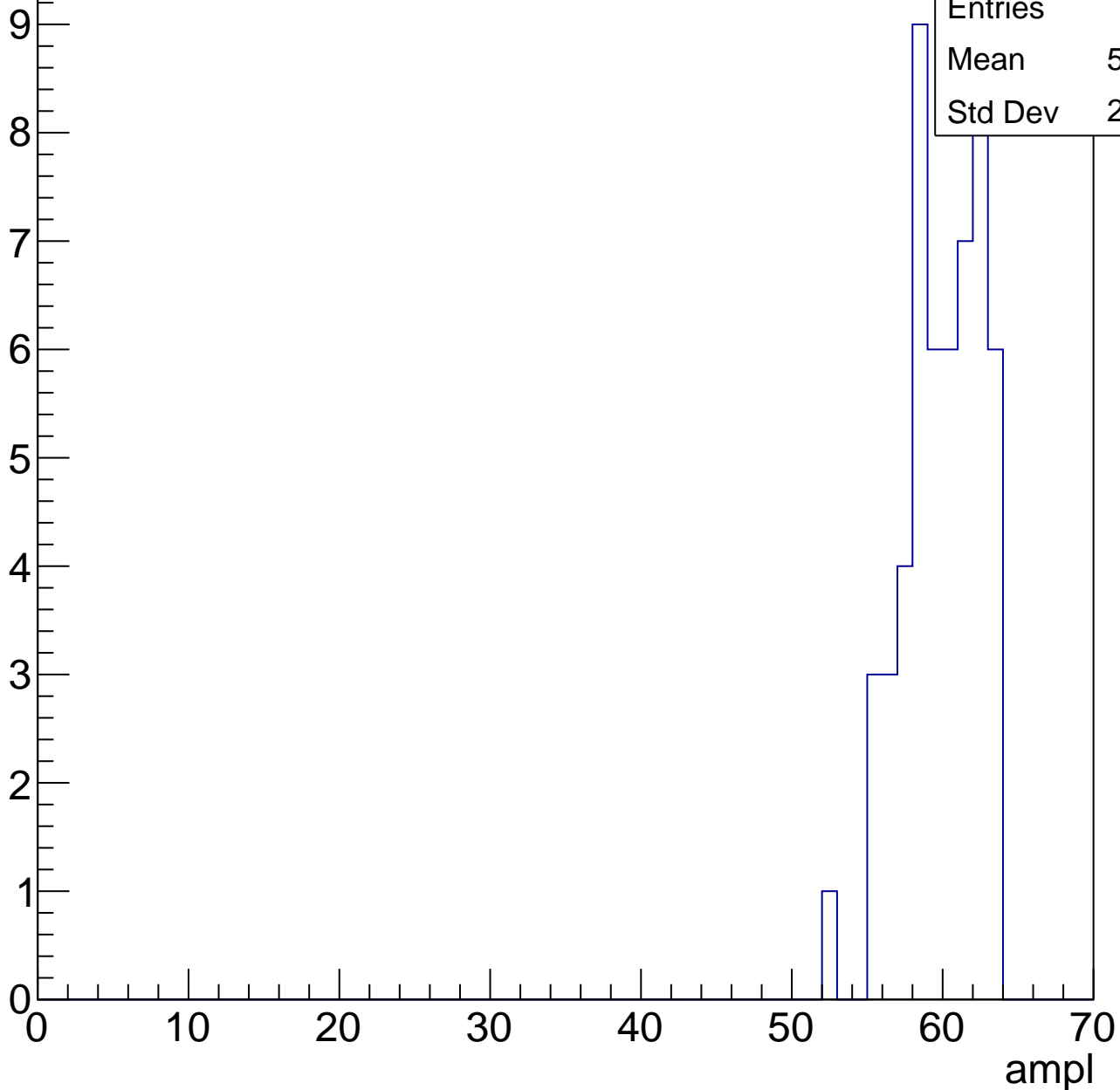


B1L103S, U20-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	59.43
Std Dev	2.537



B1L103S, U20-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch15, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	31.66
Std Dev	3.1

Gaus mean : 32.0733

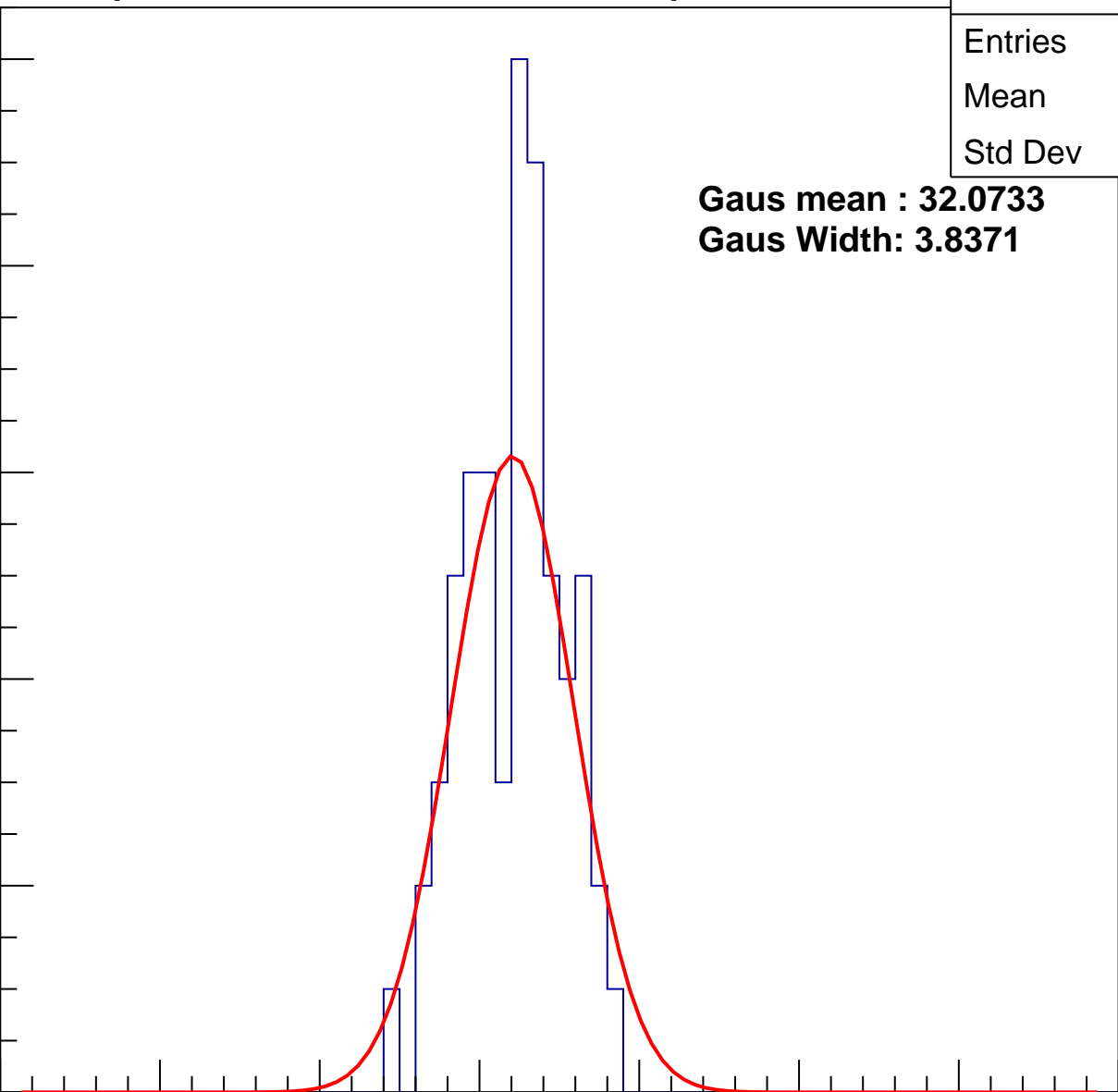
Gaus Width: 3.8371

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch15, adc1

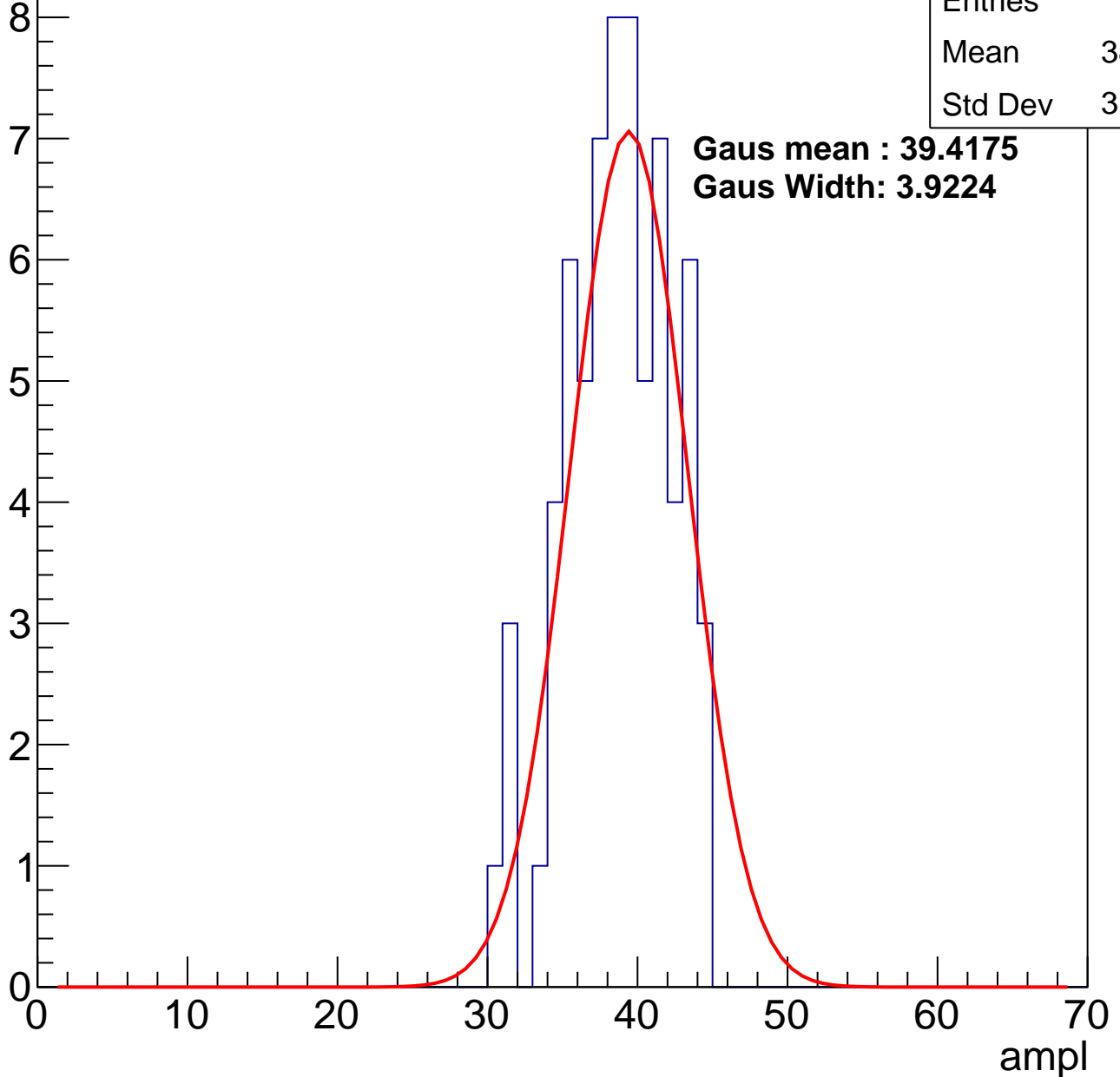
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	38.26
Std Dev	3.407

Gaus mean : 39.4175

Gaus Width: 3.9224



B1L103S, U20-ch15, adc2

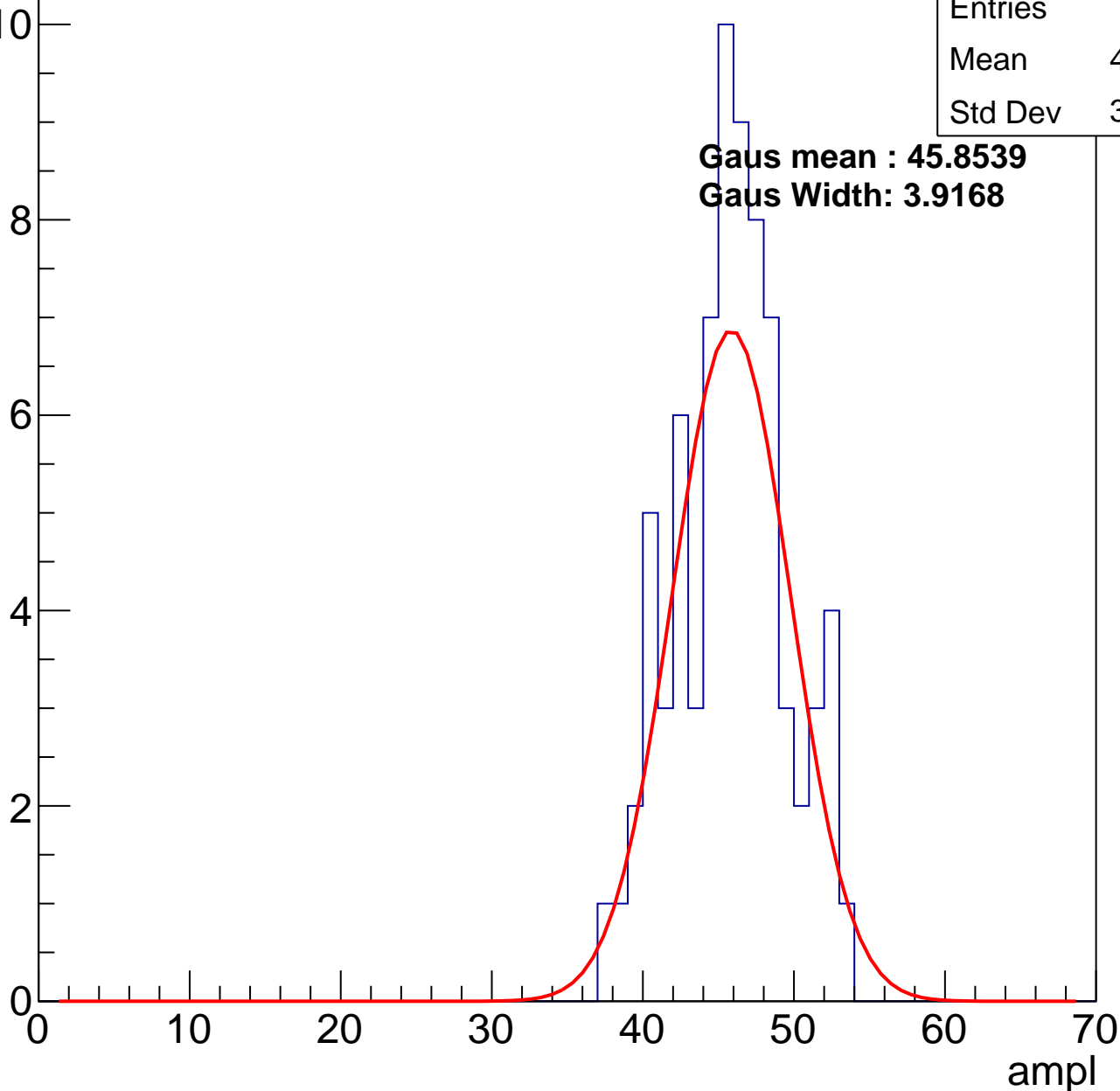
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	45.36
Std Dev	3.636

Gaus mean : 45.8539

Gaus Width: 3.9168

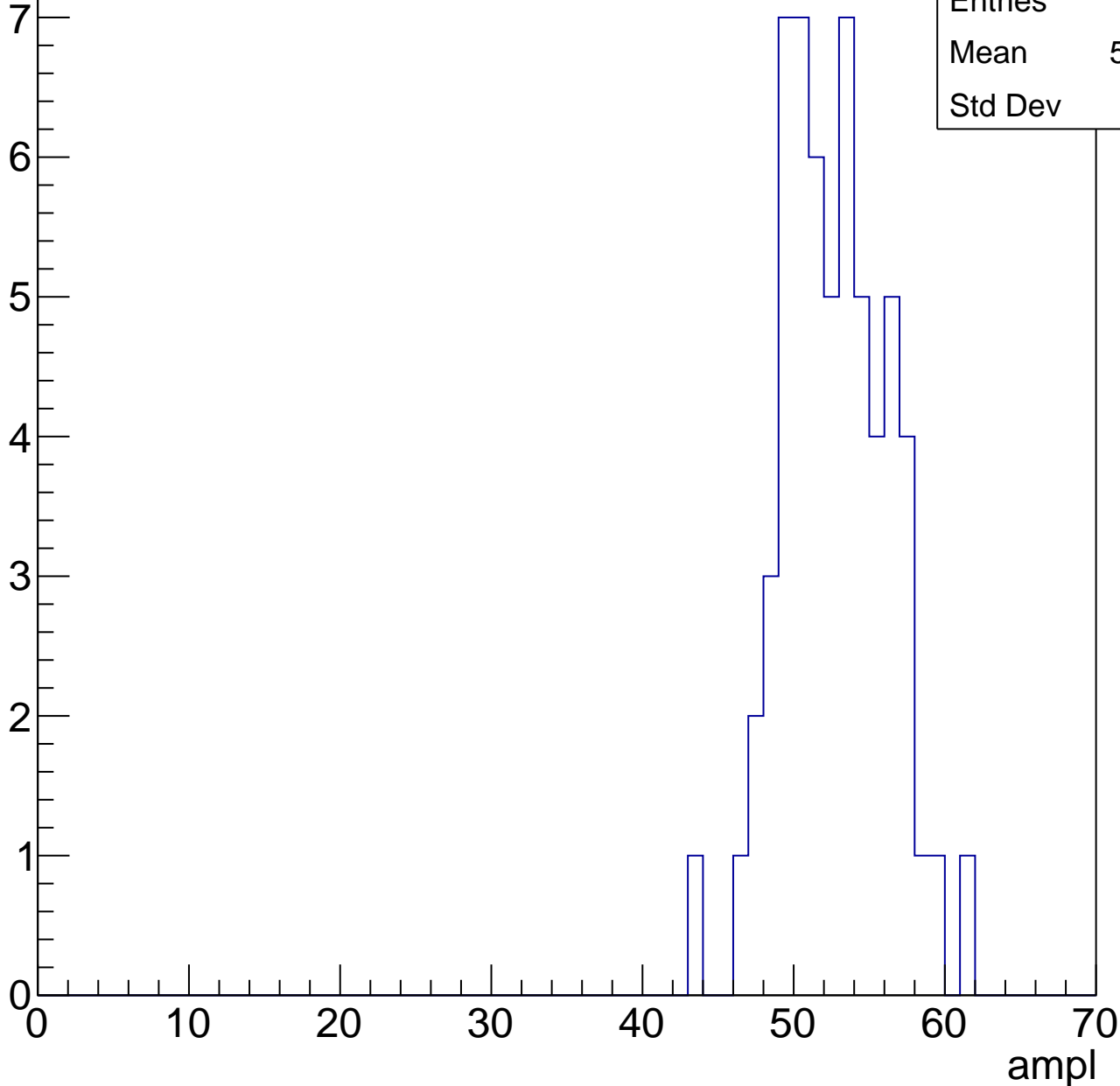


B1L103S, U20-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	52.22
Std Dev	3.46

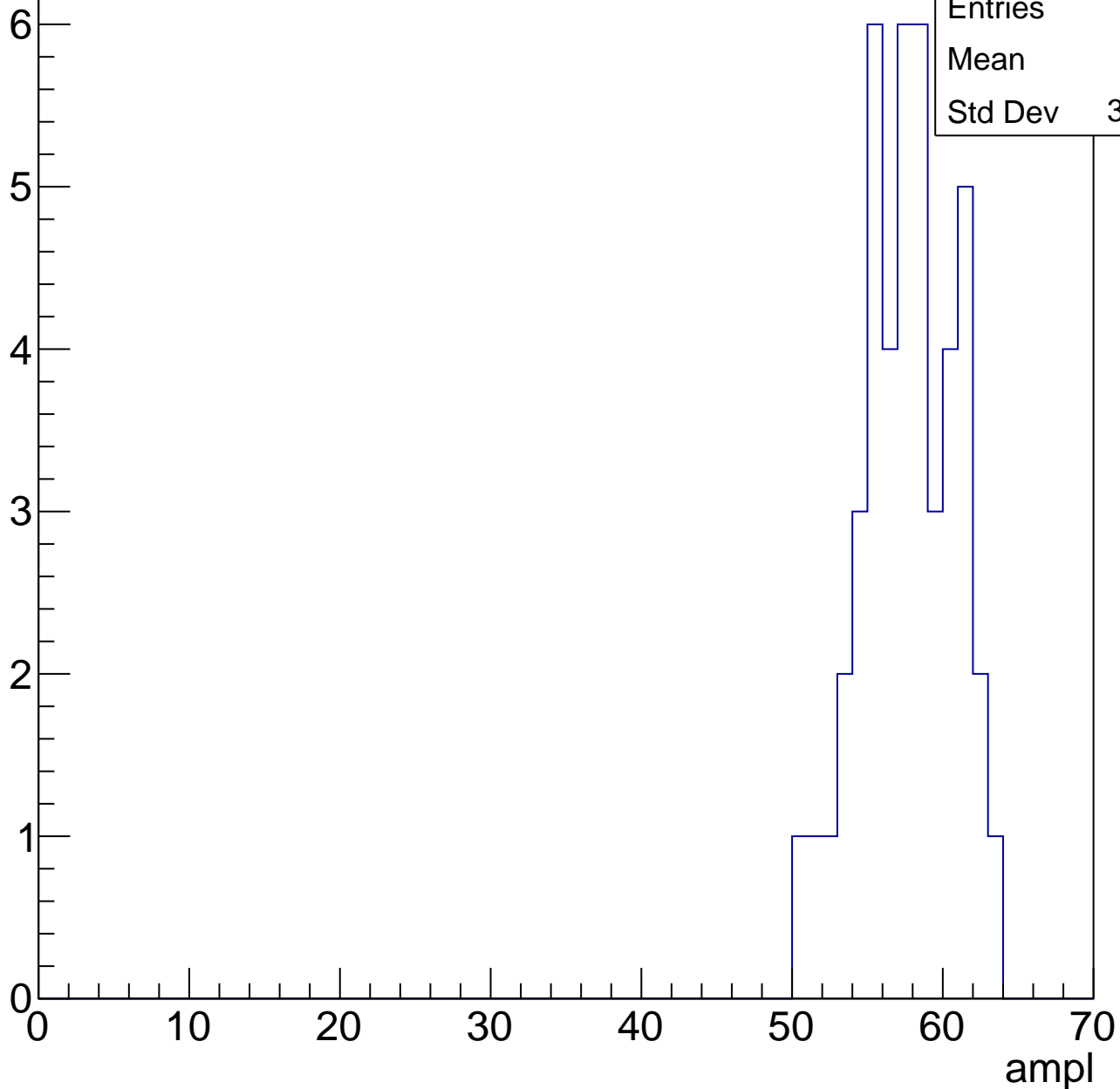


B1L103S, U20-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	57.2
Std Dev	3.038

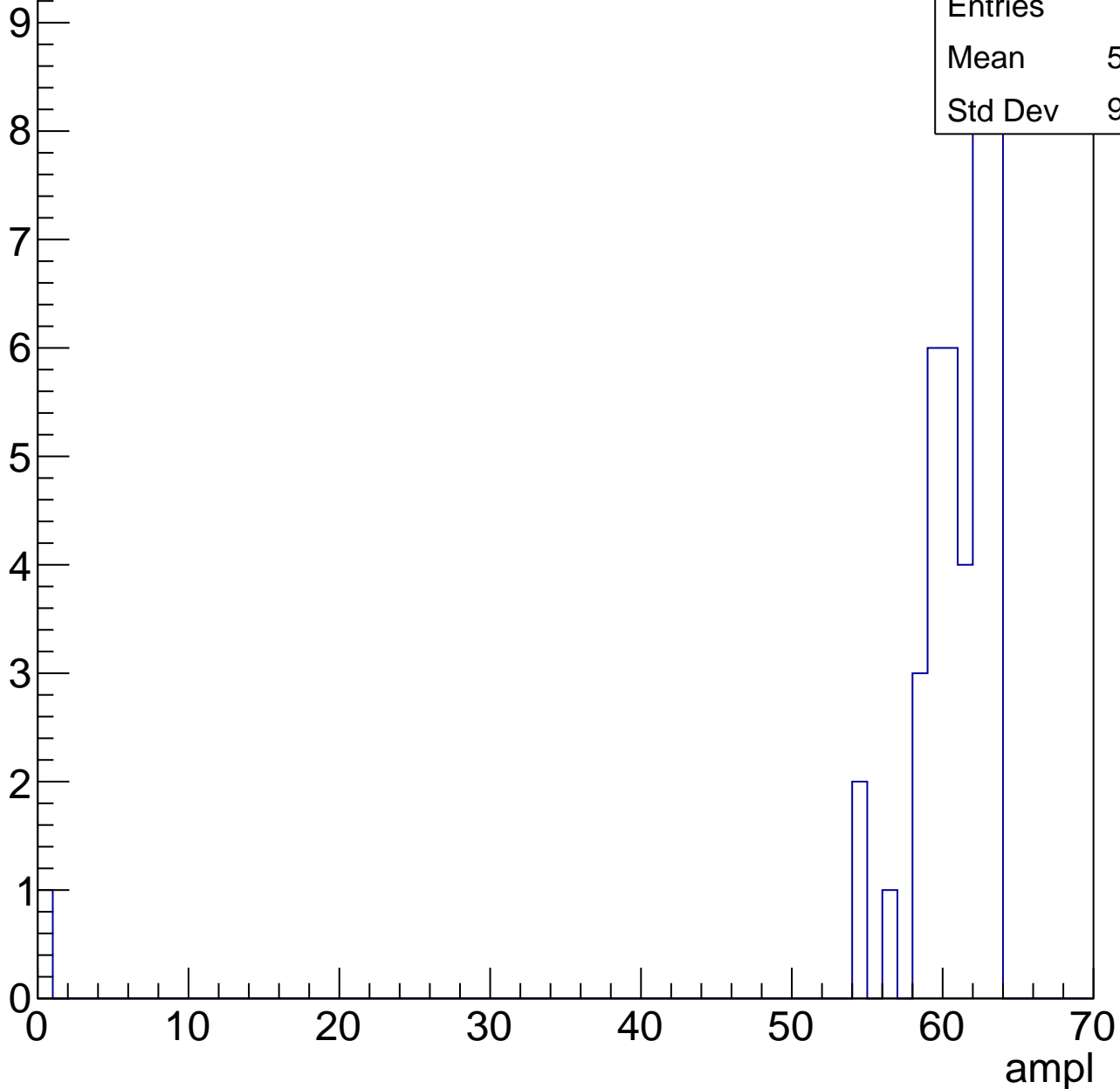


B1L103S, U20-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.98
Std Dev	9.722



B1L103S, U20-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch16, adc0

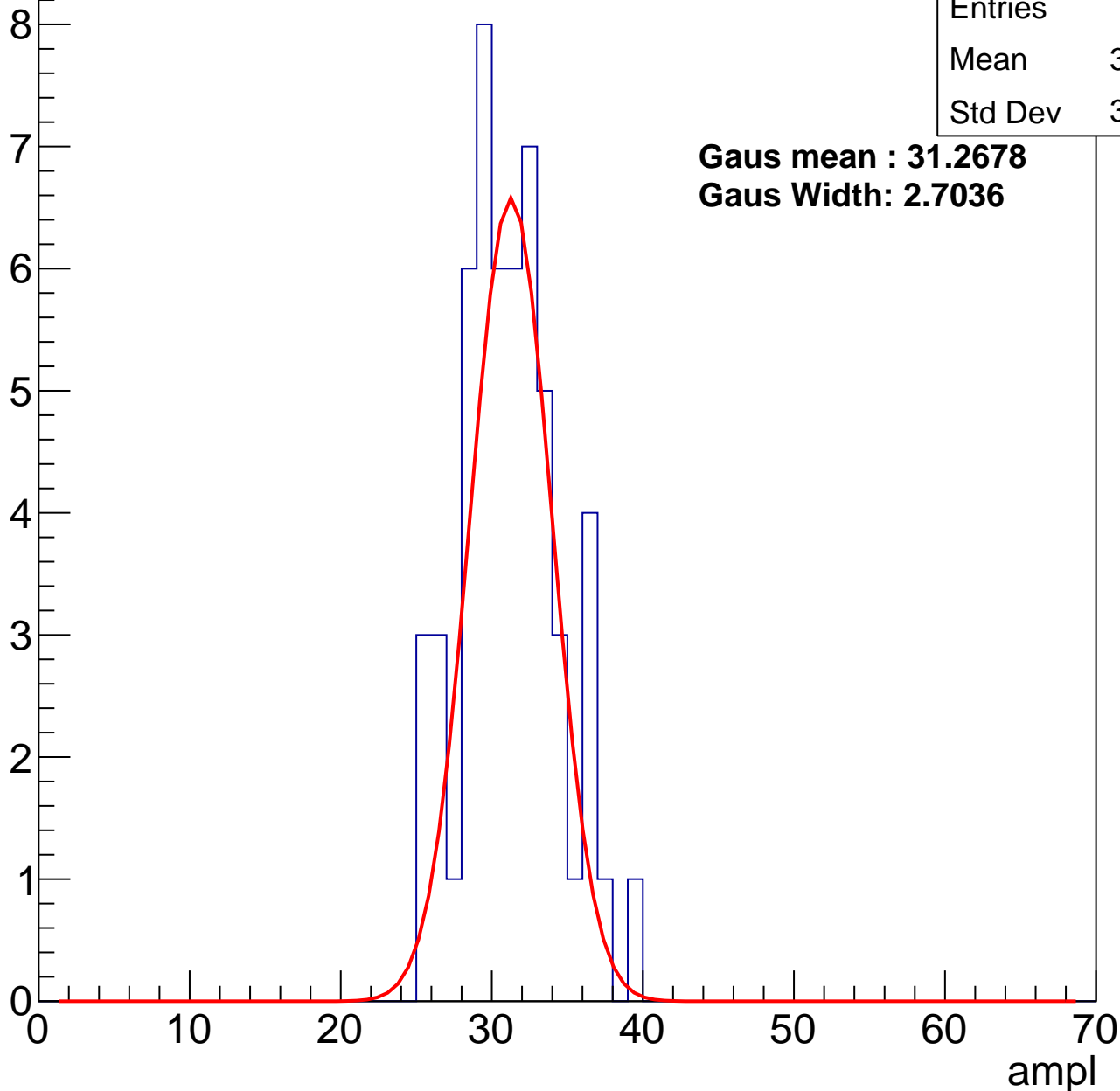
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	30.76
Std Dev	3.185

Gaus mean : 31.2678

Gaus Width: 2.7036



B1L103S, U20-ch16, adc1

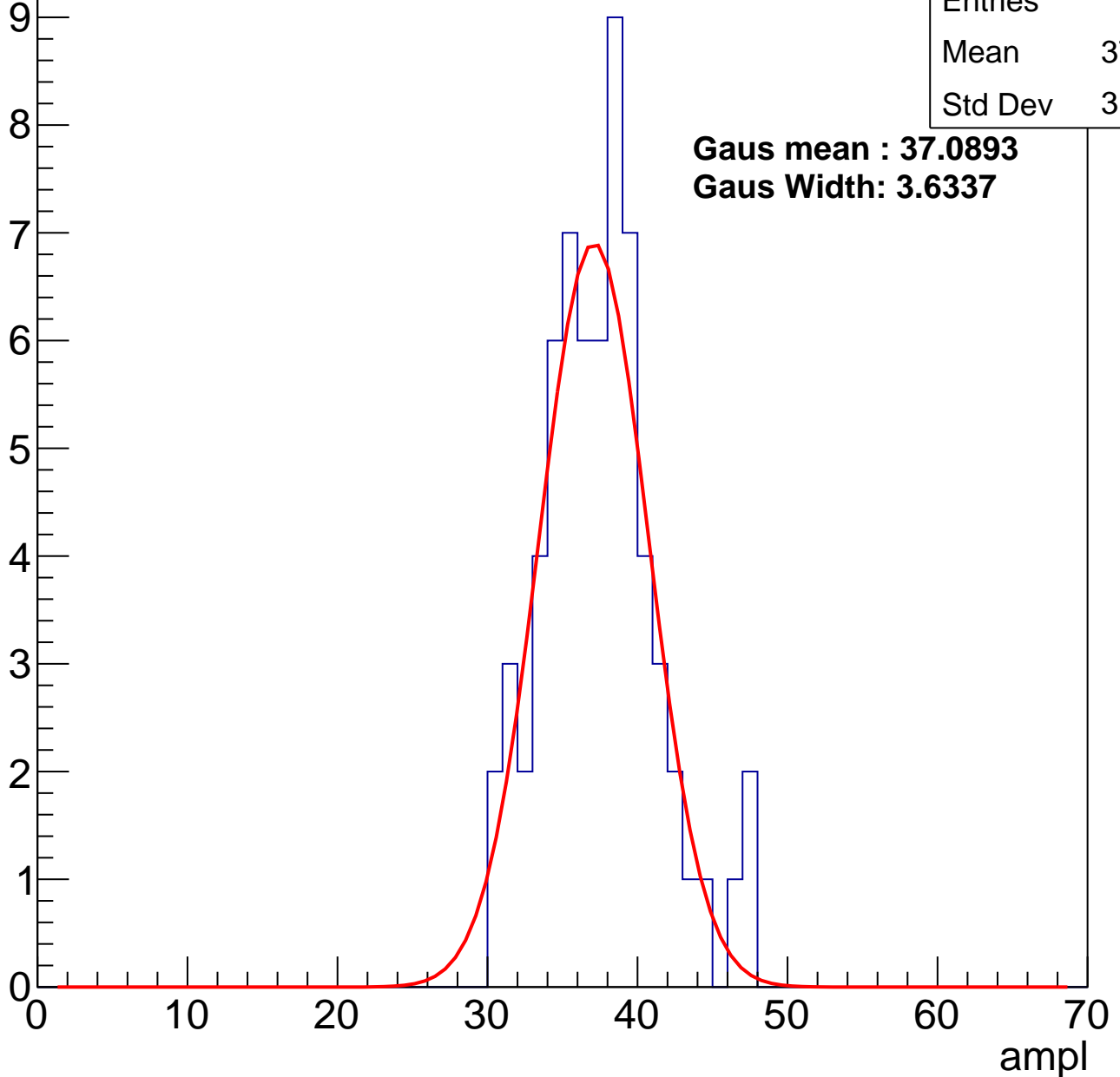
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	37.05
Std Dev	3.784

Gaus mean : 37.0893

Gaus Width: 3.6337



B1L103S, U20-ch16, adc2

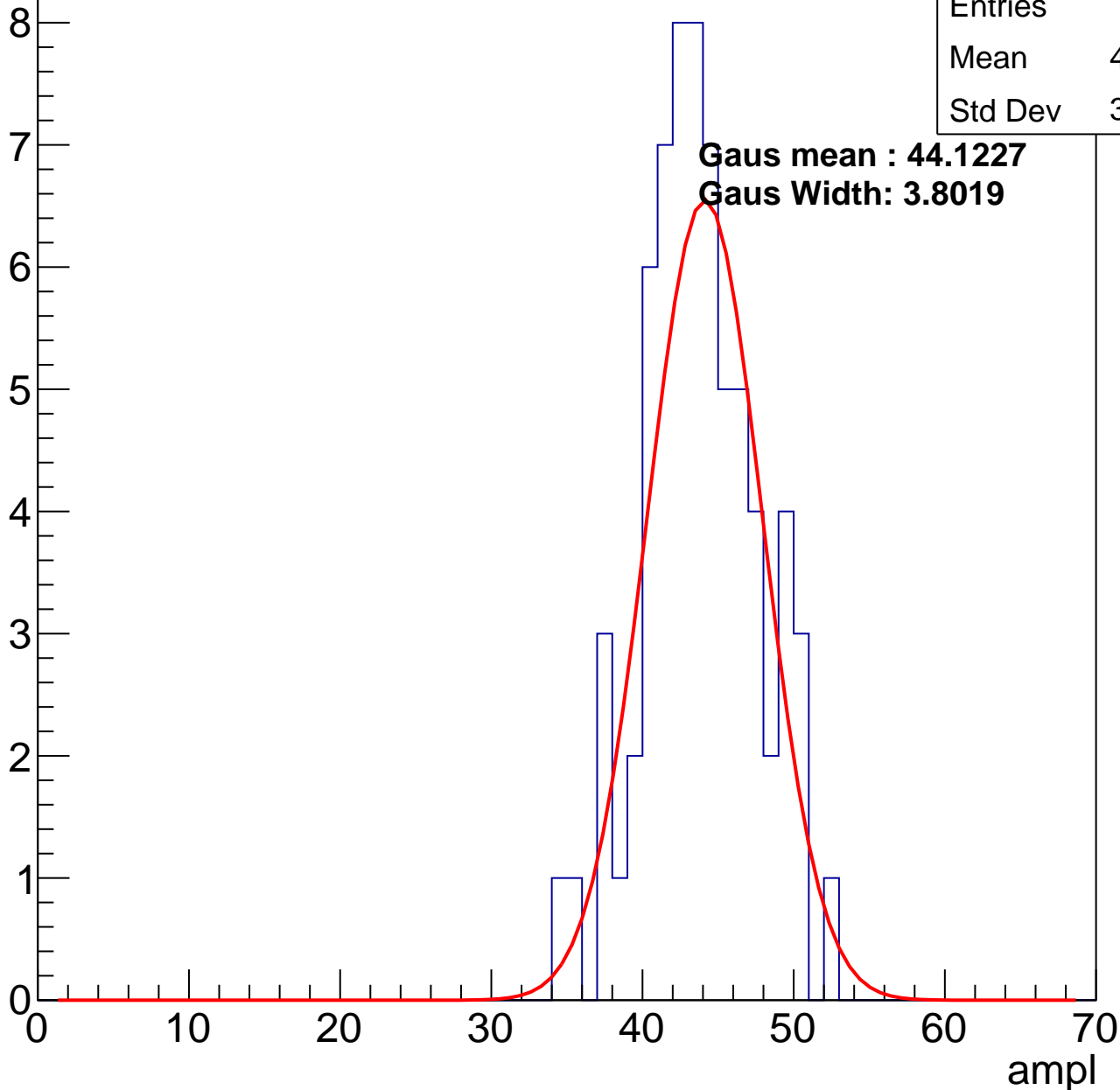
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.35
Std Dev	3.737

Gaus mean : 44.1227

Gaus Width: 3.8019

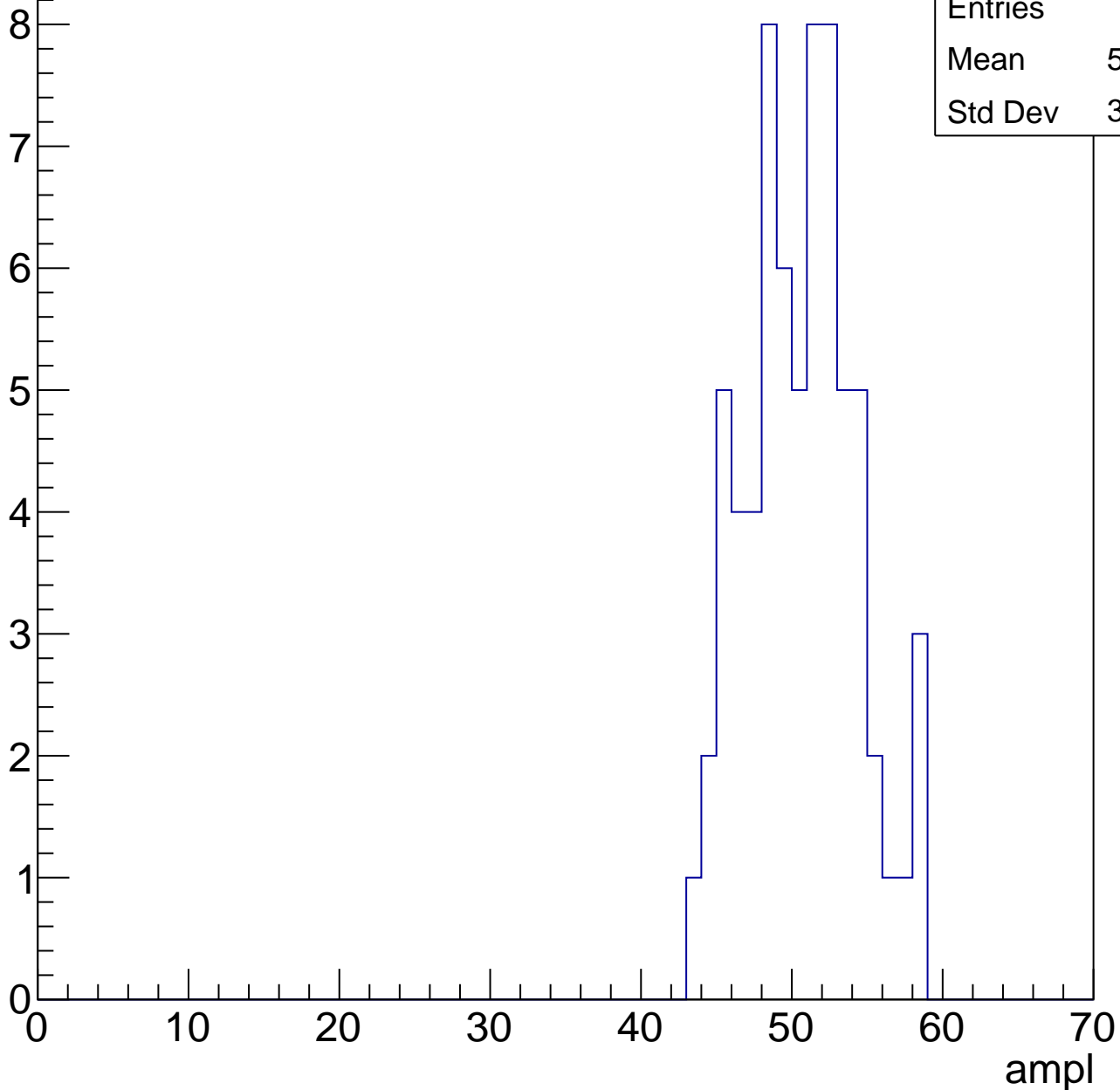


B1L103S, U20-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

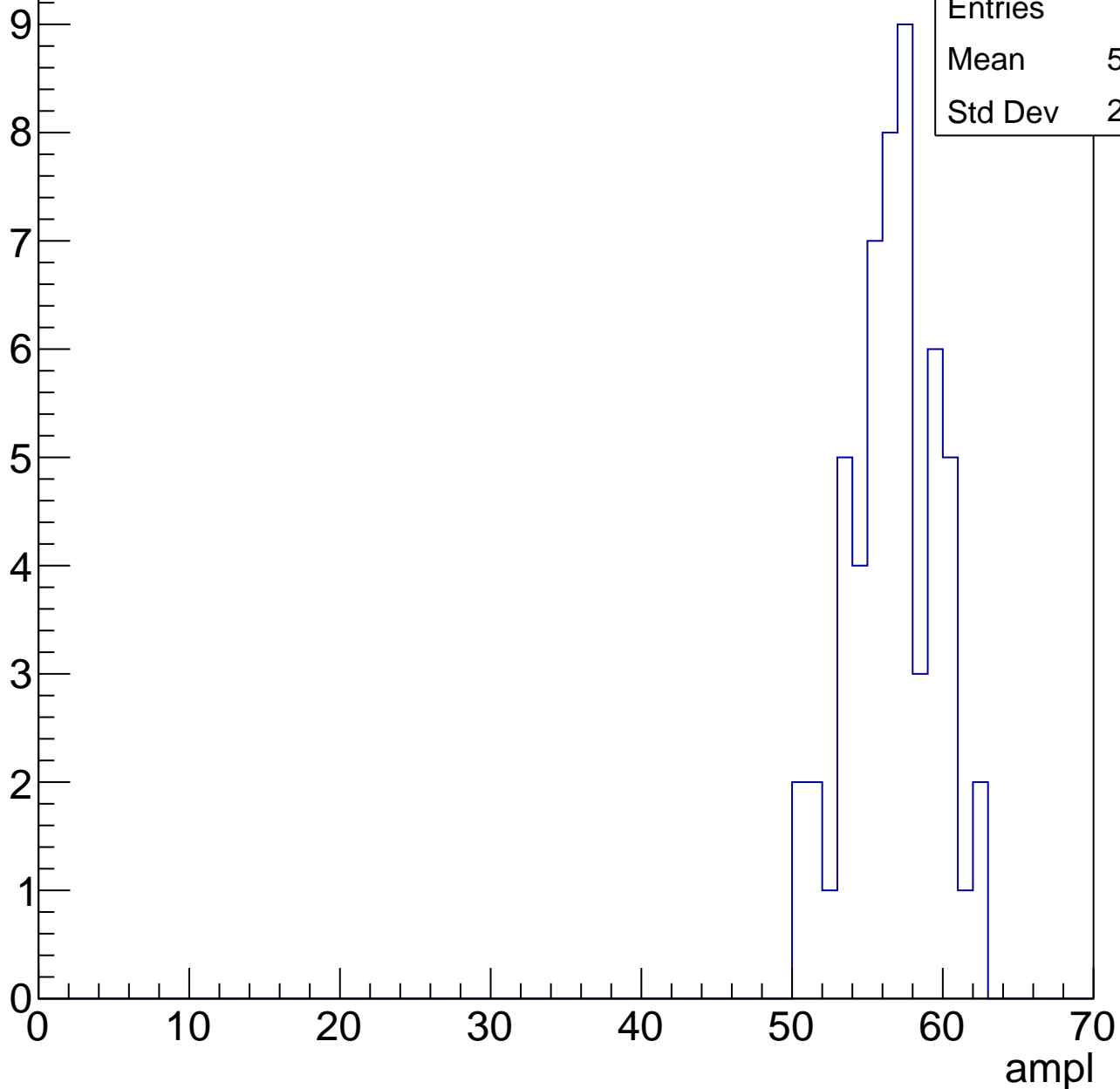
Entries	68
Mean	50.18
Std Dev	3.585



B1L103S, U20-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

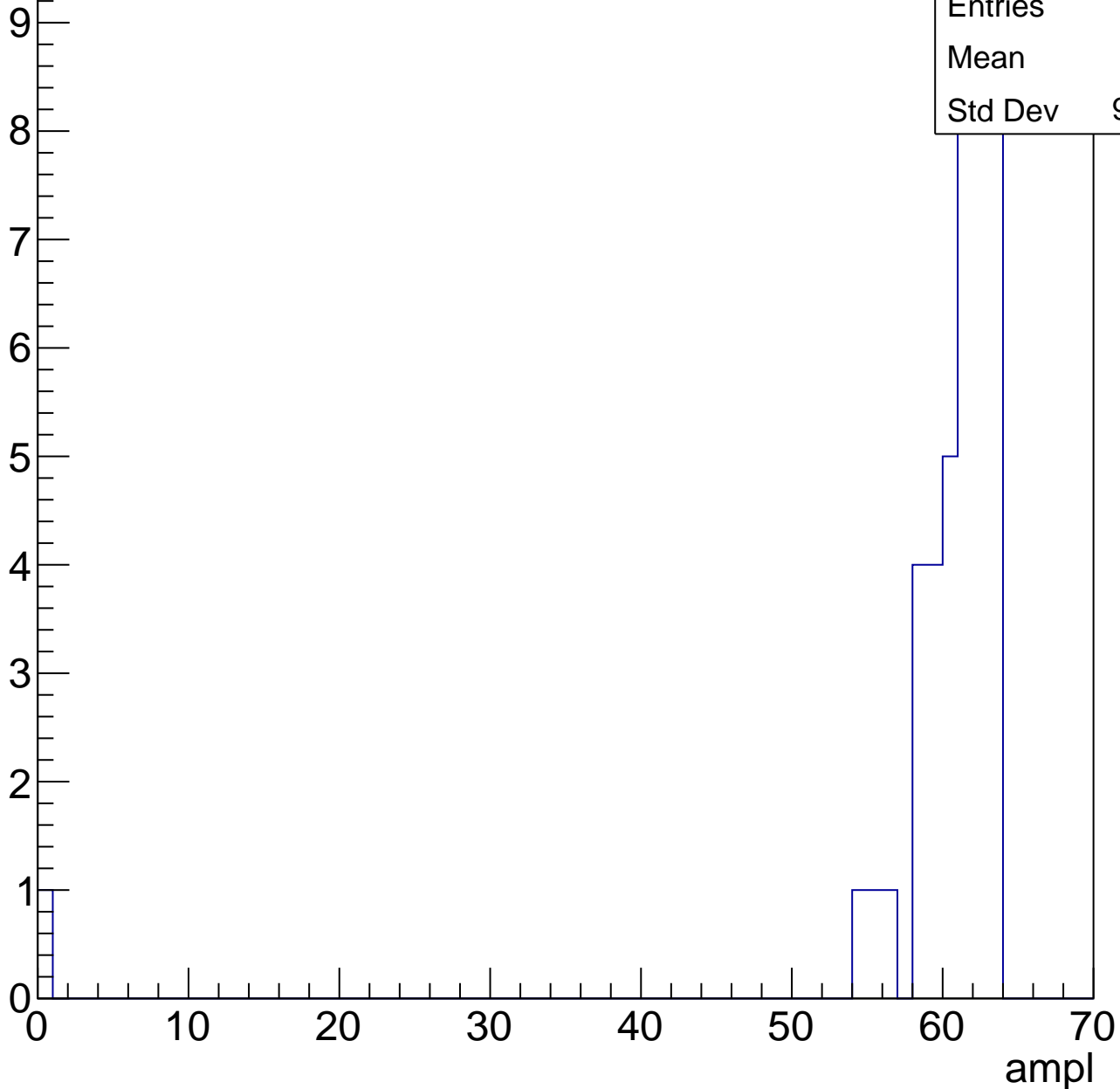


B1L103S, U20-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.1
Std Dev	9.481



B1L103S, U20-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch17, adc0

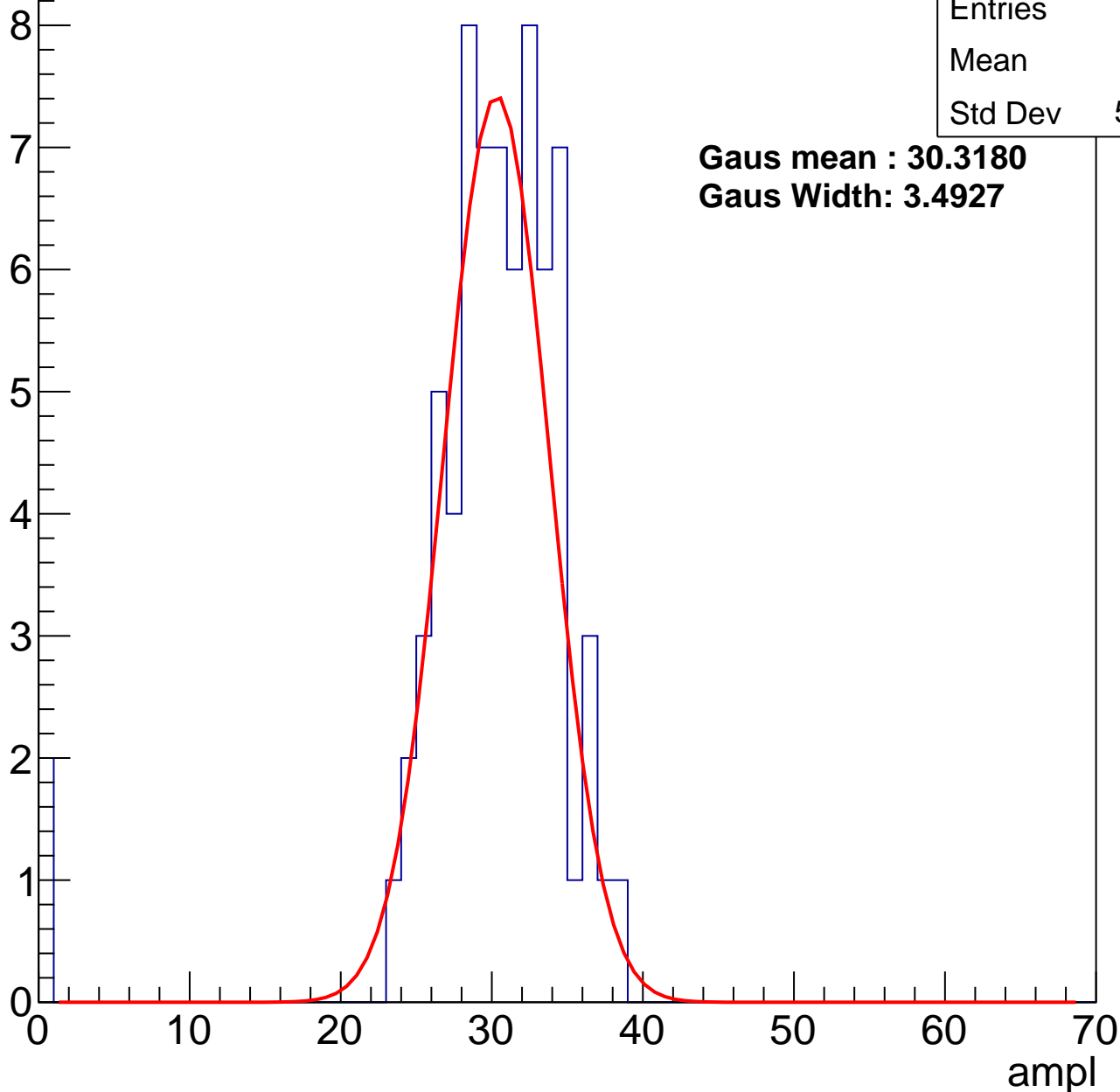
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.4
Std Dev	5.981

Gaus mean : 30.3180

Gaus Width: 3.4927



B1L103S, U20-ch17, adc1

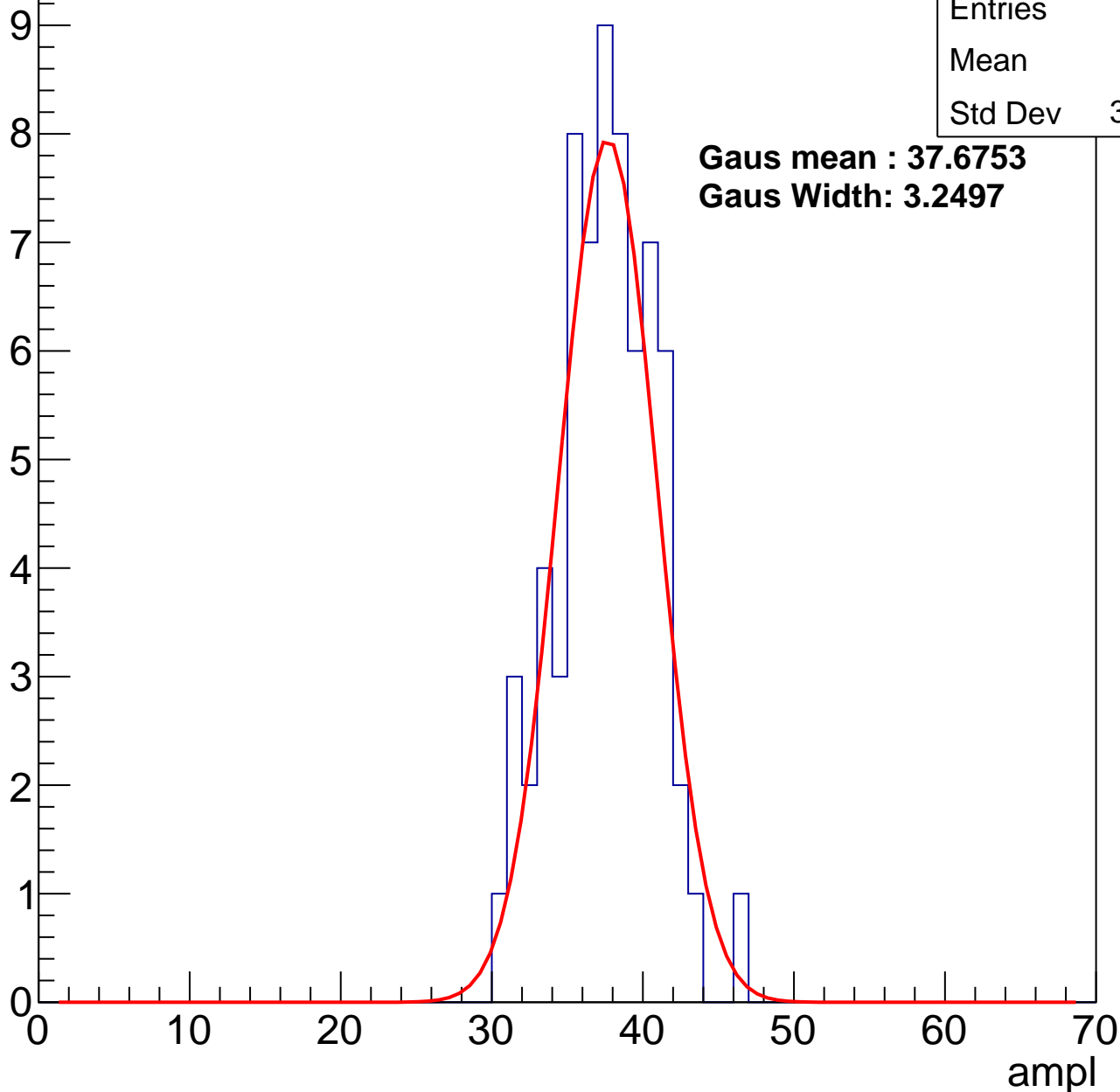
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.1
Std Dev	3.195

Gaus mean : 37.6753

Gaus Width: 3.2497



B1L103S, U20-ch17, adc2

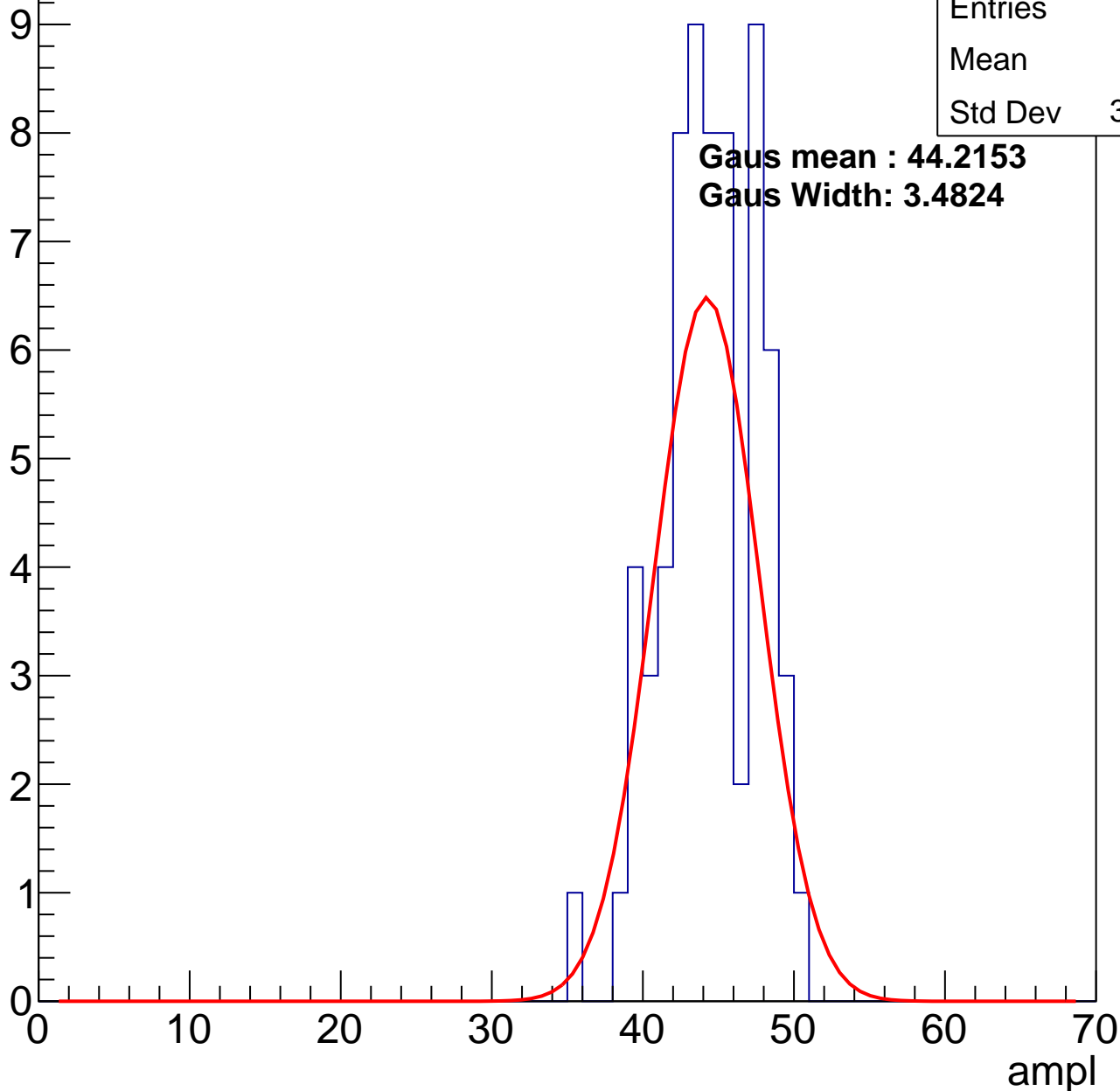
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	44
Std Dev	3.105

Gaus mean : 44.2153

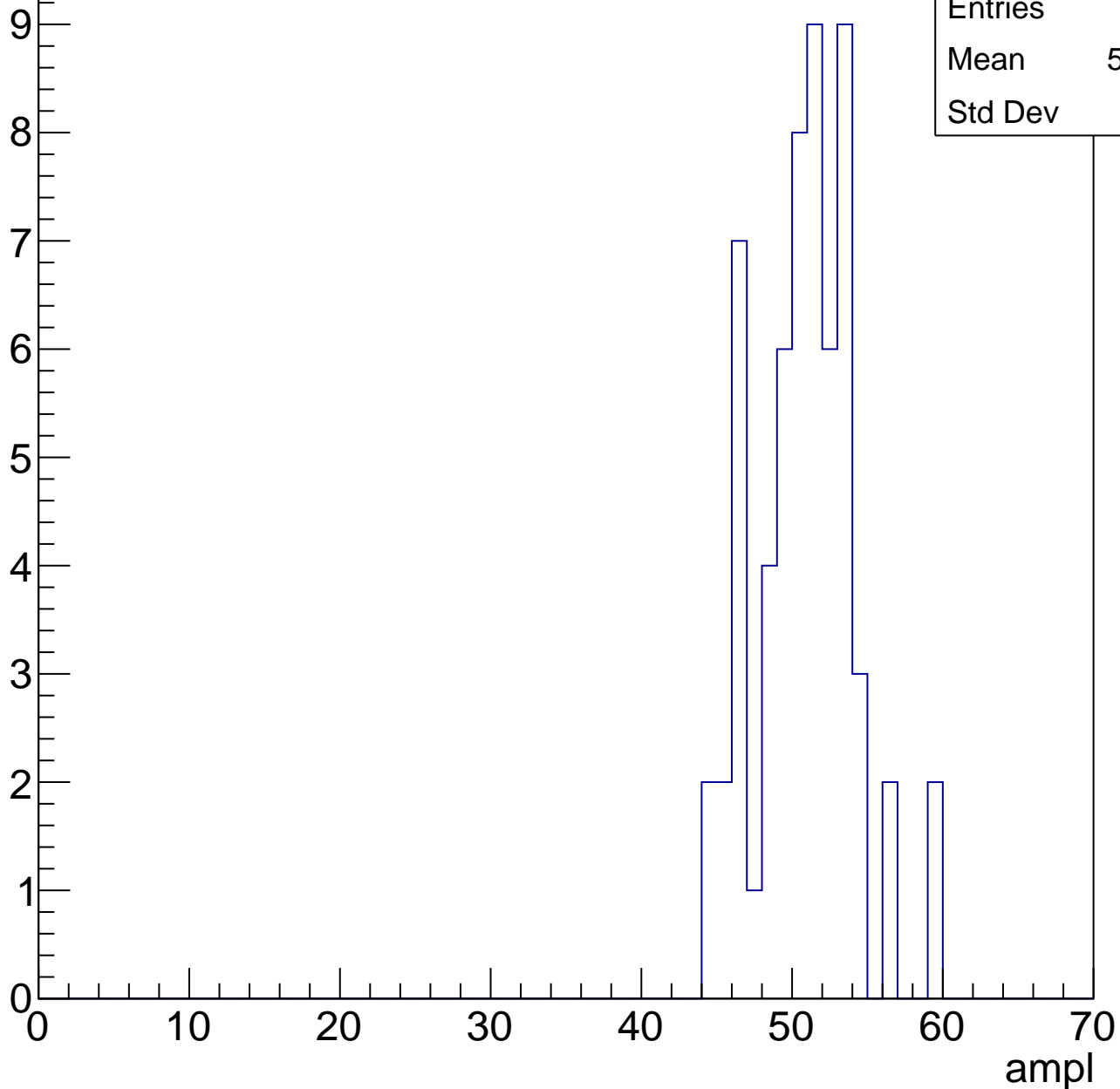
Gaus Width: 3.4824



B1L103S, U20-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

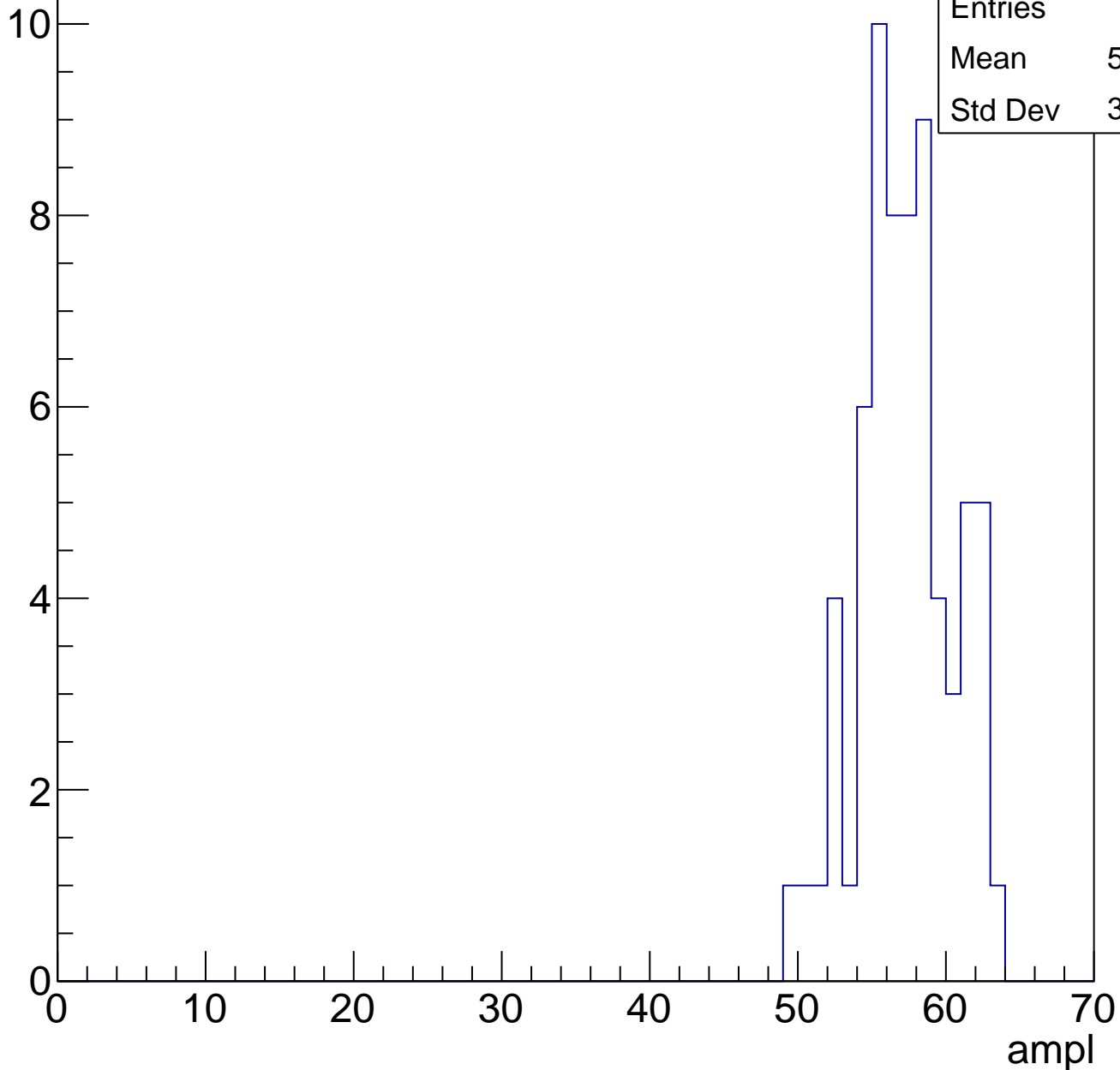


B1L103S, U20-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	56.79
Std Dev	3.132

Entry

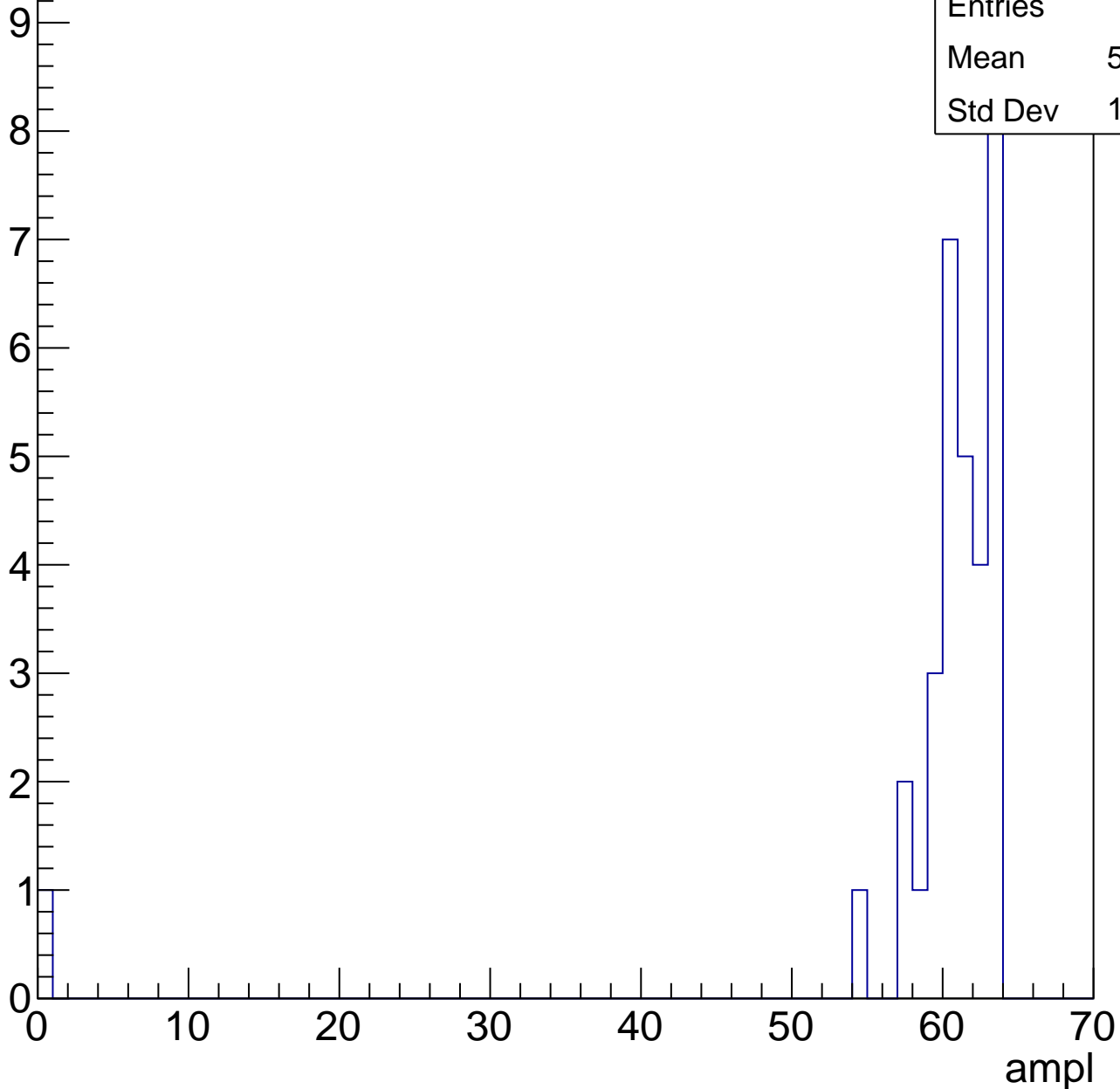


B1L103S, U20-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

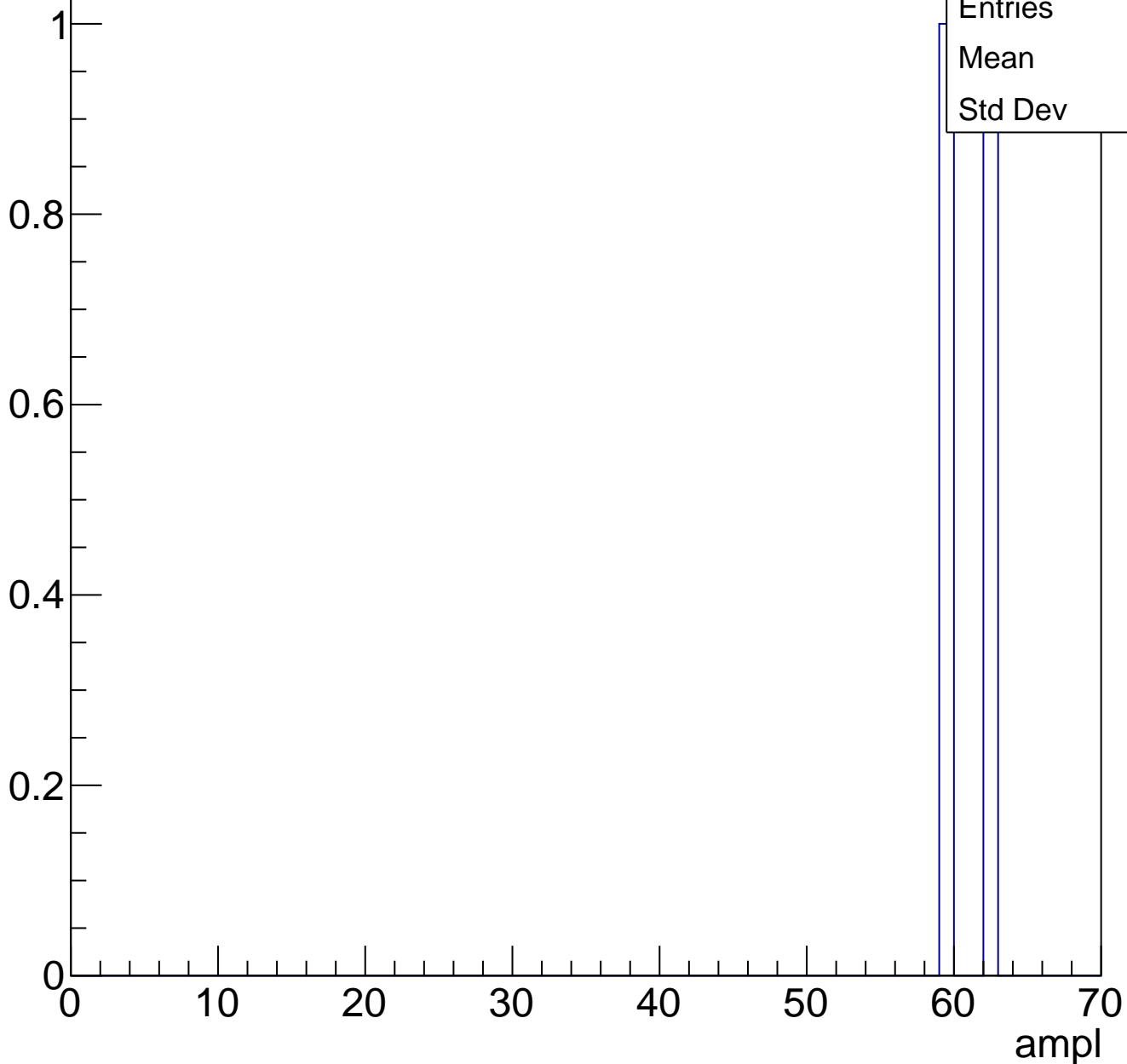
Entries	33
Mean	58.88
Std Dev	10.62



B1L103S, U20-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch18, adc0

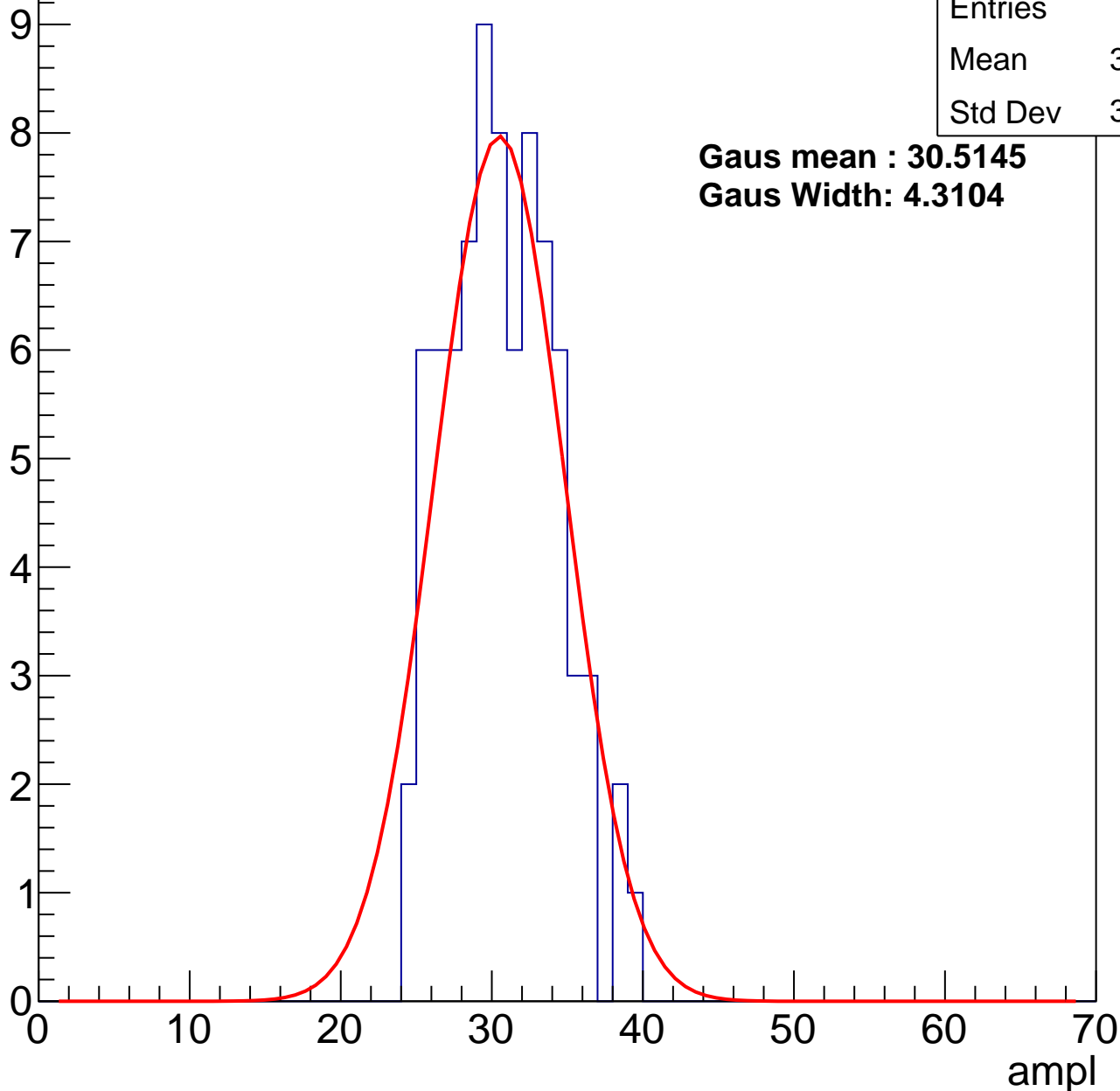
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	30.23
Std Dev	3.525

Gaus mean : 30.5145

Gaus Width: 4.3104



B1L103S, U20-ch18, adc1

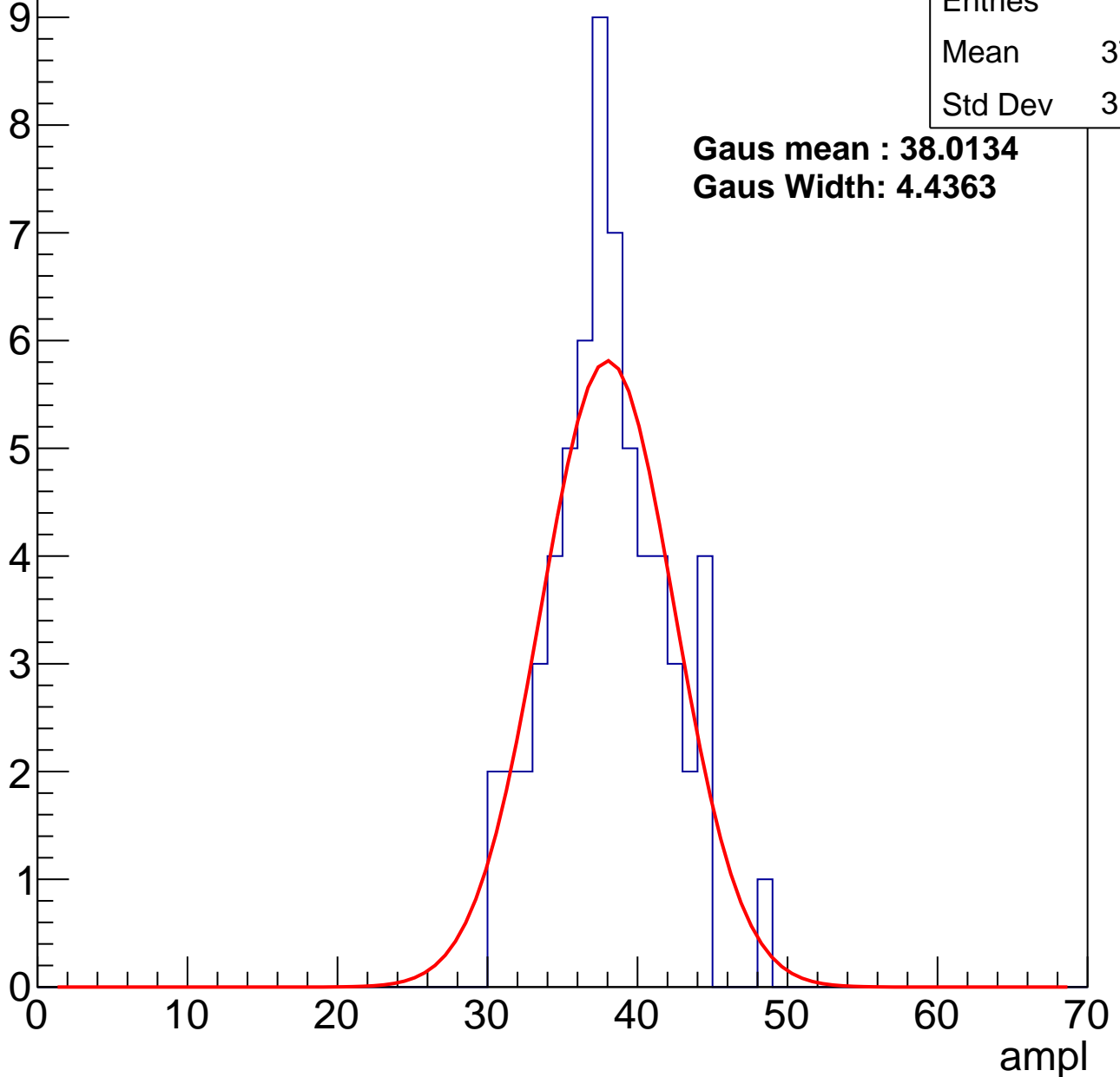
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.56
Std Dev	3.783

Gaus mean : 38.0134

Gaus Width: 4.4363



B1L103S, U20-ch18, adc2

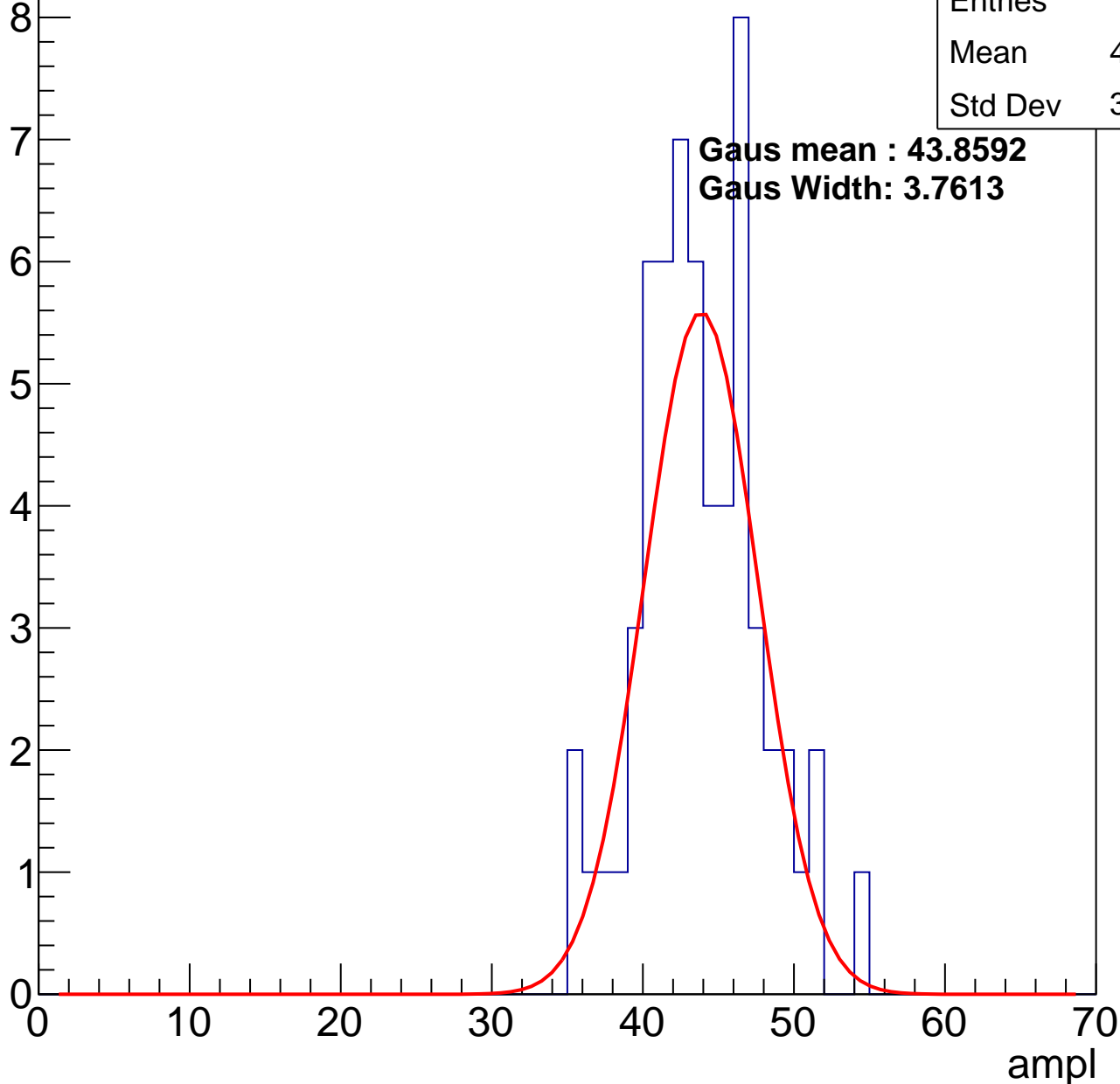
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.35
Std Dev	3.924

Gaus mean : 43.8592

Gaus Width: 3.7613

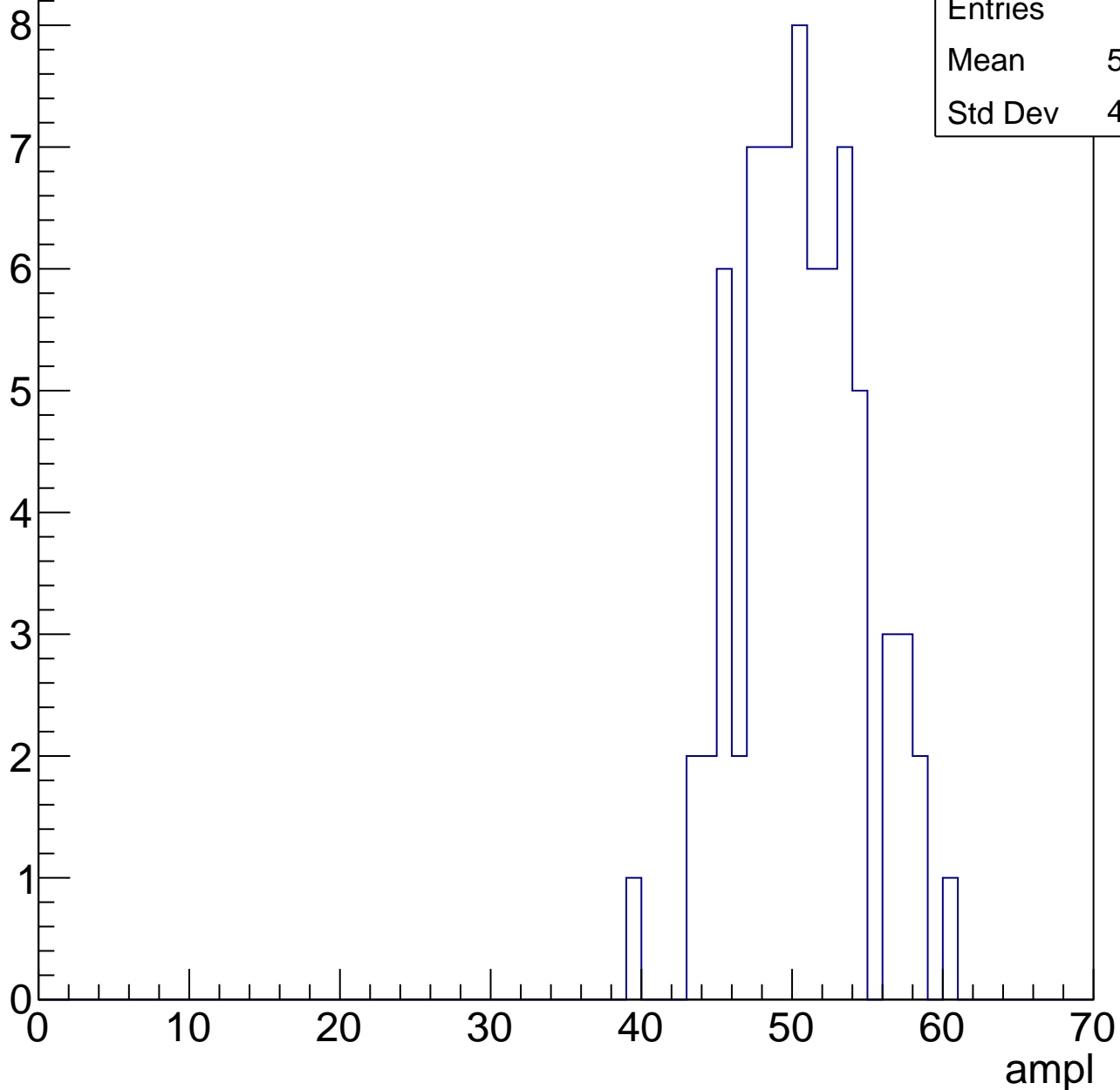


B1L103S, U20-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	50.09
Std Dev	4.044

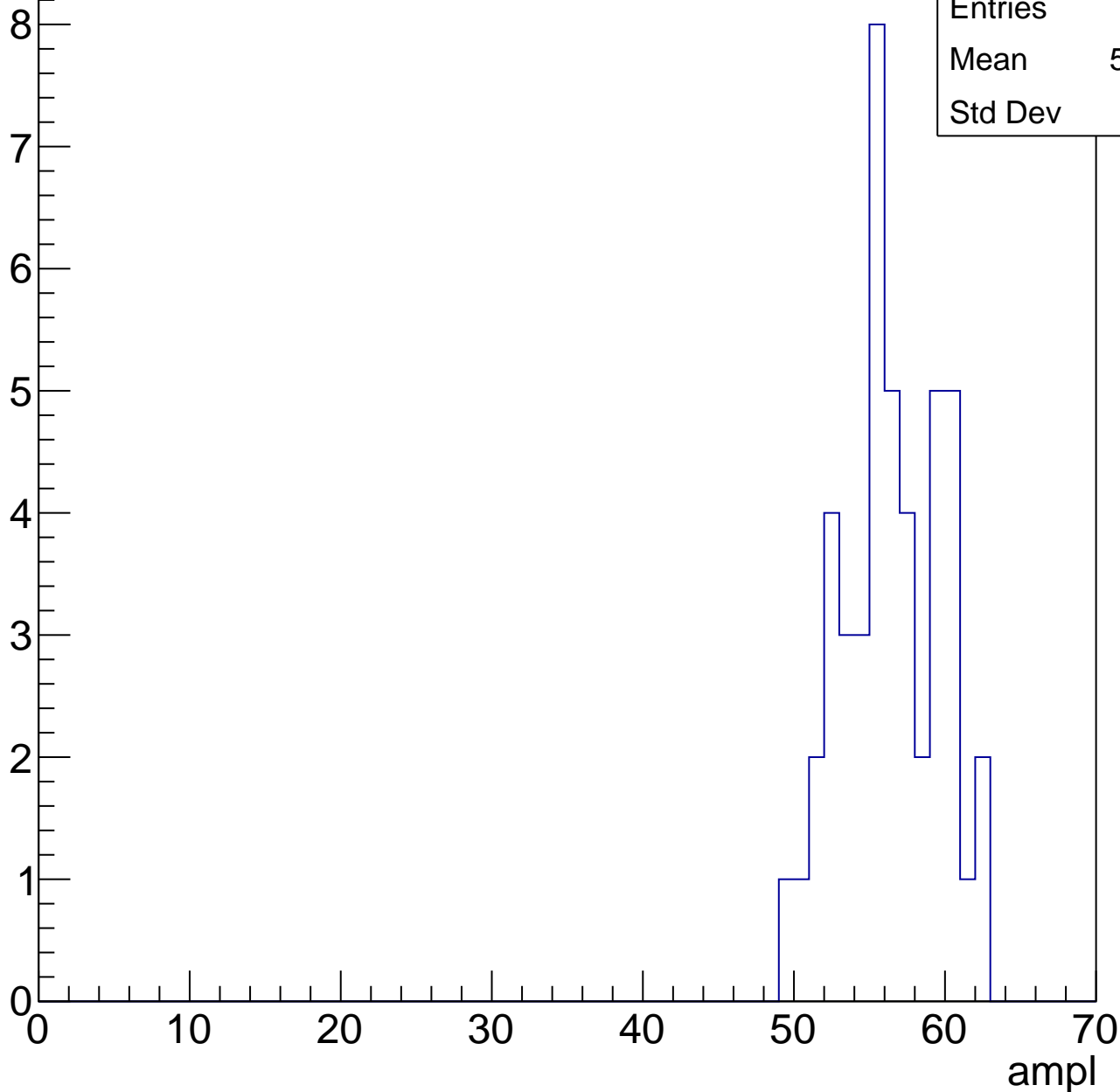


B1L103S, U20-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

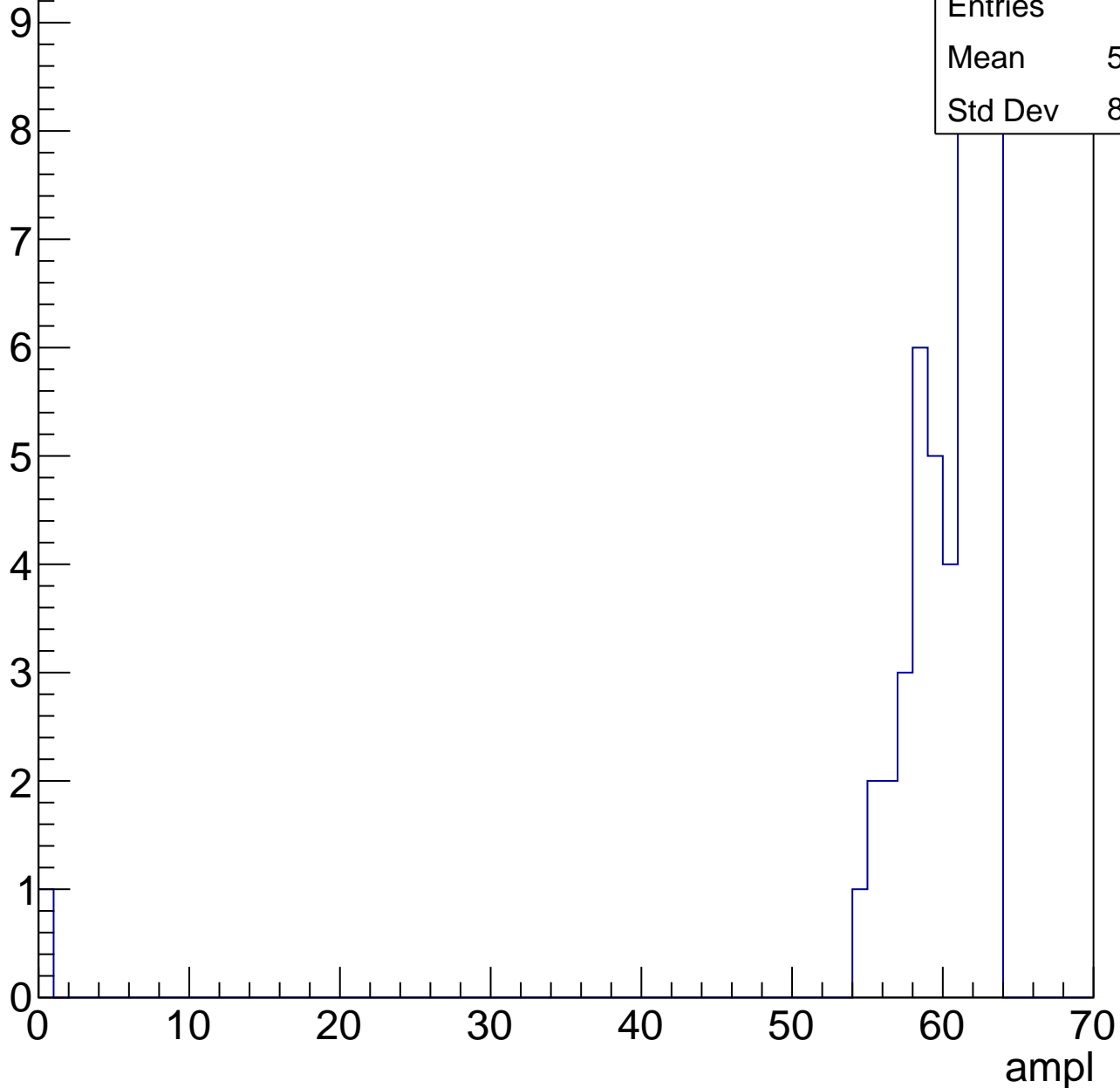
Entries	46
Mean	55.96
Std Dev	3.23



B1L103S, U20-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch19, adc0

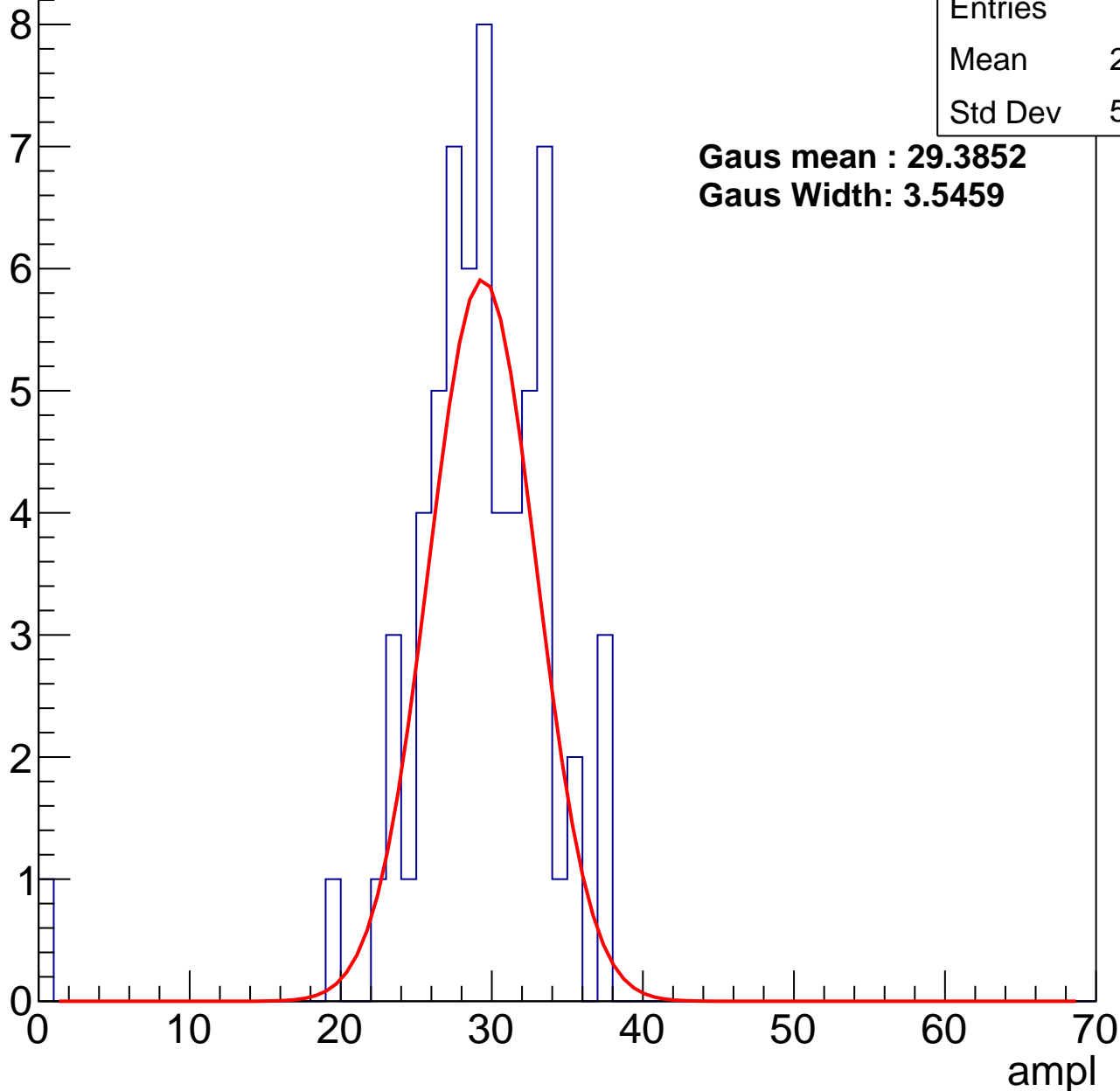
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.62
Std Dev	5.229

Gaus mean : 29.3852

Gaus Width: 3.5459



B1L103S, U20-ch19, adc1

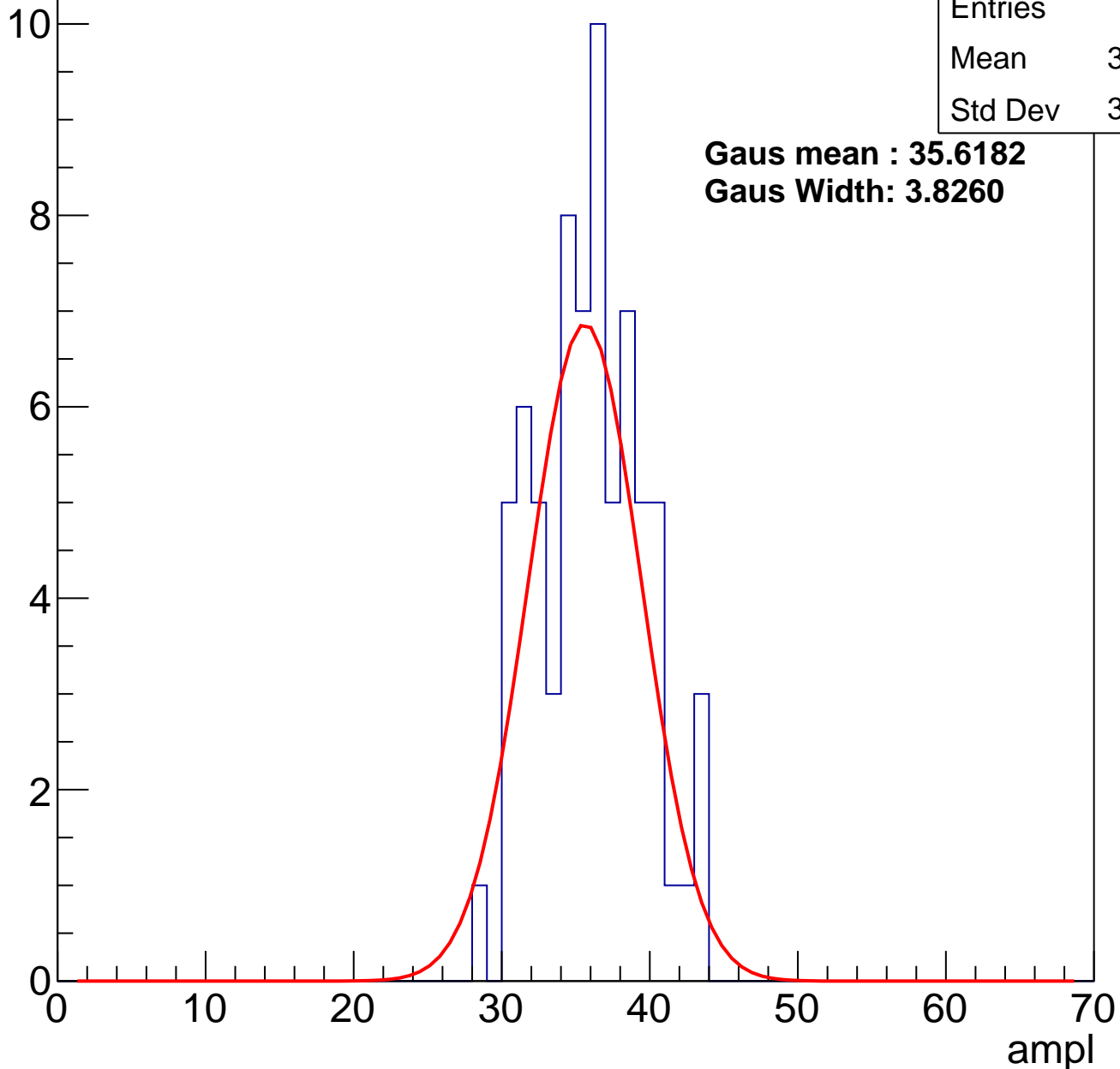
calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	35.53
Std Dev	3.516

Gaus mean : 35.6182

Gaus Width: 3.8260

Entry



B1L103S, U20-ch19, adc2

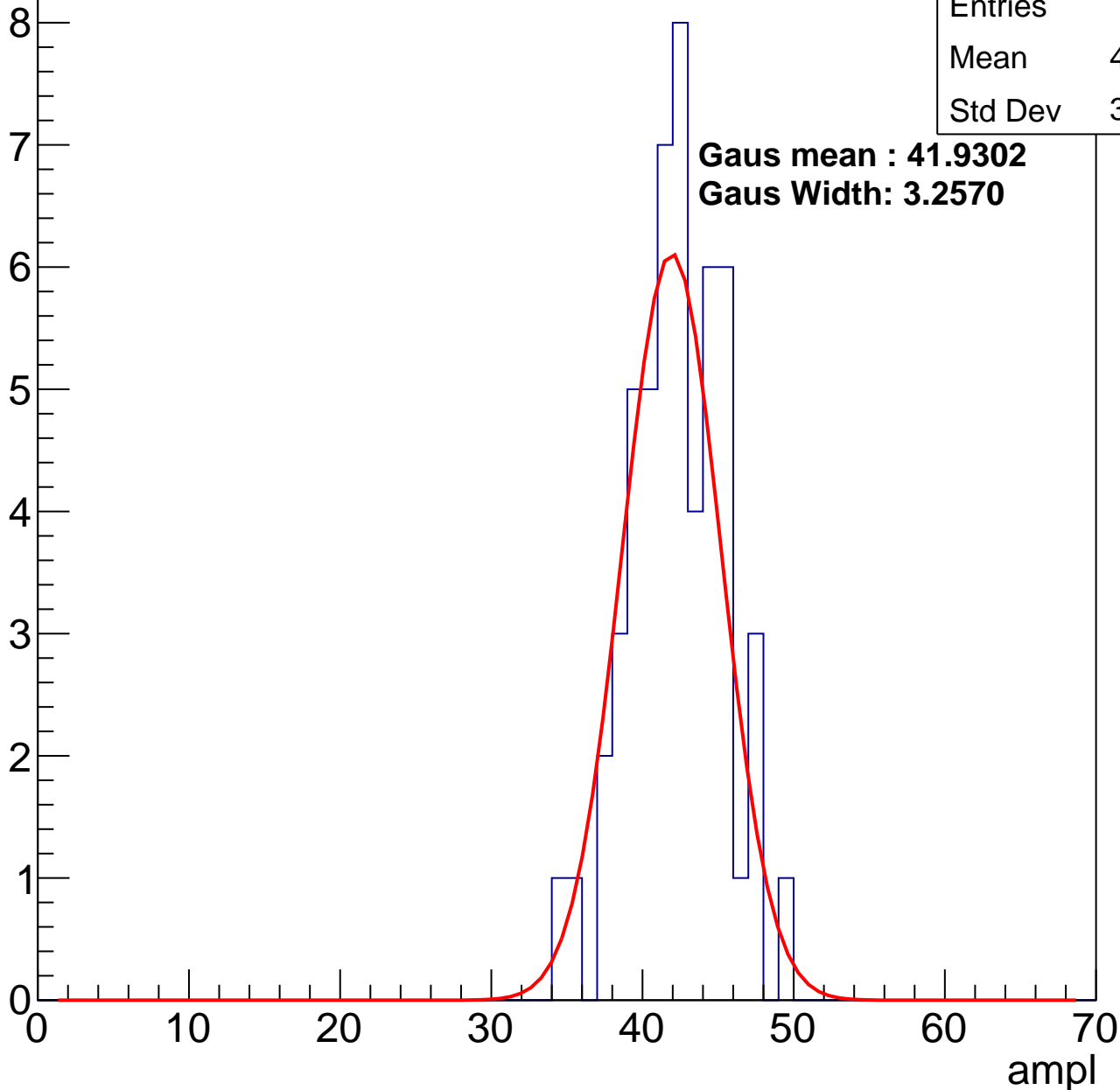
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	41.83
Std Dev	3.082

Gaus mean : 41.9302

Gaus Width: 3.2570

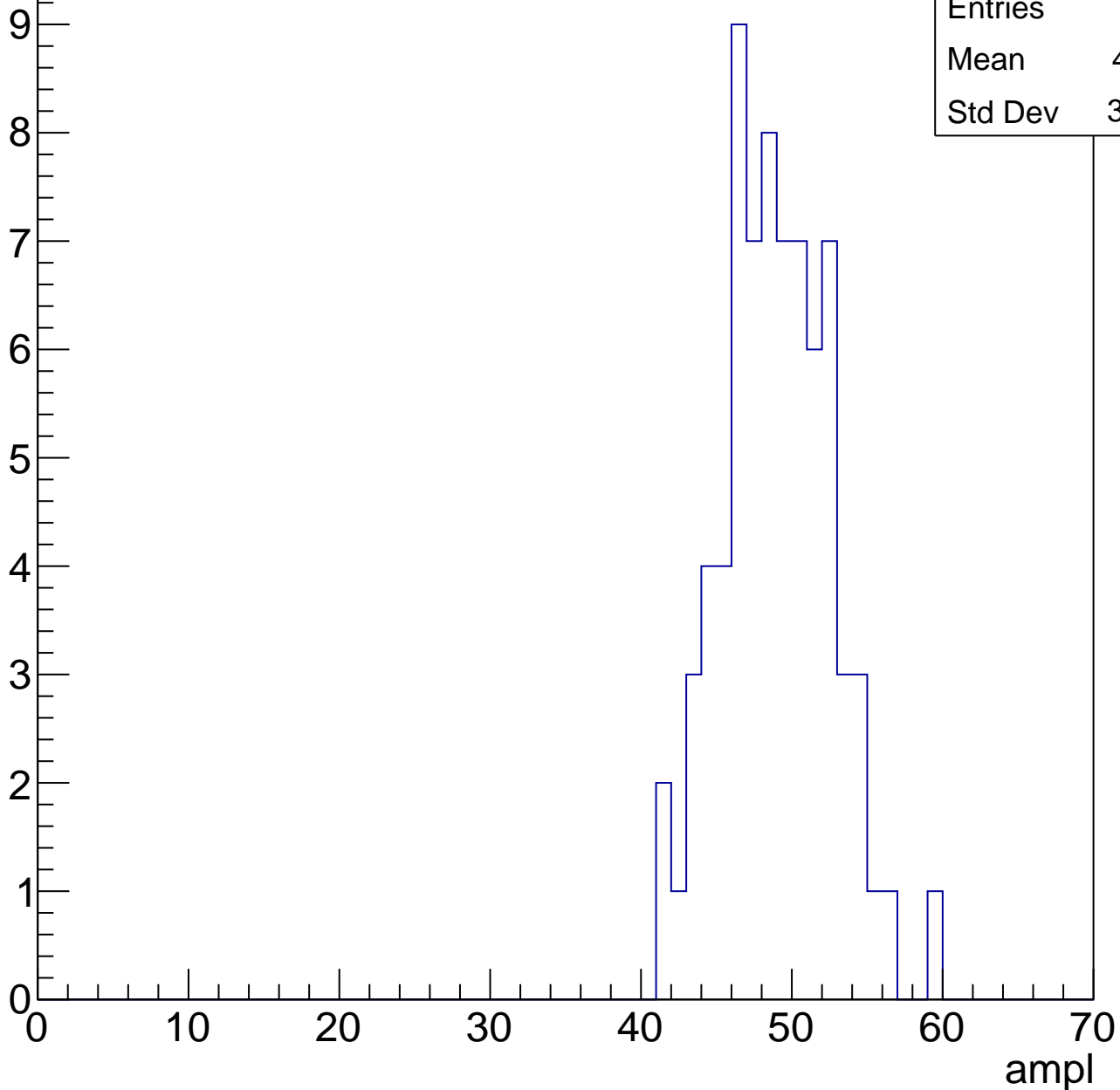


B1L103S, U20-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

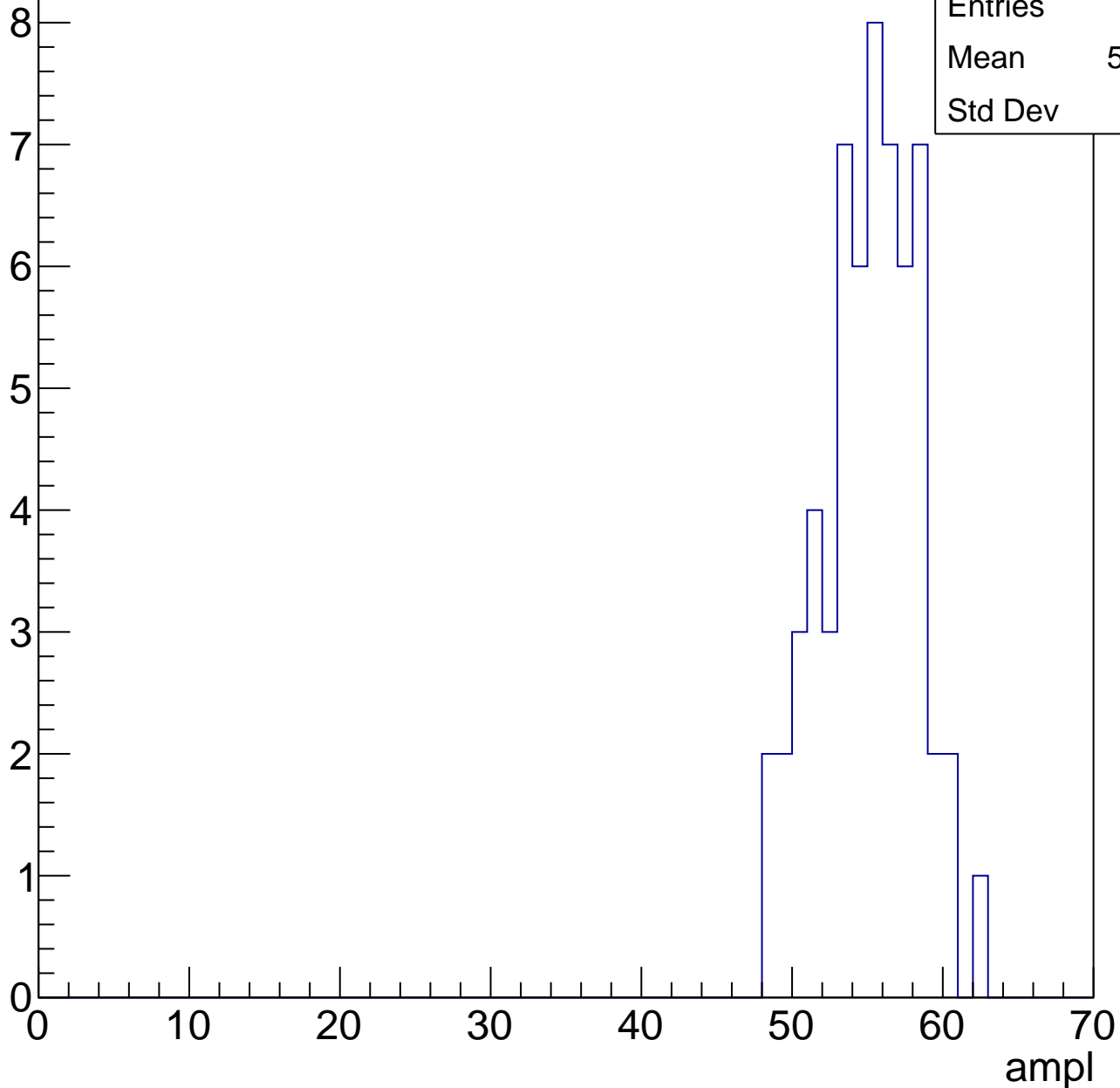
Entries	74
Mean	48.51
Std Dev	3.584



B1L103S, U20-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch19, adc5

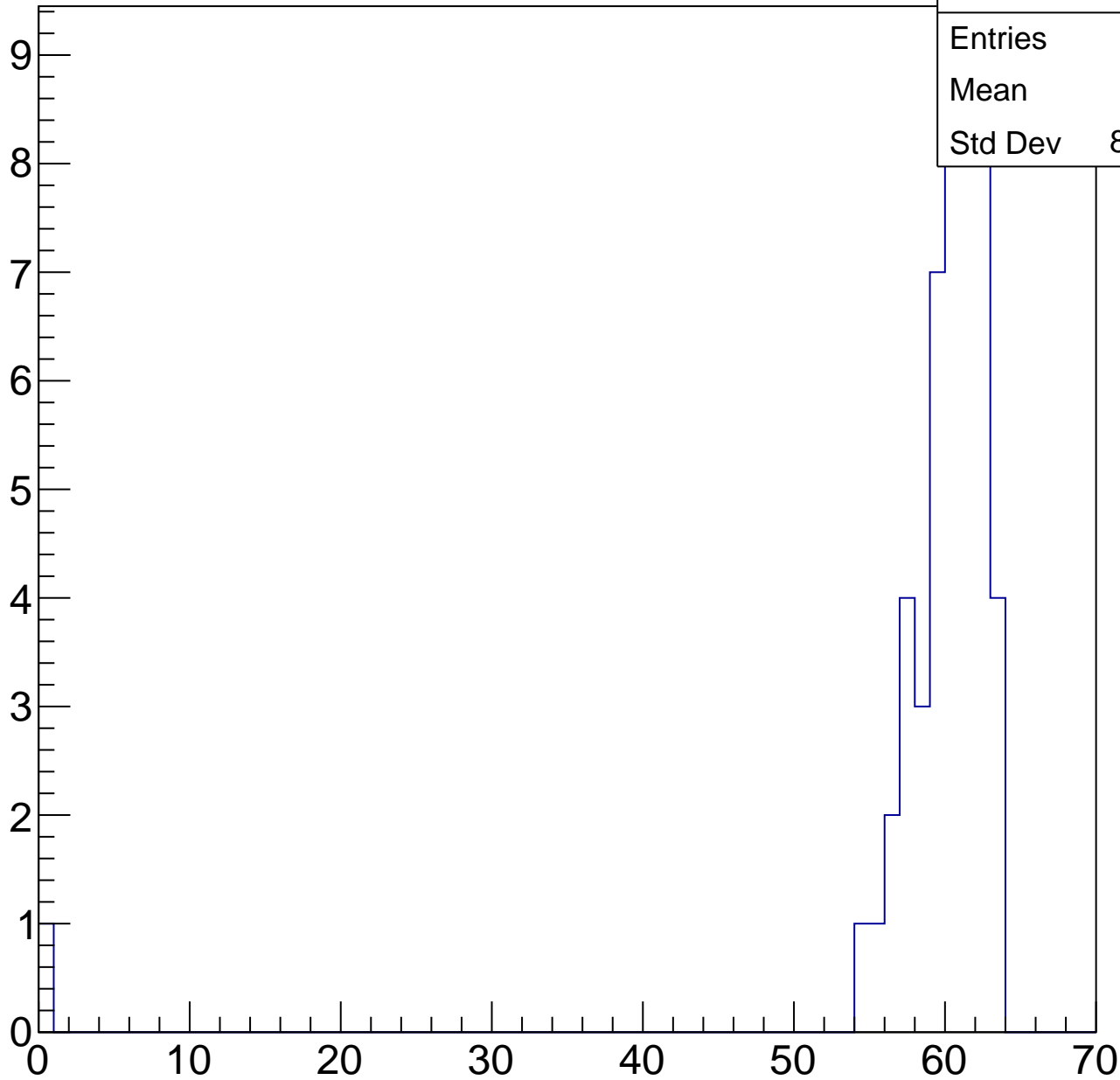
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.6
Std Dev	8.817

ampl

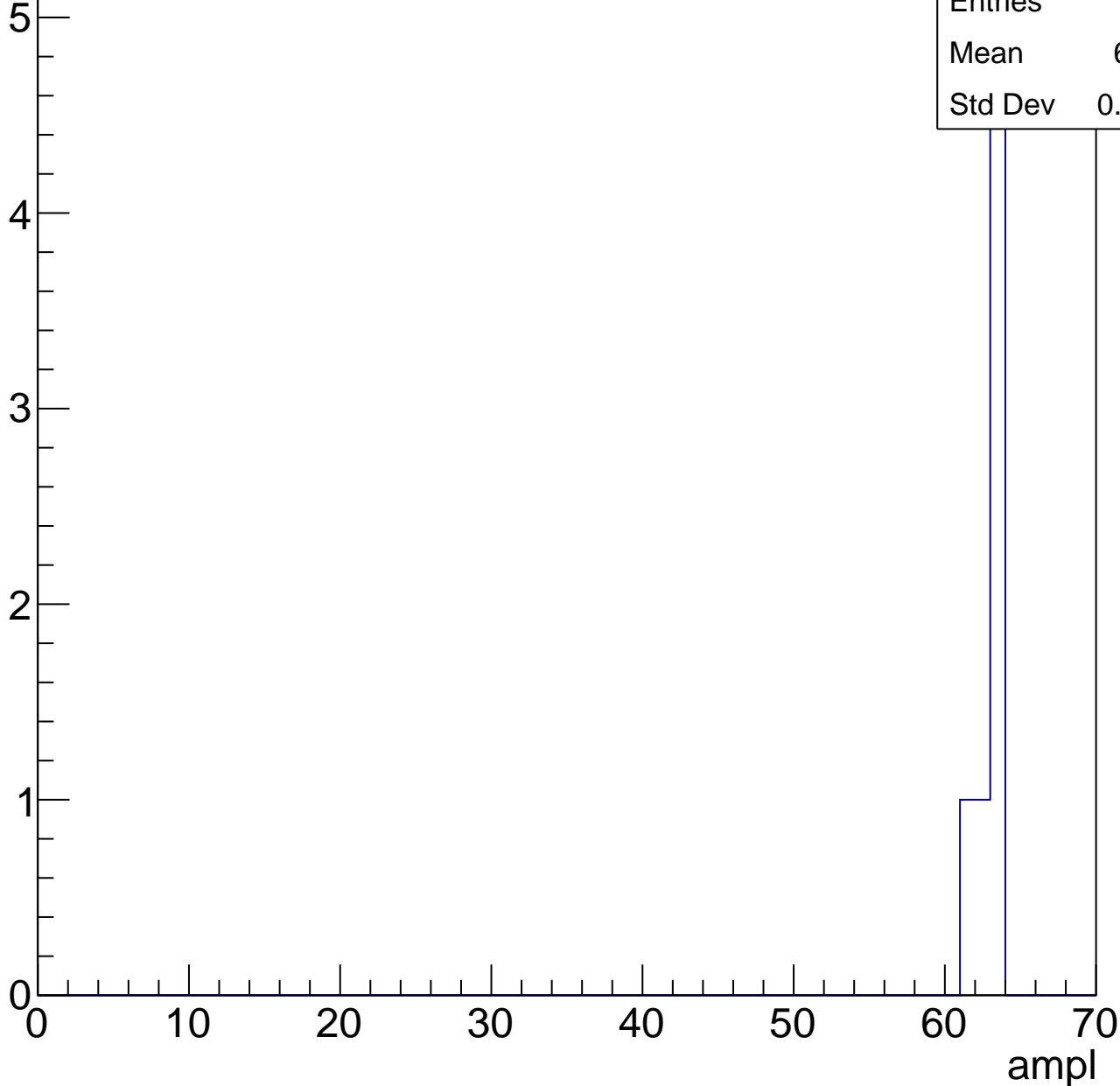


B1L103S, U20-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

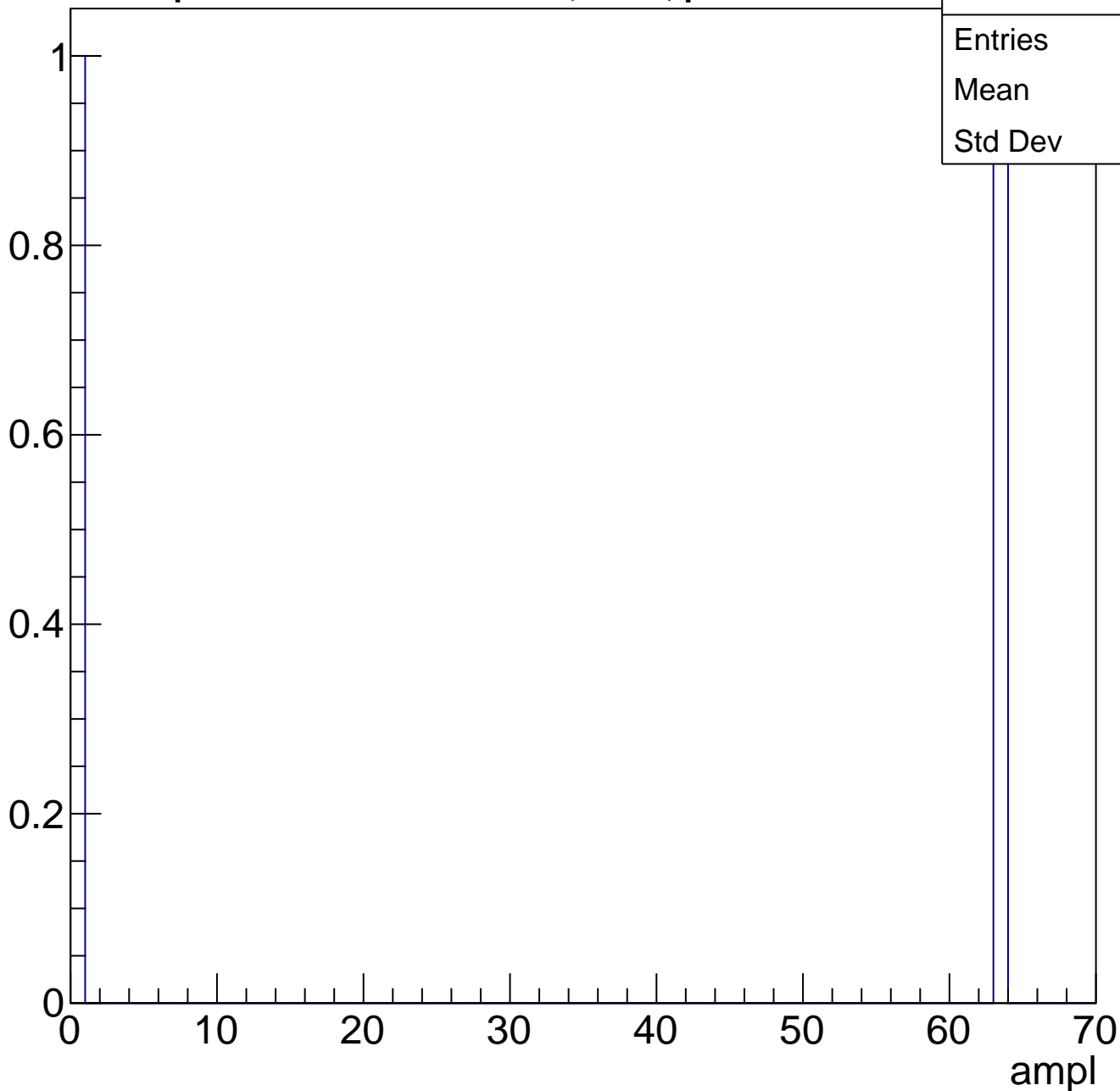
Entries	7
Mean	62.57
Std Dev	0.7284



B1L103S, U20-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch20, adc0

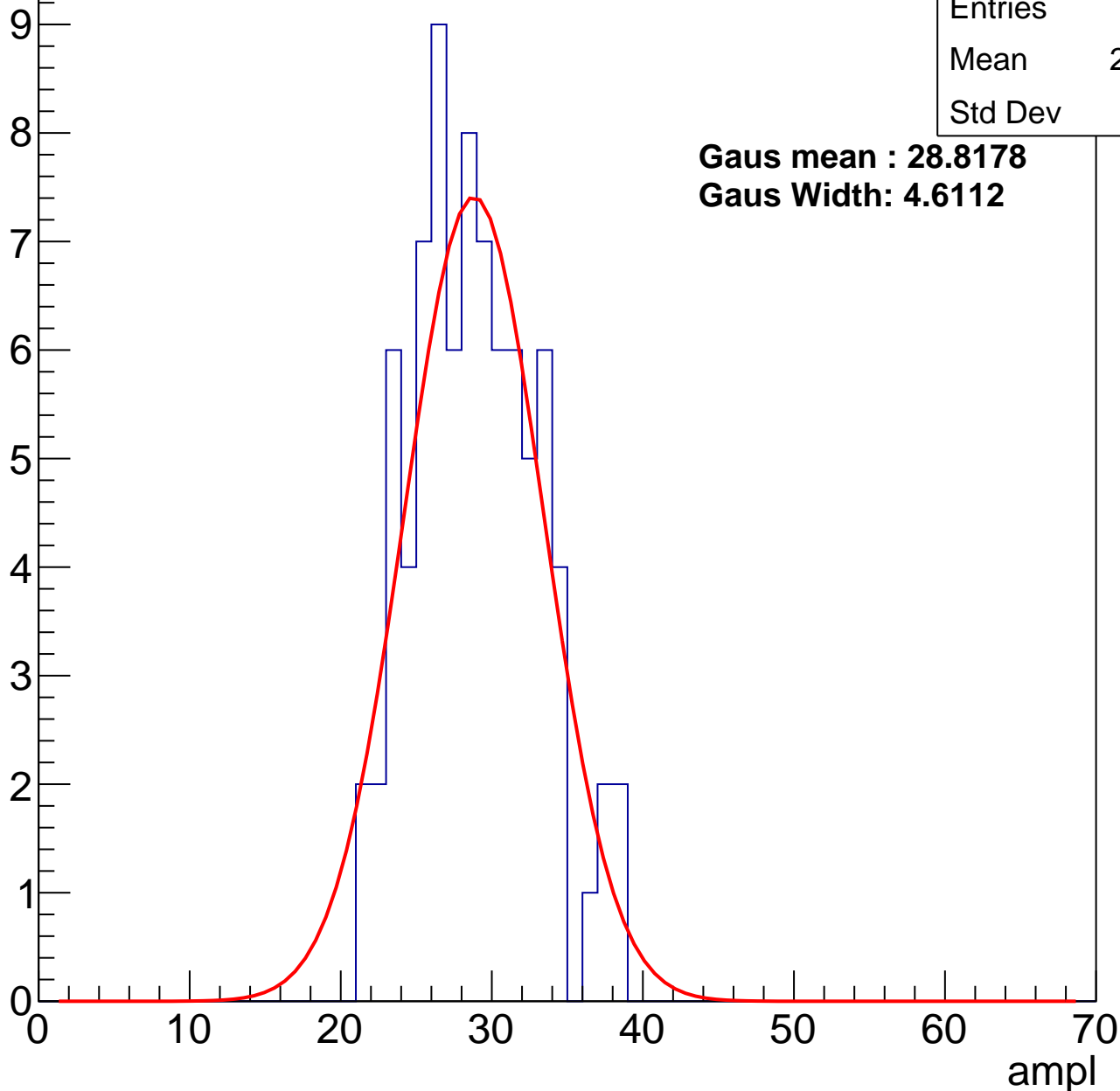
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	28.48
Std Dev	4.04

Gaus mean : 28.8178

Gaus Width: 4.6112



B1L103S, U20-ch20, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	35.66
Std Dev	3.961

Gaus mean : 36.0524

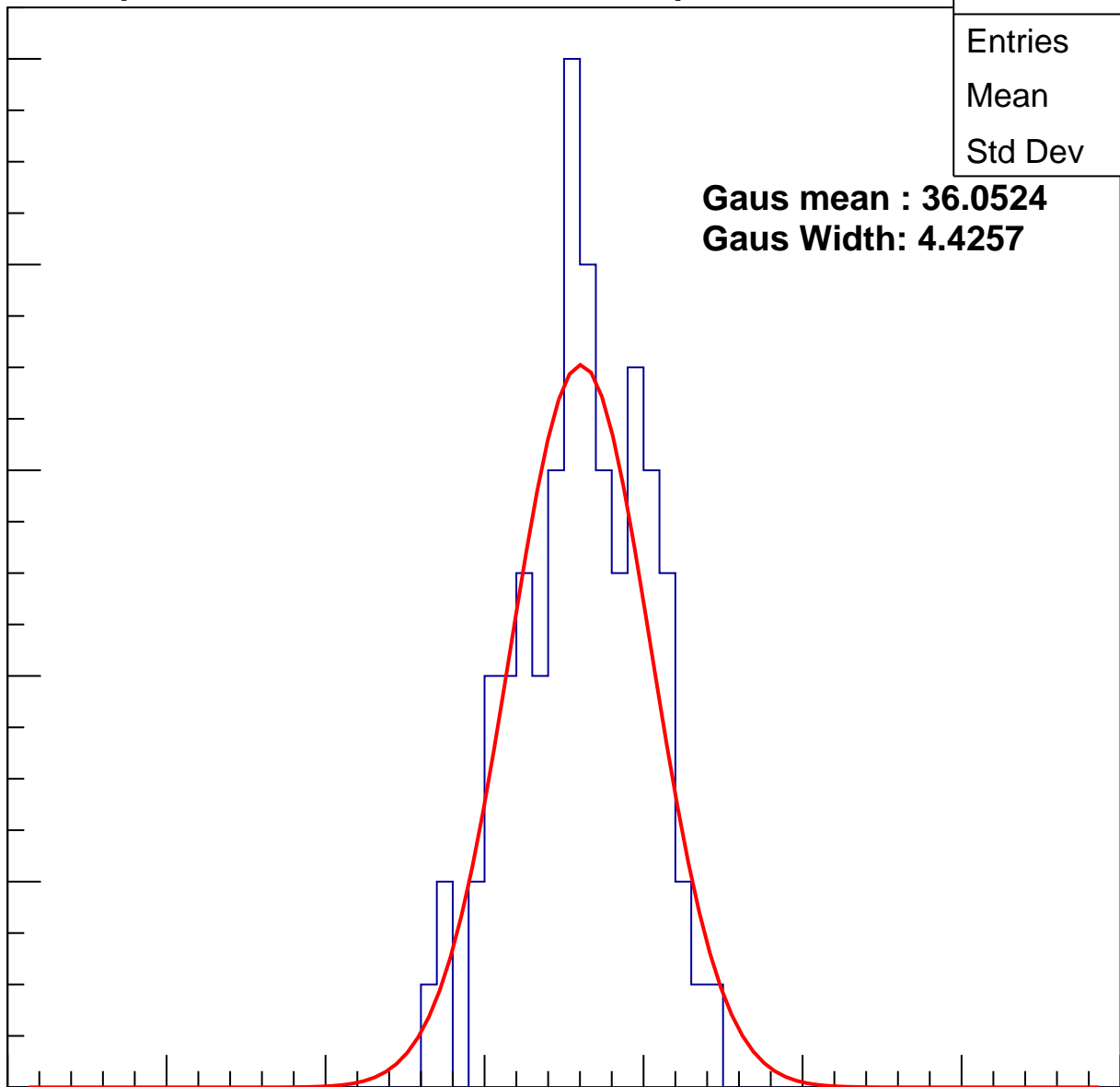
Gaus Width: 4.4257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch20, adc2

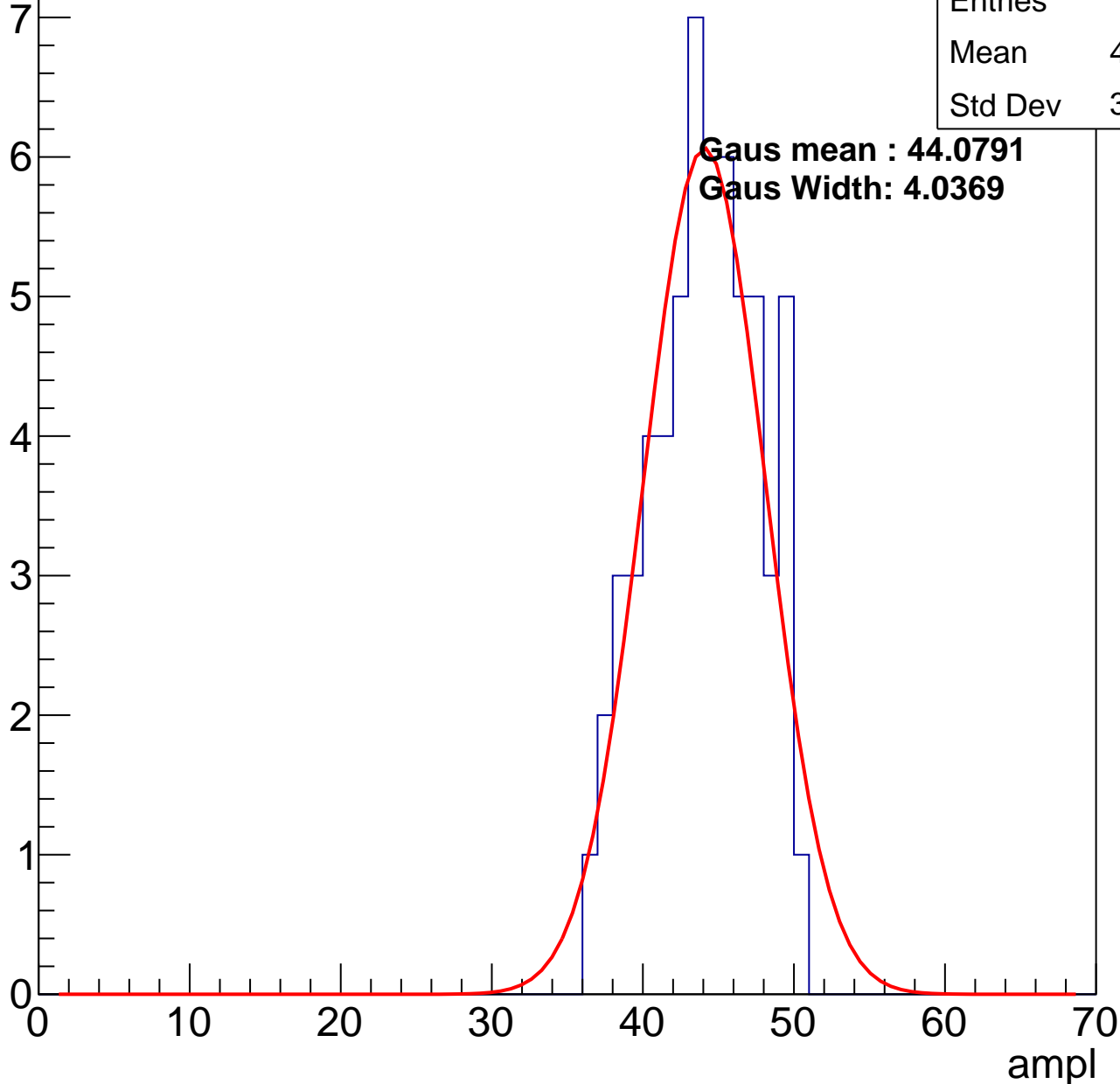
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.57
Std Dev	3.514

Gaus mean : 44.0791

Gaus Width: 4.0369

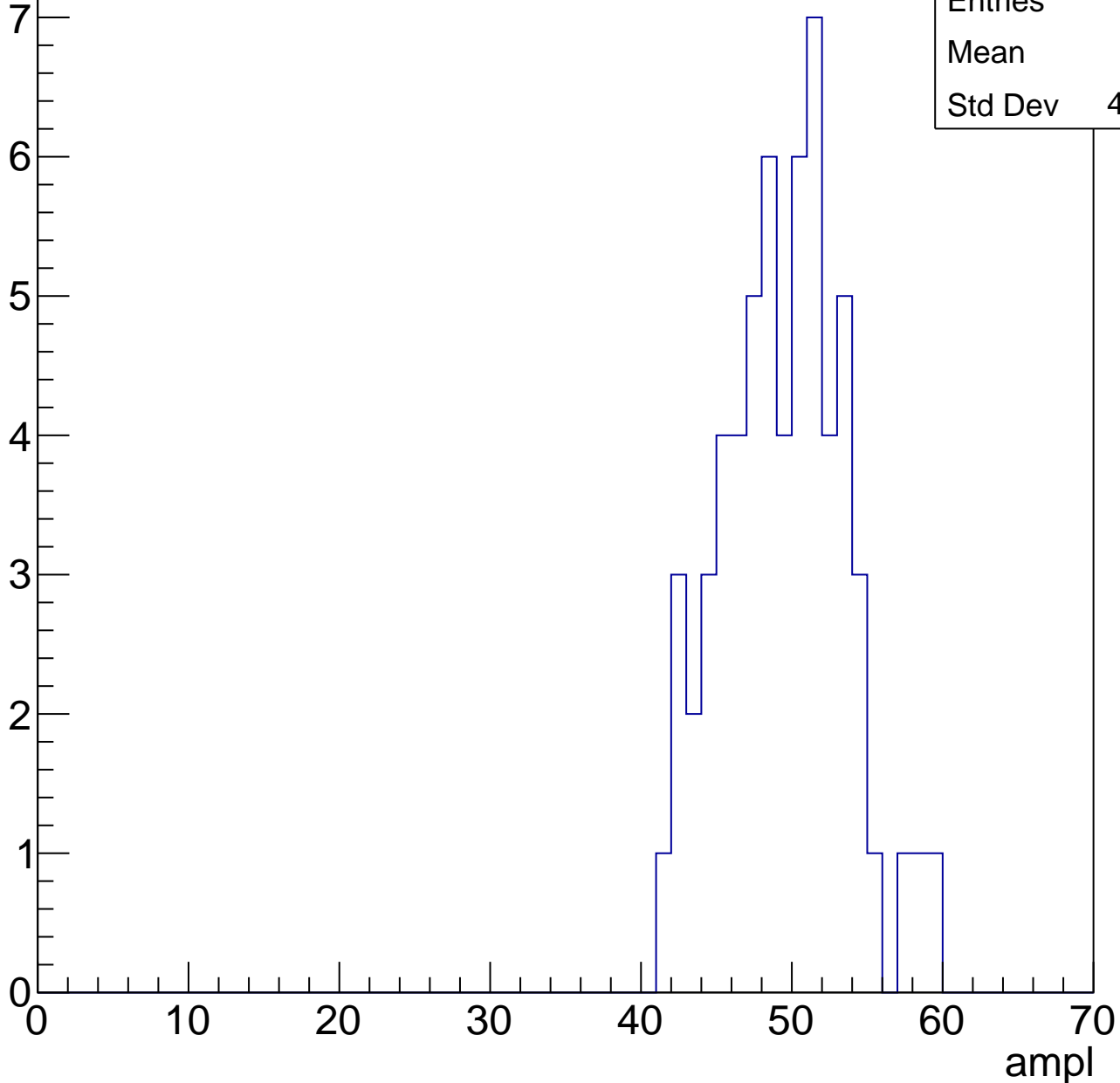


B1L103S, U20-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49
Std Dev	4.029

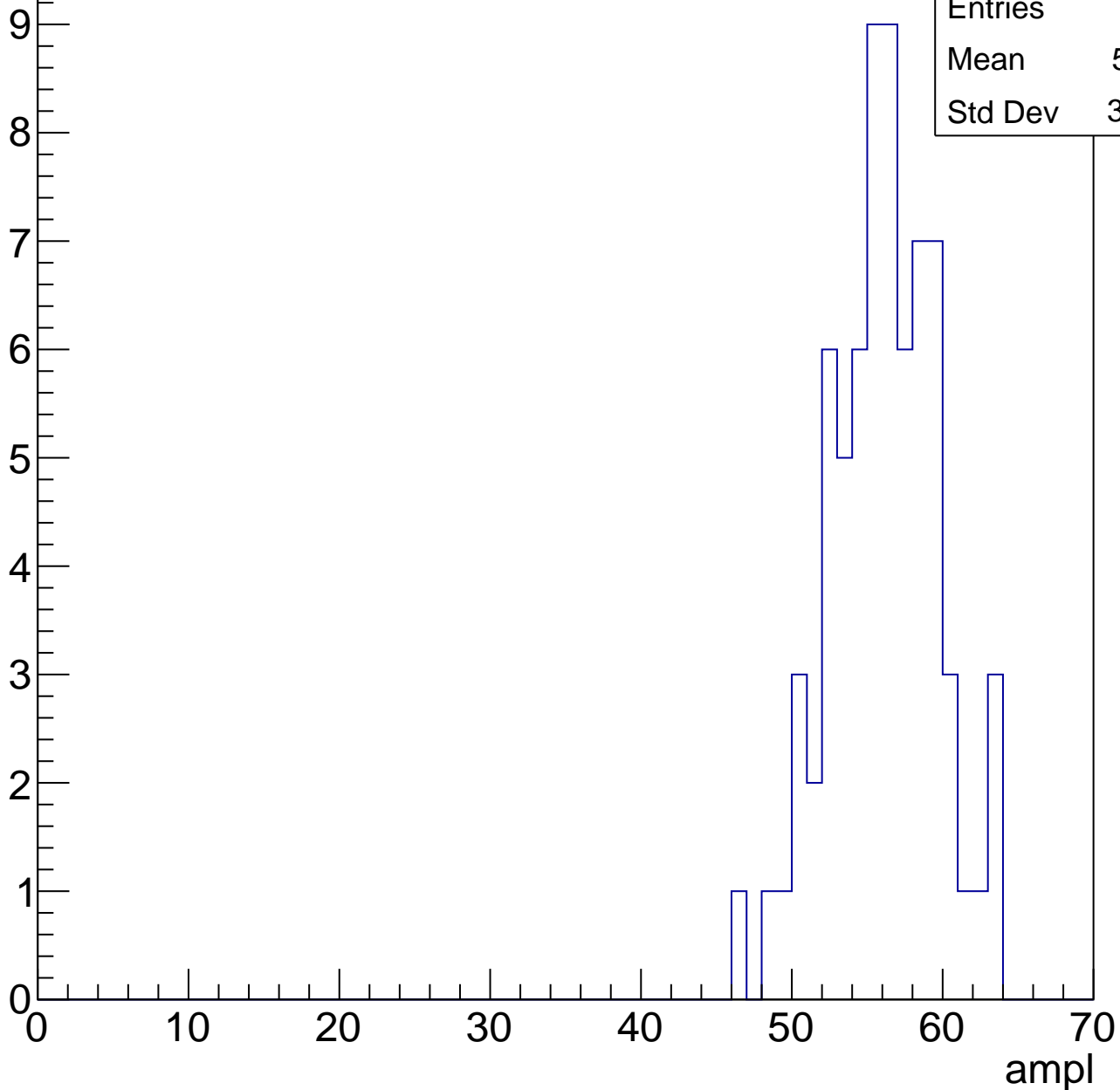


B1L103S, U20-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	55.61
Std Dev	3.534

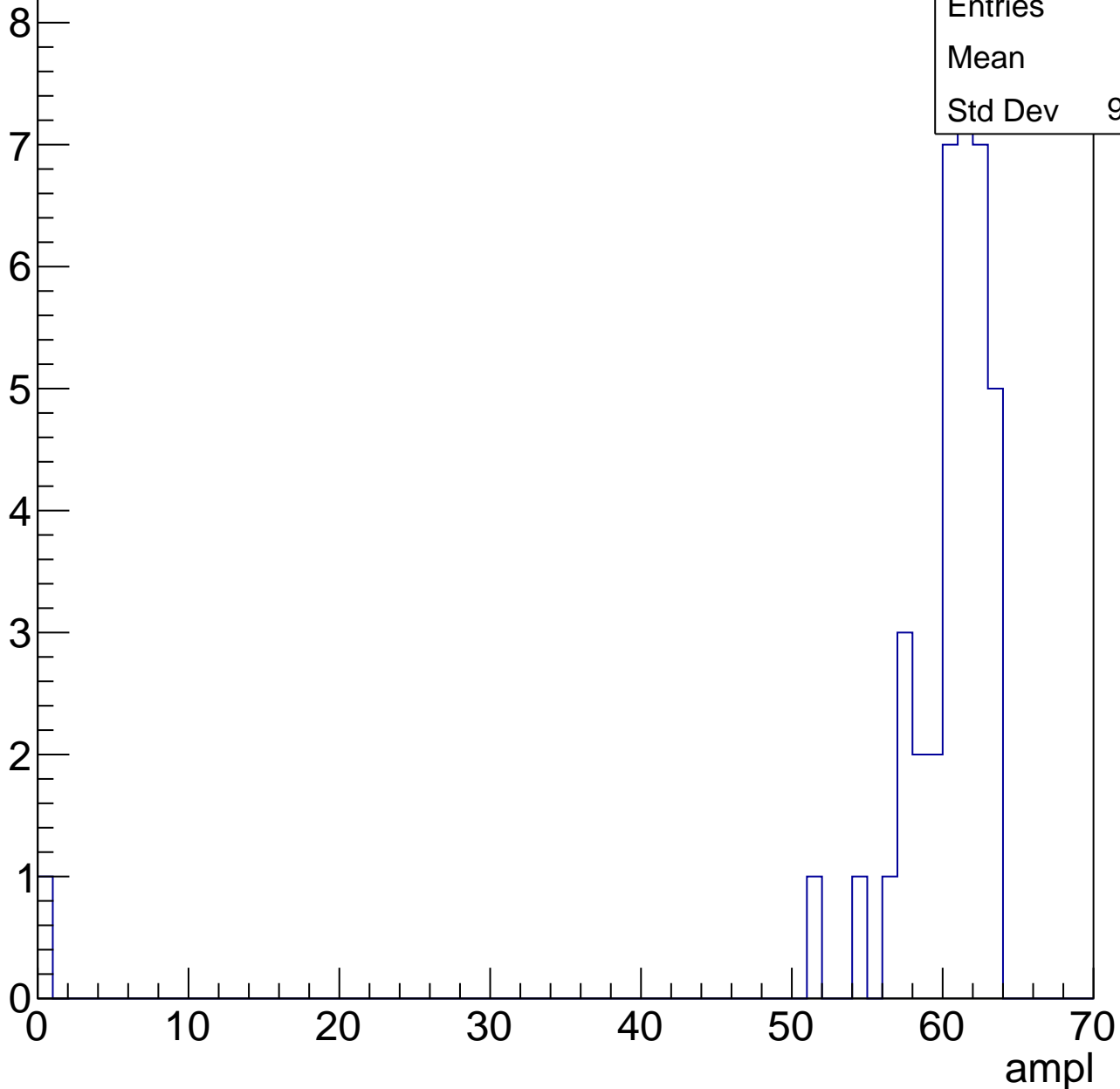


B1L103S, U20-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

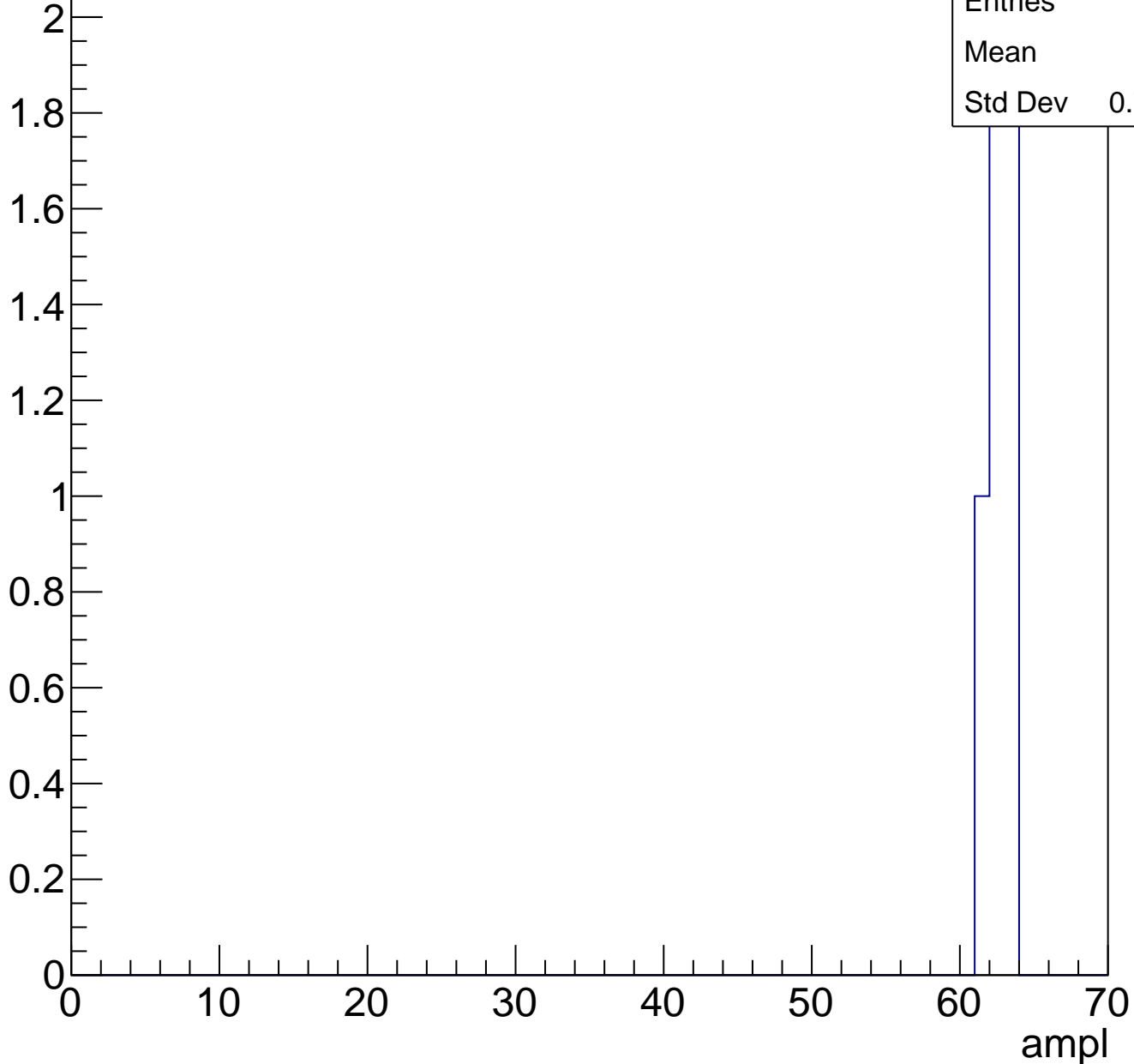
Entries	38
Mean	58.5
Std Dev	9.955



B1L103S, U20-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch21, adc0

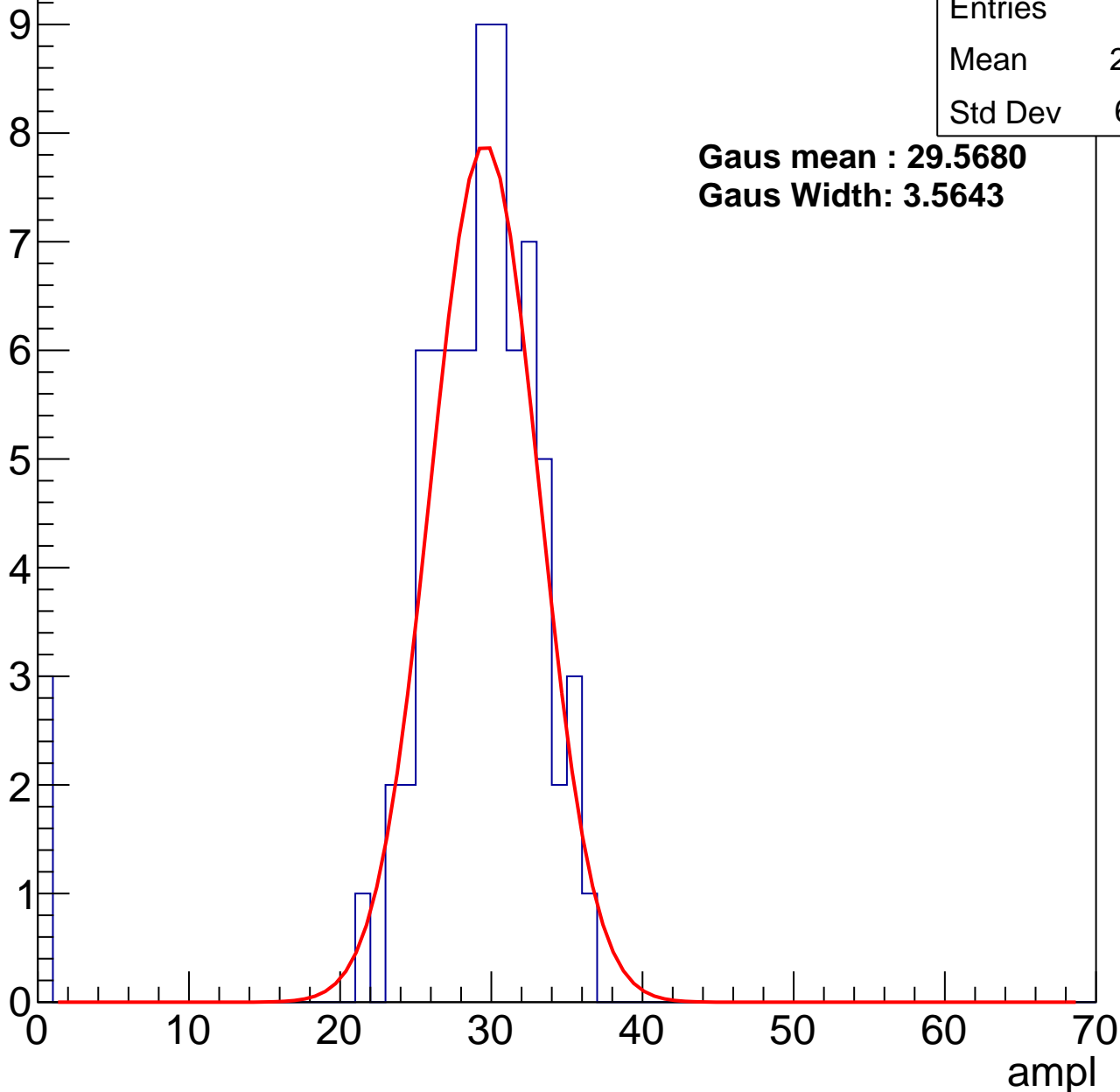
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.92
Std Dev	6.561

Gaus mean : 29.5680

Gaus Width: 3.5643



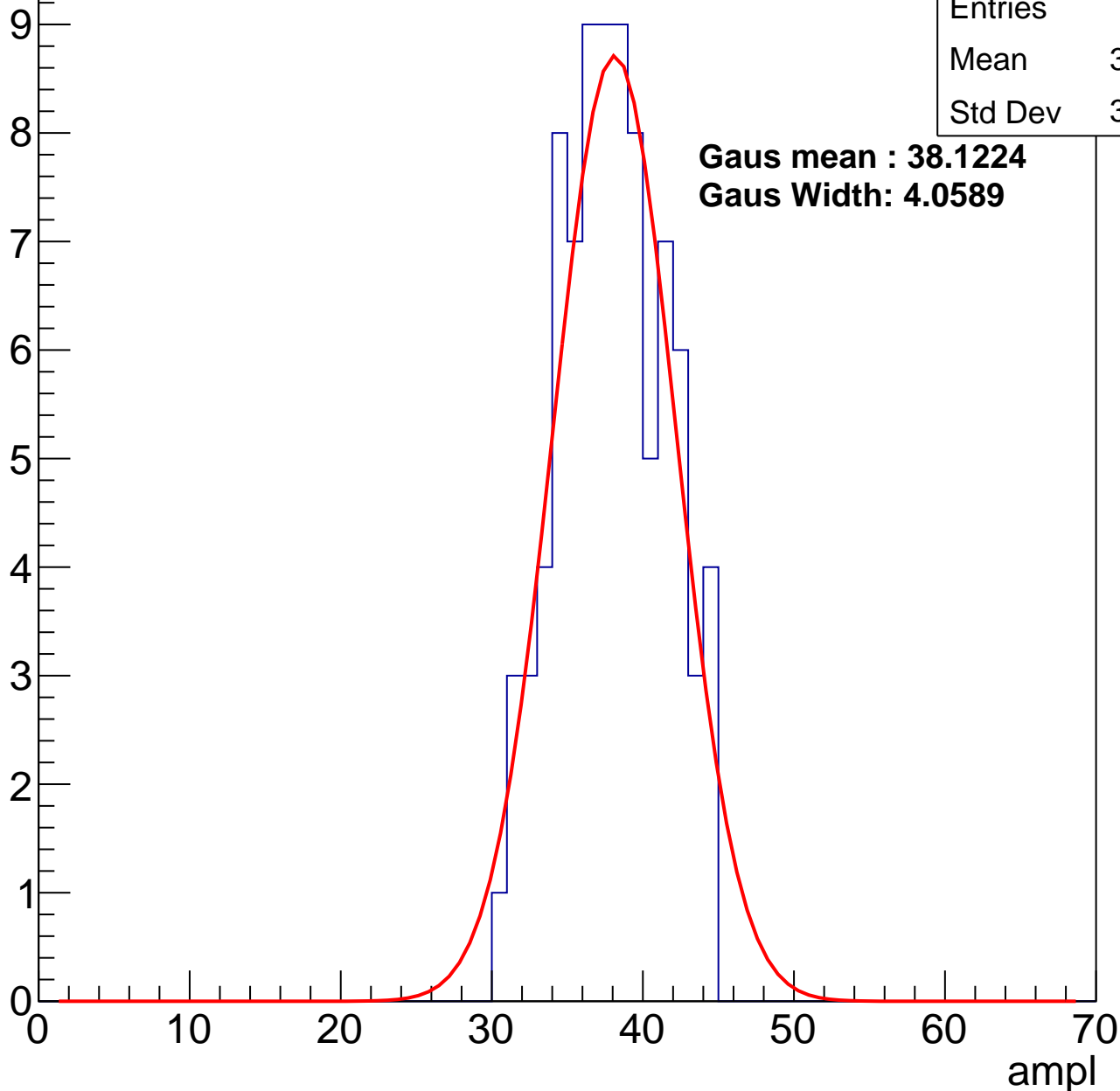
B1L103S, U20-ch21, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	37.48
Std Dev	3.473

Gaus mean : 38.1224
Gaus Width: 4.0589



B1L103S, U20-ch21, adc2

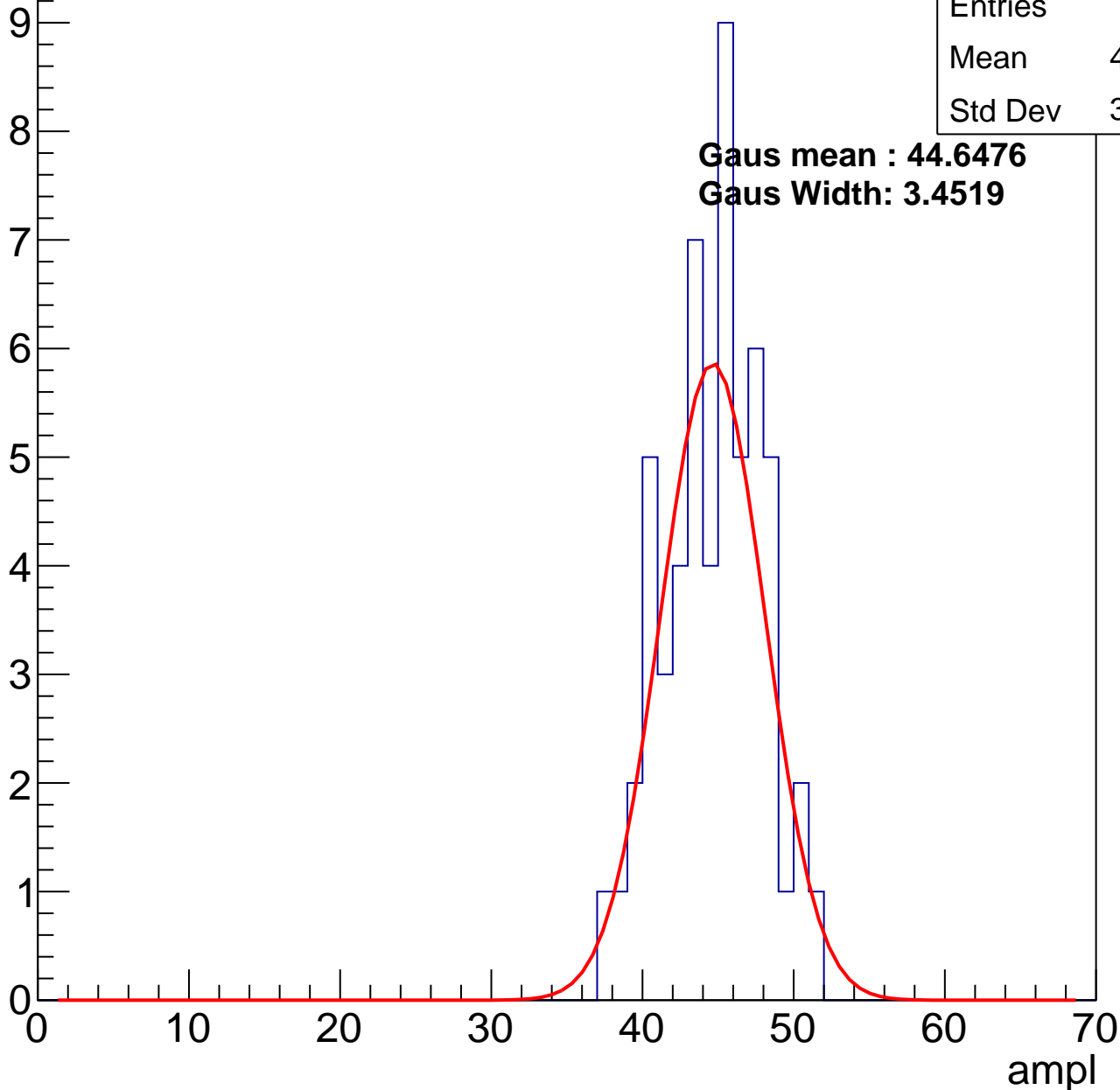
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	44.25
Std Dev	3.192

Gaus mean : 44.6476

Gaus Width: 3.4519

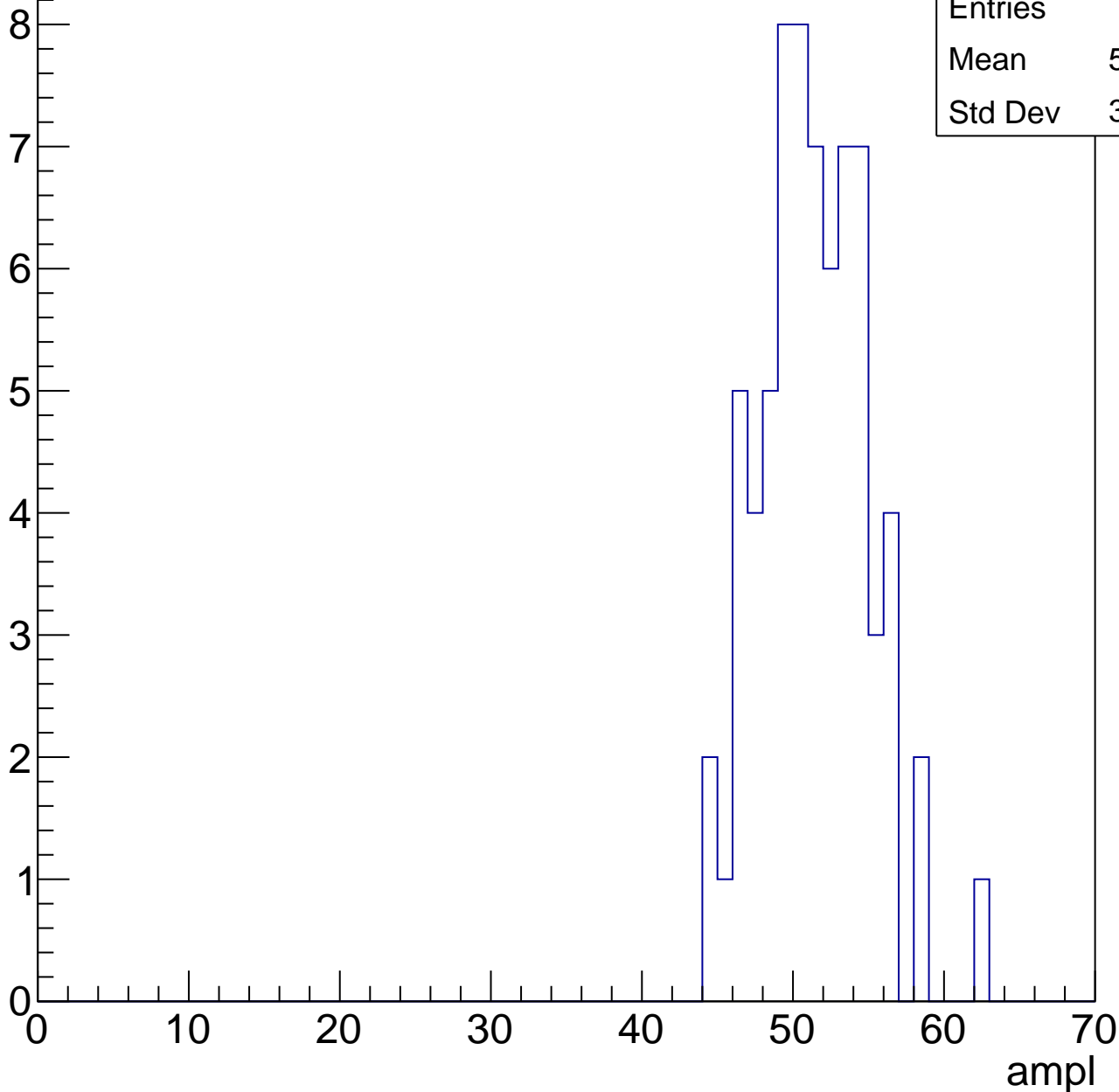


B1L103S, U20-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

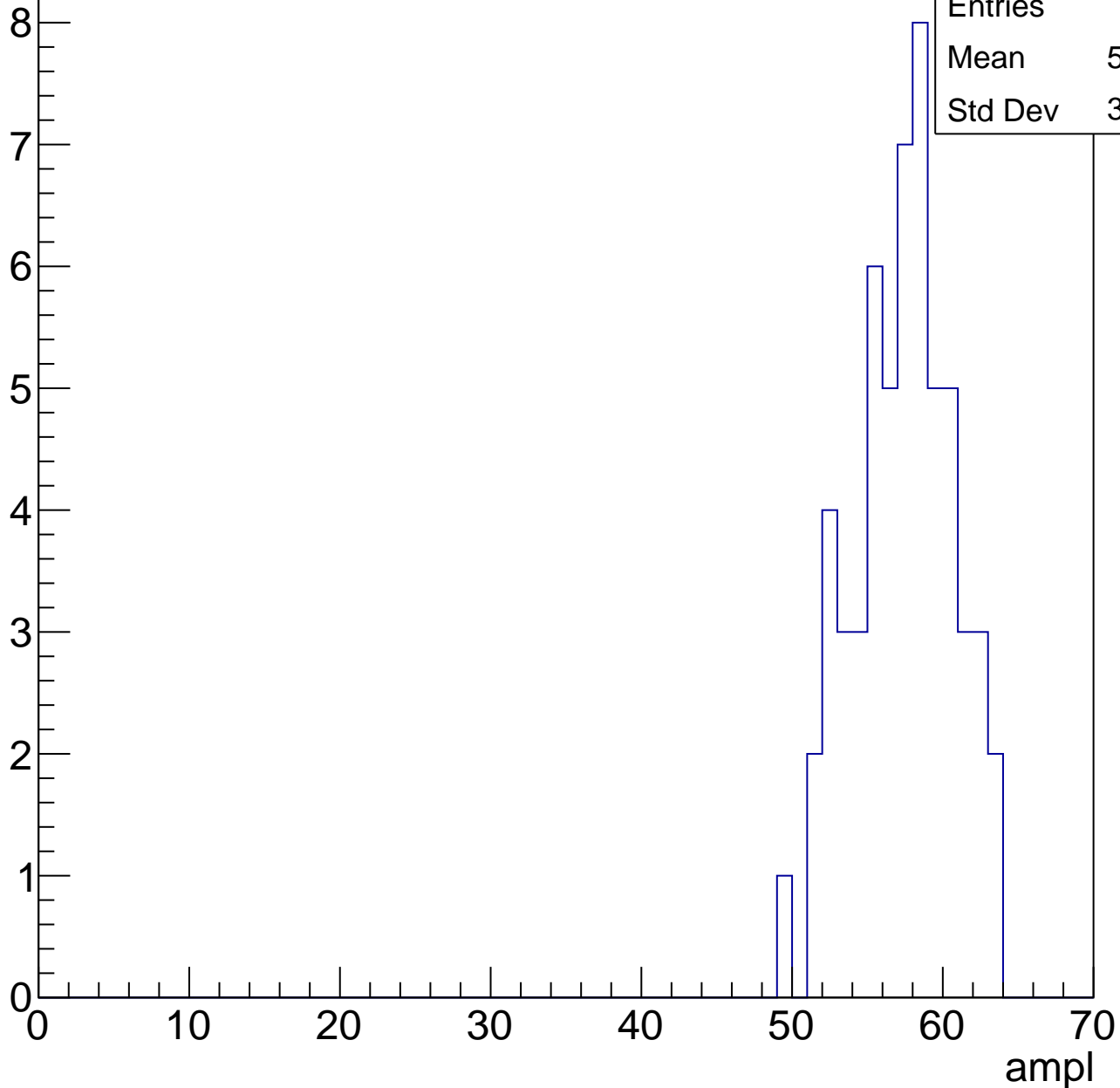
Entries	70
Mean	50.97
Std Dev	3.529



B1L103S, U20-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

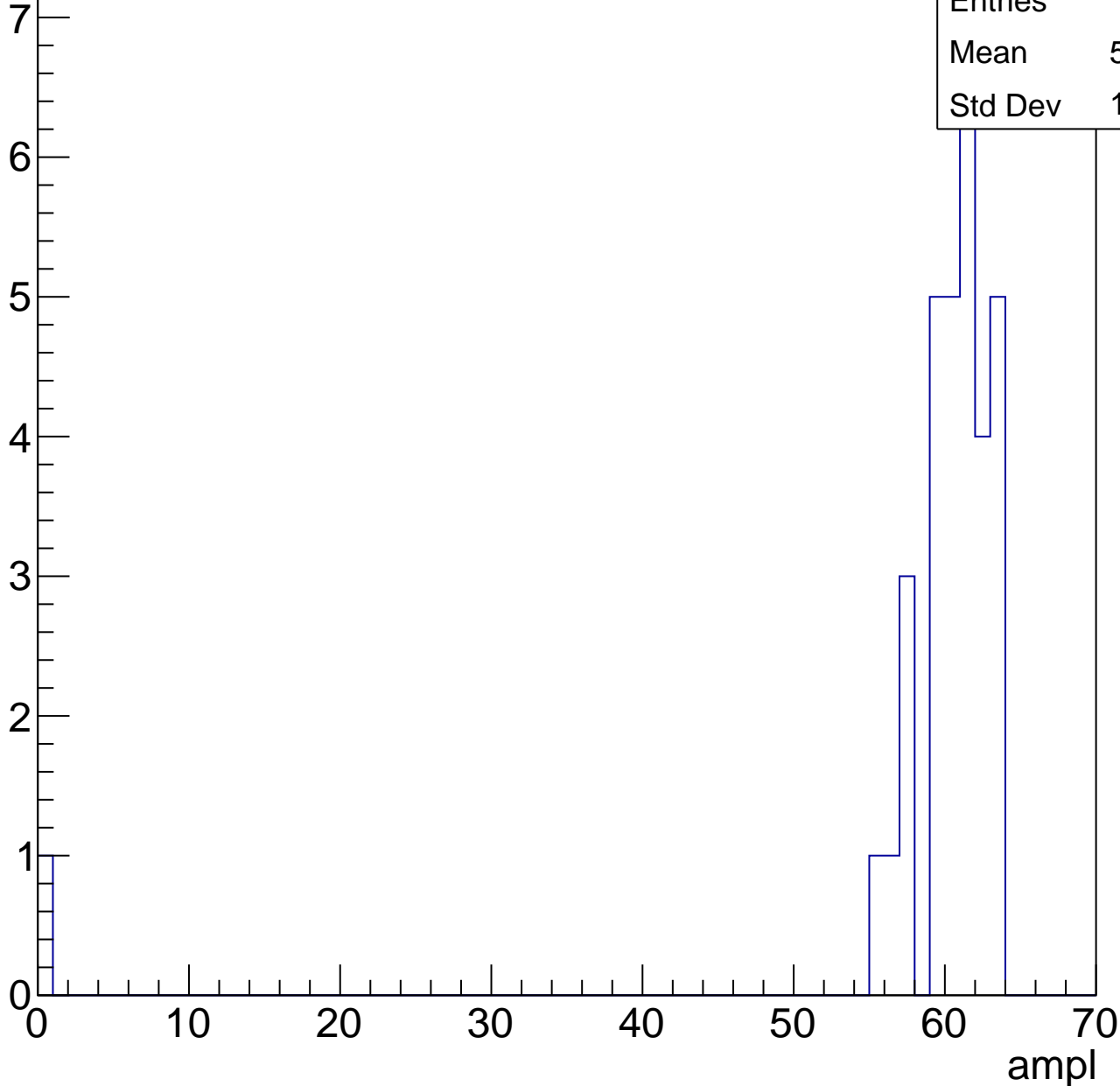


B1L103S, U20-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.34
Std Dev	10.68



B1L103S, U20-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch22, adc0

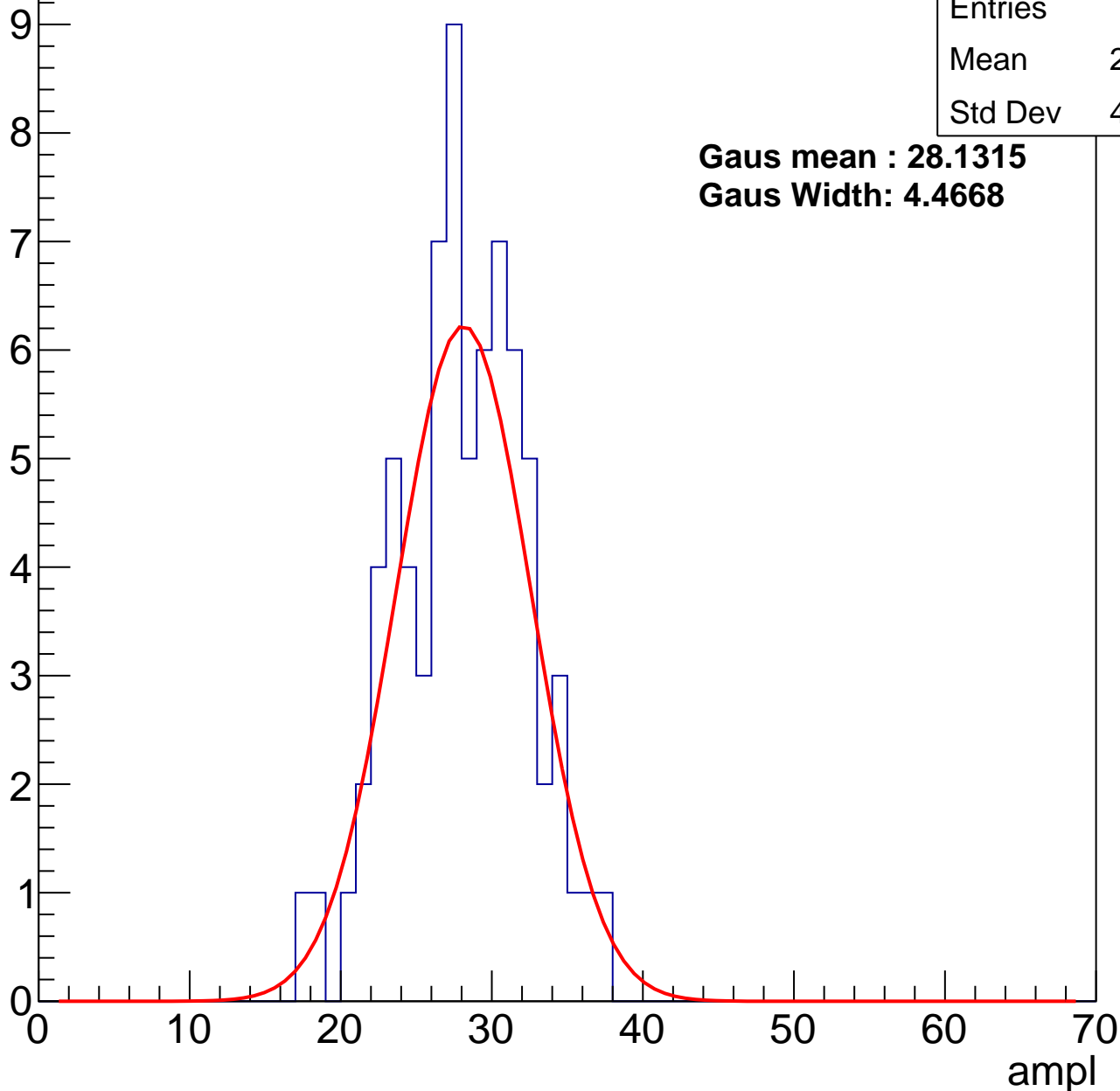
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.59
Std Dev	4.172

Gaus mean : 28.1315

Gaus Width: 4.4668



B1L103S, U20-ch22, adc1

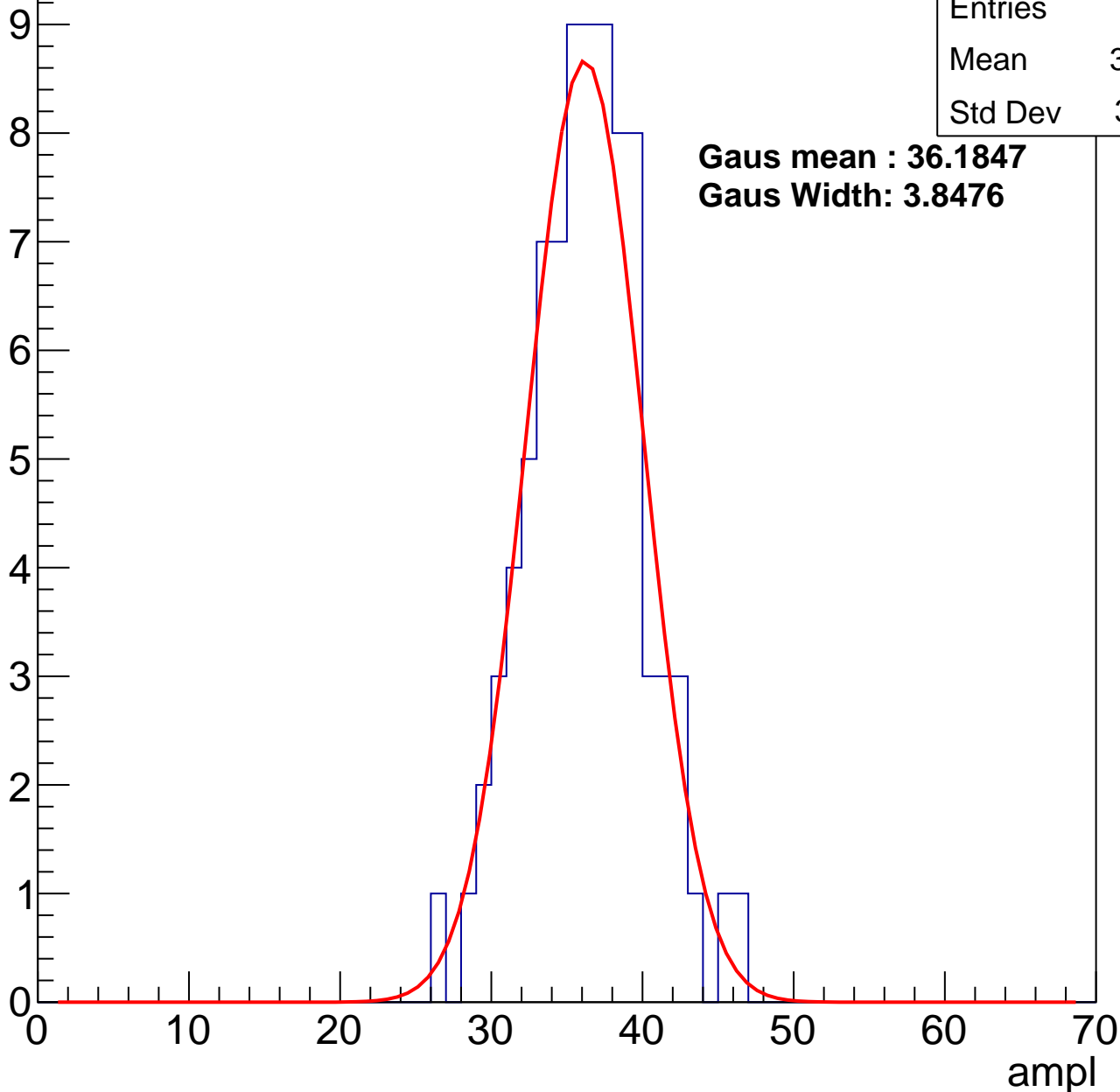
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	35.84
Std Dev	3.791

Gaus mean : 36.1847

Gaus Width: 3.8476



B1L103S, U20-ch22, adc2

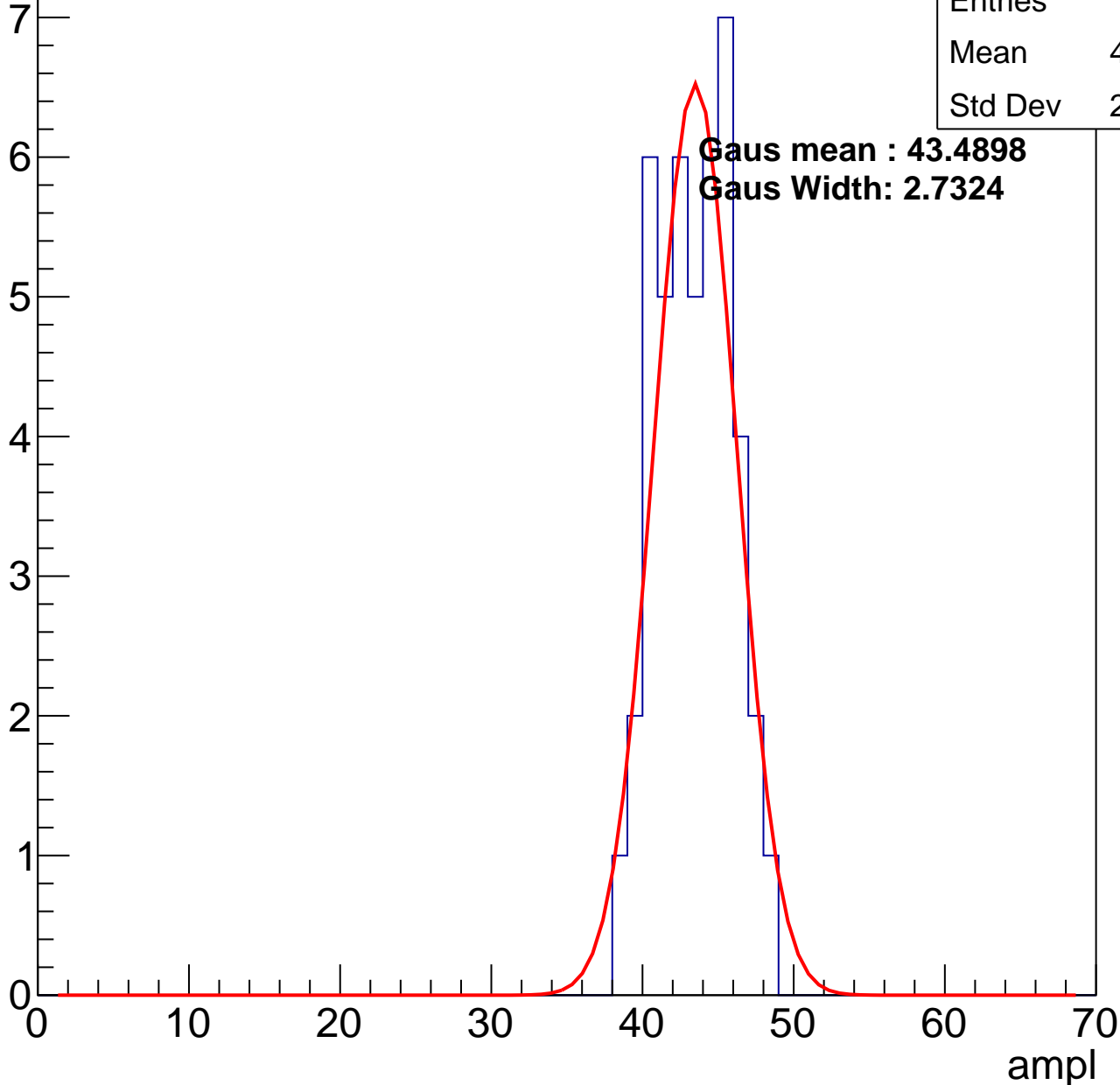
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	42.96
Std Dev	2.422

Gaus mean : 43.4898

Gaus Width: 2.7324

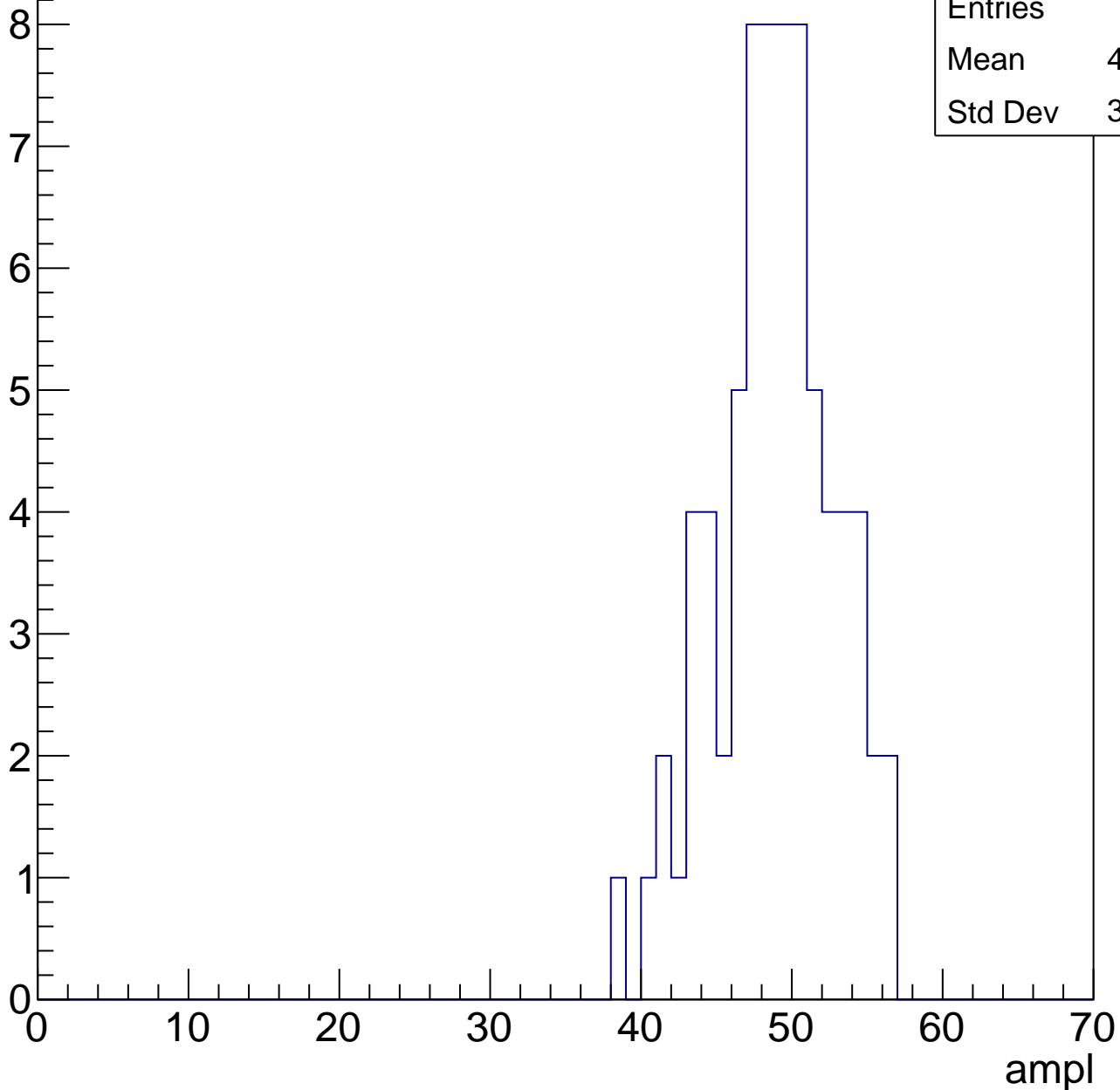


B1L103S, U20-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	48.42
Std Dev	3.906

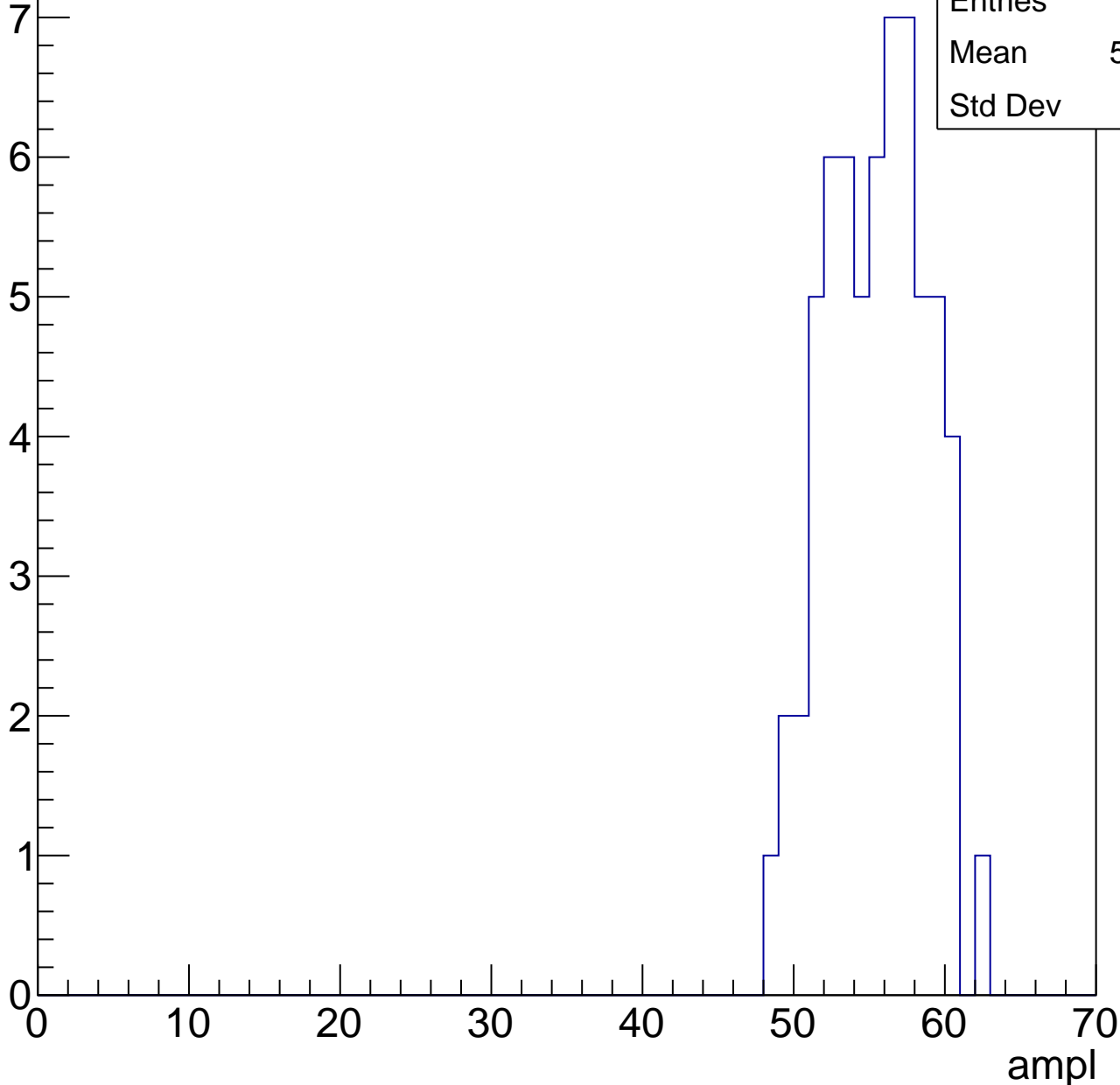


B1L103S, U20-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.98
Std Dev	3.22

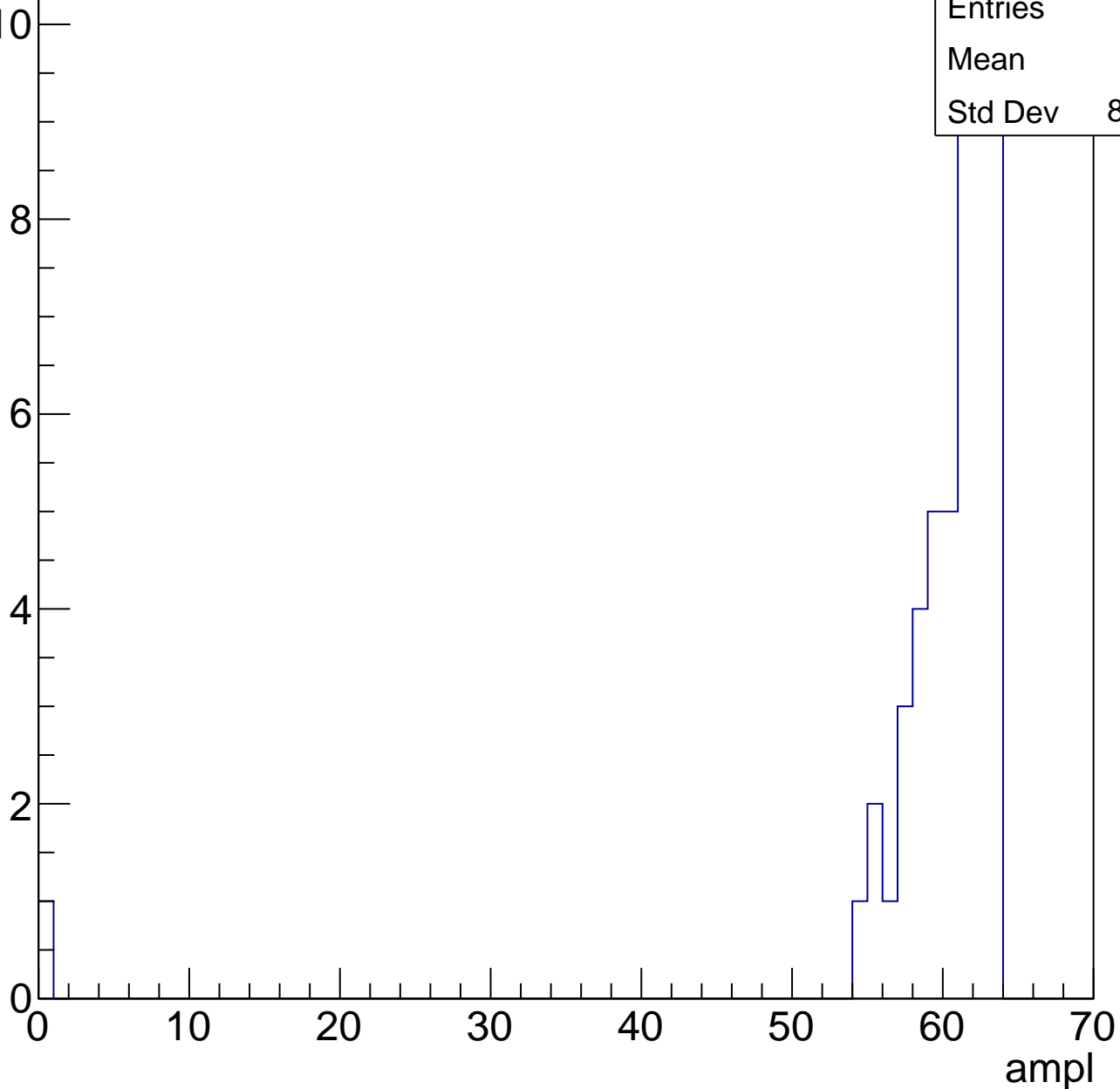


B1L103S, U20-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	59.1
Std Dev	8.769



B1L103S, U20-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

59.67

Std Dev

1.247

B1L103S, U20-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch23, adc0

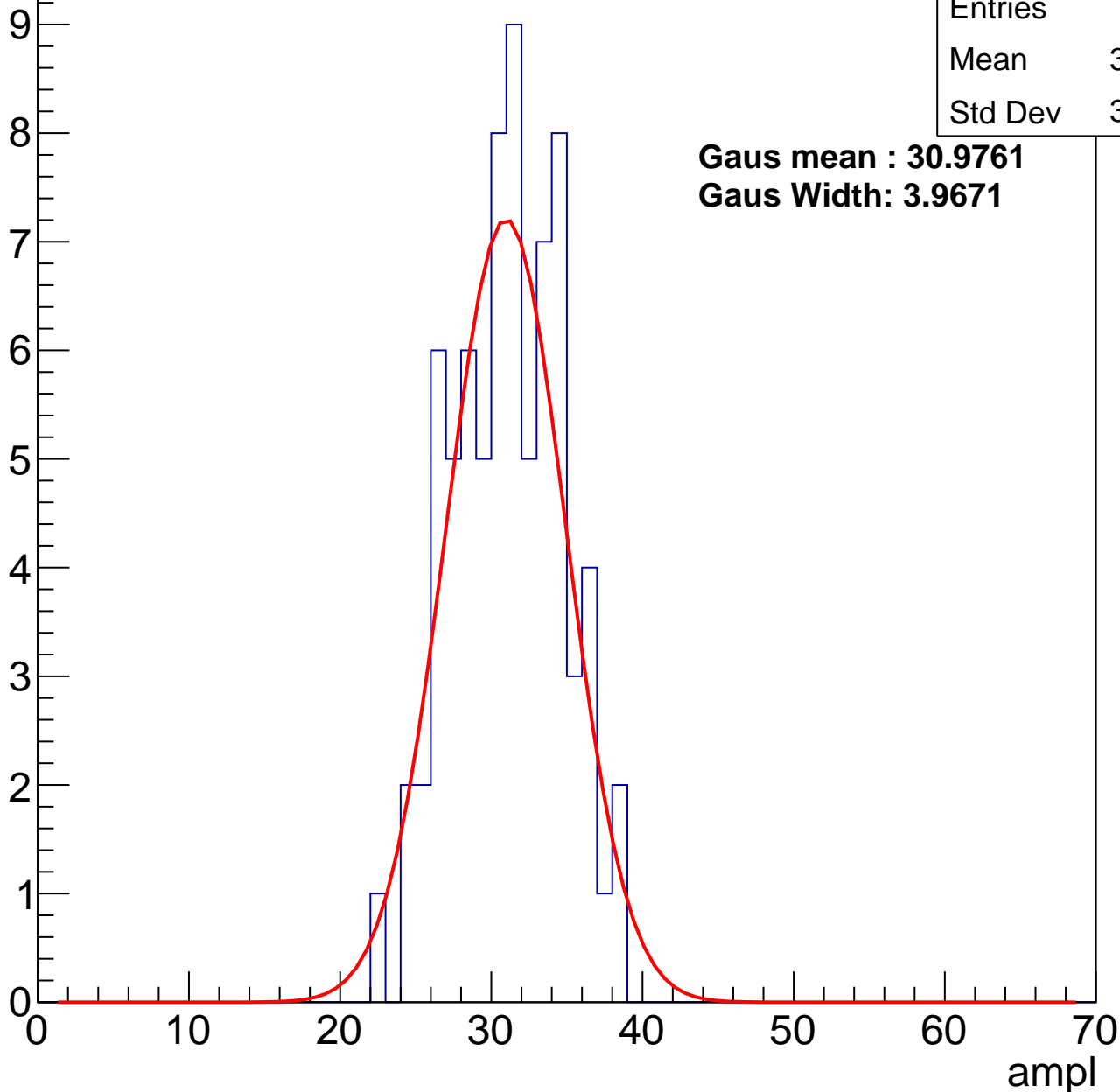
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.65
Std Dev	3.566

Gaus mean : 30.9761

Gaus Width: 3.9671



B1L103S, U20-ch23, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	38.73
Std Dev	3.5

Gaus mean : 39.0413

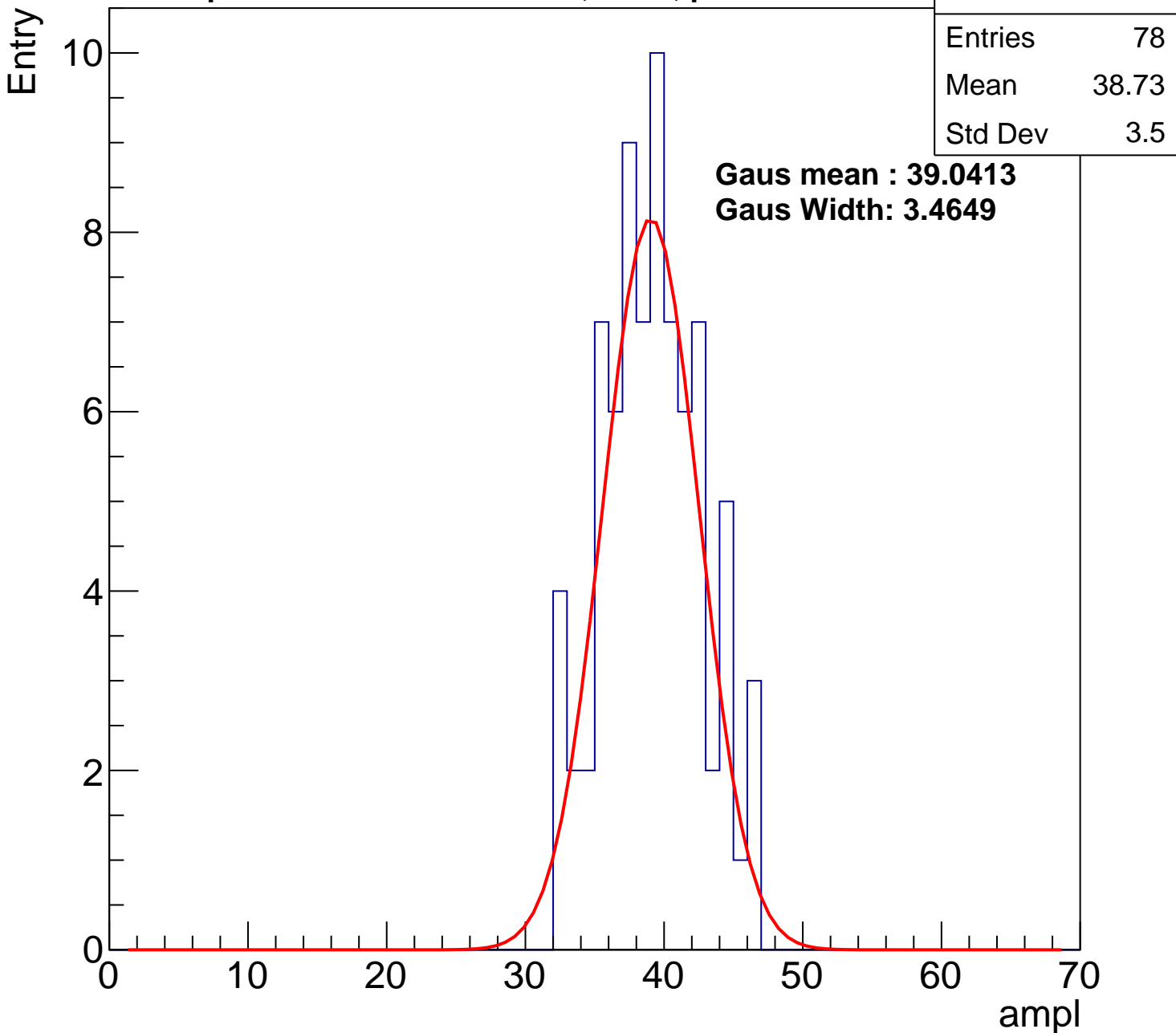
Gaus Width: 3.4649

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch23, adc2

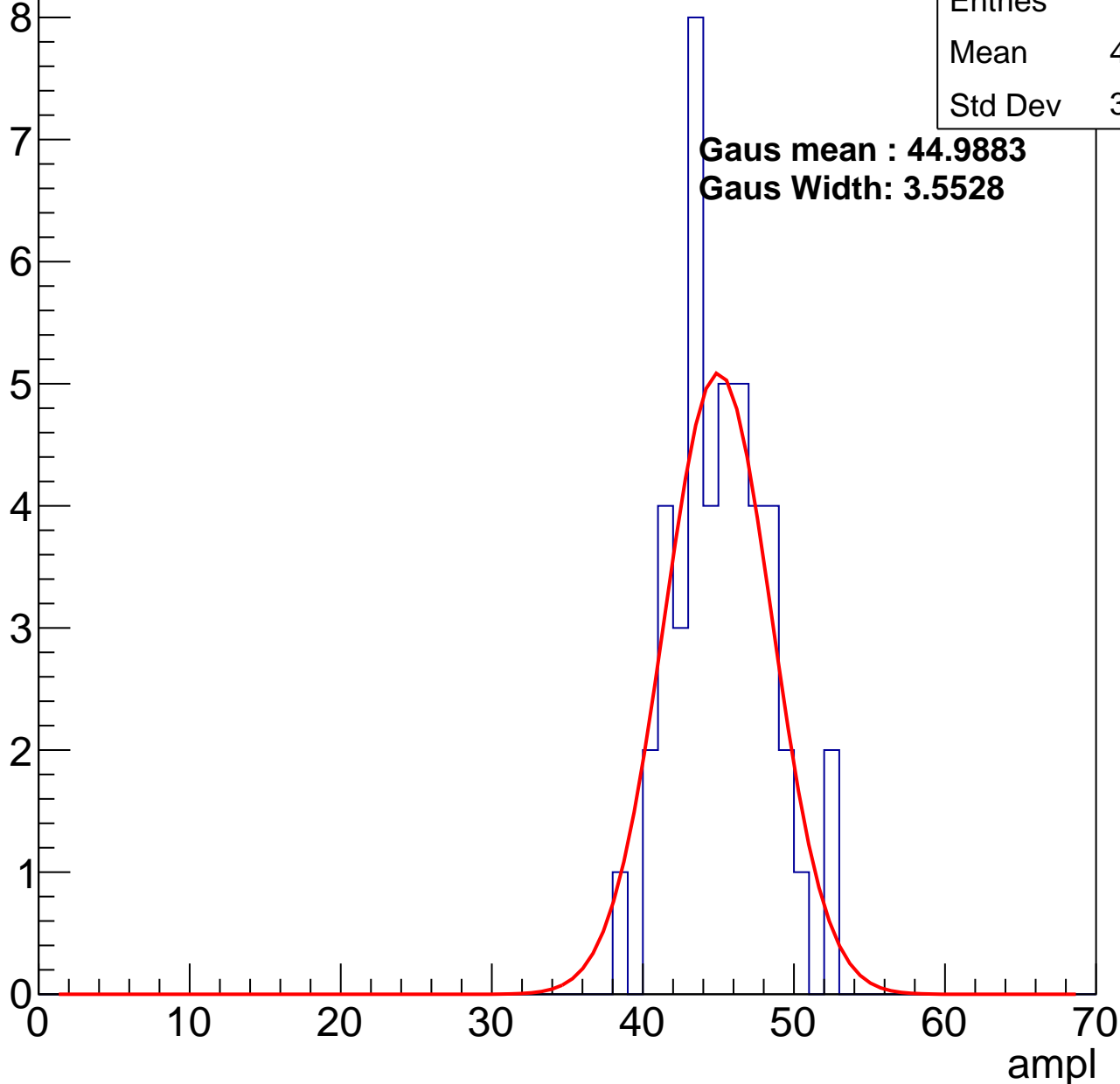
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	44.78
Std Dev	3.112

Gaus mean : 44.9883

Gaus Width: 3.5528

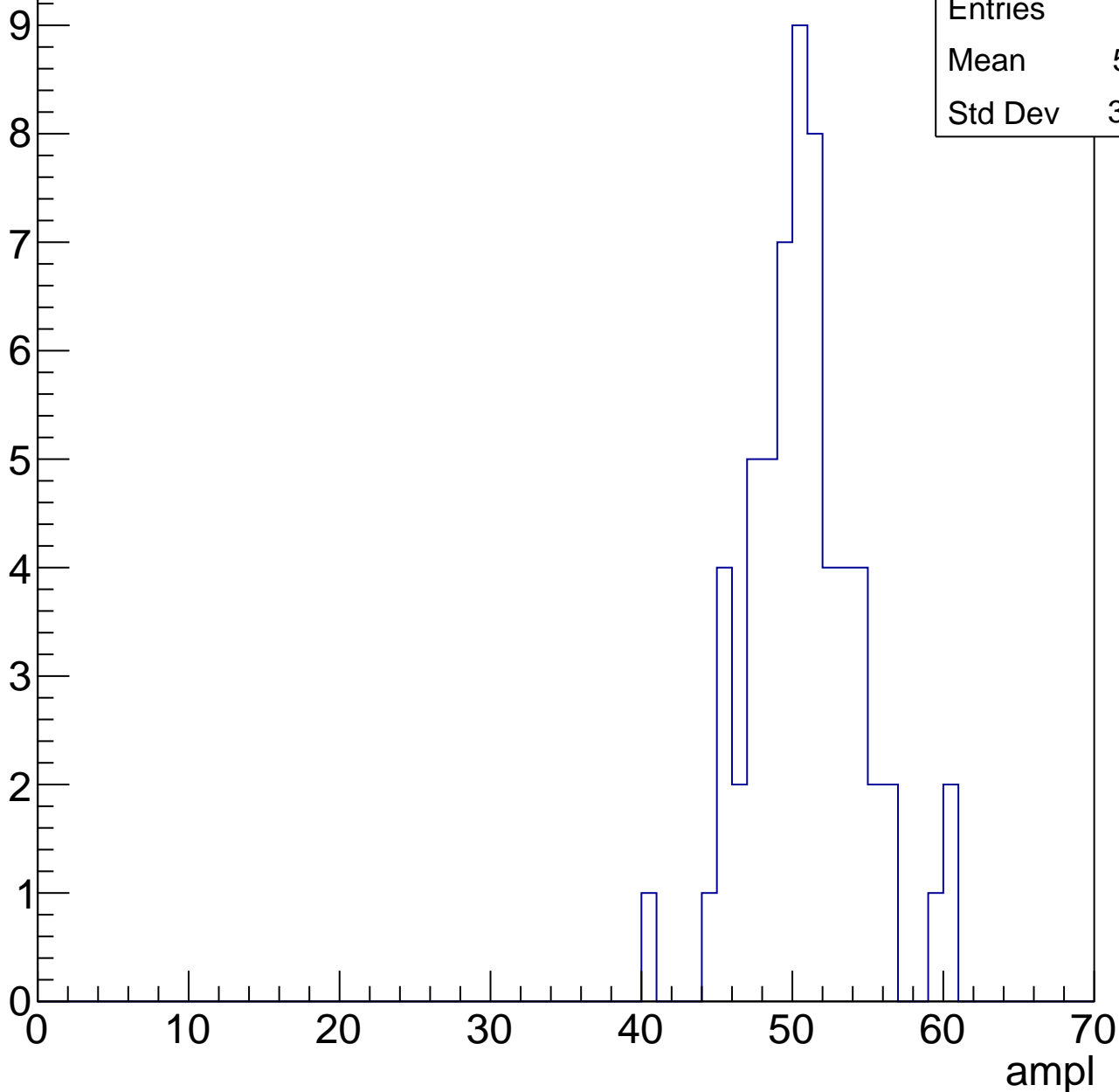


B1L103S, U20-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.31
Std Dev	3.757

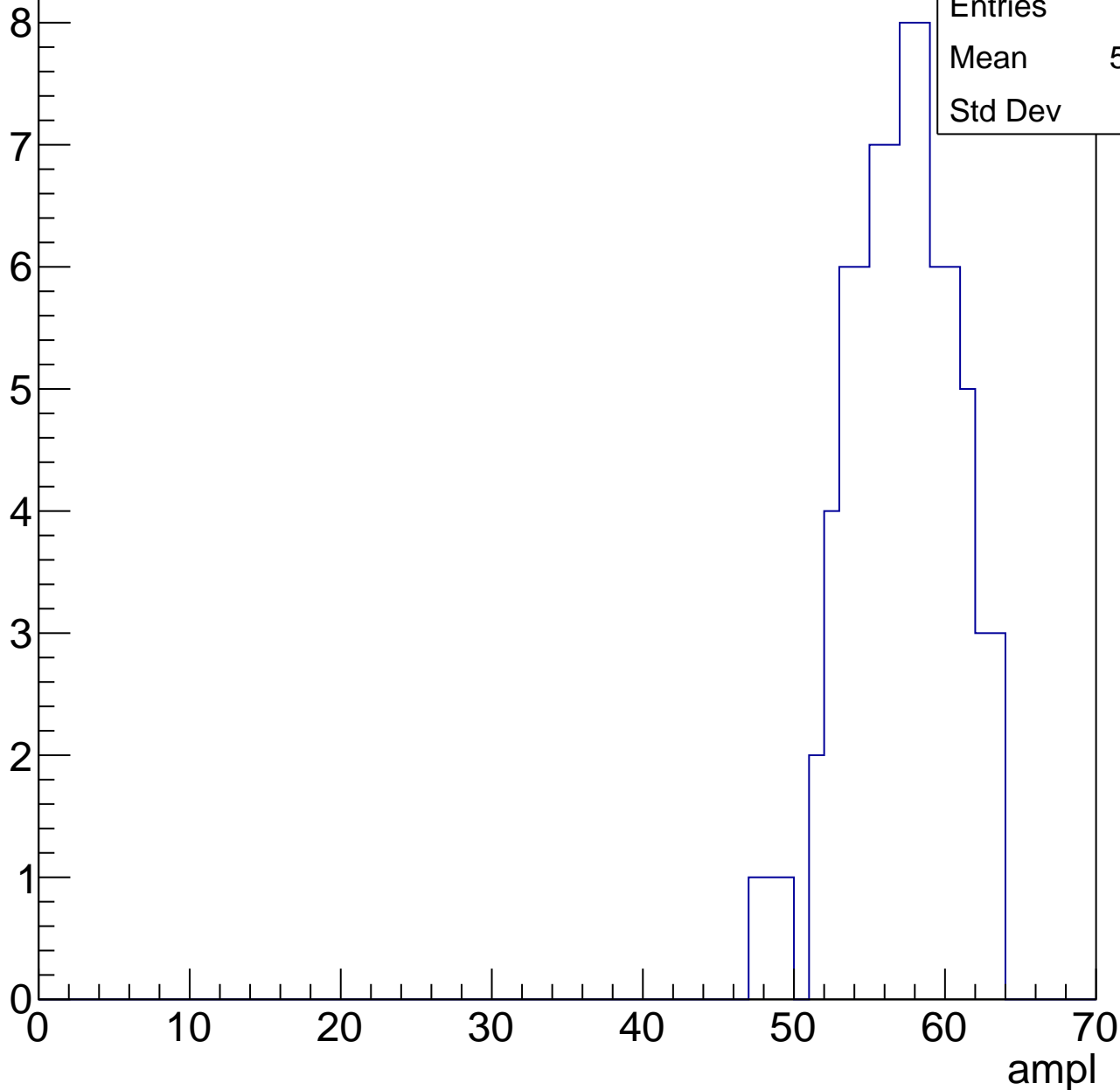


B1L103S, U20-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	56.58
Std Dev	3.56

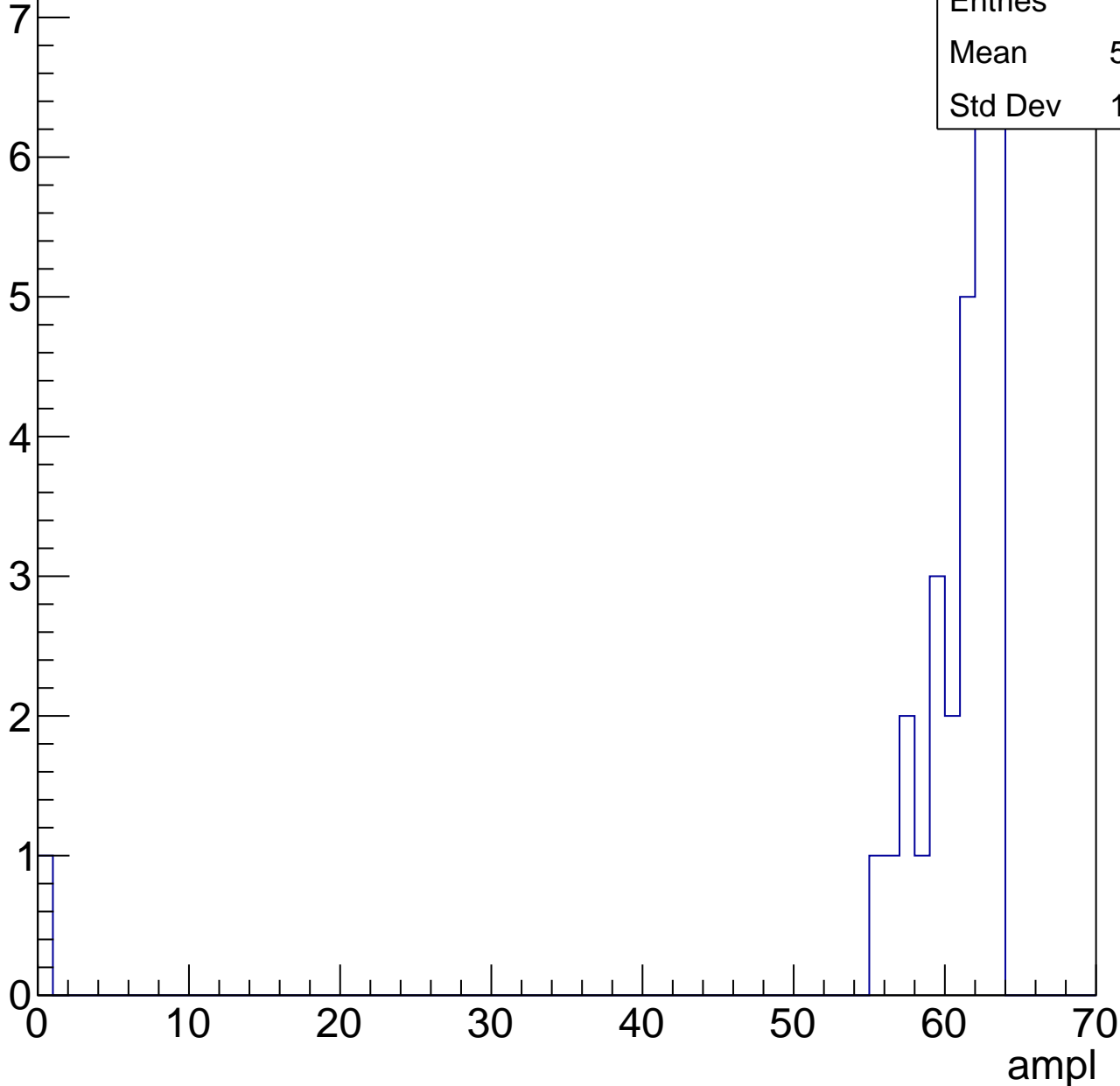


B1L103S, U20-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

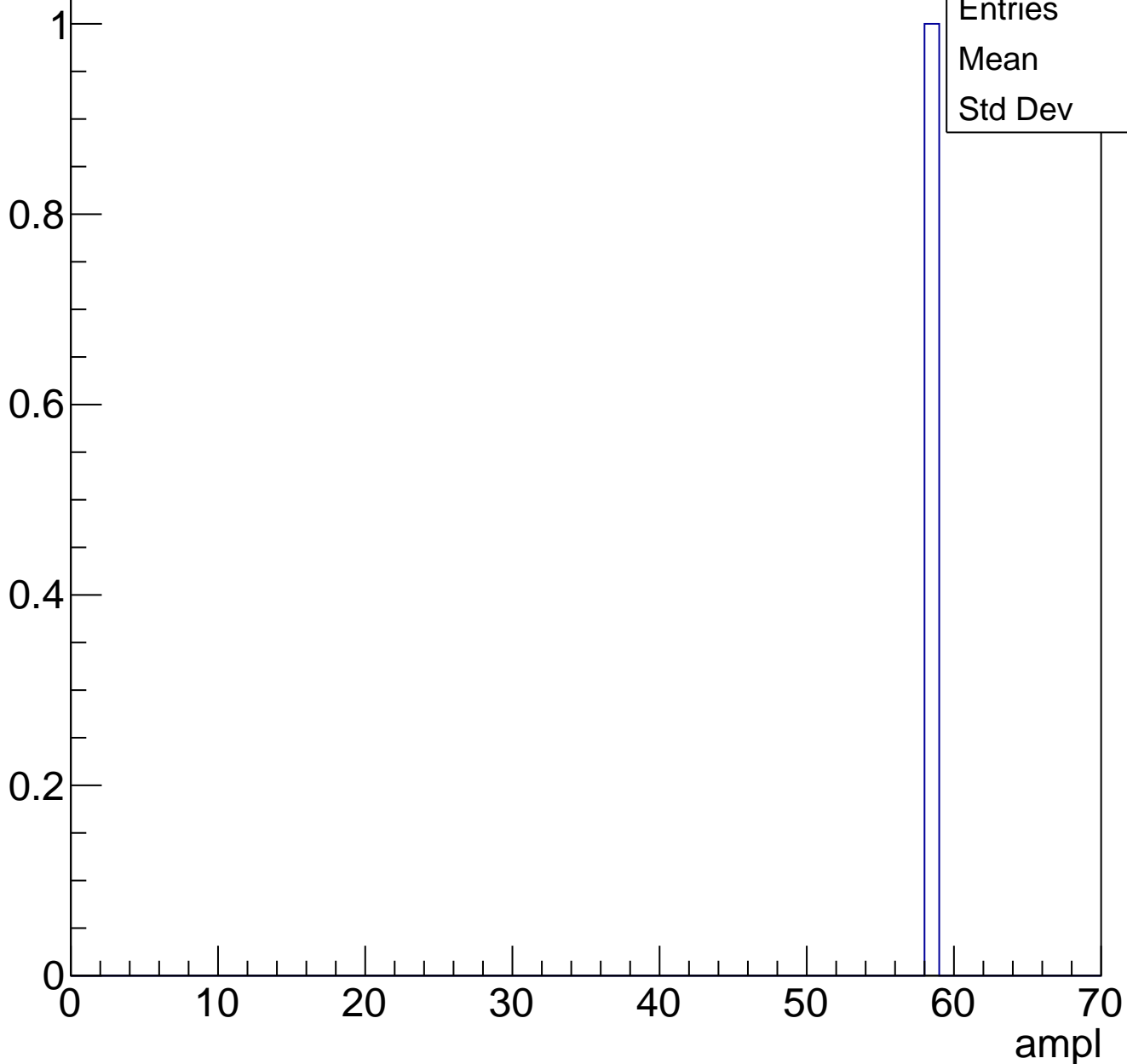
Entries	30
Mean	58.67
Std Dev	11.12



B1L103S, U20-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch24, adc0

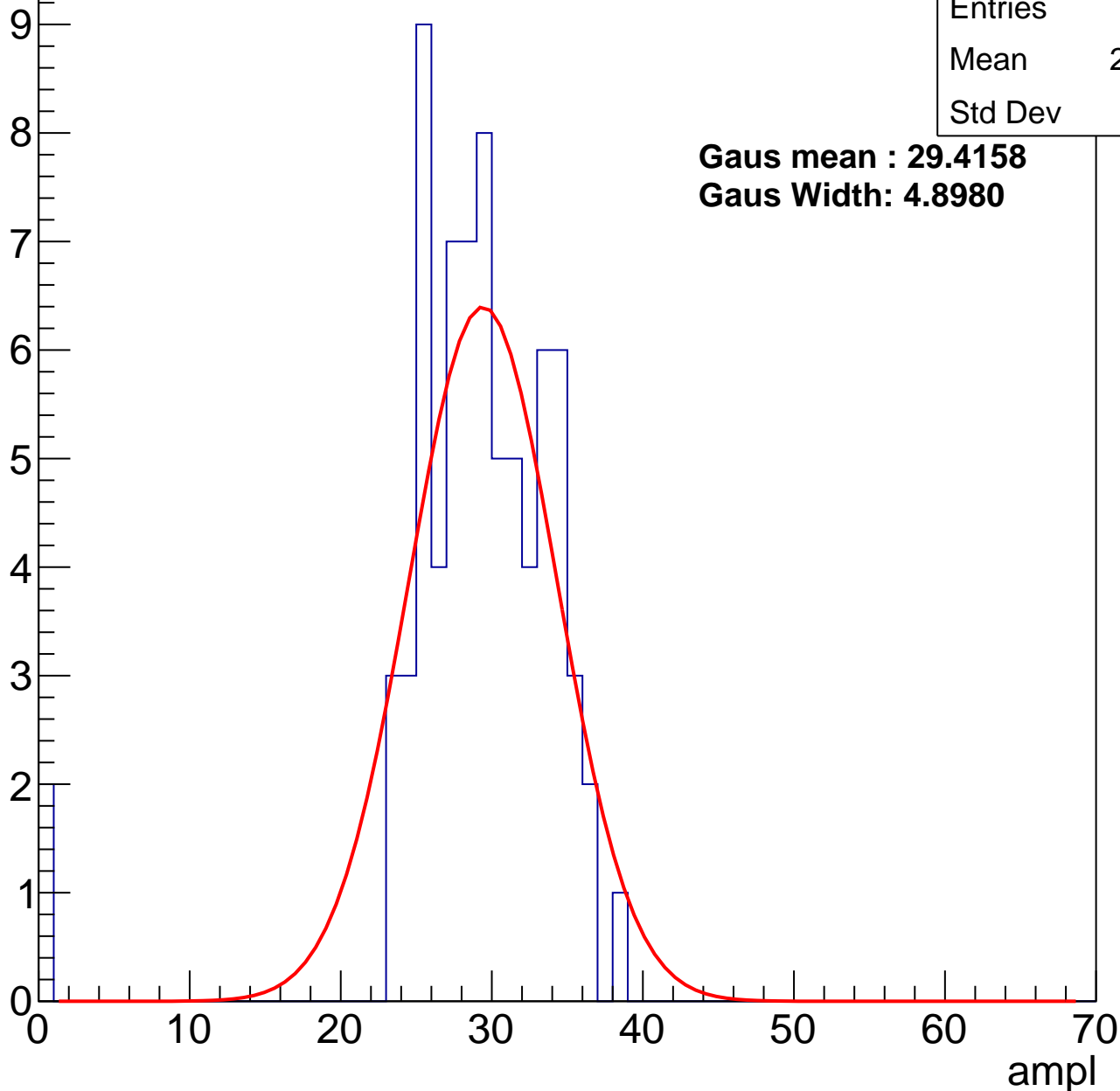
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.49
Std Dev	5.95

Gaus mean : 29.4158

Gaus Width: 4.8980



B1L103S, U20-ch24, adc1

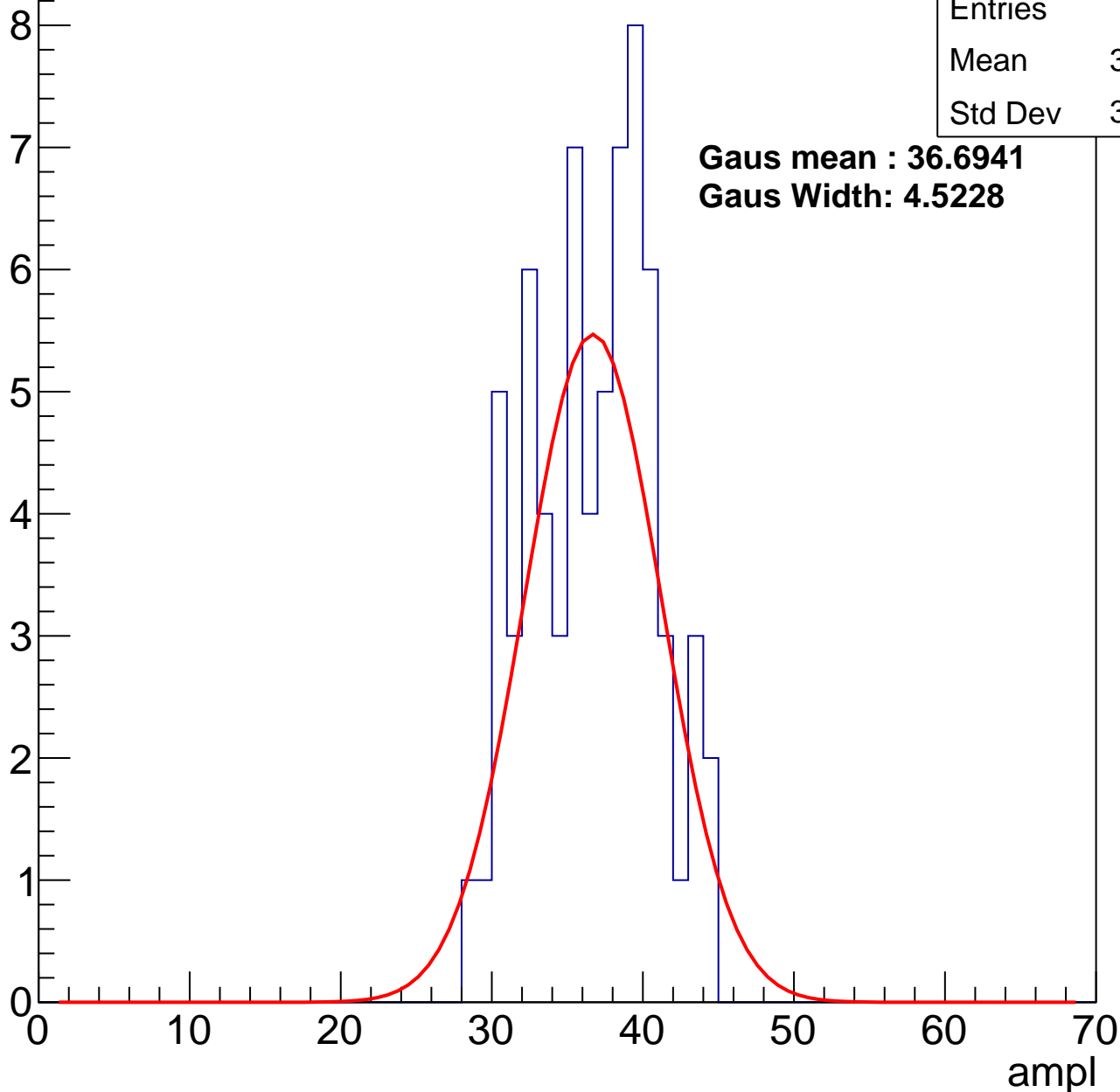
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.23
Std Dev	3.997

Gaus mean : 36.6941

Gaus Width: 4.5228



B1L103S, U20-ch24, adc2

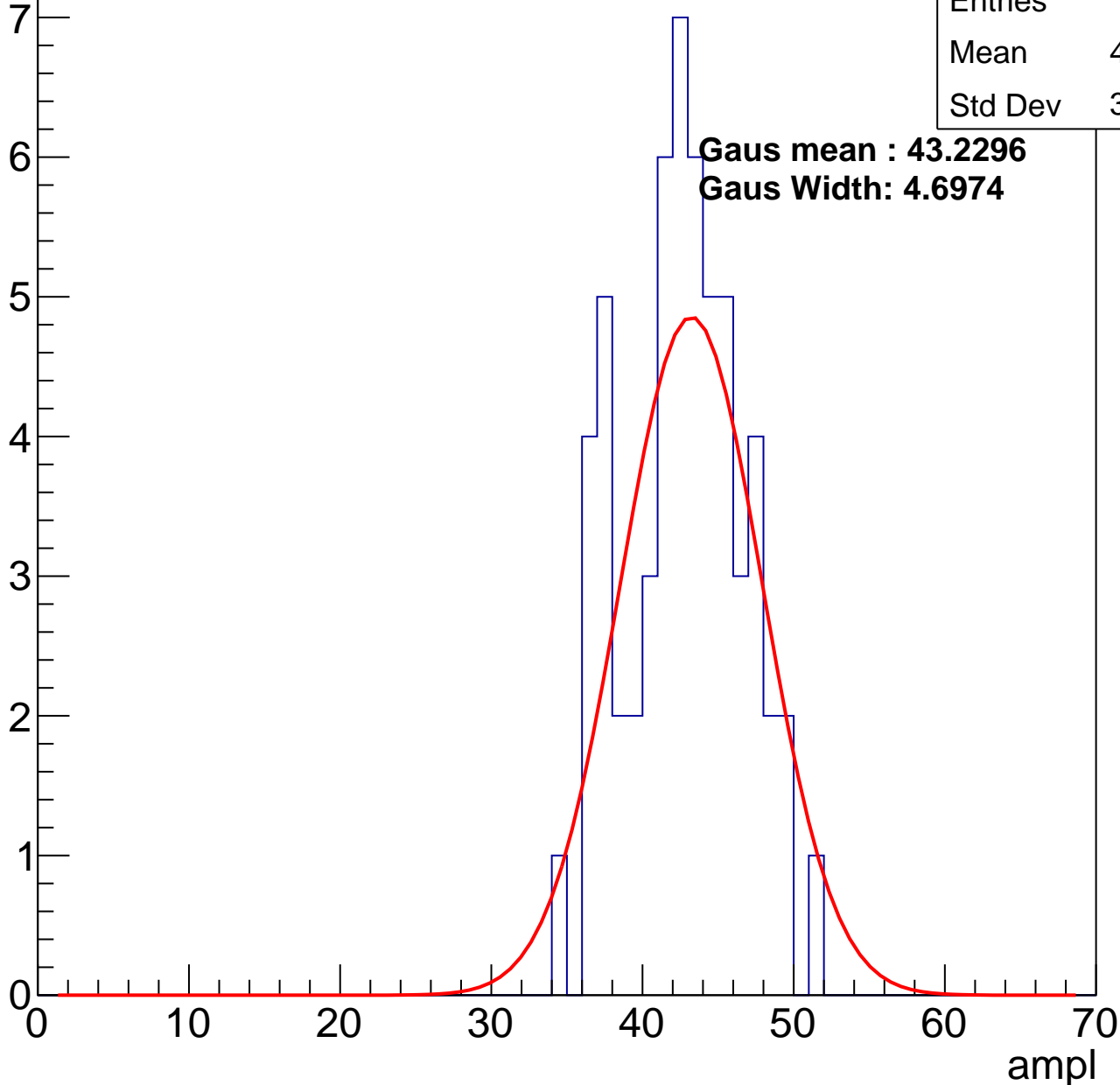
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.26
Std Dev	3.867

Gaus mean : 43.2296

Gaus Width: 4.6974

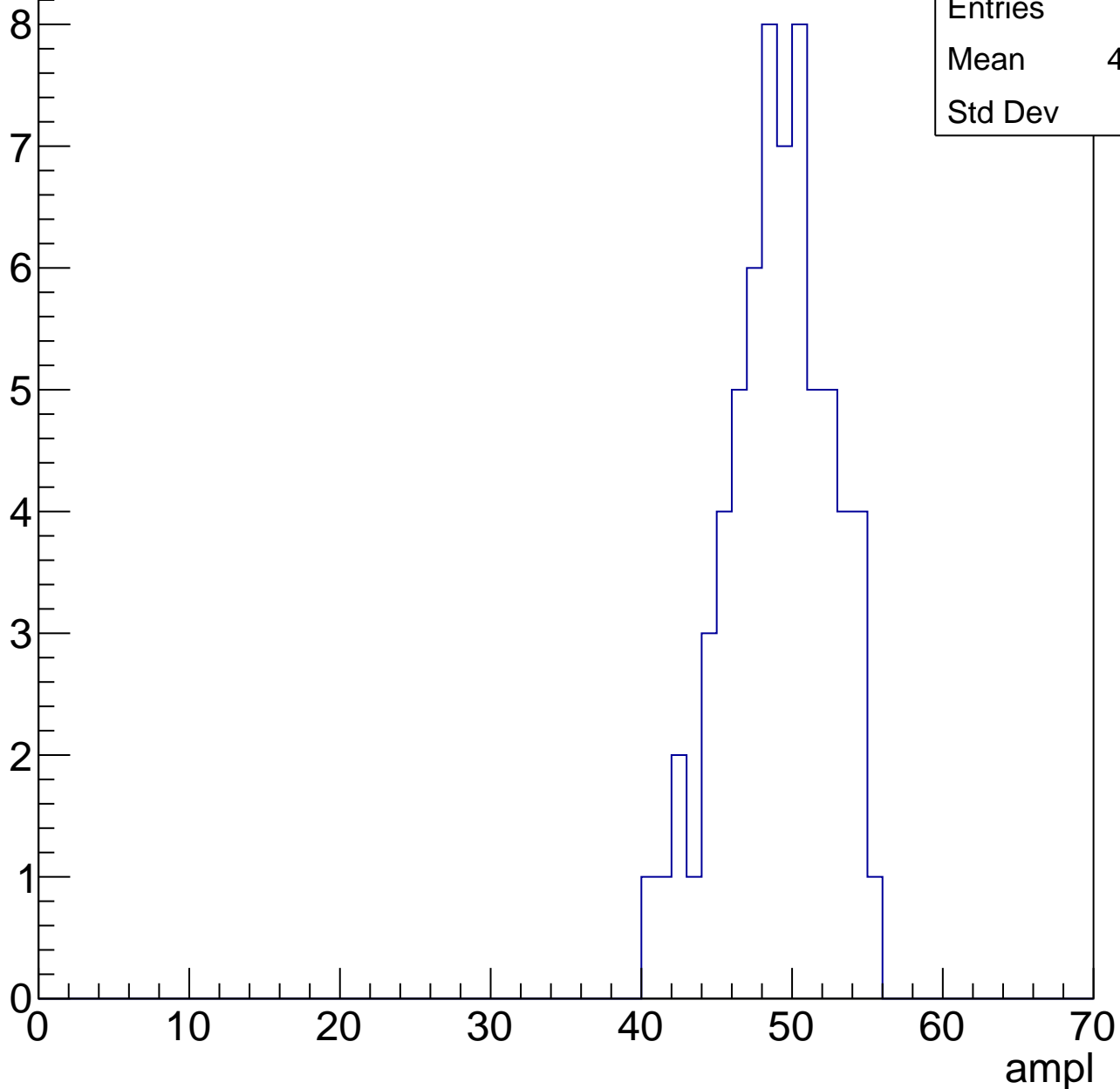


B1L103S, U20-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

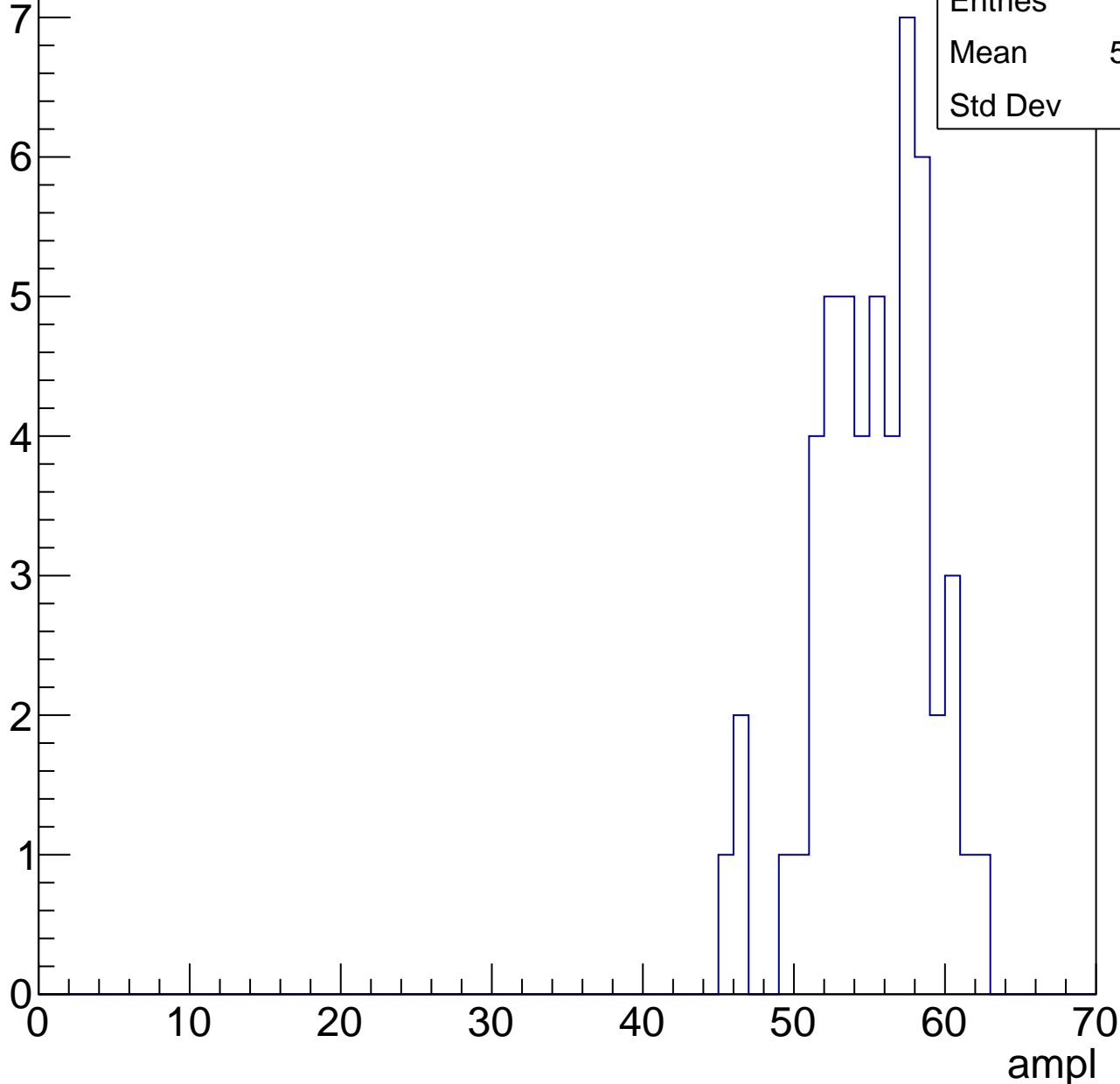
Entries	65
Mean	48.57
Std Dev	3.41



B1L103S, U20-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch24, adc5

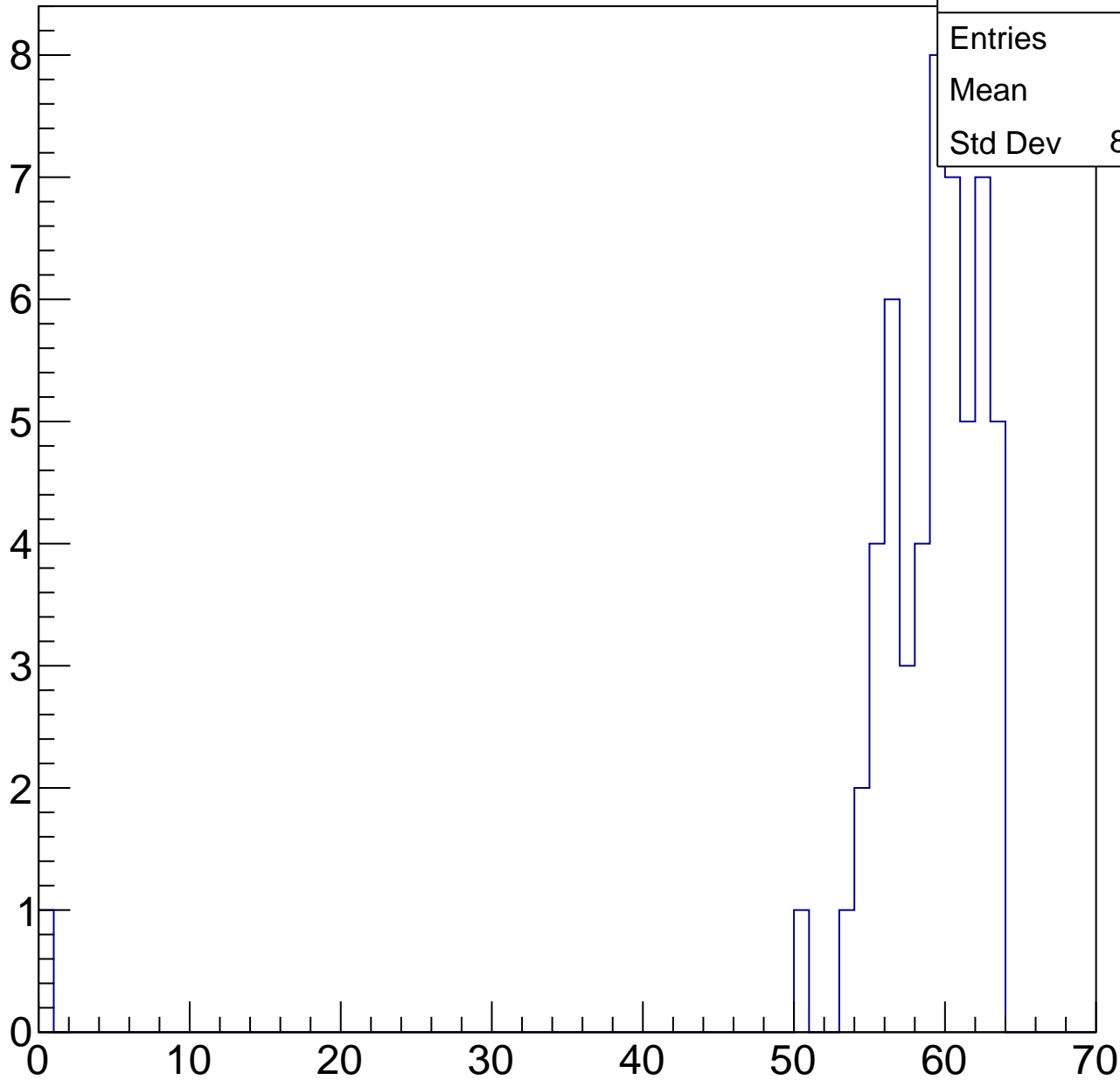
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	57.7
Std Dev	8.456

ampl

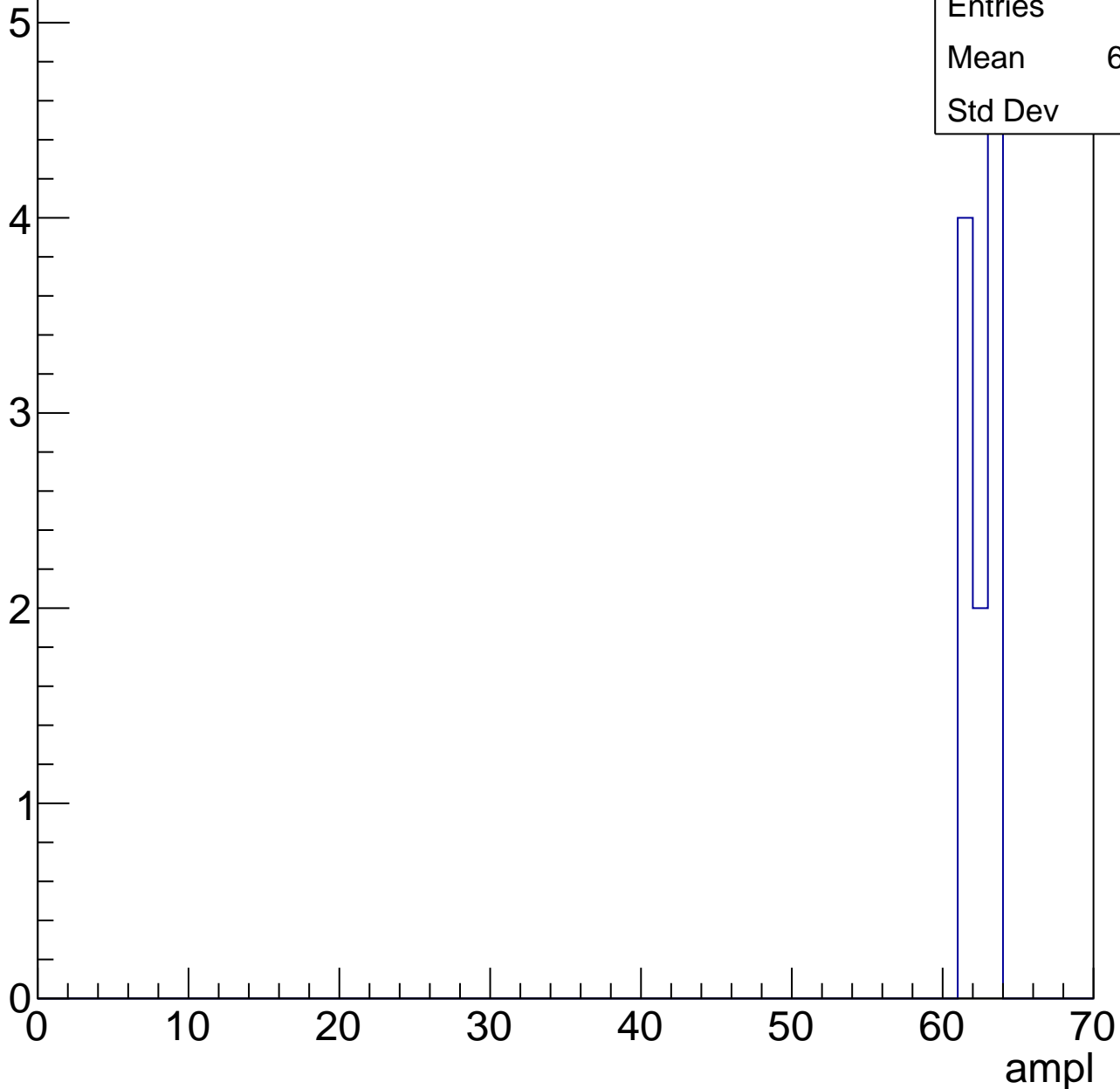


B1L103S, U20-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	62.09
Std Dev	0.9



B1L103S, U20-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch25, adc0

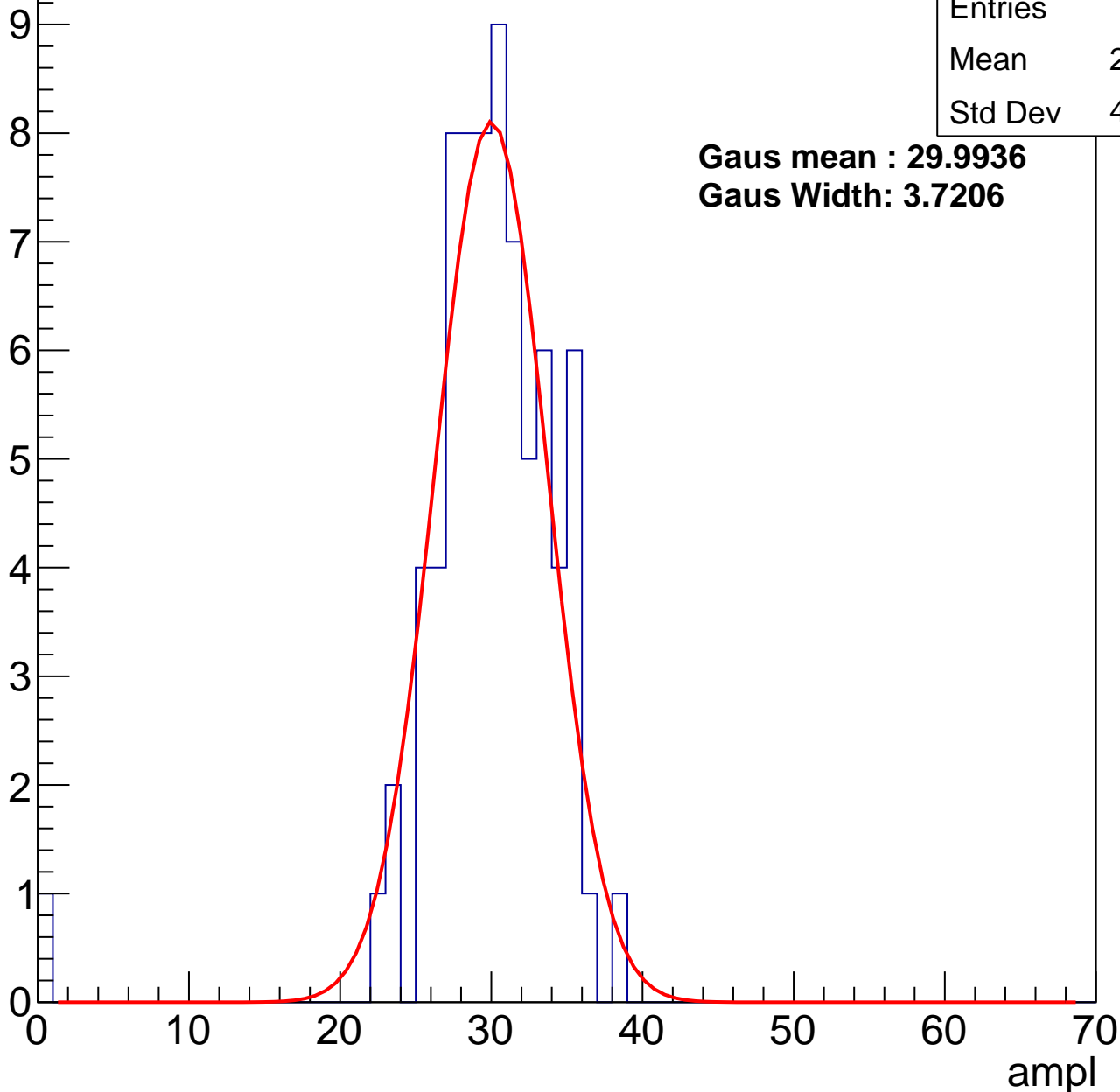
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.45
Std Dev	4.776

Gaus mean : 29.9936

Gaus Width: 3.7206



B1L103S, U20-ch25, adc1

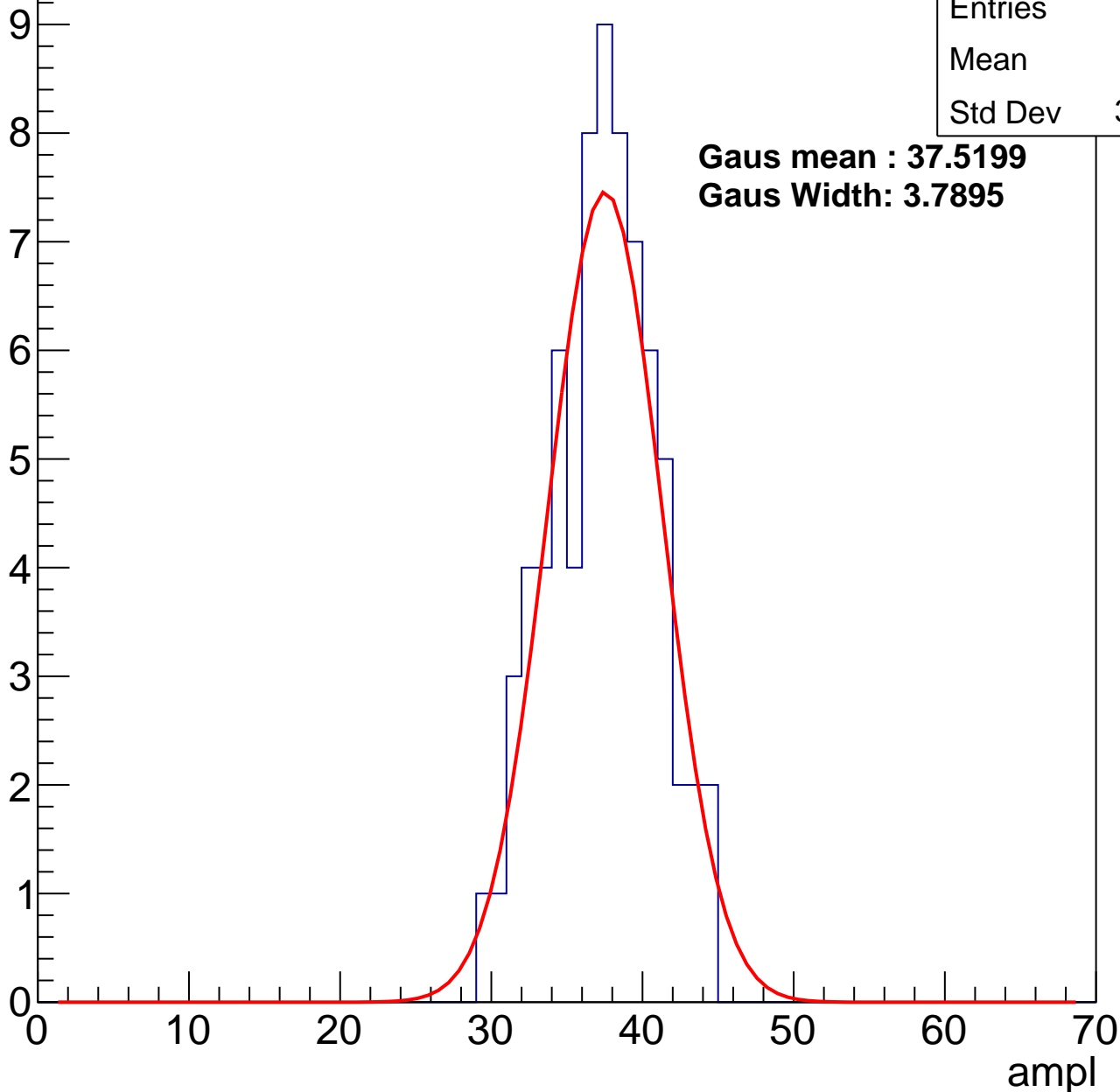
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	36.9
Std Dev	3.441

Gaus mean : 37.5199

Gaus Width: 3.7895



B1L103S, U20-ch25, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	44.54
Std Dev	3.666

Gaus mean : 44.7222

Gaus Width: 3.7201

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

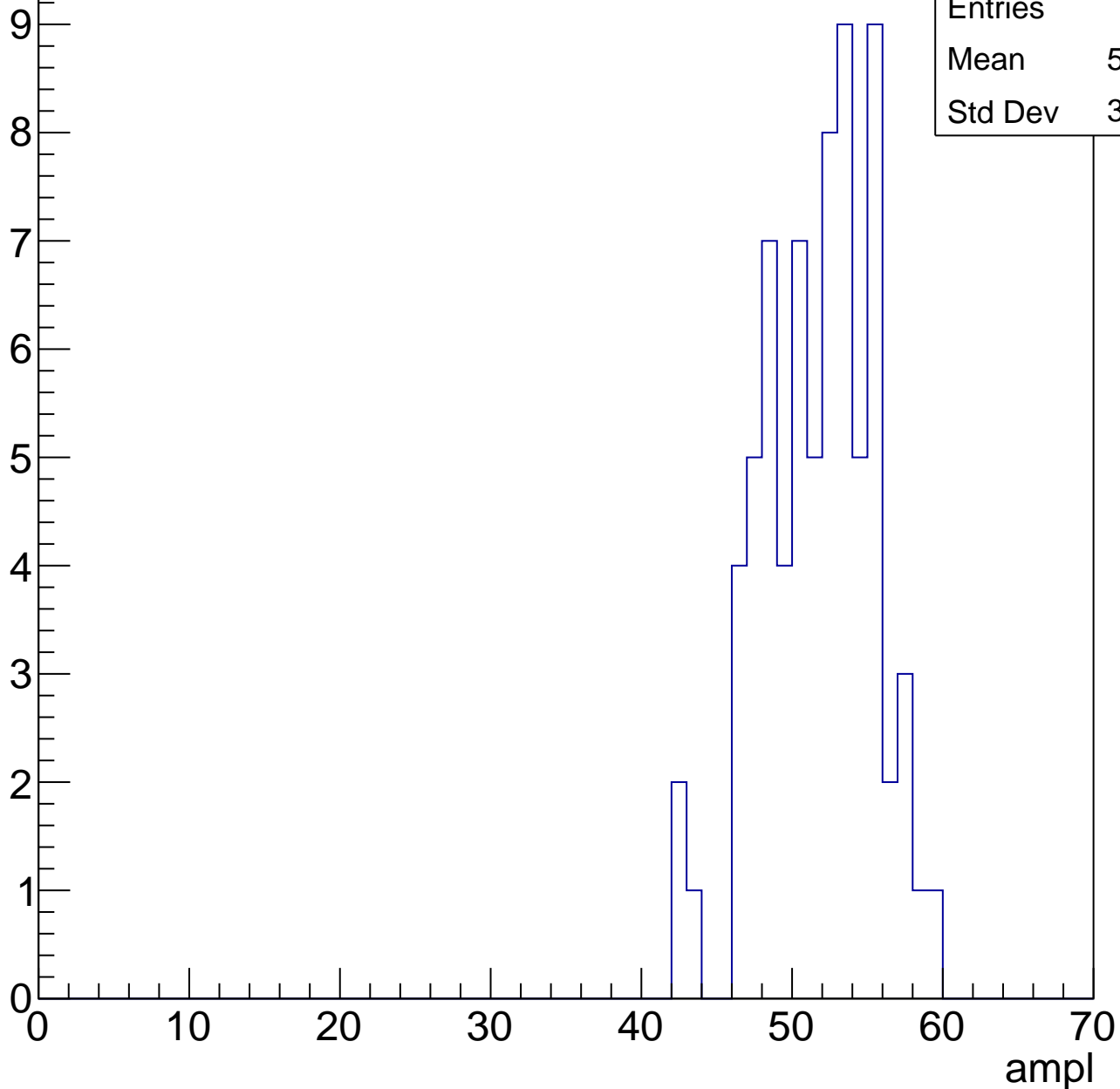
70

B1L103S, U20-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

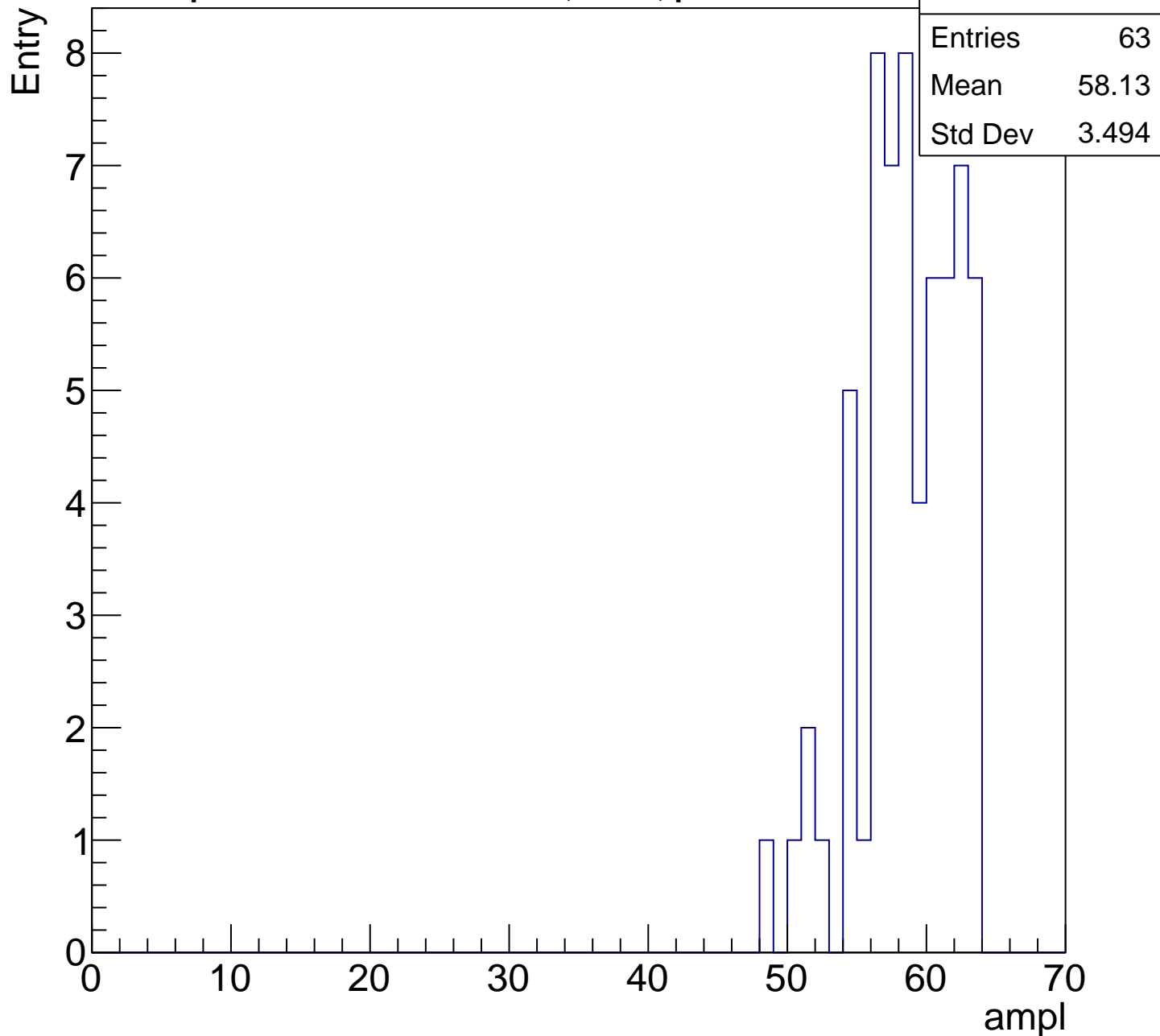
Entry

Entries	73
Mean	51.25
Std Dev	3.686



B1L103S, U20-ch25, adc4

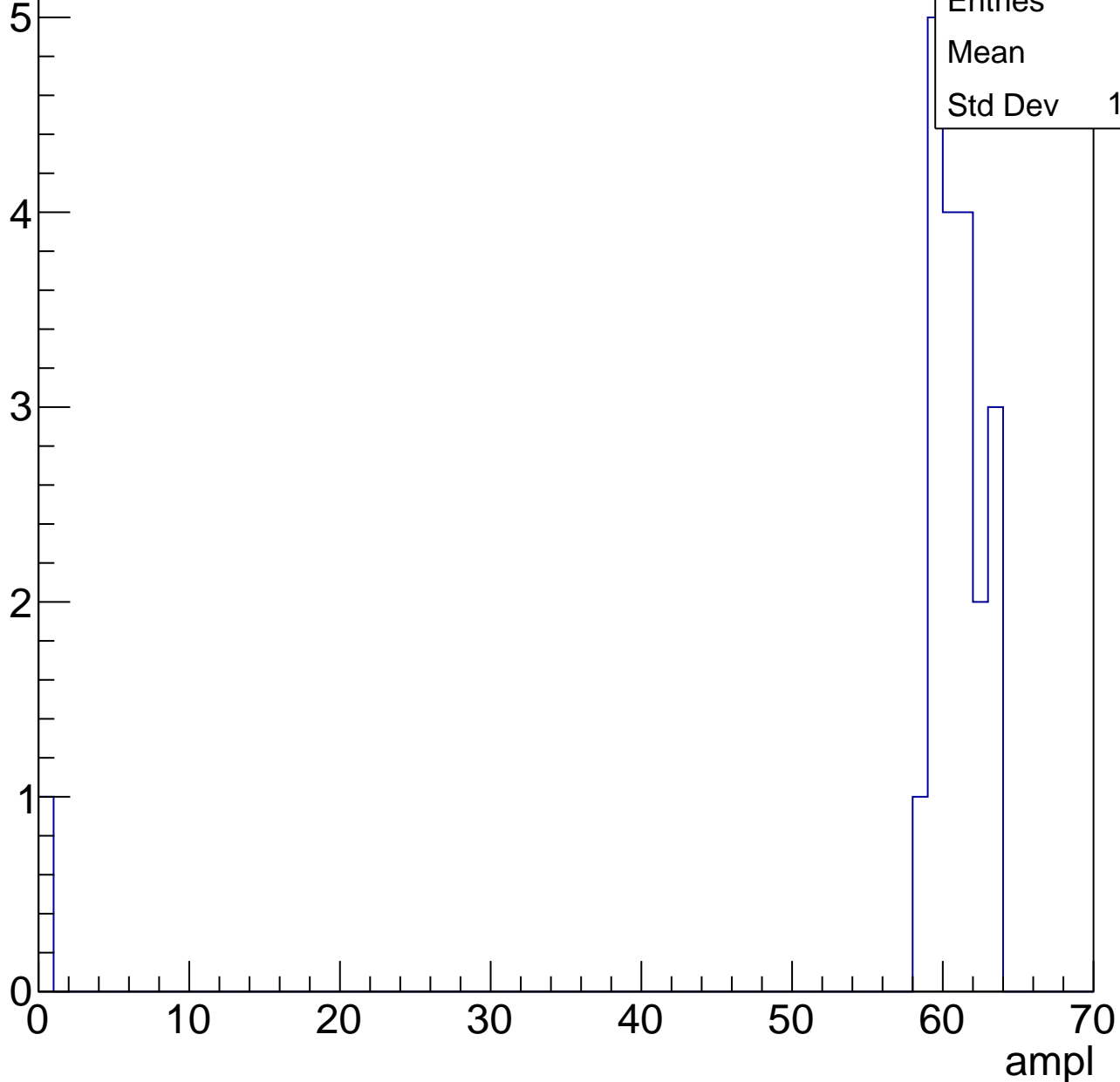
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U20-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	20
Mean	57.5
Std Dev	13.27

B1L103S, U20-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch26, adc0

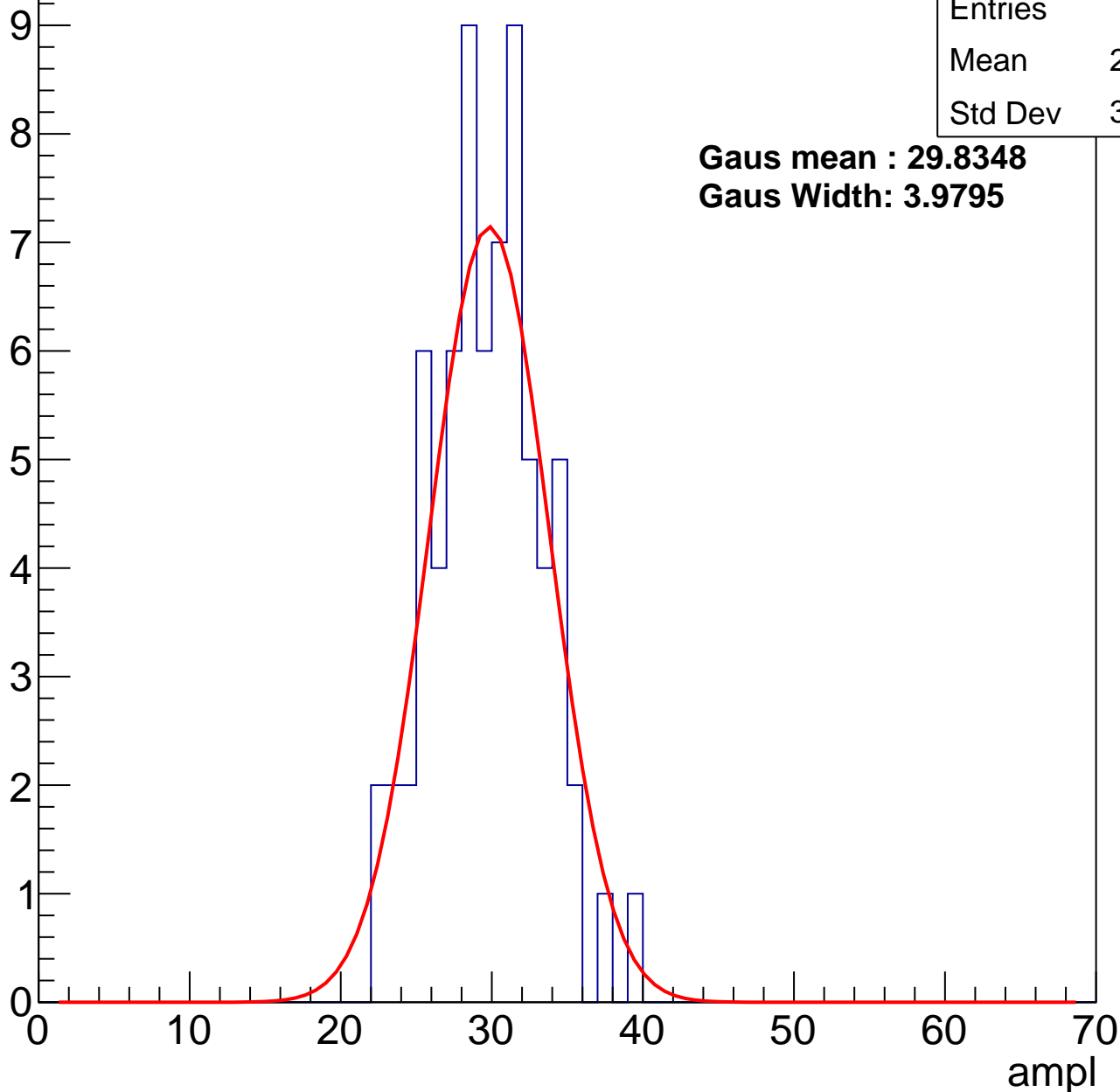
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.25
Std Dev	3.547

Gaus mean : 29.8348

Gaus Width: 3.9795



B1L103S, U20-ch26, adc1

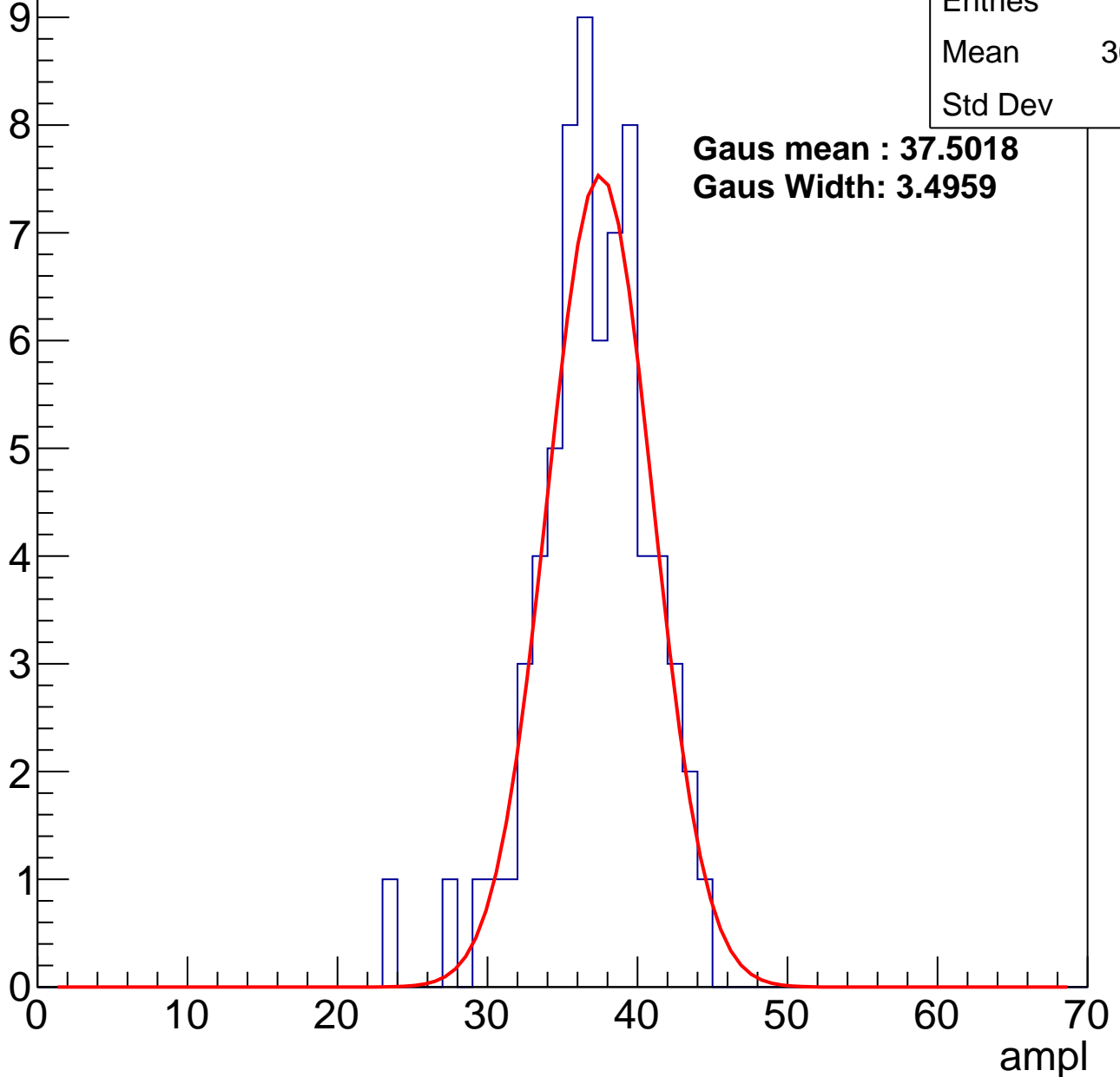
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.55
Std Dev	3.79

Gaus mean : 37.5018

Gaus Width: 3.4959



B1L103S, U20-ch26, adc2

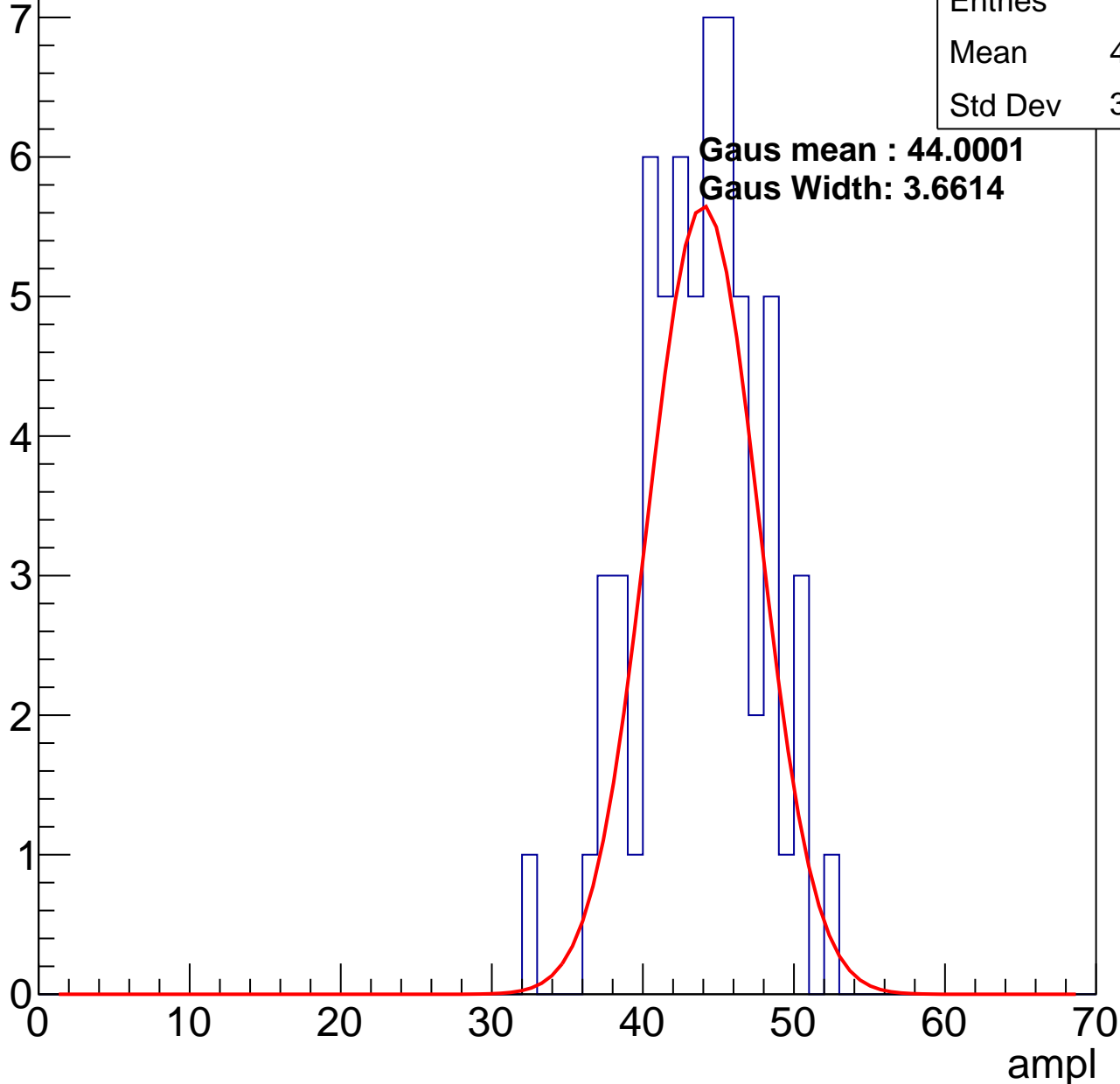
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.26
Std Dev	3.906

Gaus mean : 44.0001

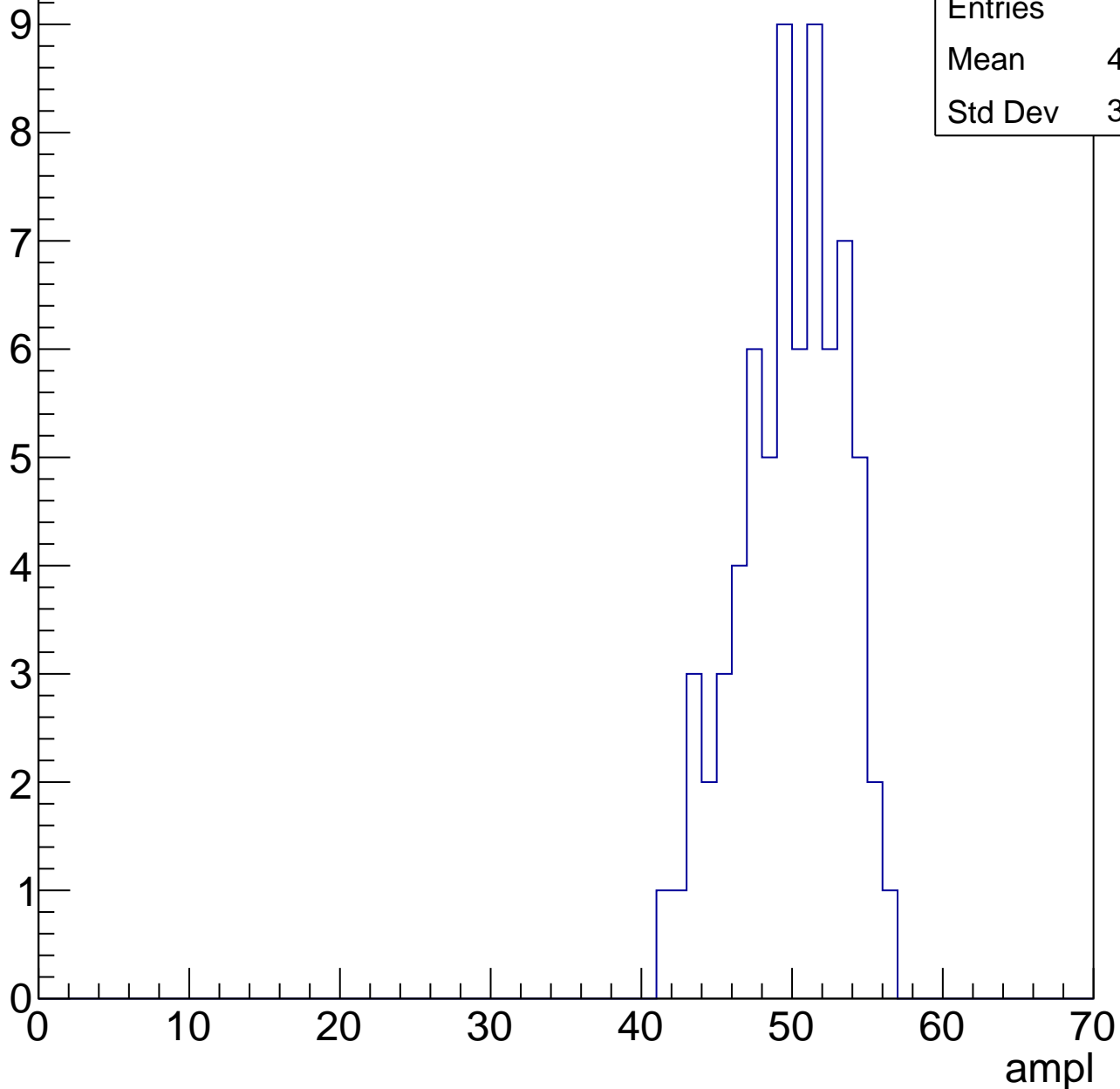
Gaus Width: 3.6614



B1L103S, U20-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

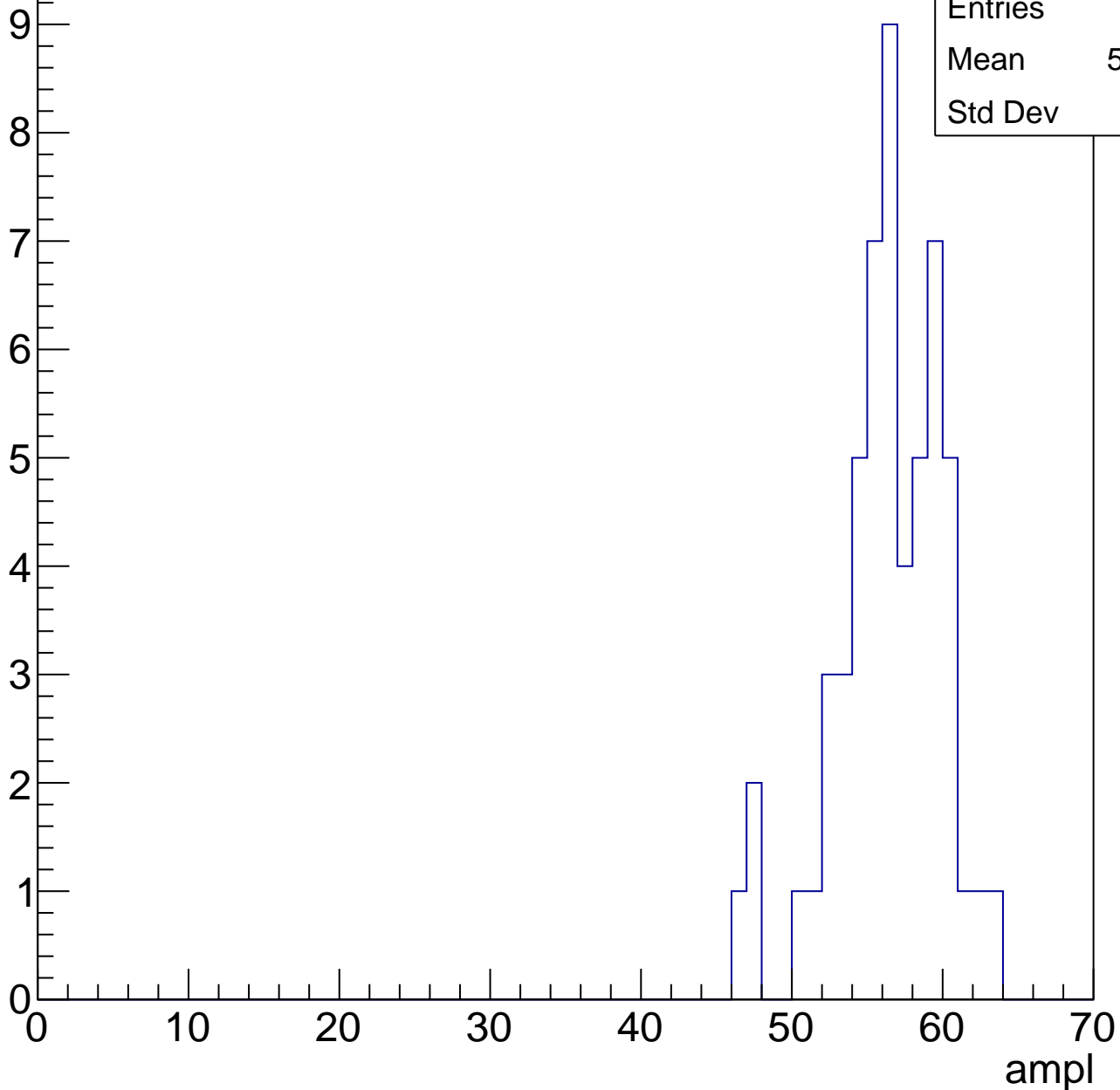


B1L103S, U20-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

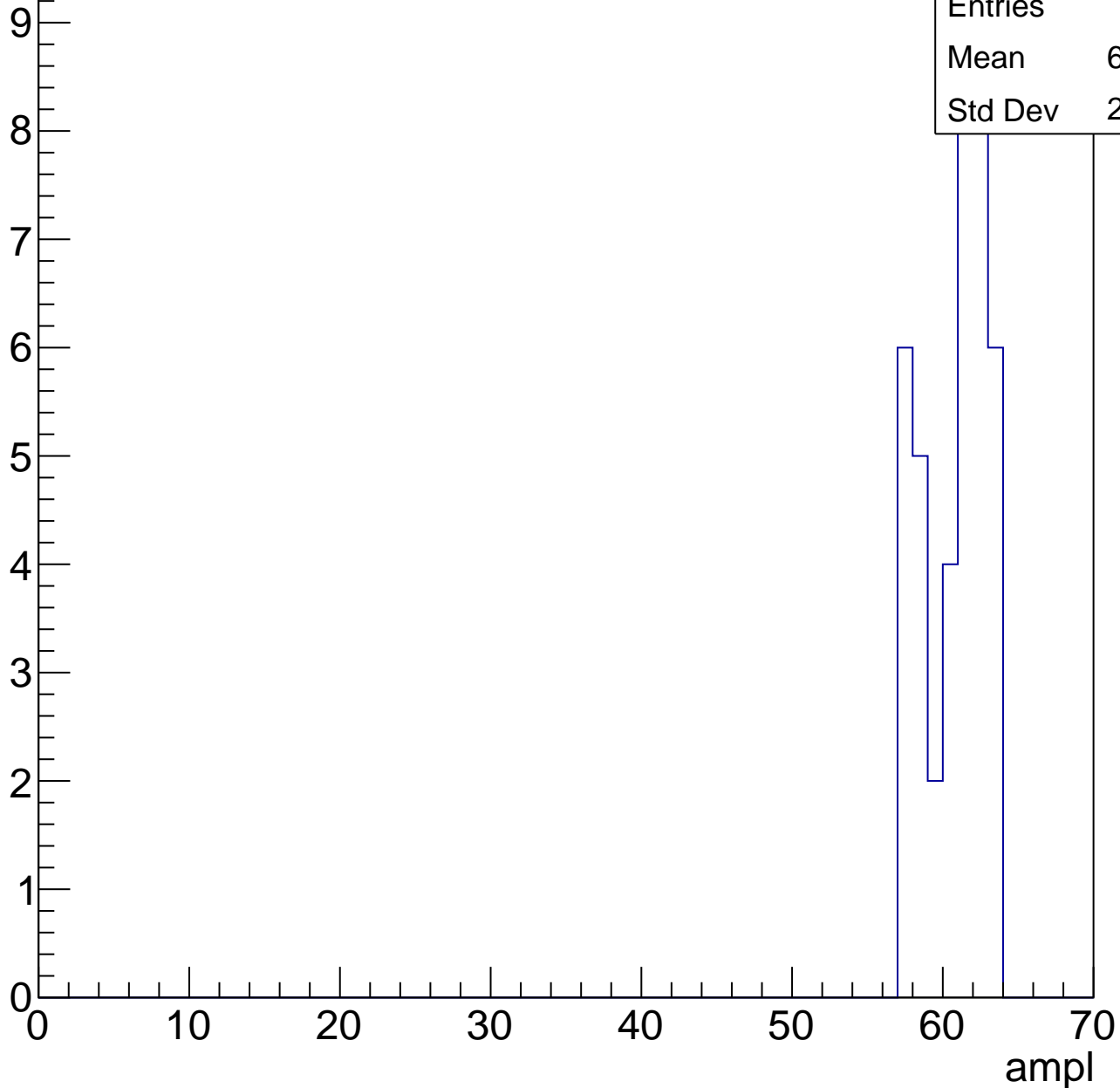
Entries	56
Mean	55.93
Std Dev	3.54



B1L103S, U20-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

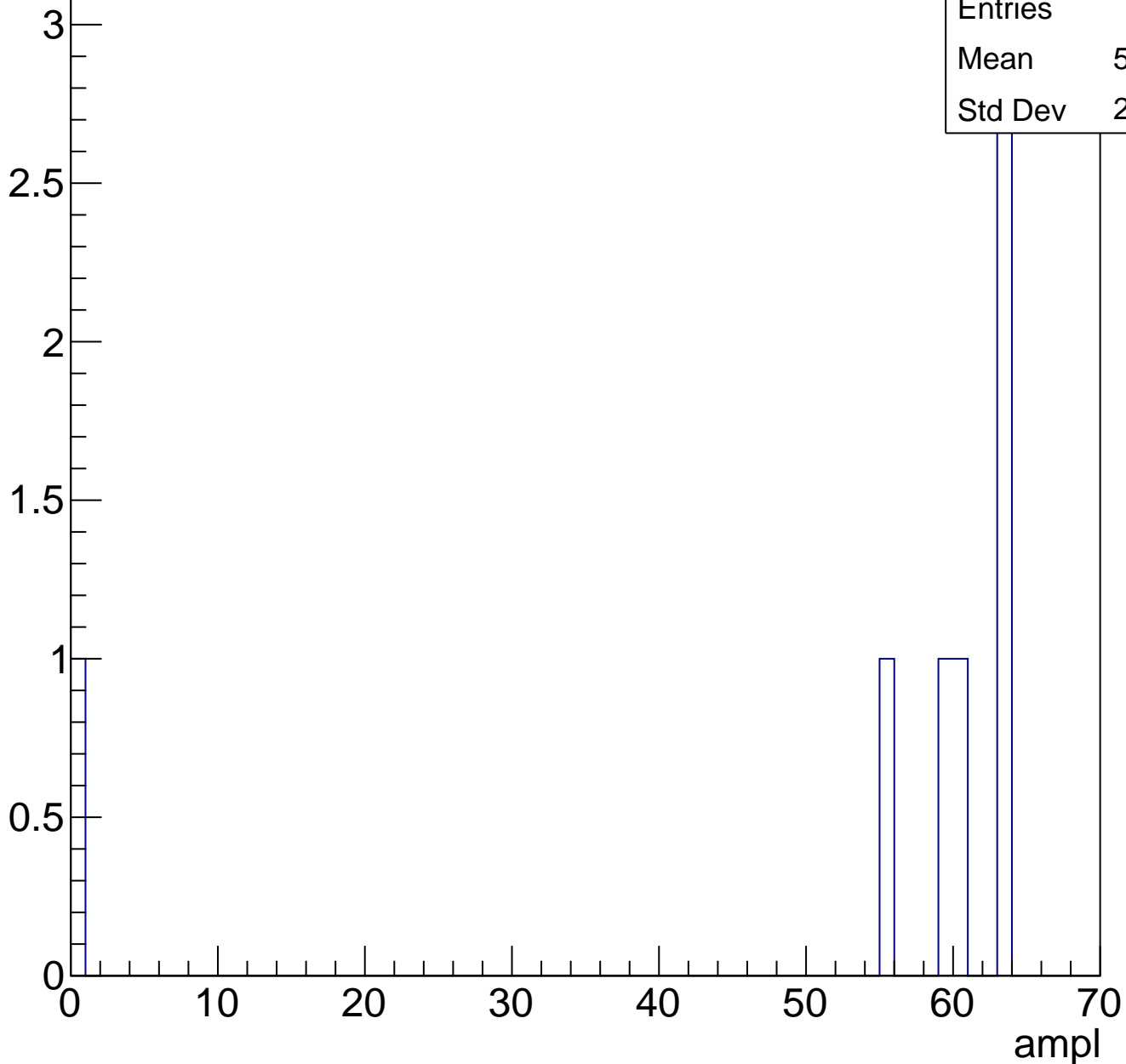
Entry



B1L103S, U20-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U20-ch27, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	100
Mean	32.28
Std Dev	3.922

Gaus mean : 32.8257

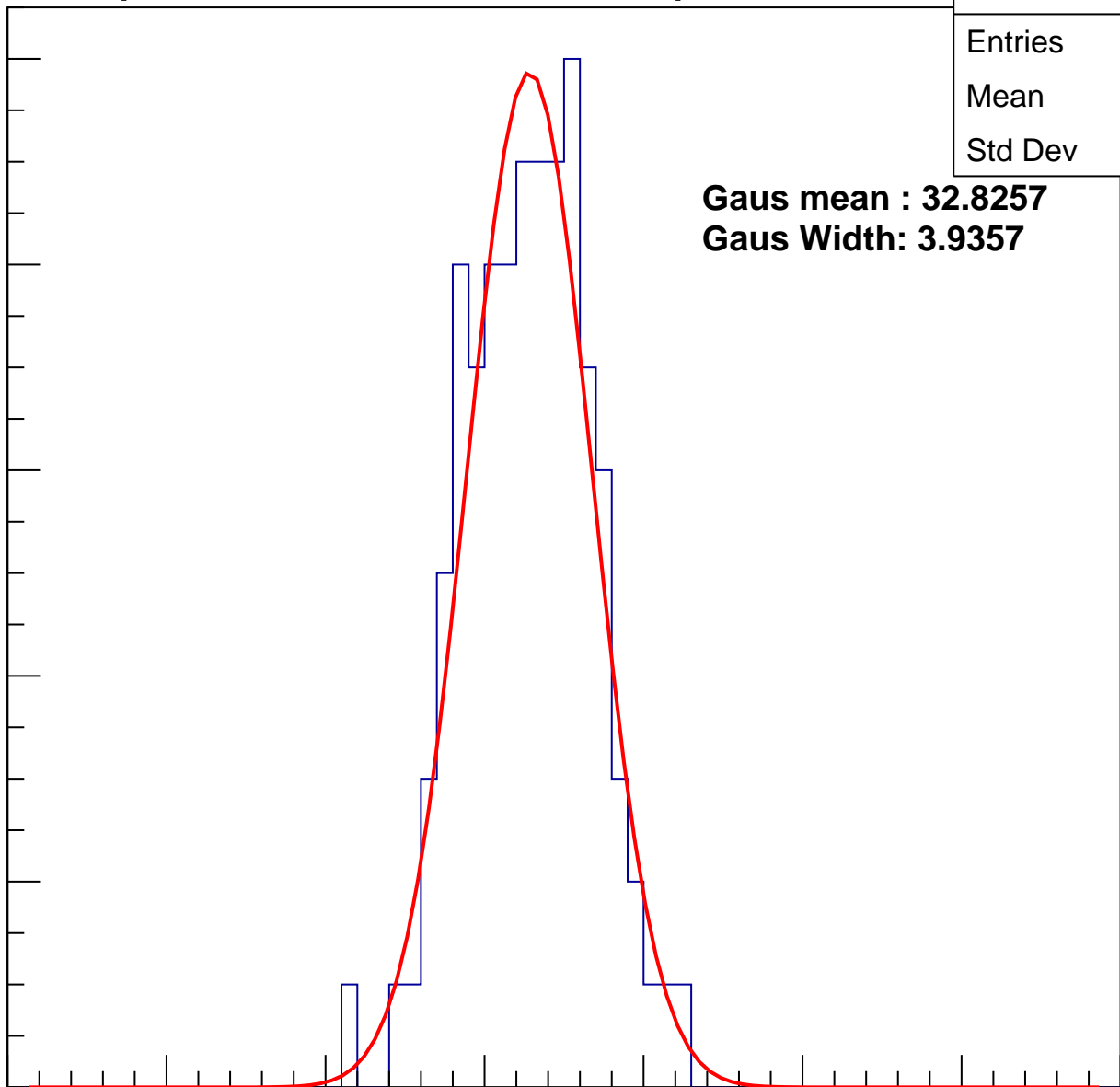
Gaus Width: 3.9357

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch27, adc1

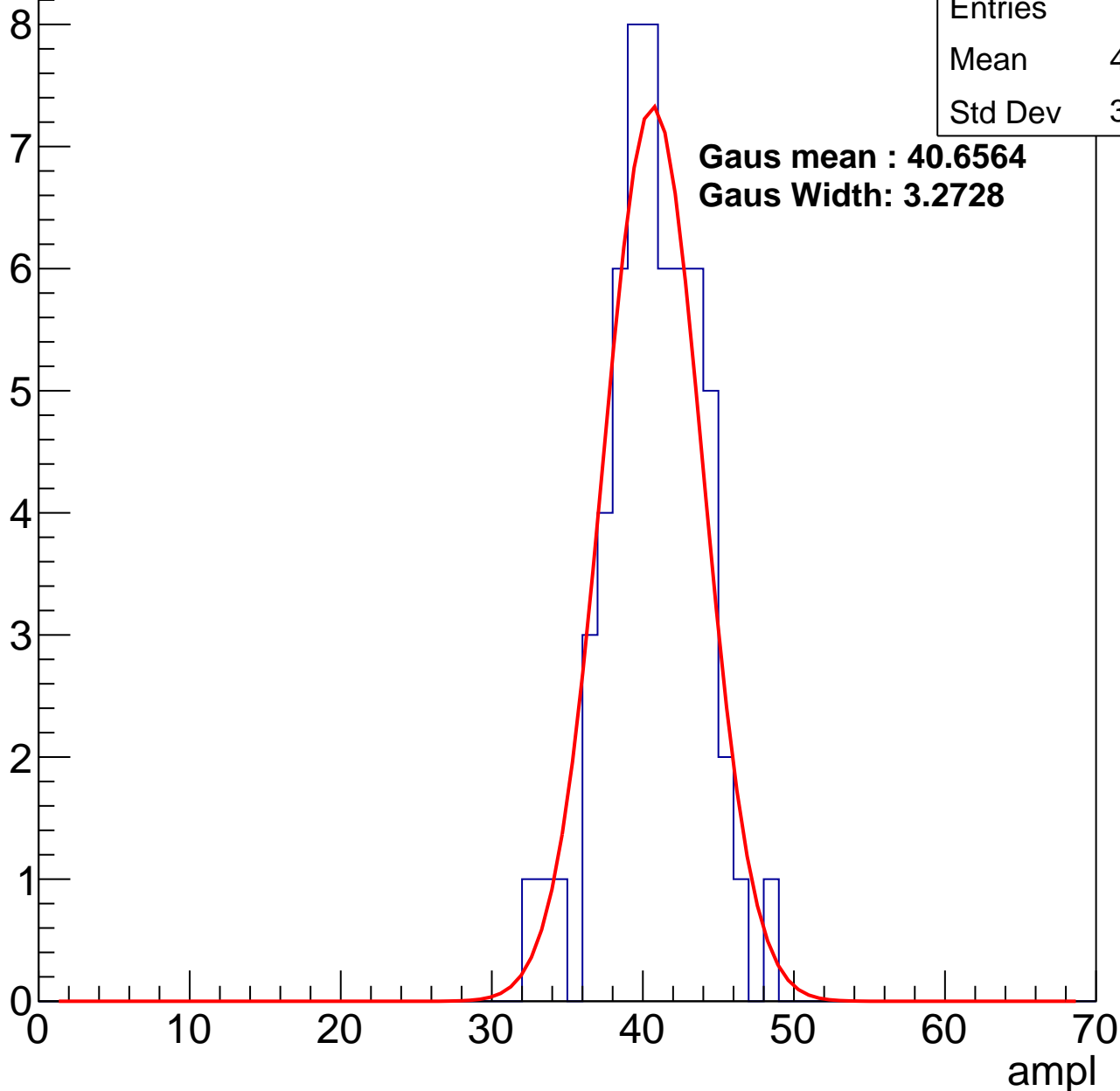
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	40.25
Std Dev	3.122

Gaus mean : 40.6564

Gaus Width: 3.2728



B1L103S, U20-ch27, adc2

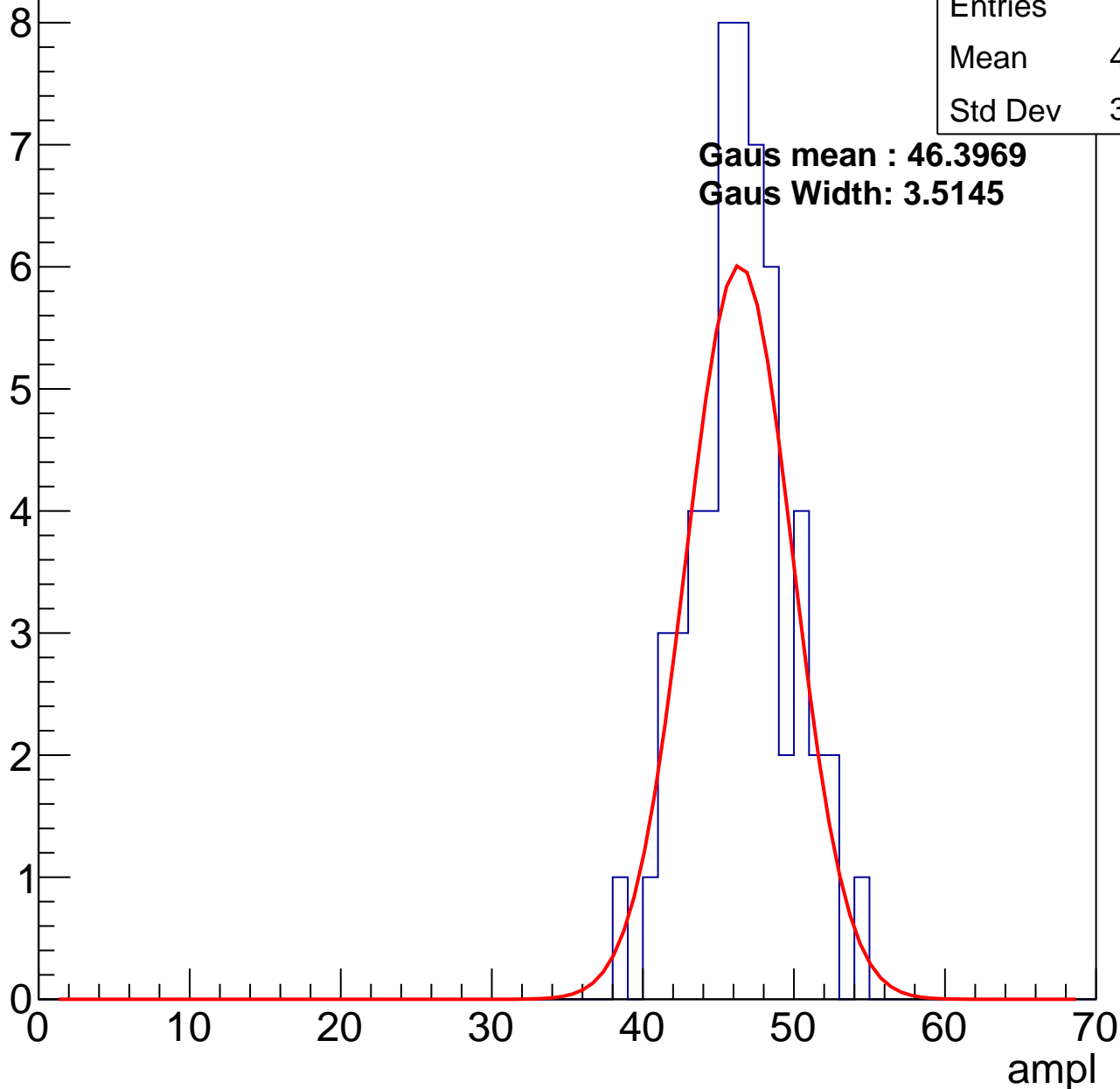
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	46.04
Std Dev	3.224

Gaus mean : 46.3969

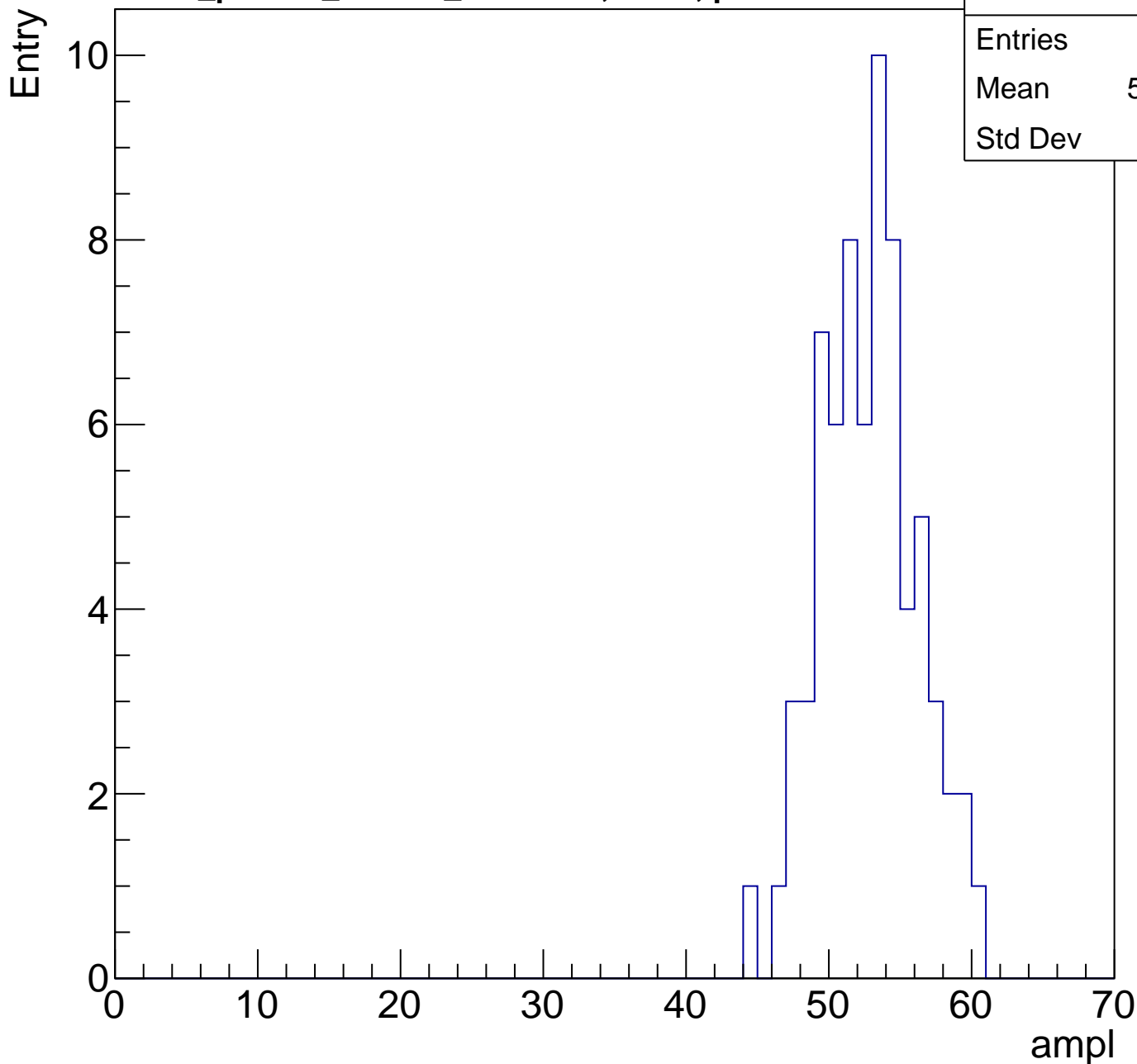
Gaus Width: 3.5145



B1L103S, U20-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	52.36
Std Dev	3.33

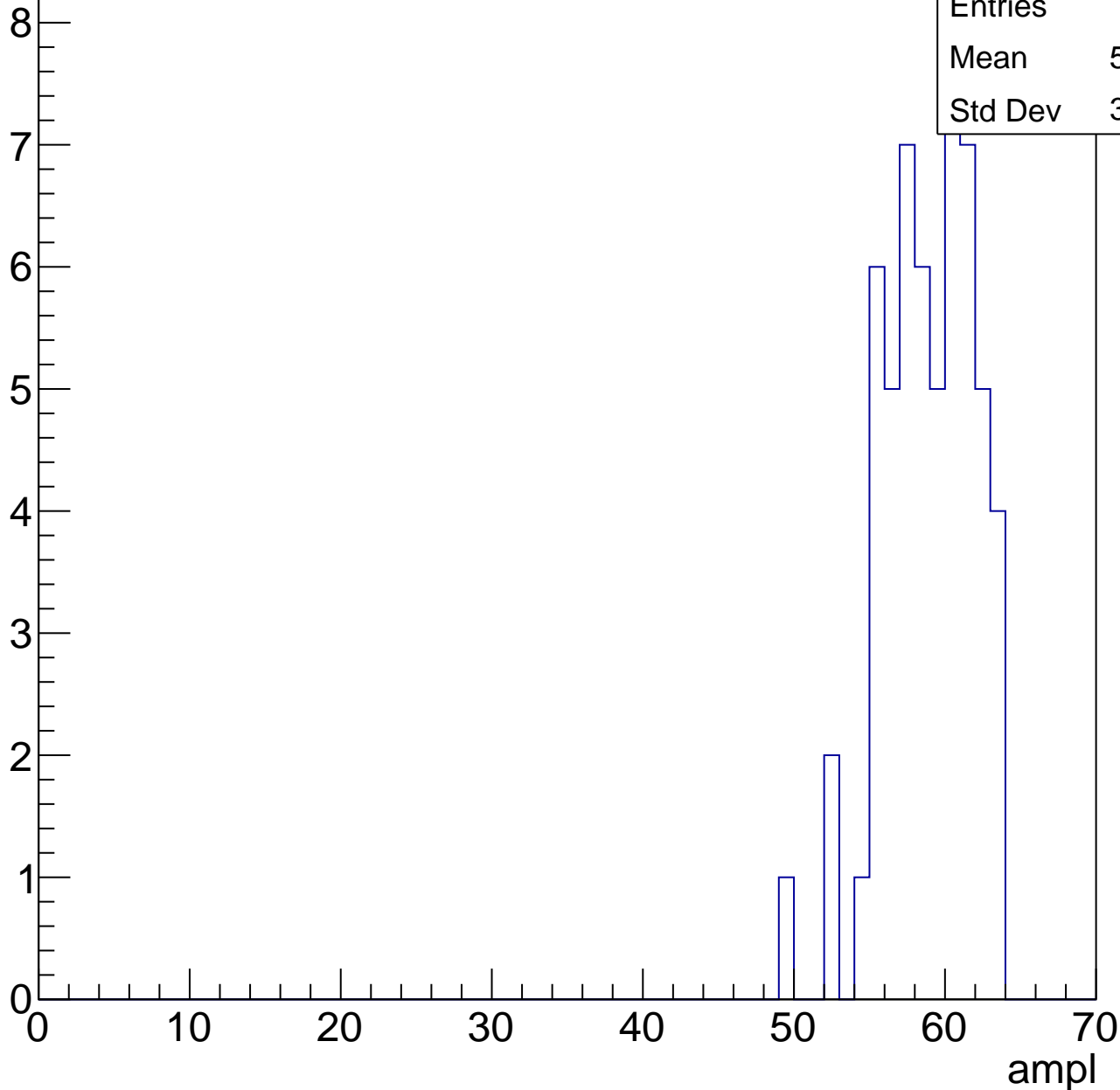


B1L103S, U20-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	58.39
Std Dev	3.025

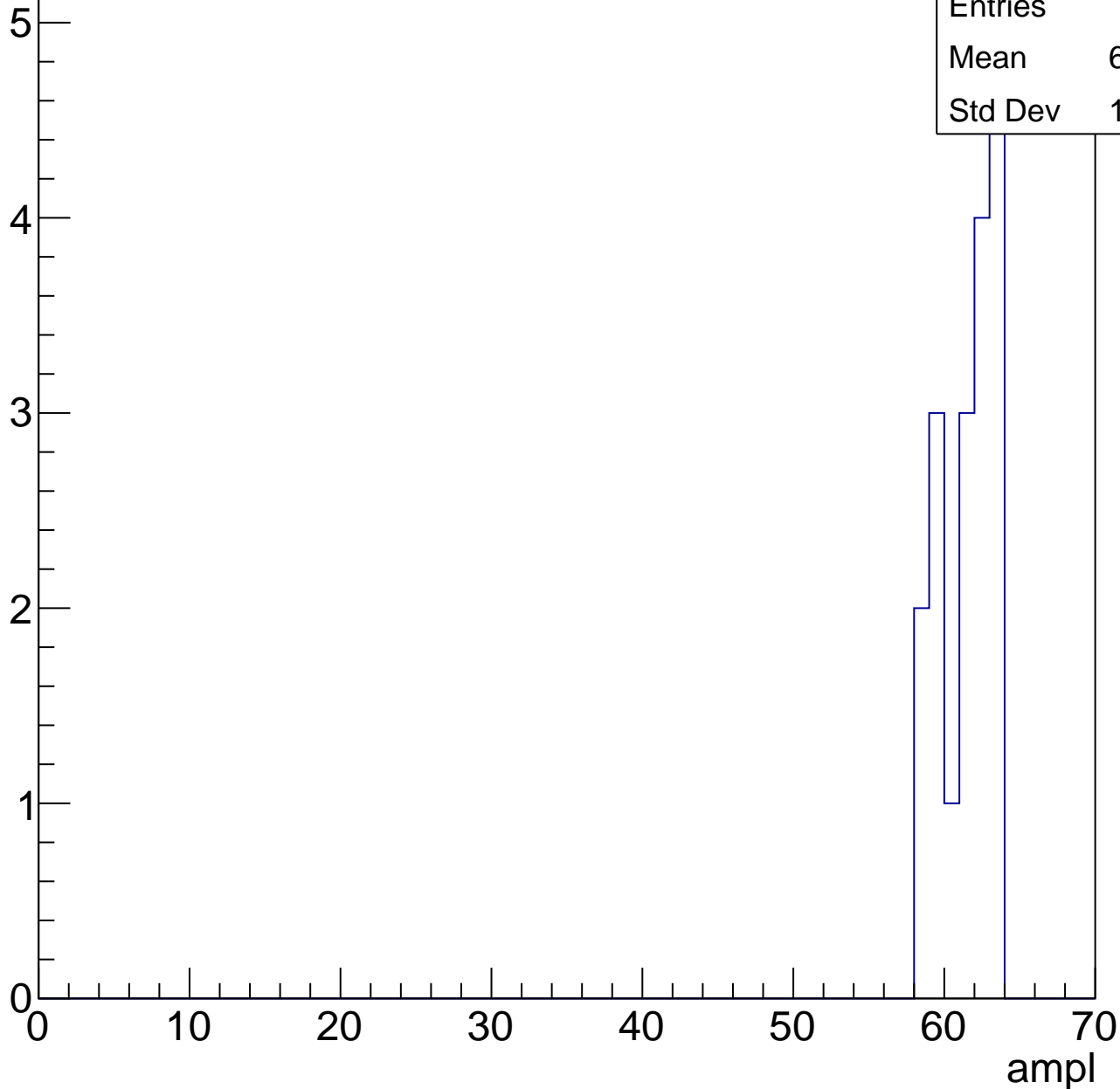


B1L103S, U20-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	18
Mean	61.06
Std Dev	1.747



B1L103S, U20-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch28, adc0

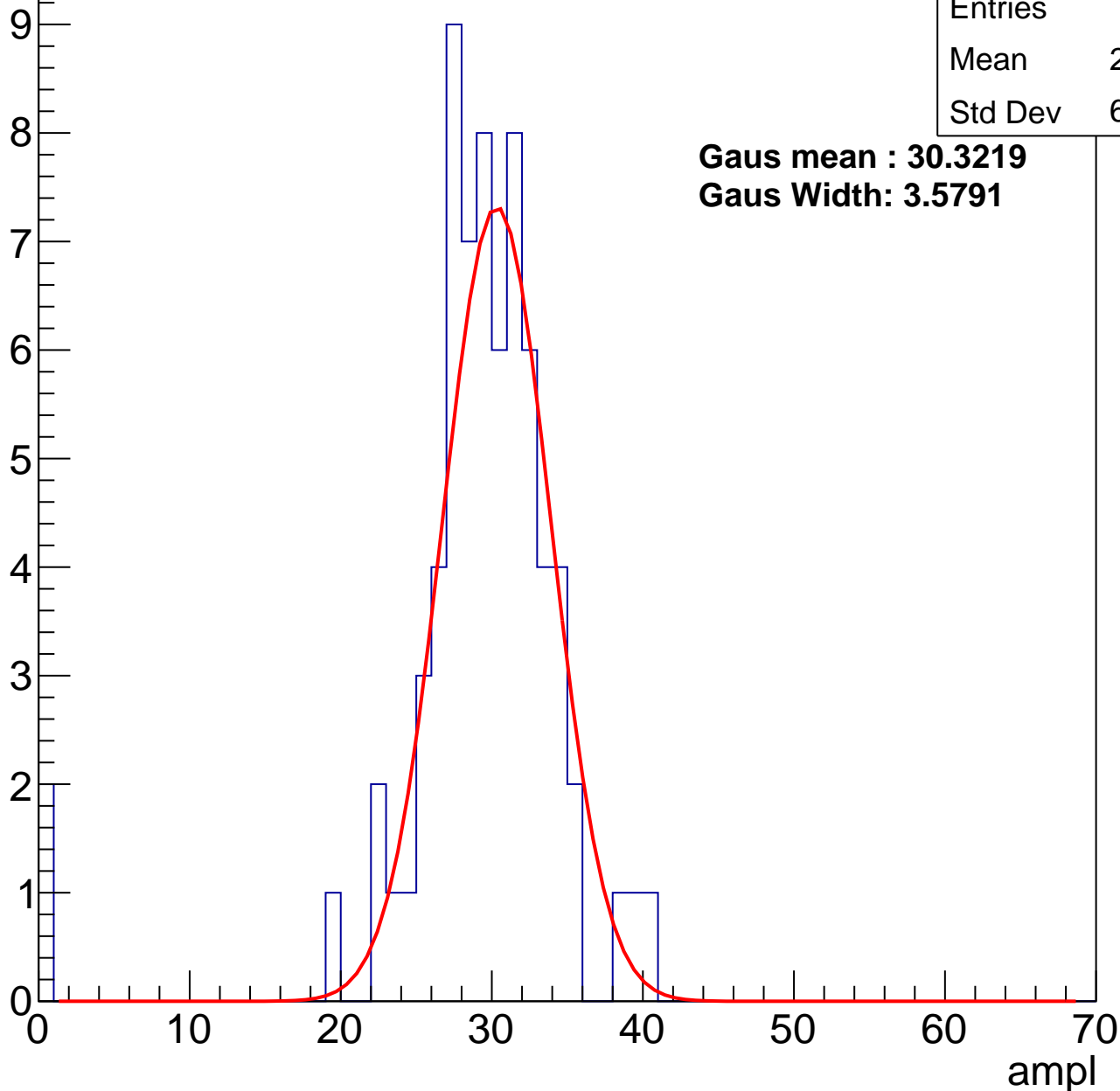
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.66
Std Dev	6.155

Gaus mean : 30.3219

Gaus Width: 3.5791



B1L103S, U20-ch28, adc1

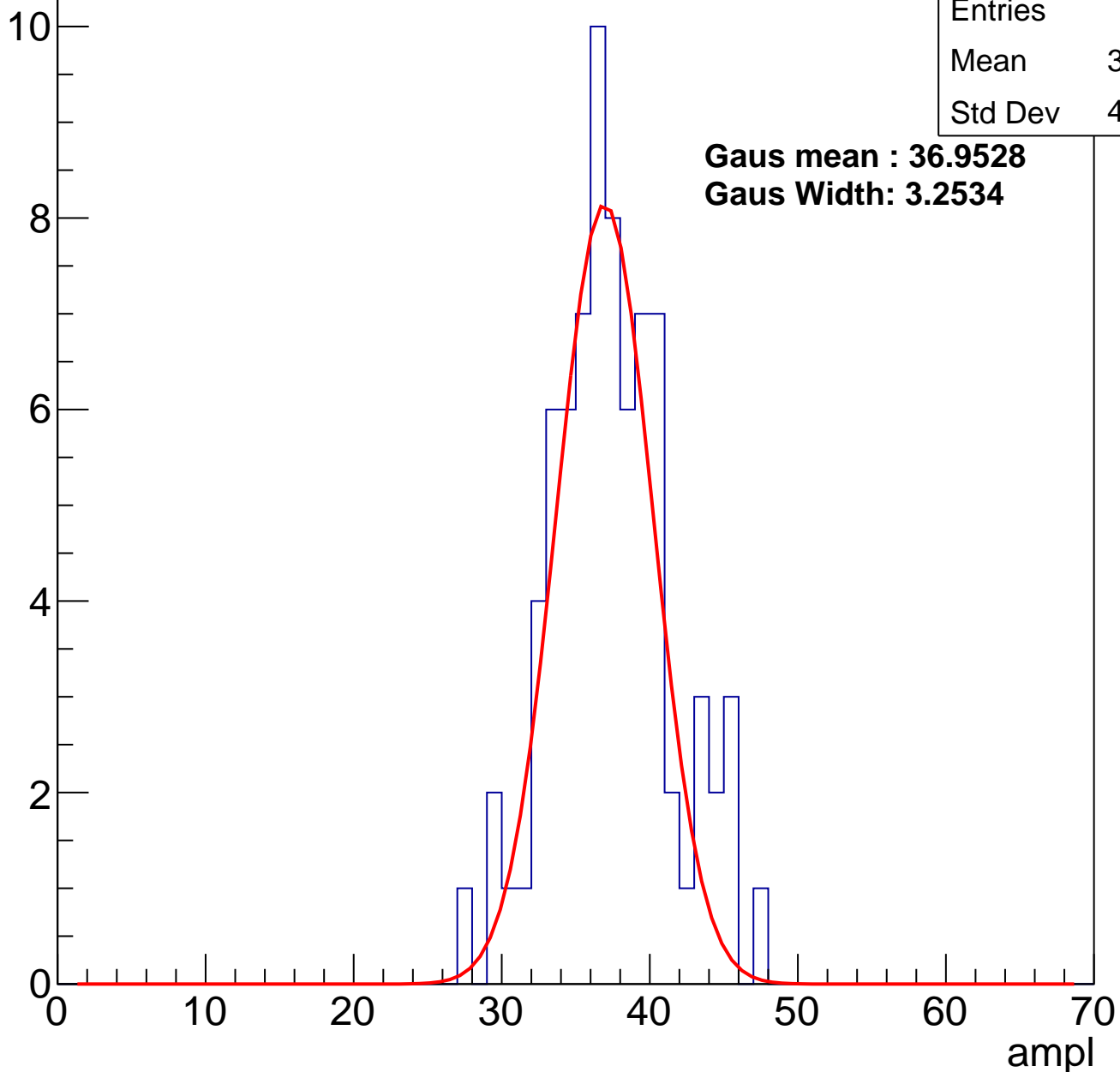
calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	36.94
Std Dev	4.007

Gaus mean : 36.9528

Gaus Width: 3.2534

Entry



B1L103S, U20-ch28, adc2

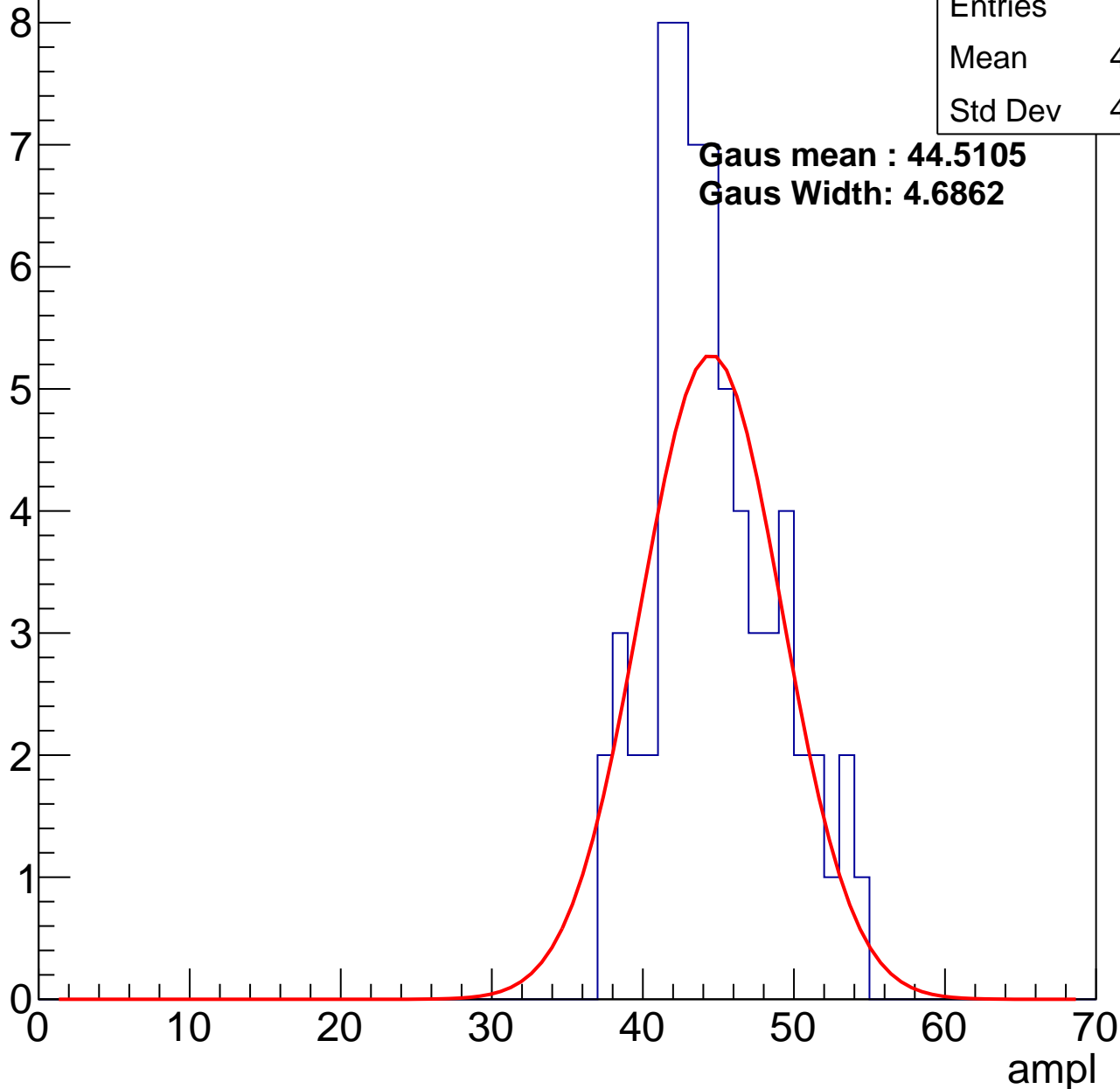
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	44.29
Std Dev	4.056

Gaus mean : 44.5105

Gaus Width: 4.6862

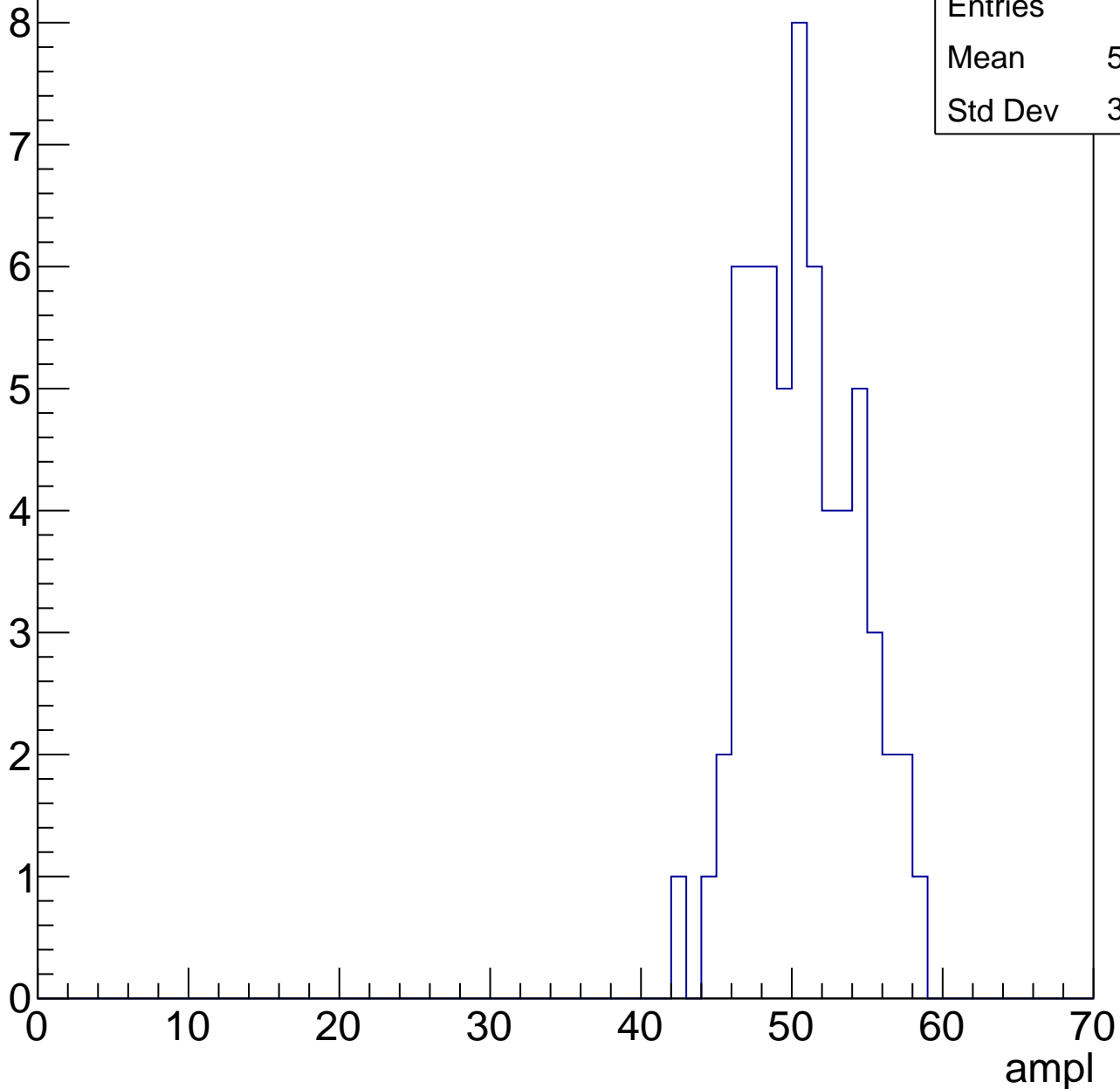


B1L103S, U20-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

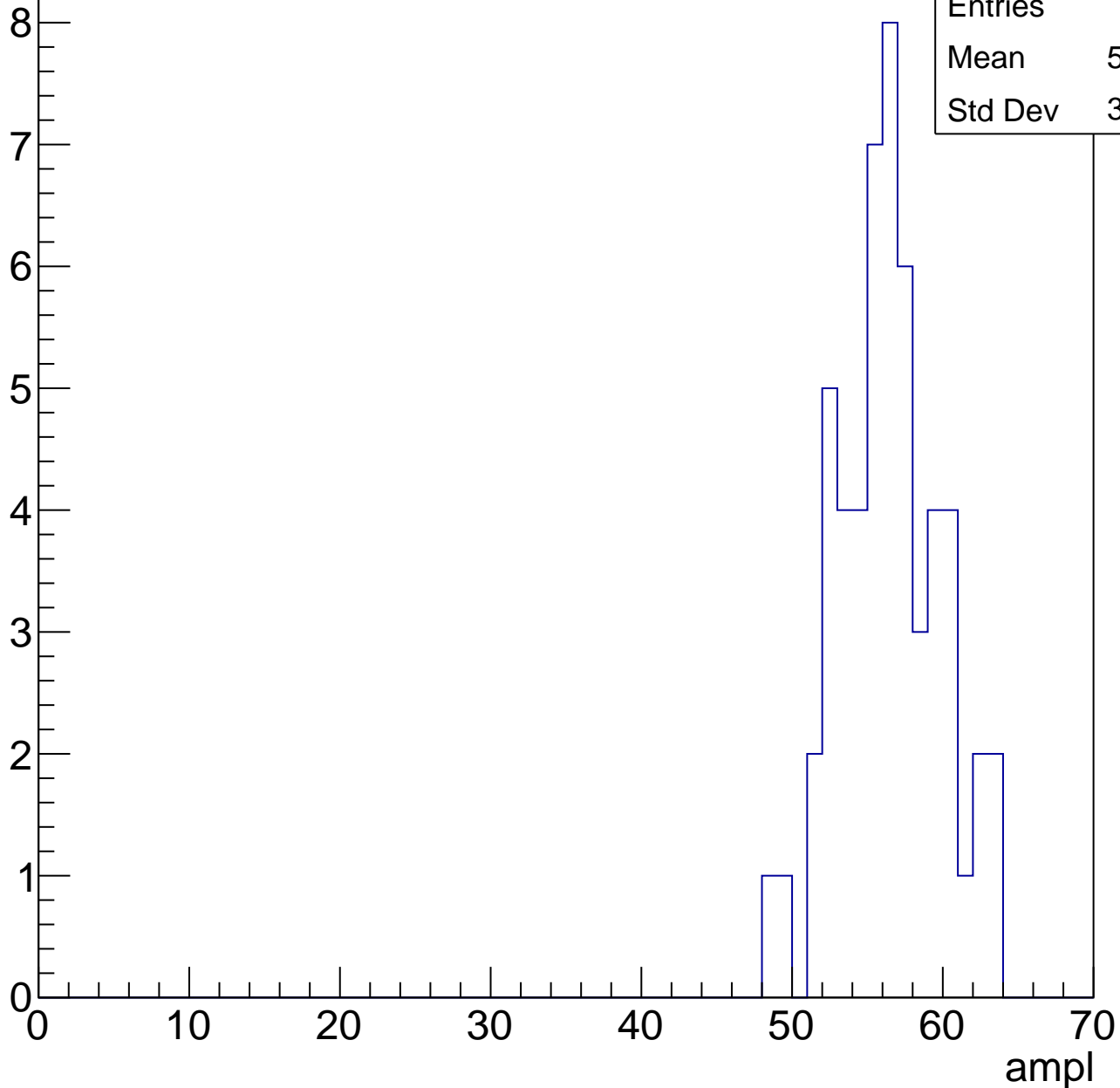
Entries	62
Mean	50.19
Std Dev	3.533



B1L103S, U20-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

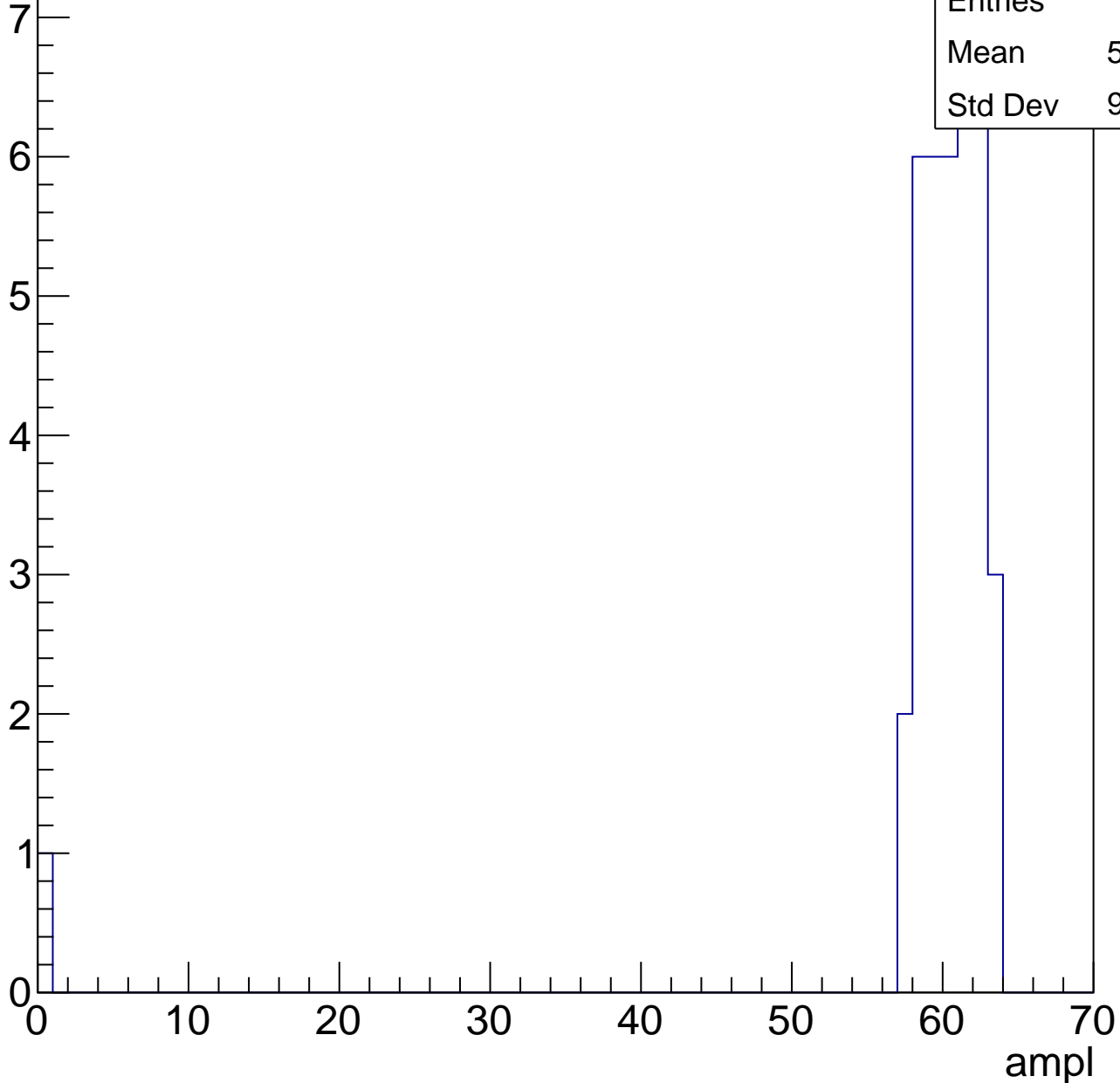


B1L103S, U20-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.58
Std Dev	9.778

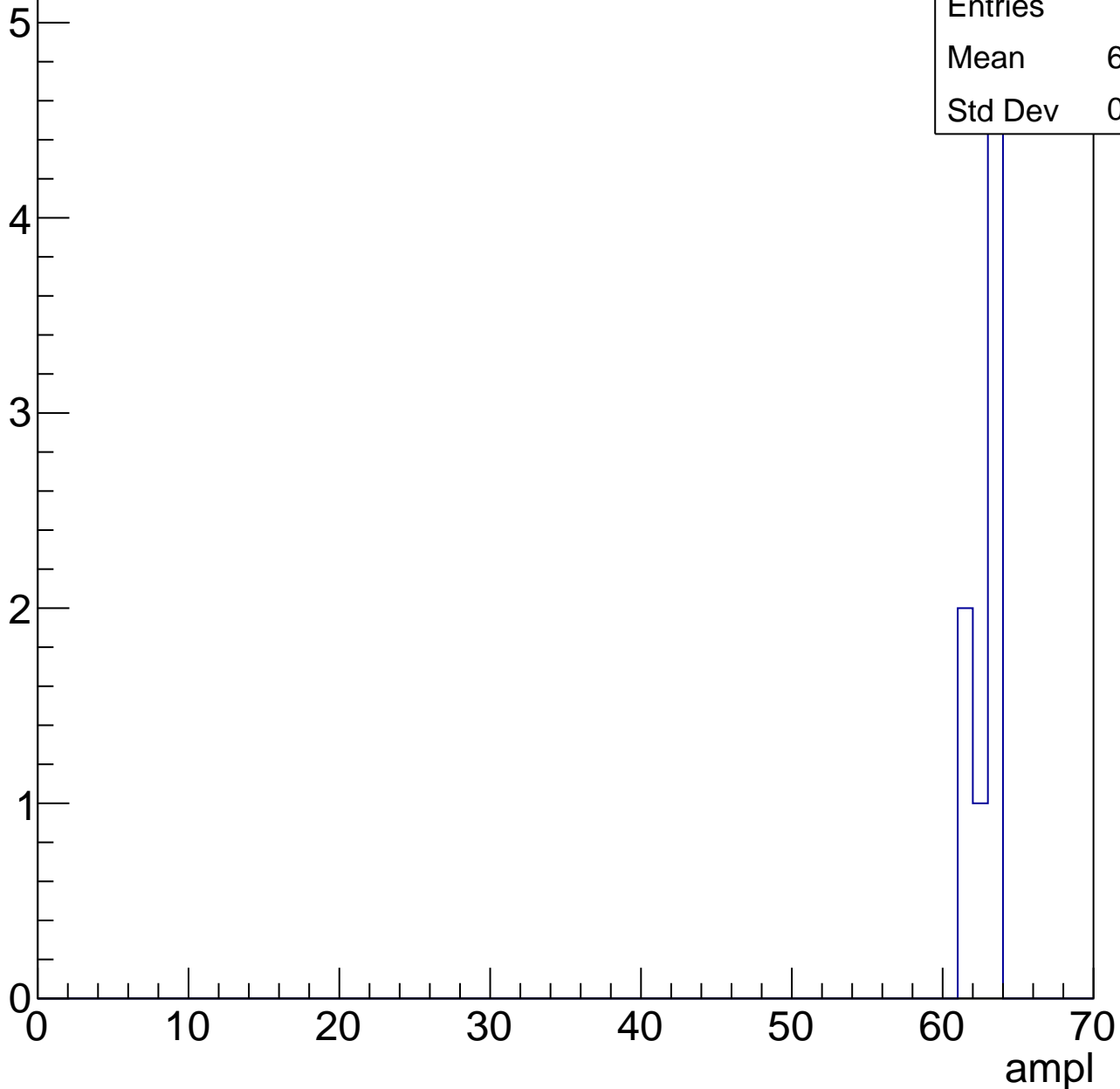


B1L103S, U20-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.38
Std Dev	0.857



B1L103S, U20-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch29, adc0

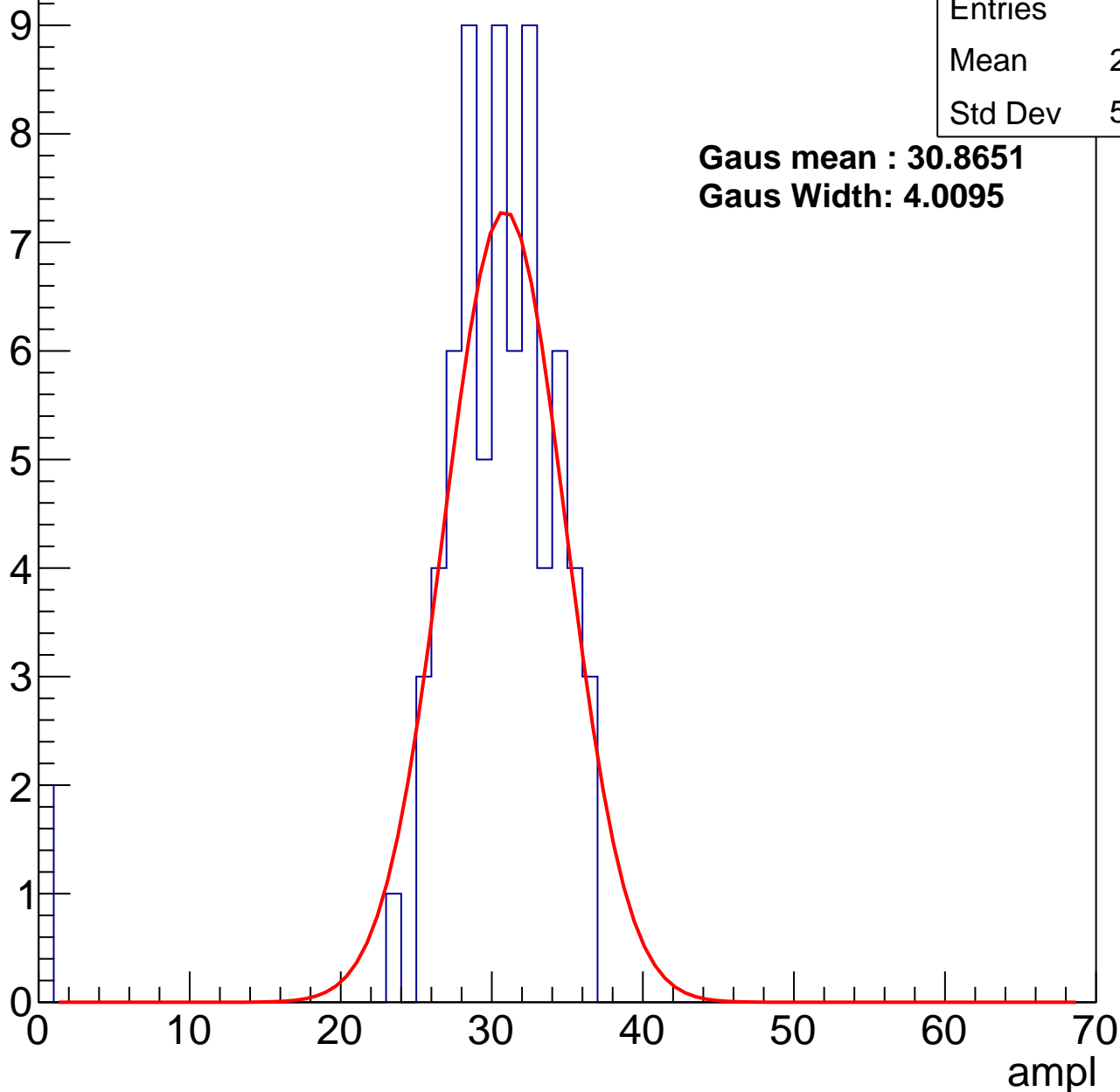
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.42
Std Dev	5.864

Gaus mean : 30.8651

Gaus Width: 4.0095



B1L103S, U20-ch29, adc1

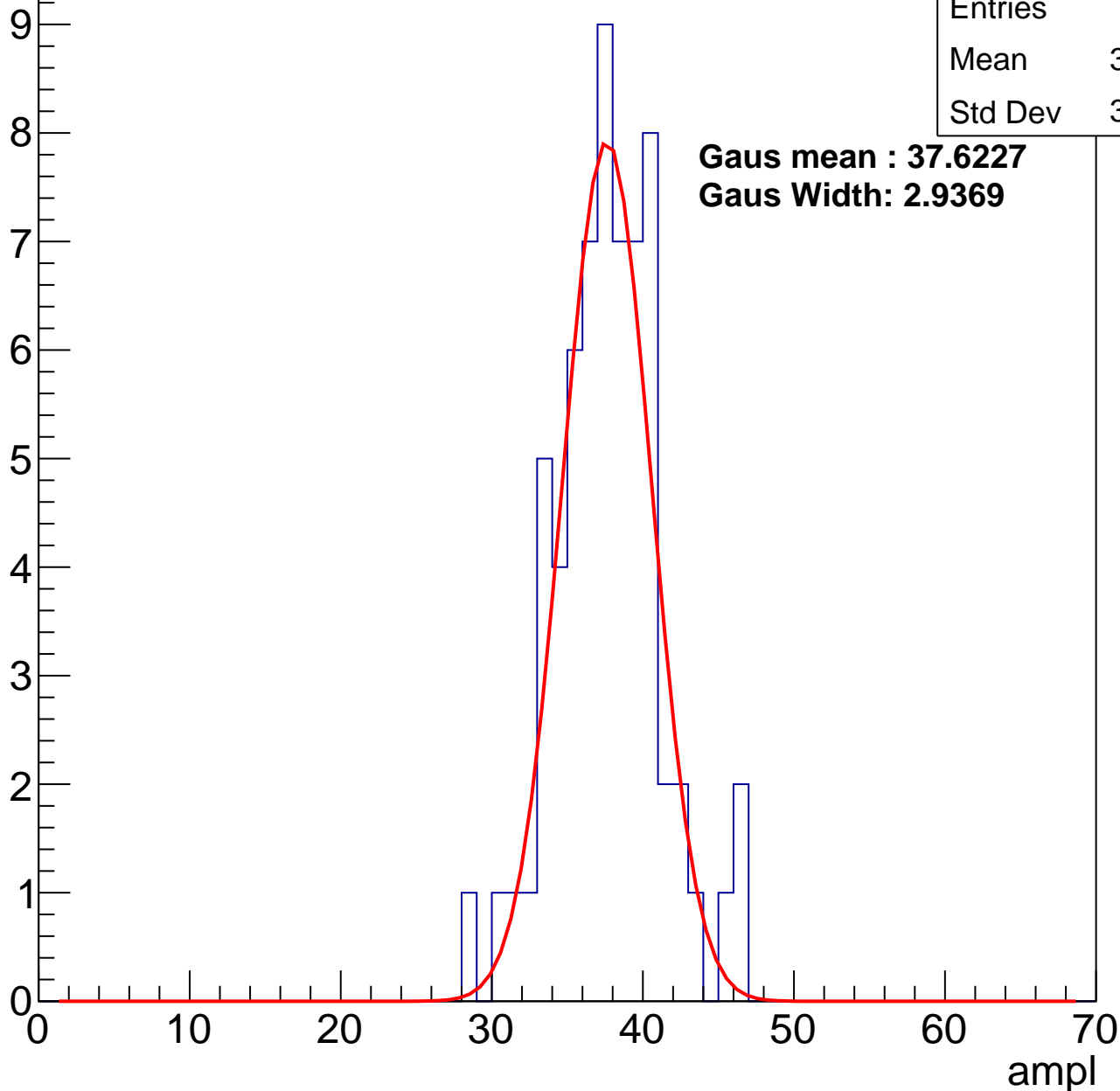
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	37.26
Std Dev	3.474

Gaus mean : 37.6227

Gaus Width: 2.9369



B1L103S, U20-ch29, adc2

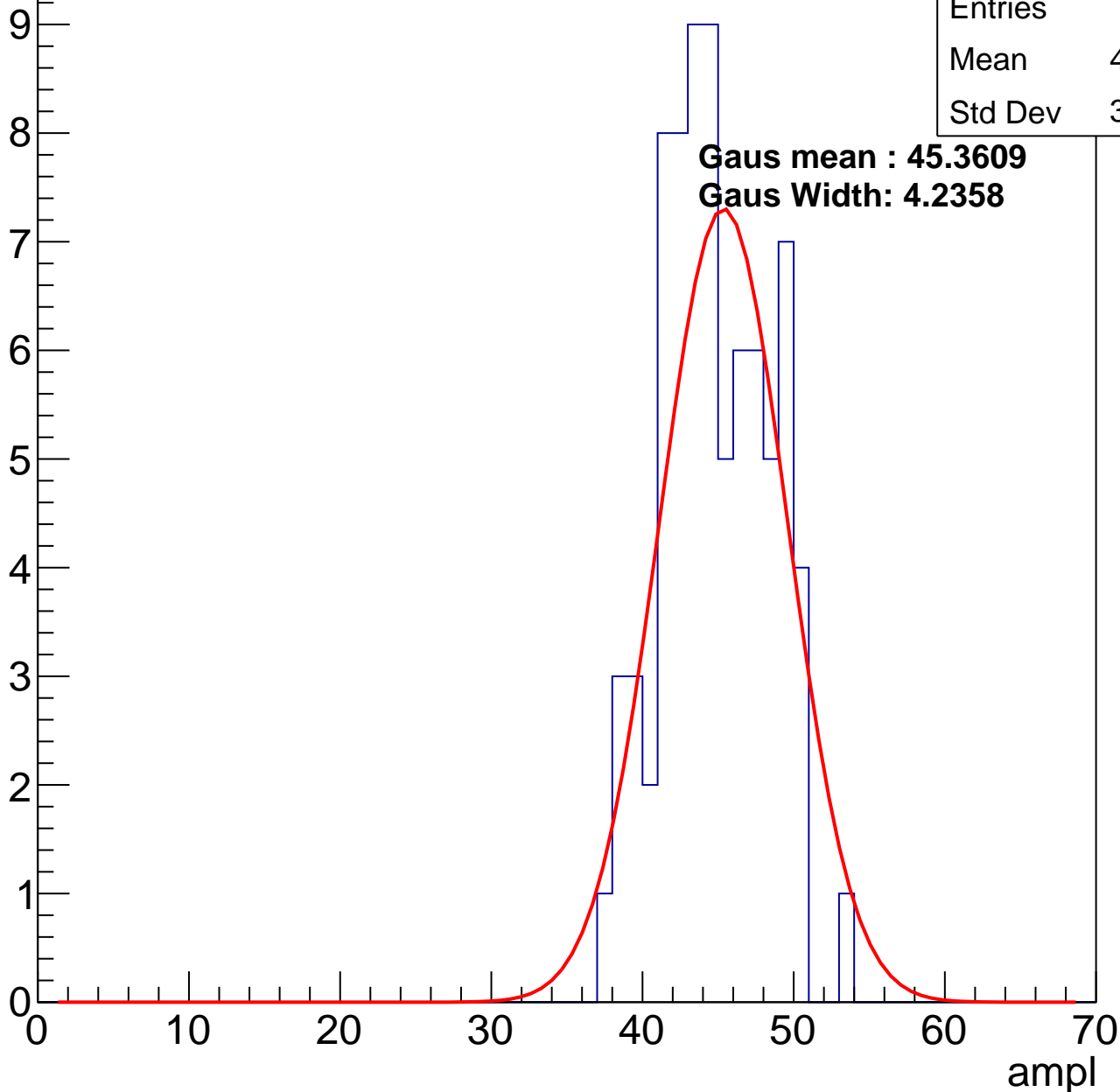
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	44.34
Std Dev	3.478

Gaus mean : 45.3609

Gaus Width: 4.2358



B1L103S, U20-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	51.6
Std Dev	3.6

Entry

10

8

6

4

2

0

0

10

20

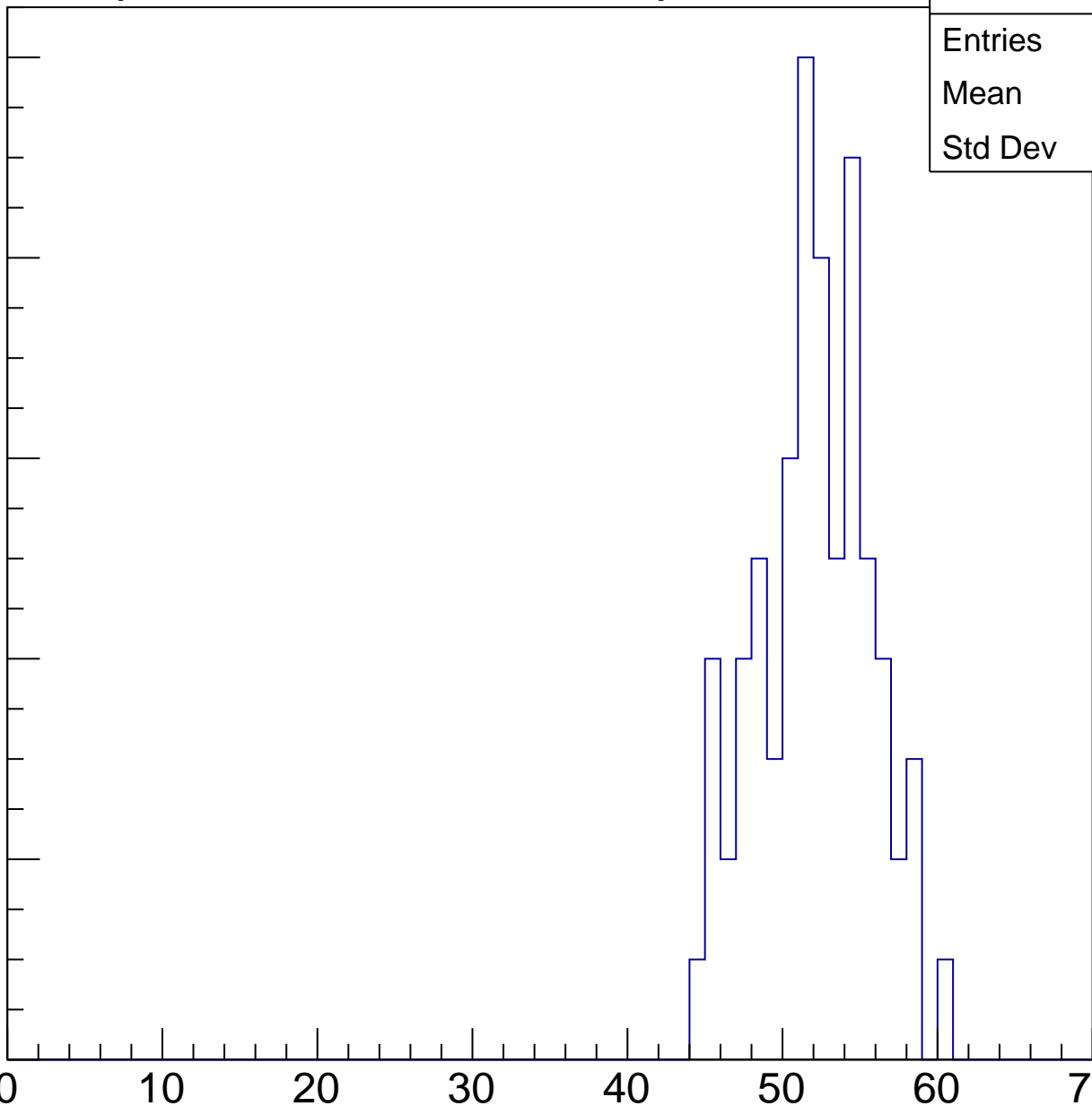
30

40

50

60

ampl

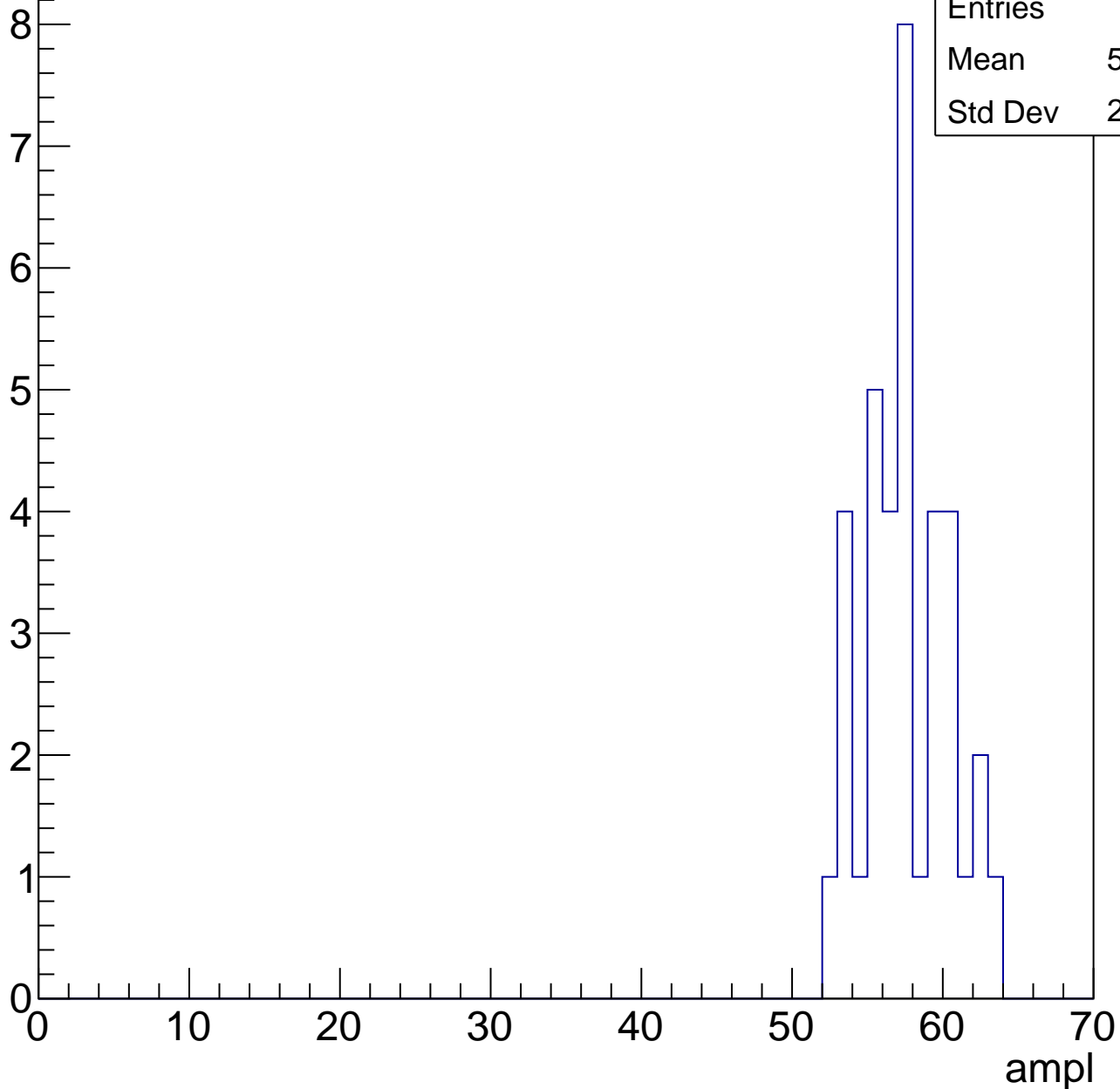


B1L103S, U20-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	57.08
Std Dev	2.773

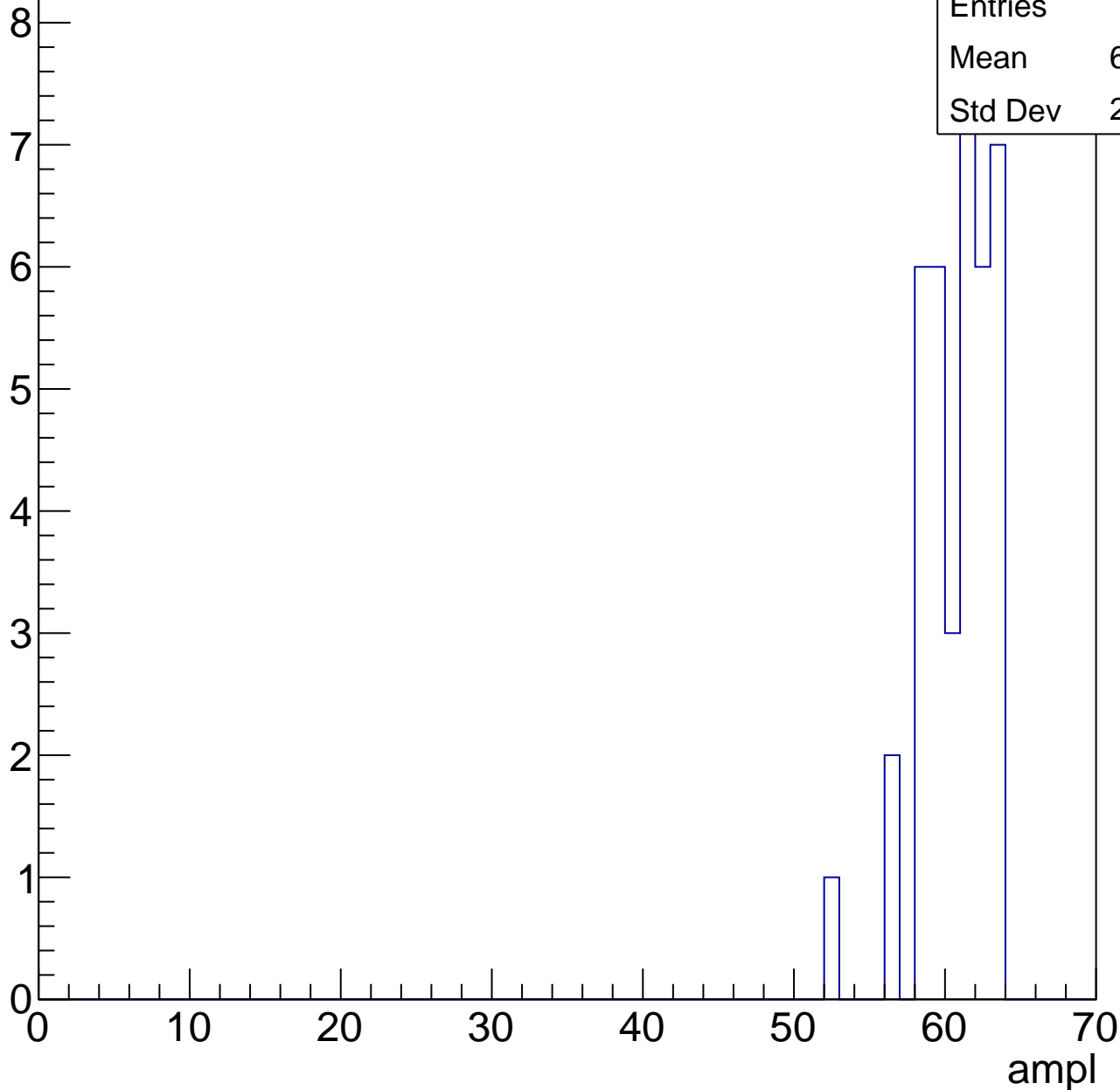


B1L103S, U20-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

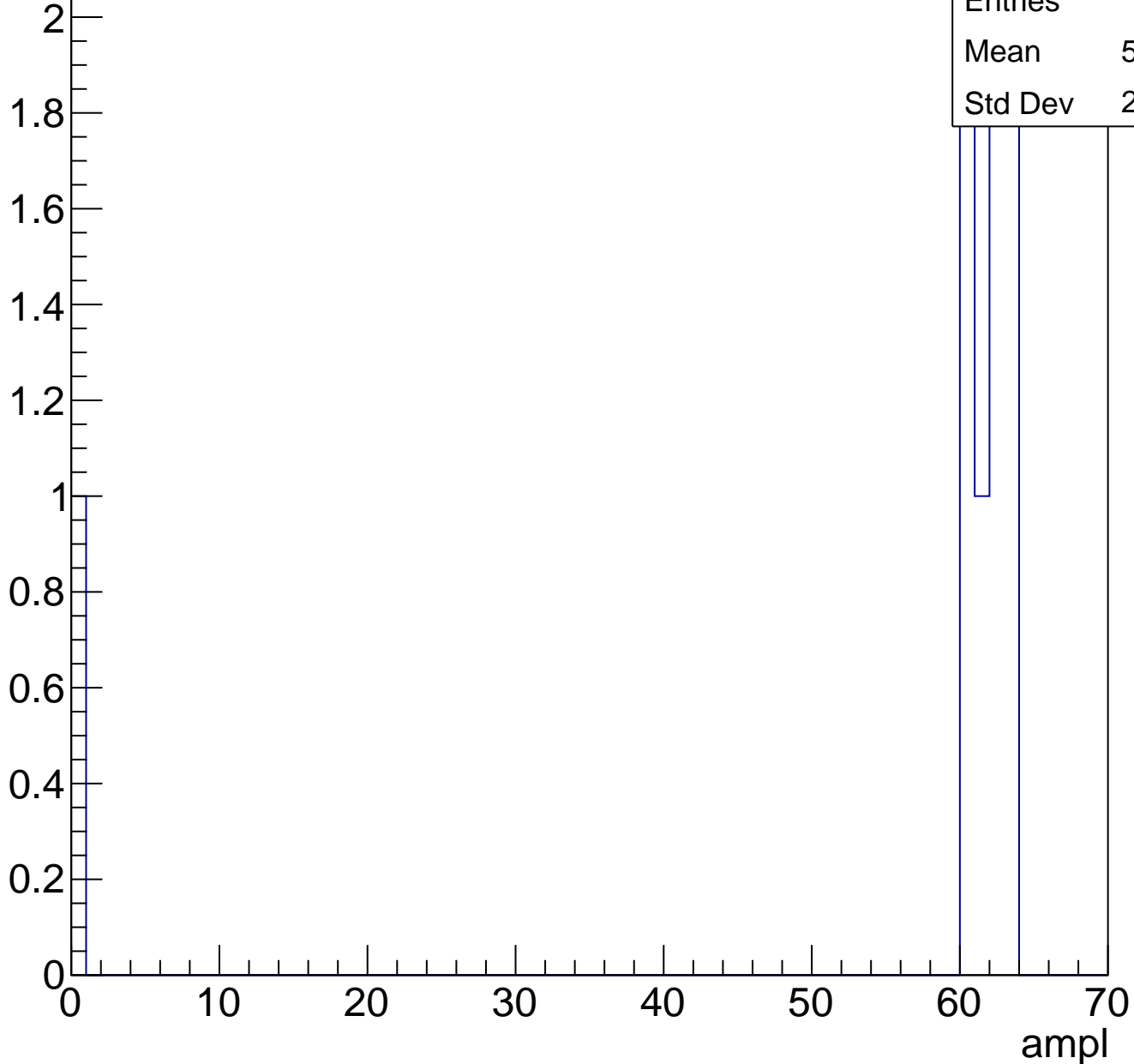
Entries	39
Mean	60.18
Std Dev	2.374



B1L103S, U20-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch30, adc0

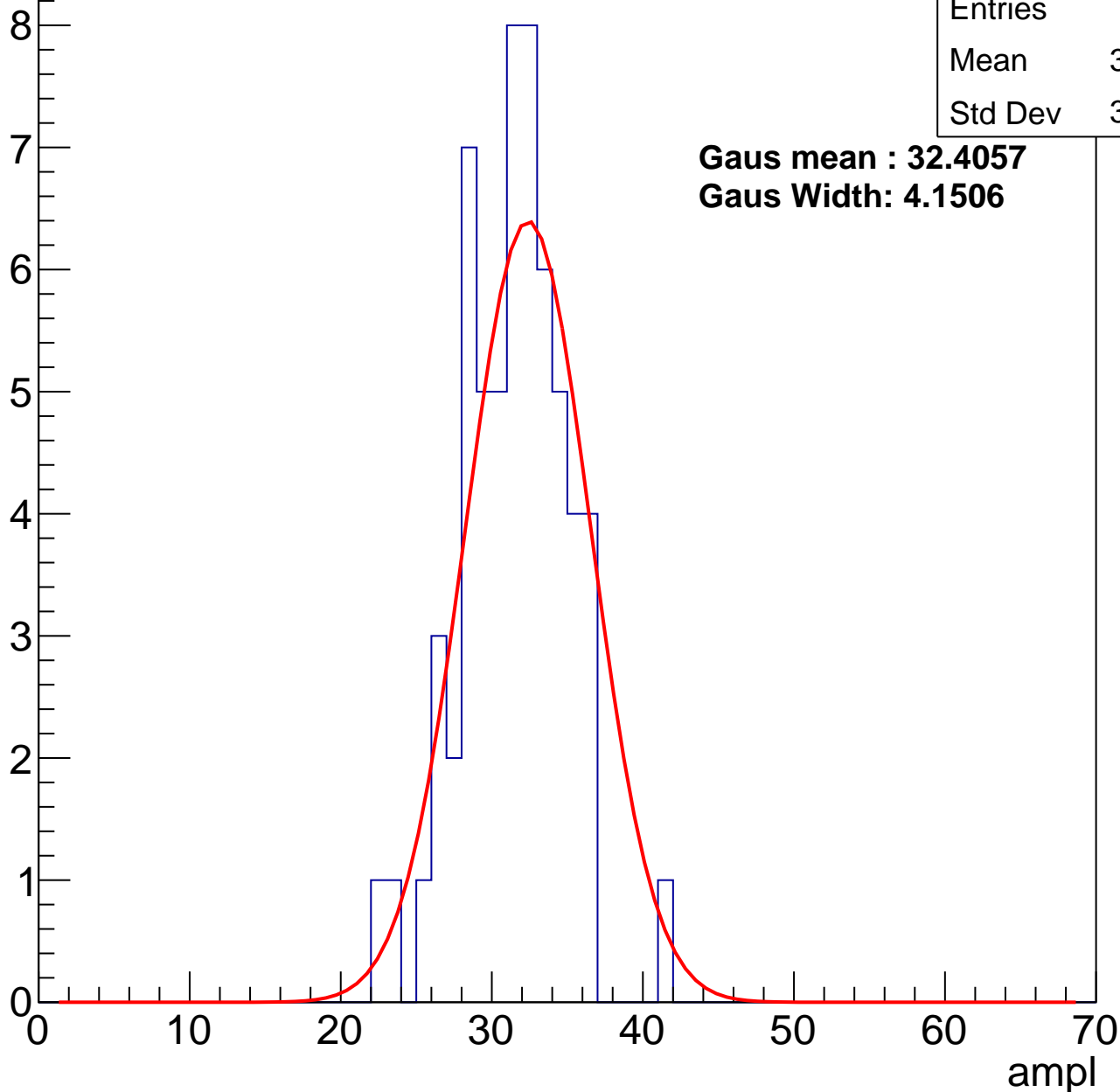
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	30.98
Std Dev	3.428

Gaus mean : 32.4057

Gaus Width: 4.1506



B1L103S, U20-ch30, adc1

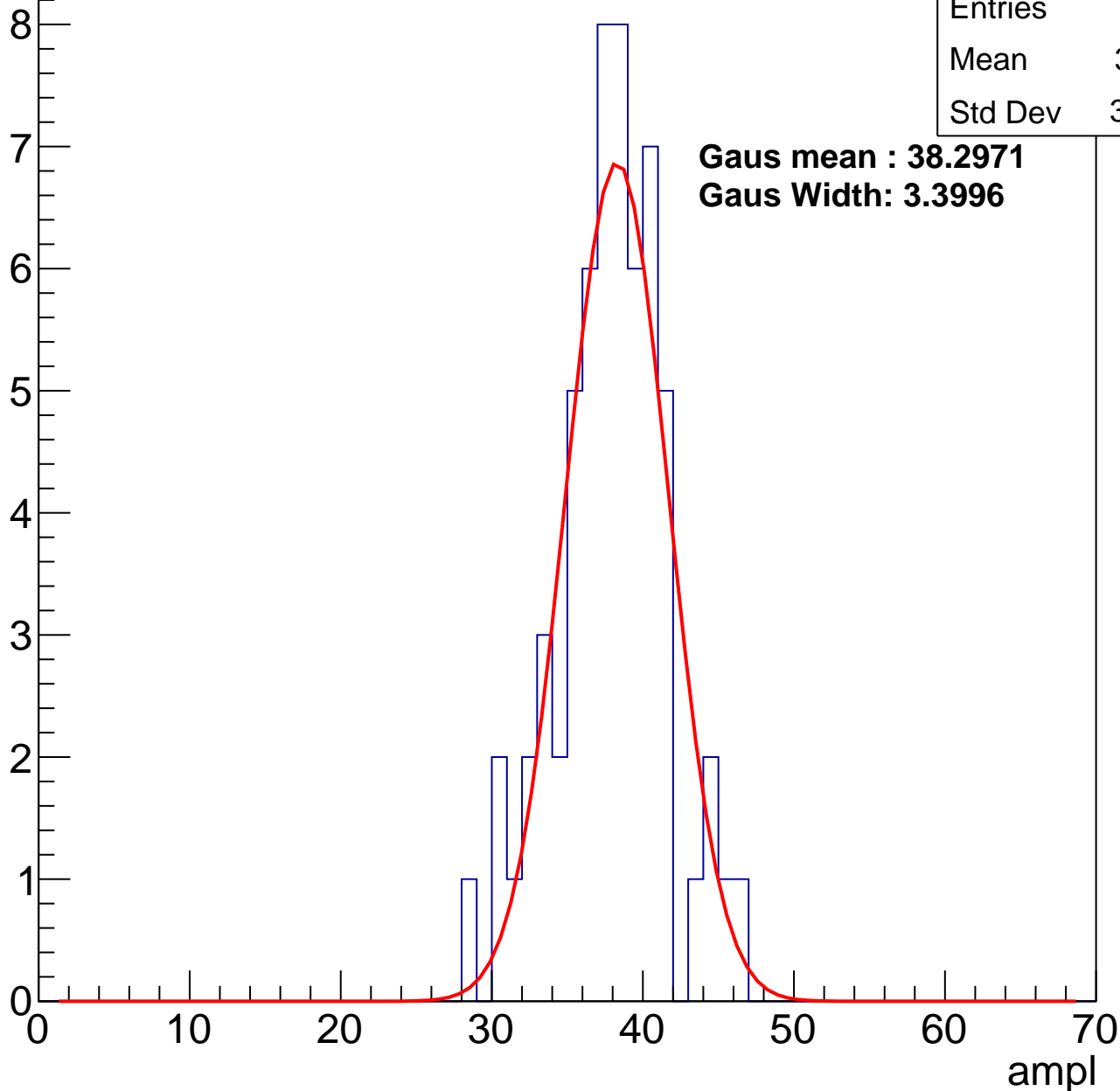
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.41
Std Dev	3.623

Gaus mean : 38.2971

Gaus Width: 3.3996



B1L103S, U20-ch30, adc2

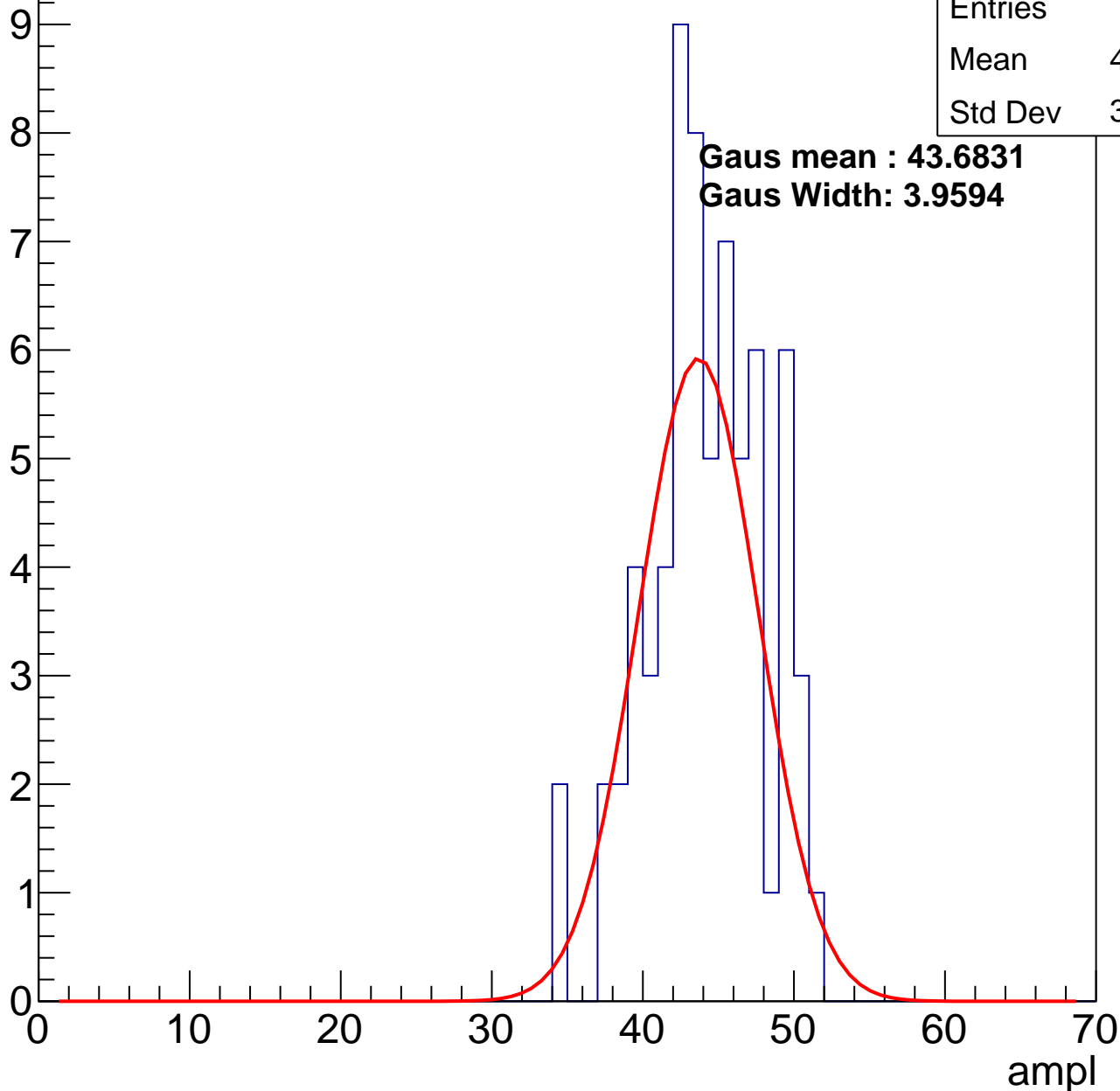
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.68
Std Dev	3.825

Gaus mean : 43.6831

Gaus Width: 3.9594

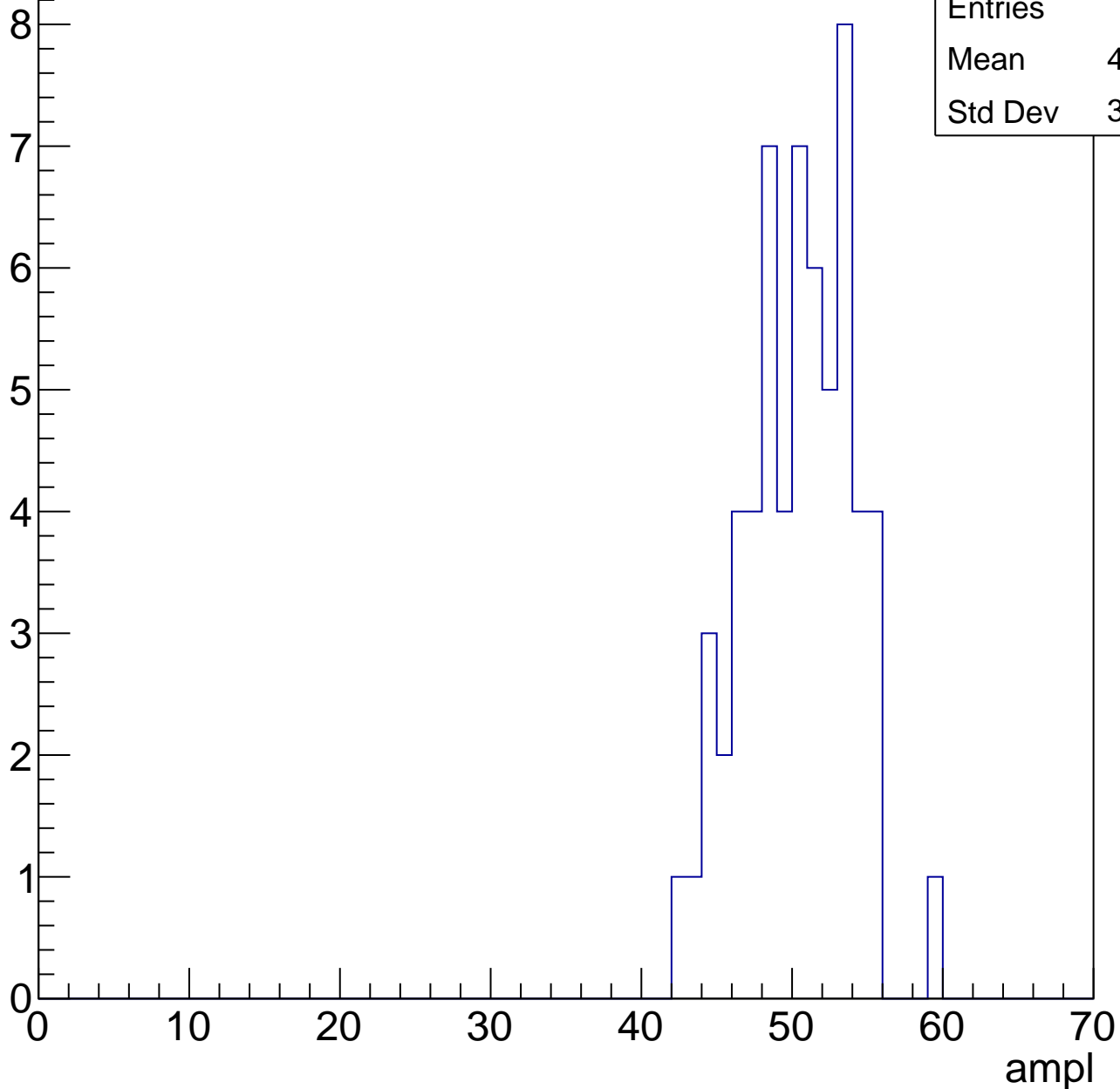


B1L103S, U20-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.93
Std Dev	3.496

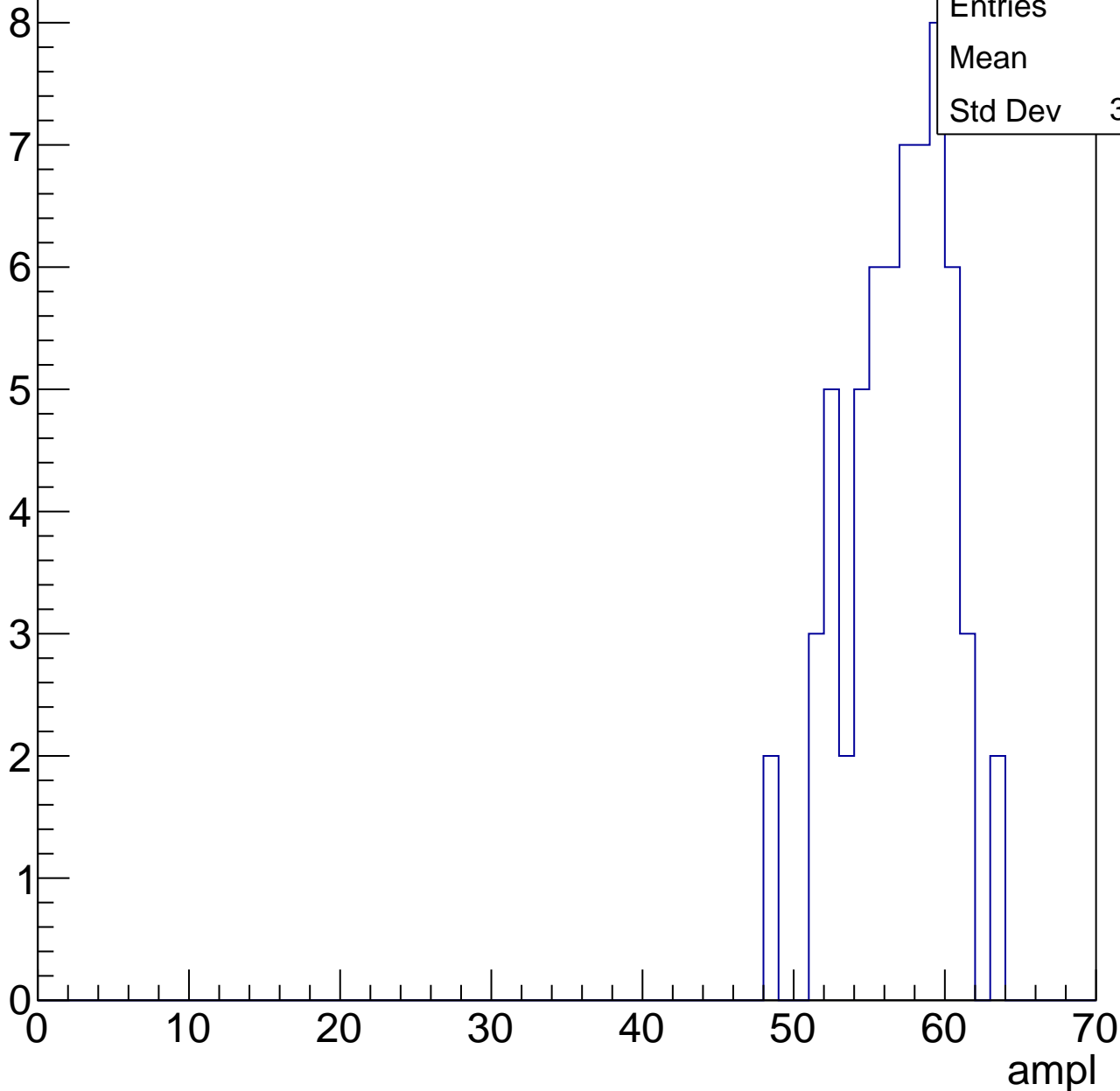


B1L103S, U20-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.4
Std Dev	3.343

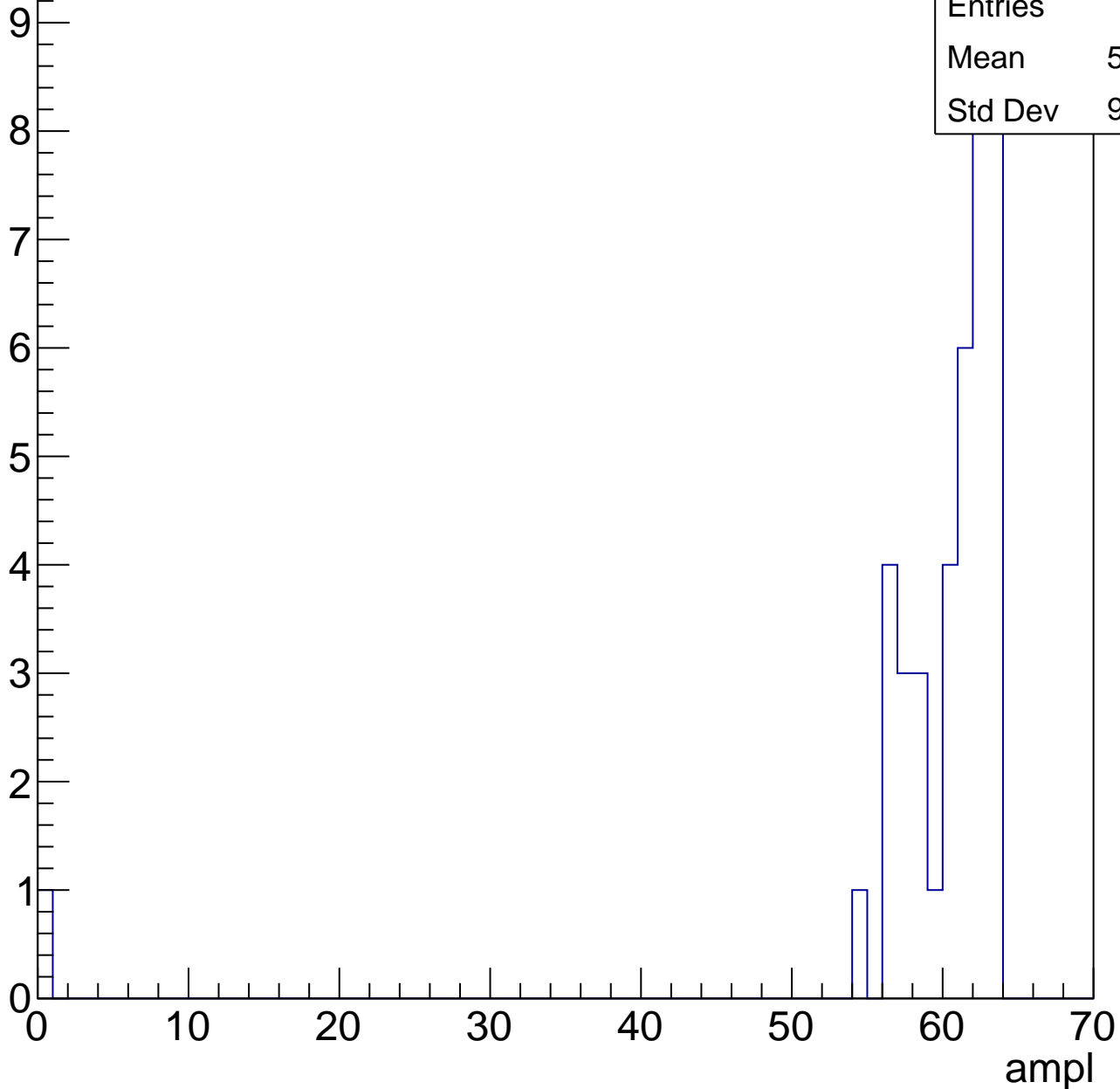


B1L103S, U20-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.75
Std Dev	9.736



B1L103S, U20-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch30, adc7

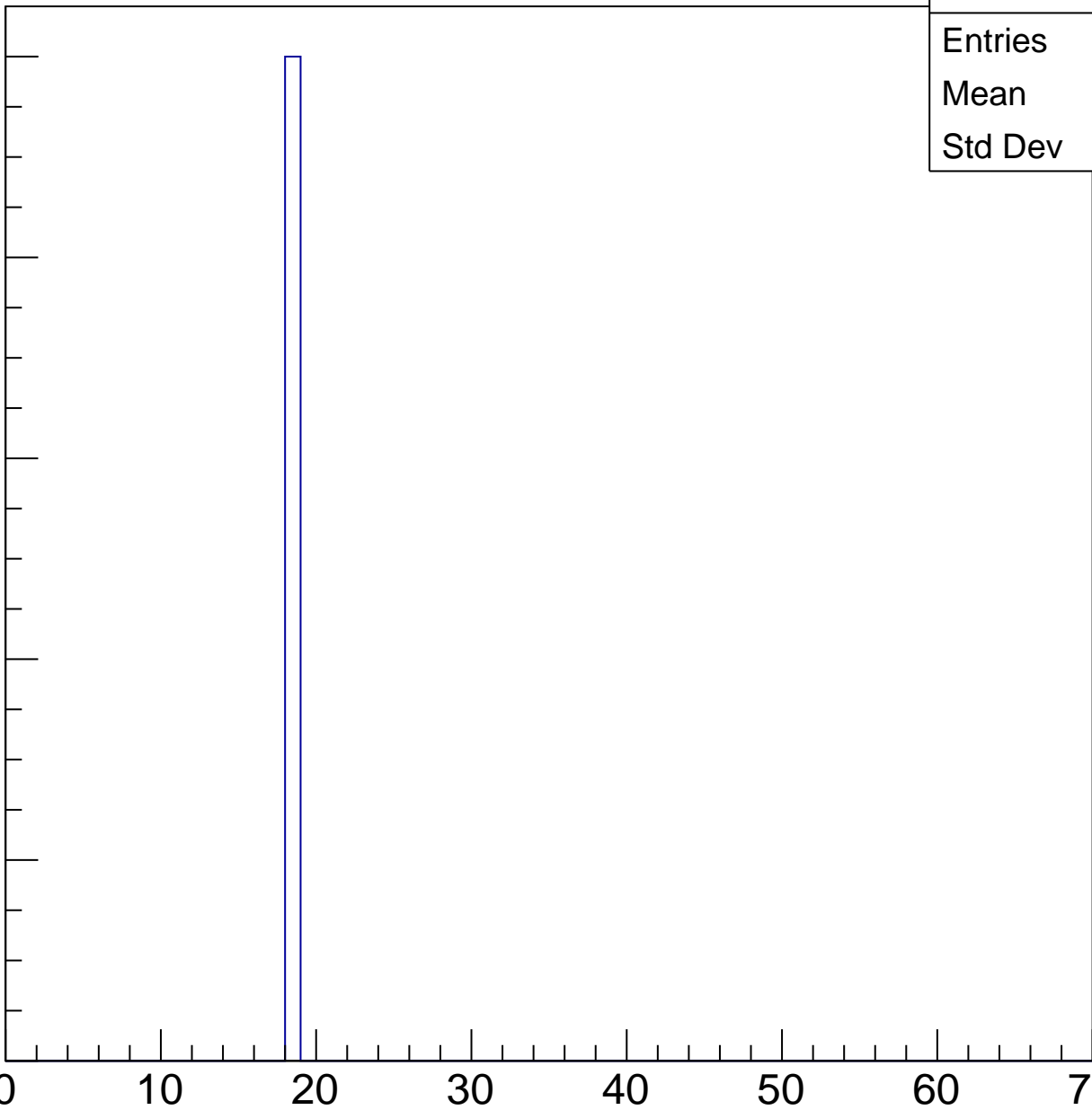
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	18
Std Dev	0

ampl



B1L103S, U20-ch31, adc0

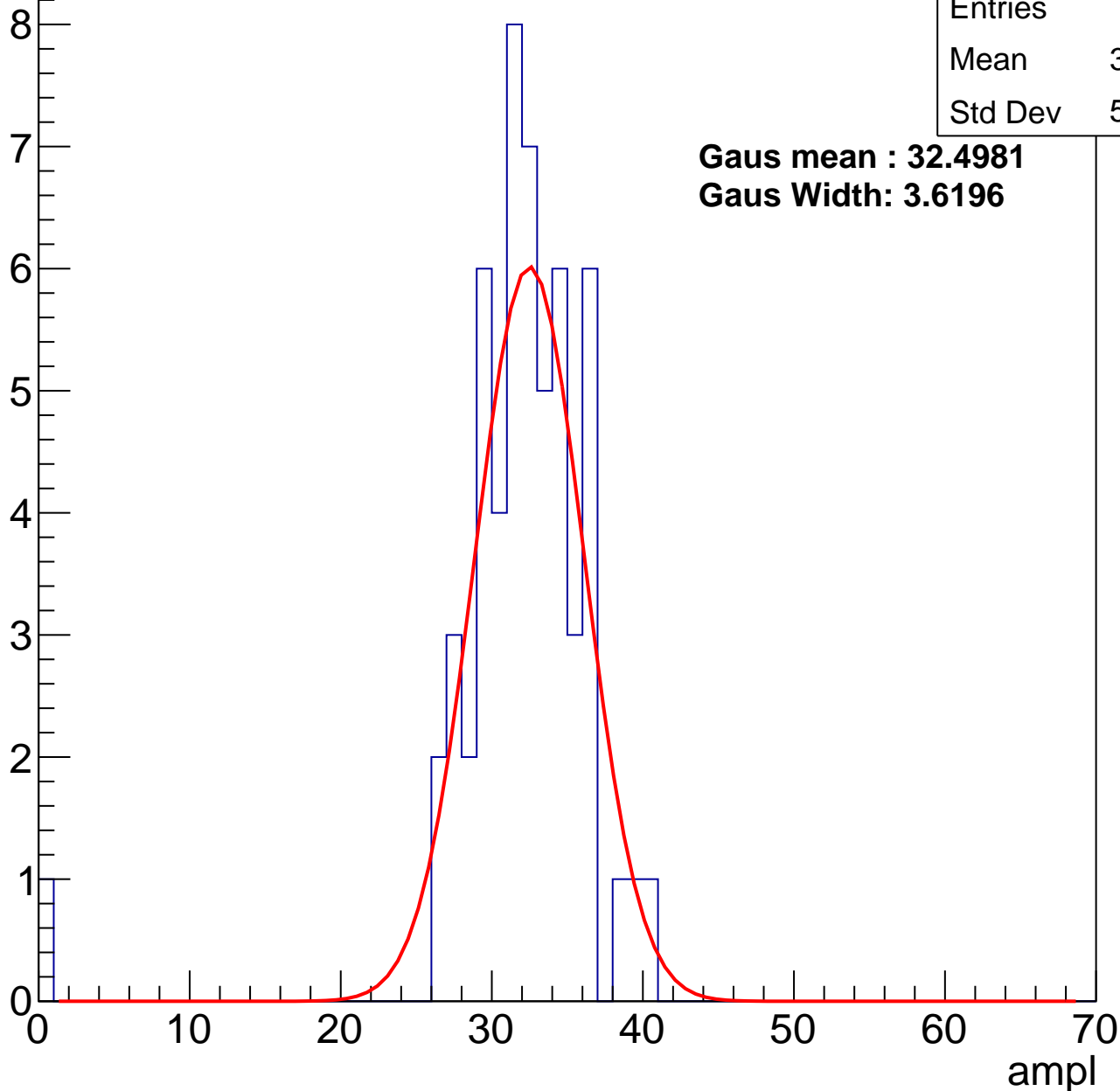
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	31.46
Std Dev	5.288

Gaus mean : 32.4981

Gaus Width: 3.6196



B1L103S, U20-ch31, adc1

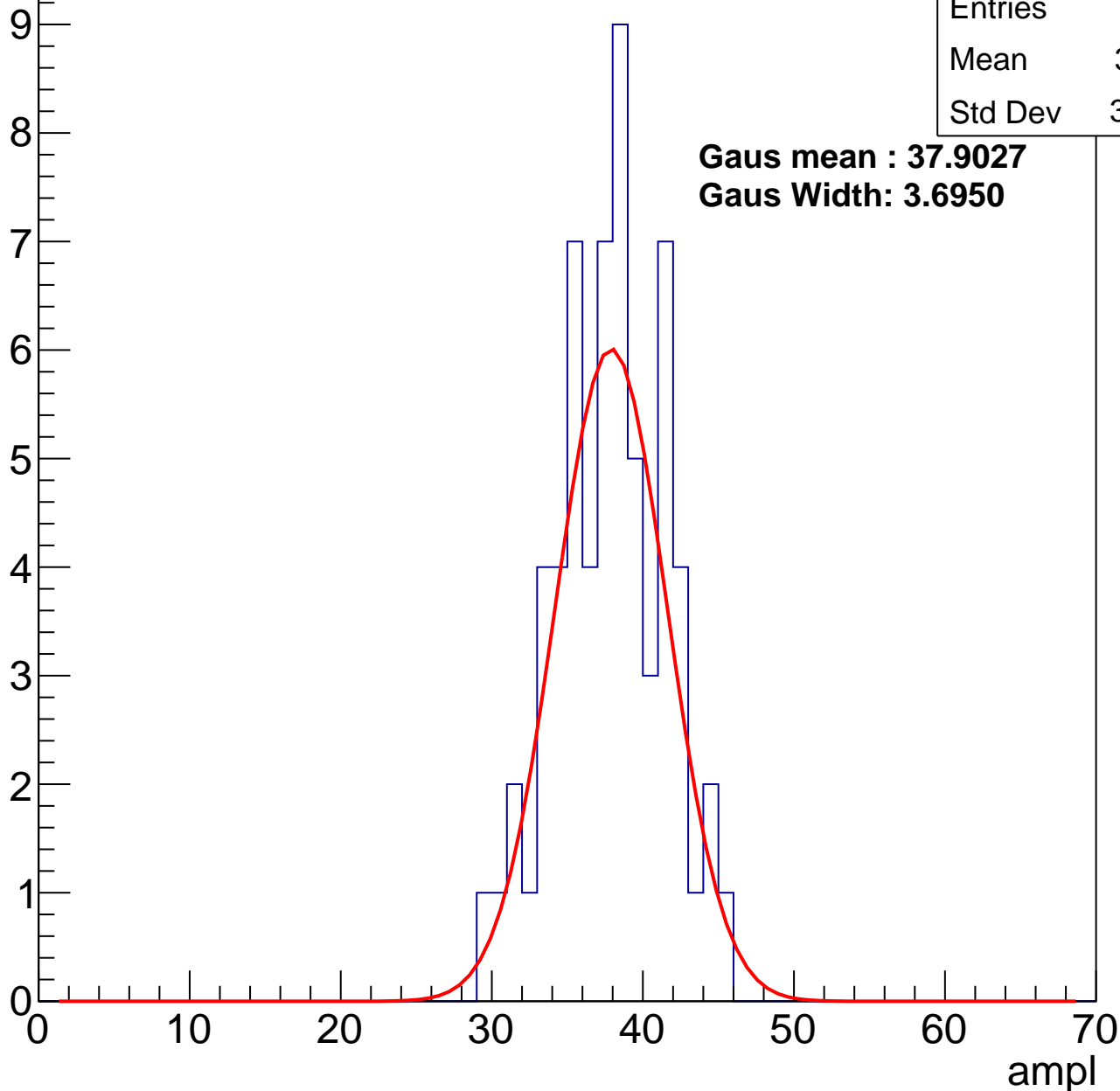
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.41
Std Dev	3.557

Gaus mean : 37.9027

Gaus Width: 3.6950



B1L103S, U20-ch31, adc2

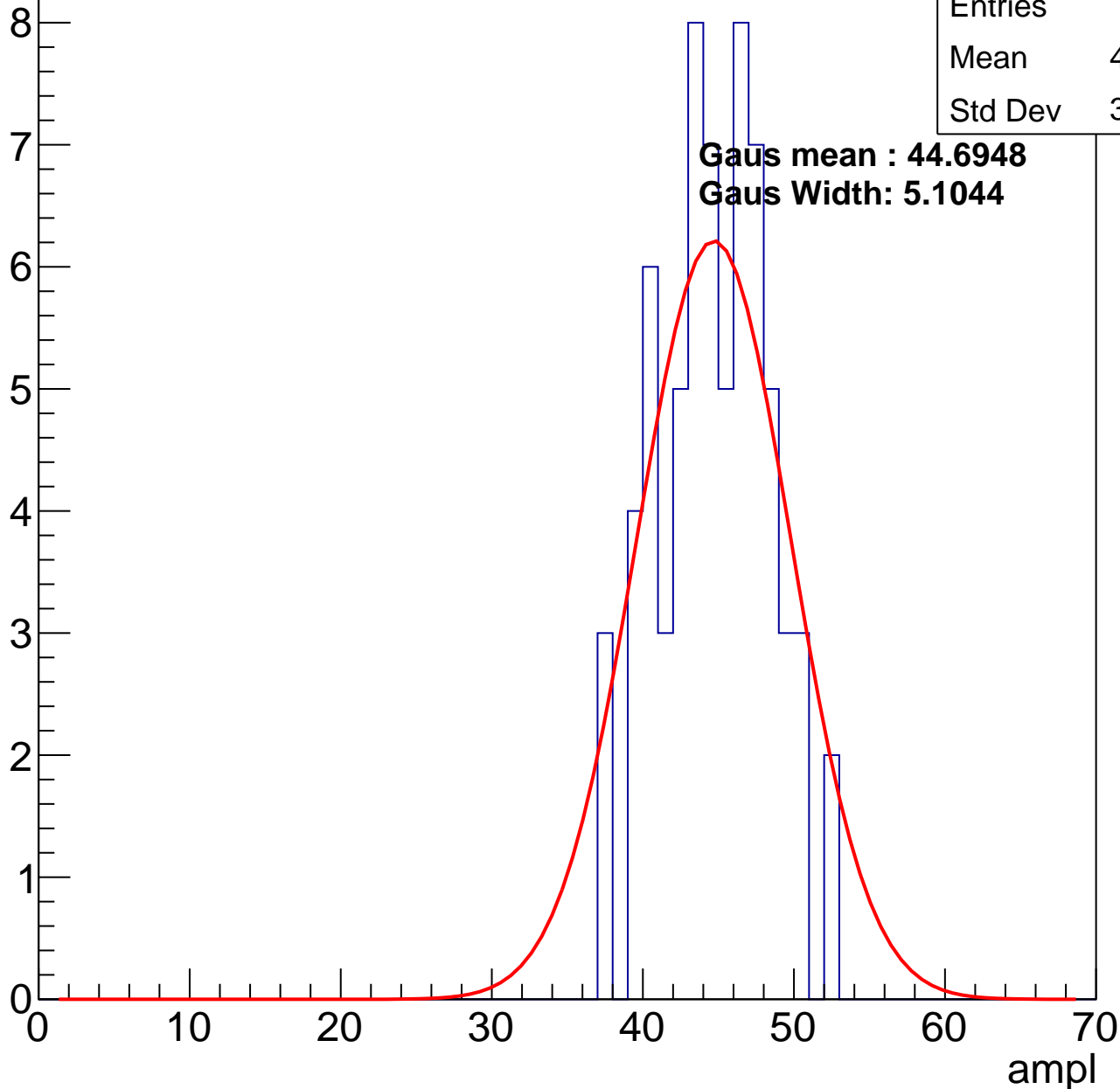
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	44.28
Std Dev	3.579

Gaus mean : 44.6948

Gaus Width: 5.1044

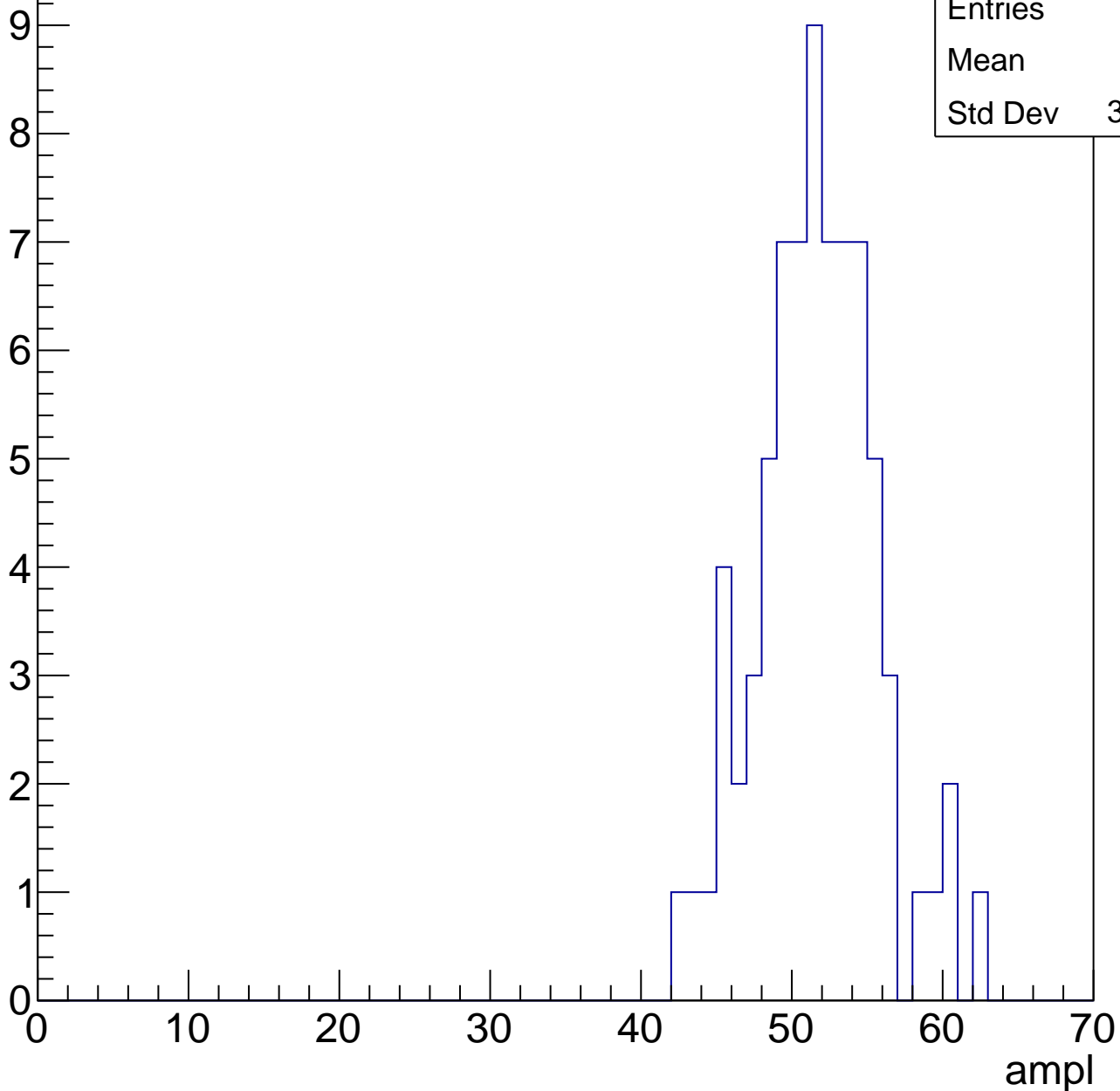


B1L103S, U20-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	51.2
Std Dev	3.963



B1L103S, U20-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	50
Mean	55.94
Std Dev	8.365

Entry

10

8

6

4

2

0

0

10

20

30

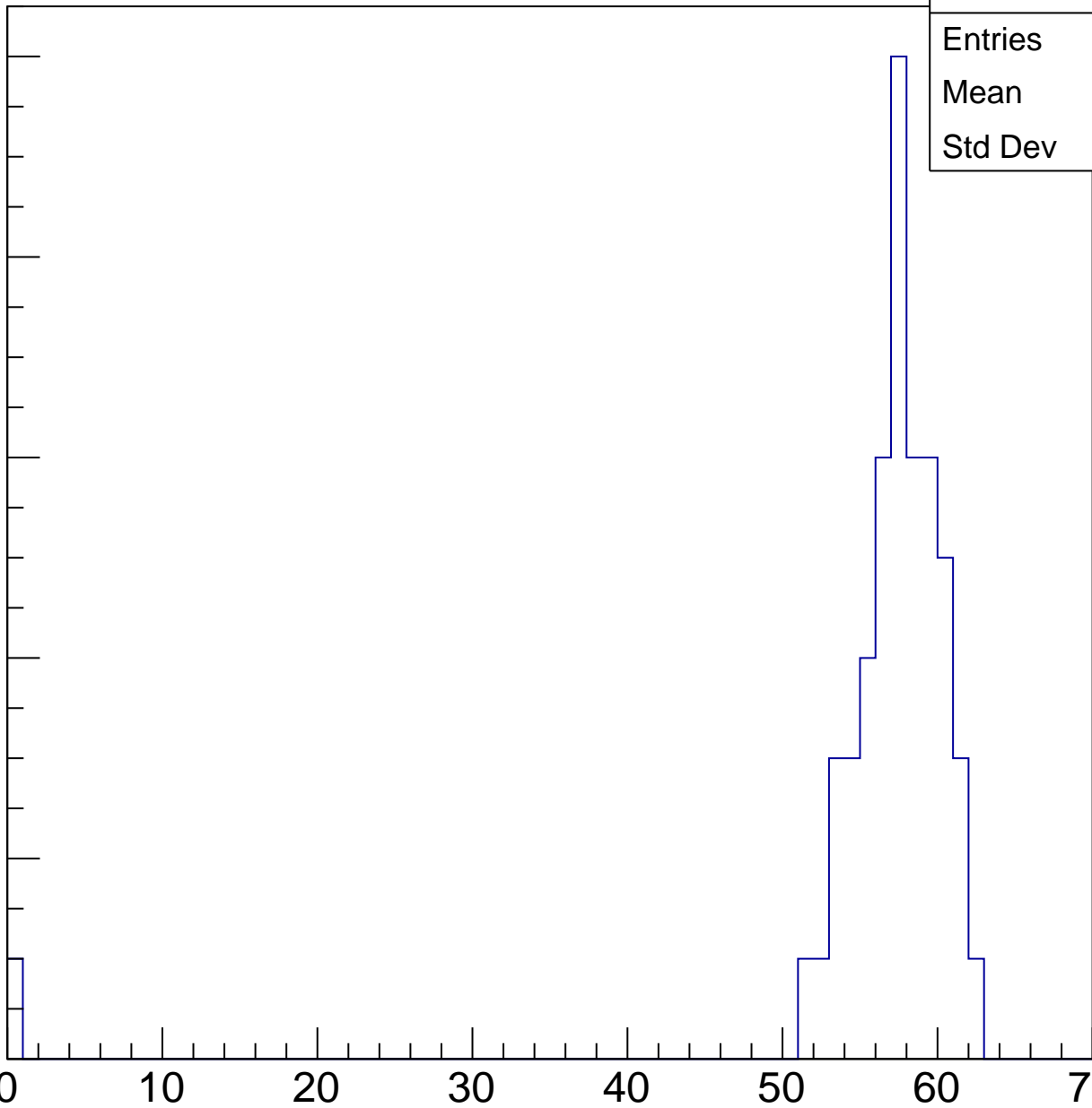
40

50

60

70

ampl

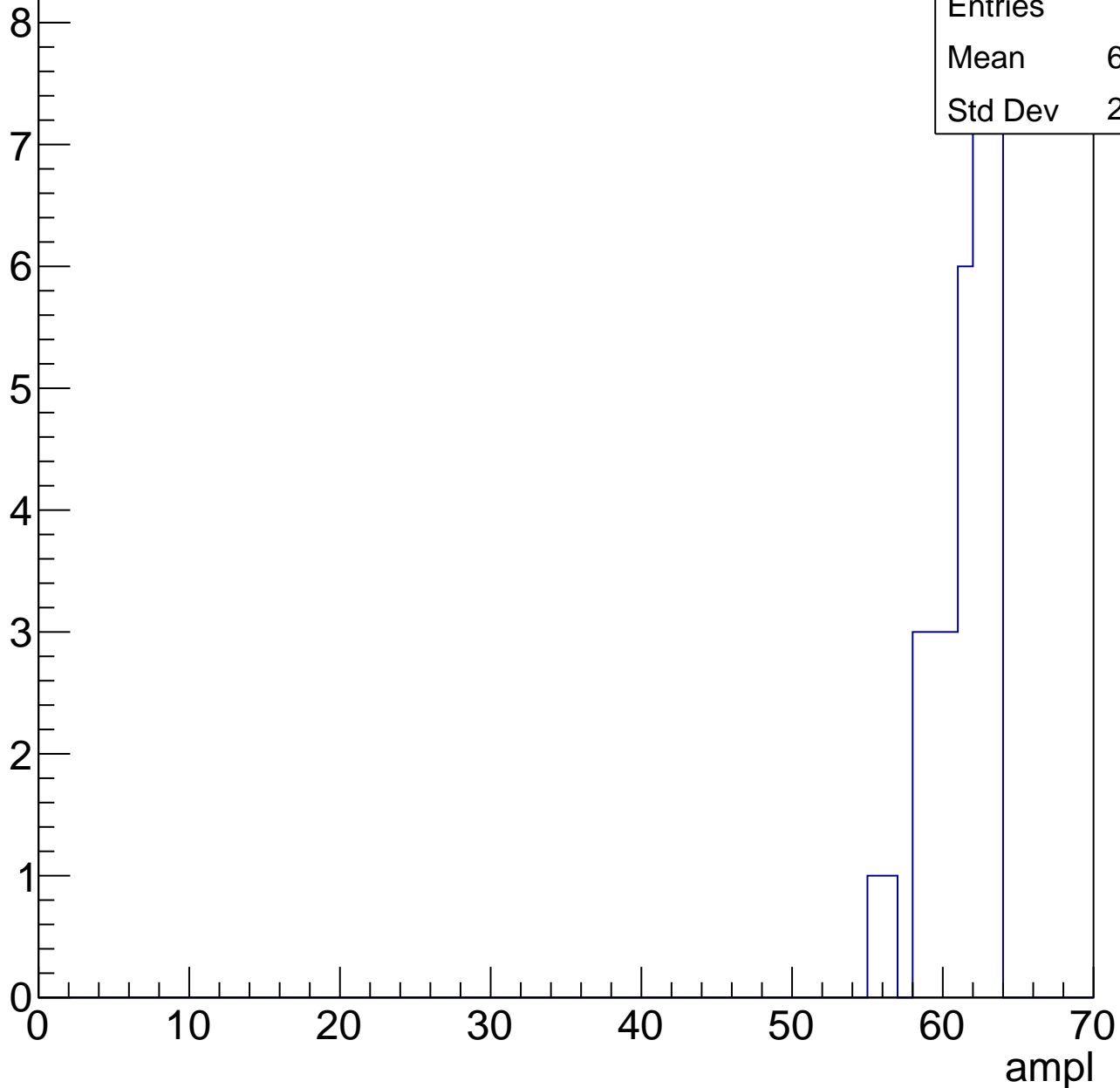


B1L103S, U20-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	60.85
Std Dev	2.076



B1L103S, U20-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch32, adc0

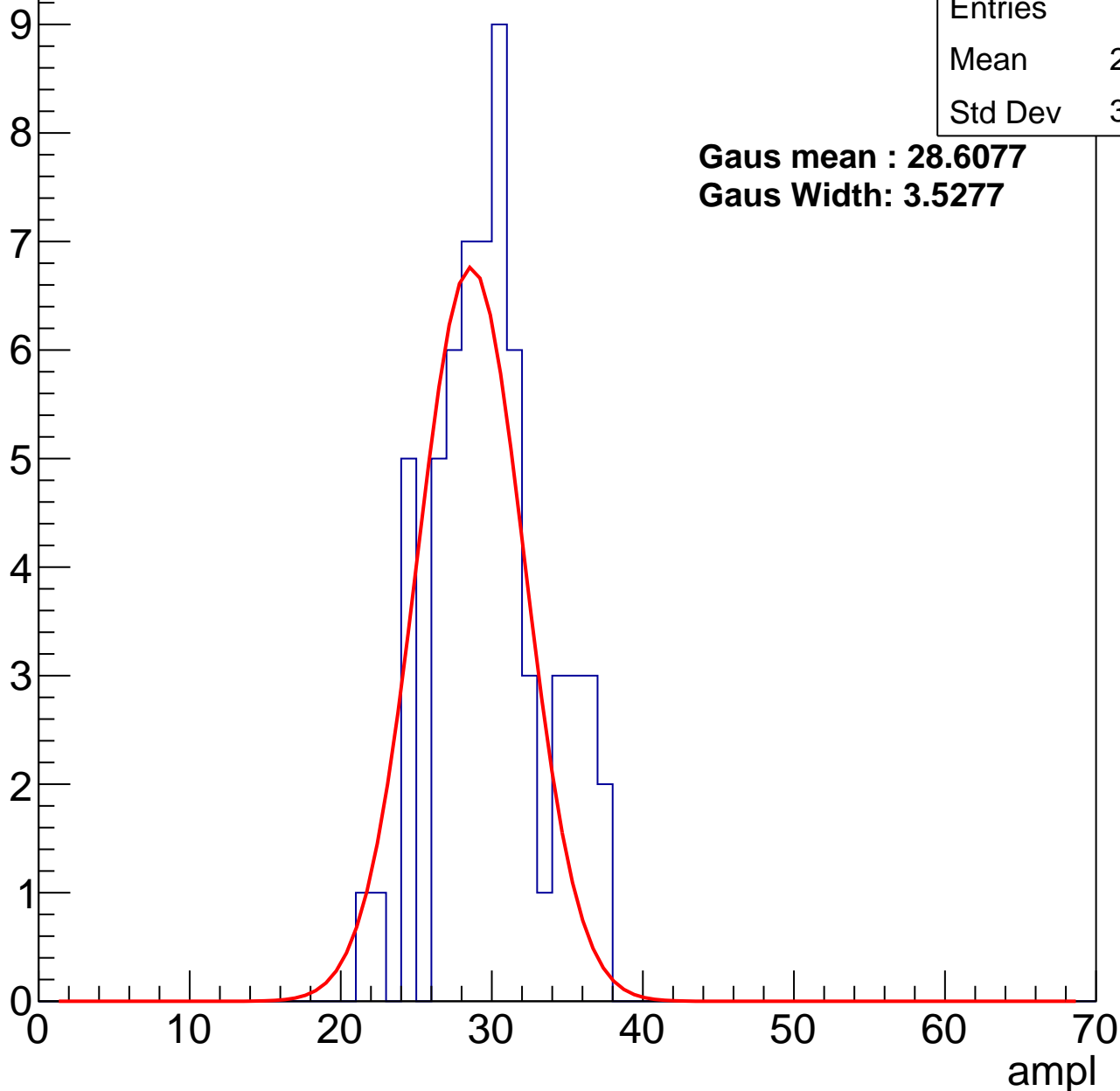
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.48
Std Dev	3.667

Gaus mean : 28.6077

Gaus Width: 3.5277



B1L103S, U20-ch32, adc1

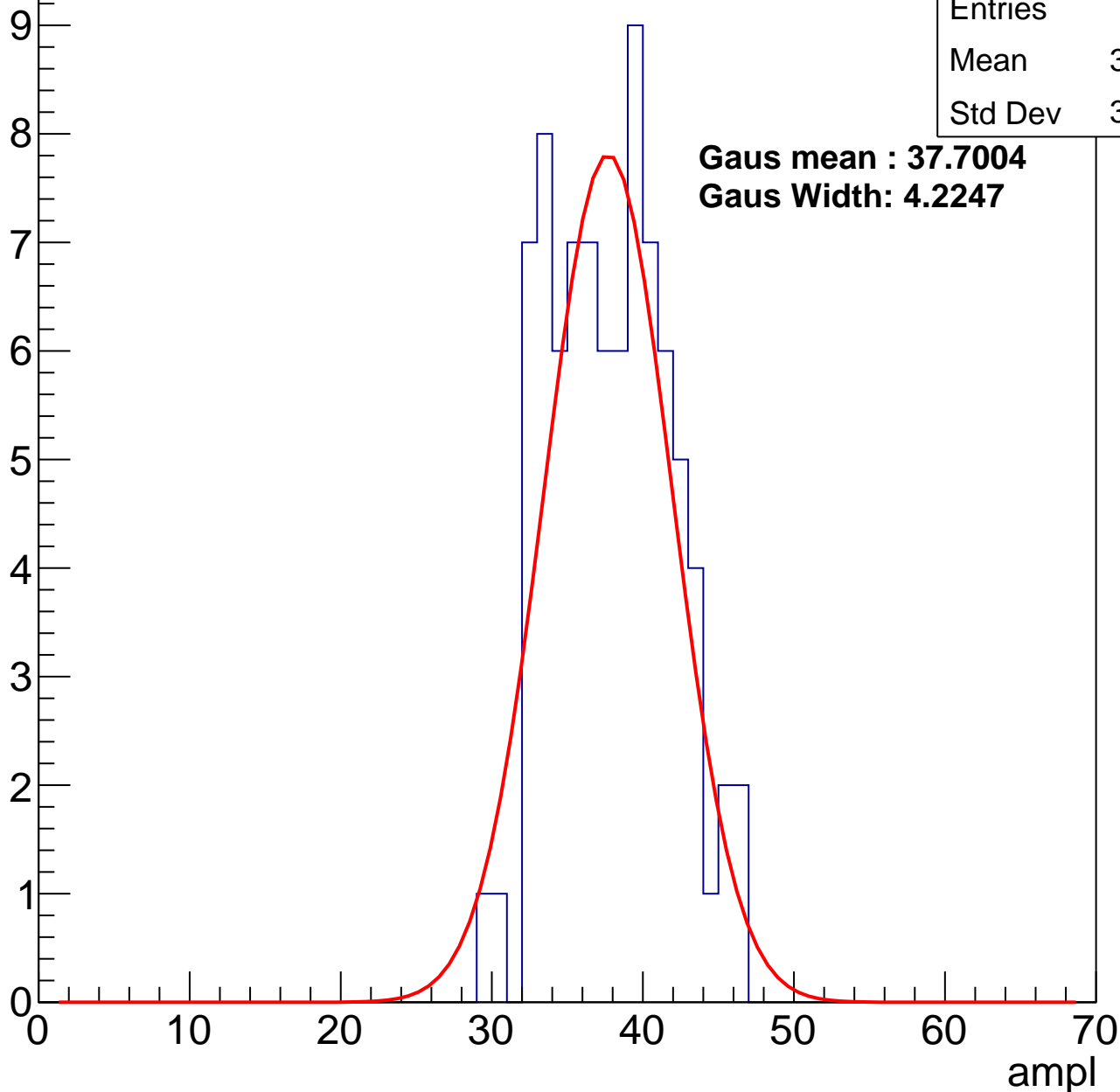
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	37.45
Std Dev	3.906

Gaus mean : 37.7004

Gaus Width: 4.2247



B1L103S, U20-ch32, adc2

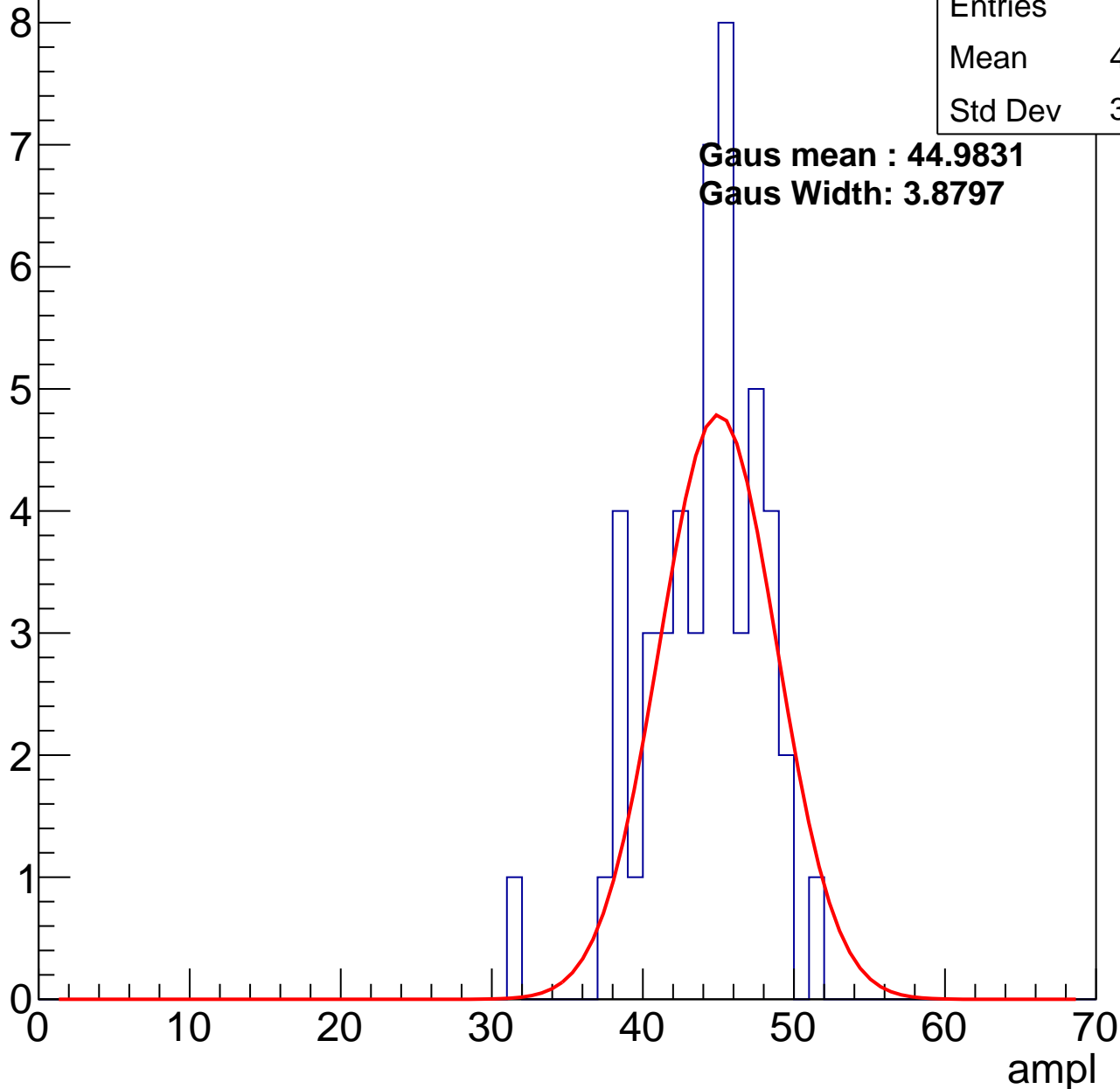
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	43.62
Std Dev	3.746

Gaus mean : 44.9831

Gaus Width: 3.8797



B1L103S, U20-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	49.5
Std Dev	3.992

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

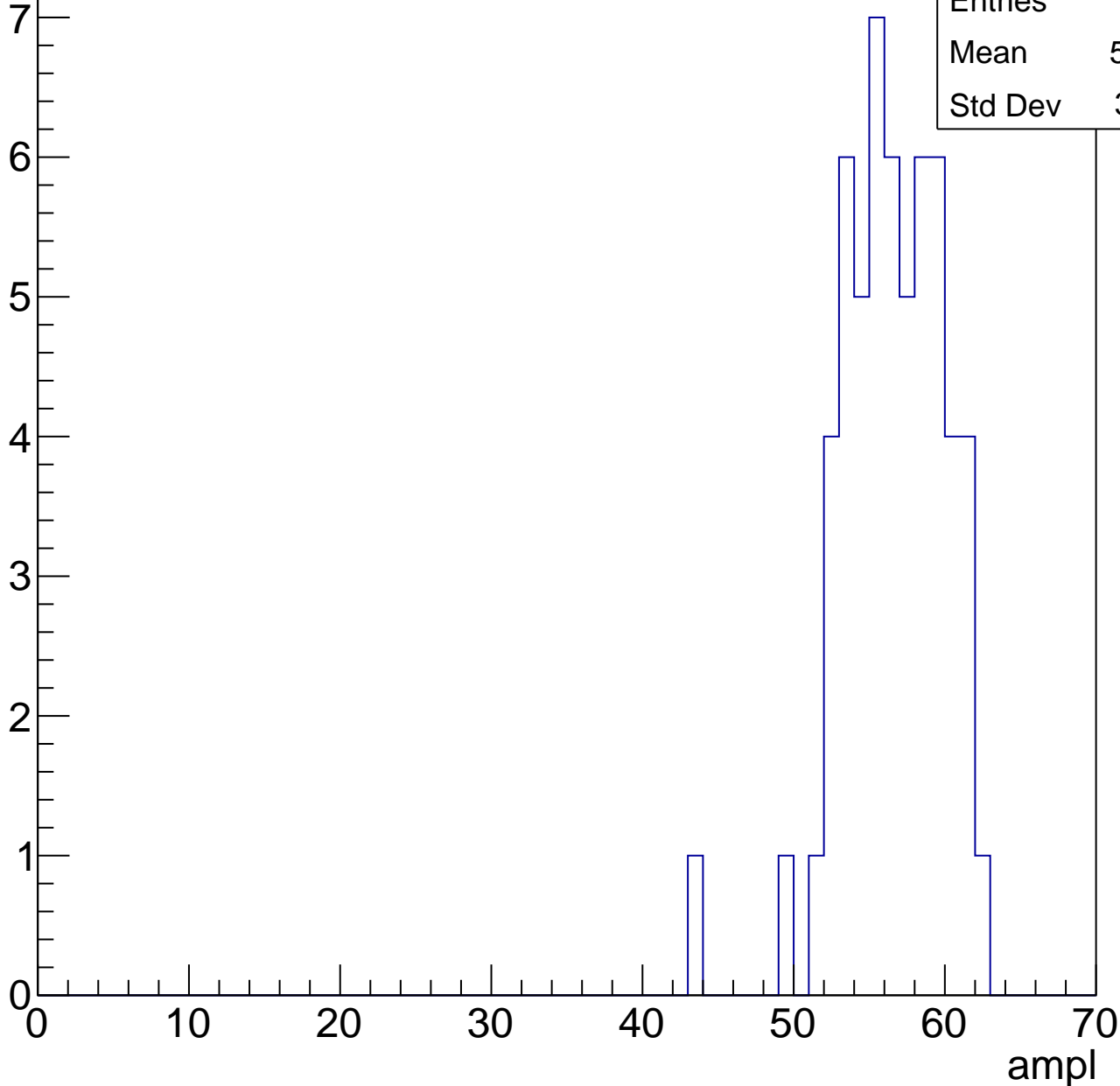
70

B1L103S, U20-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.02
Std Dev	3.431

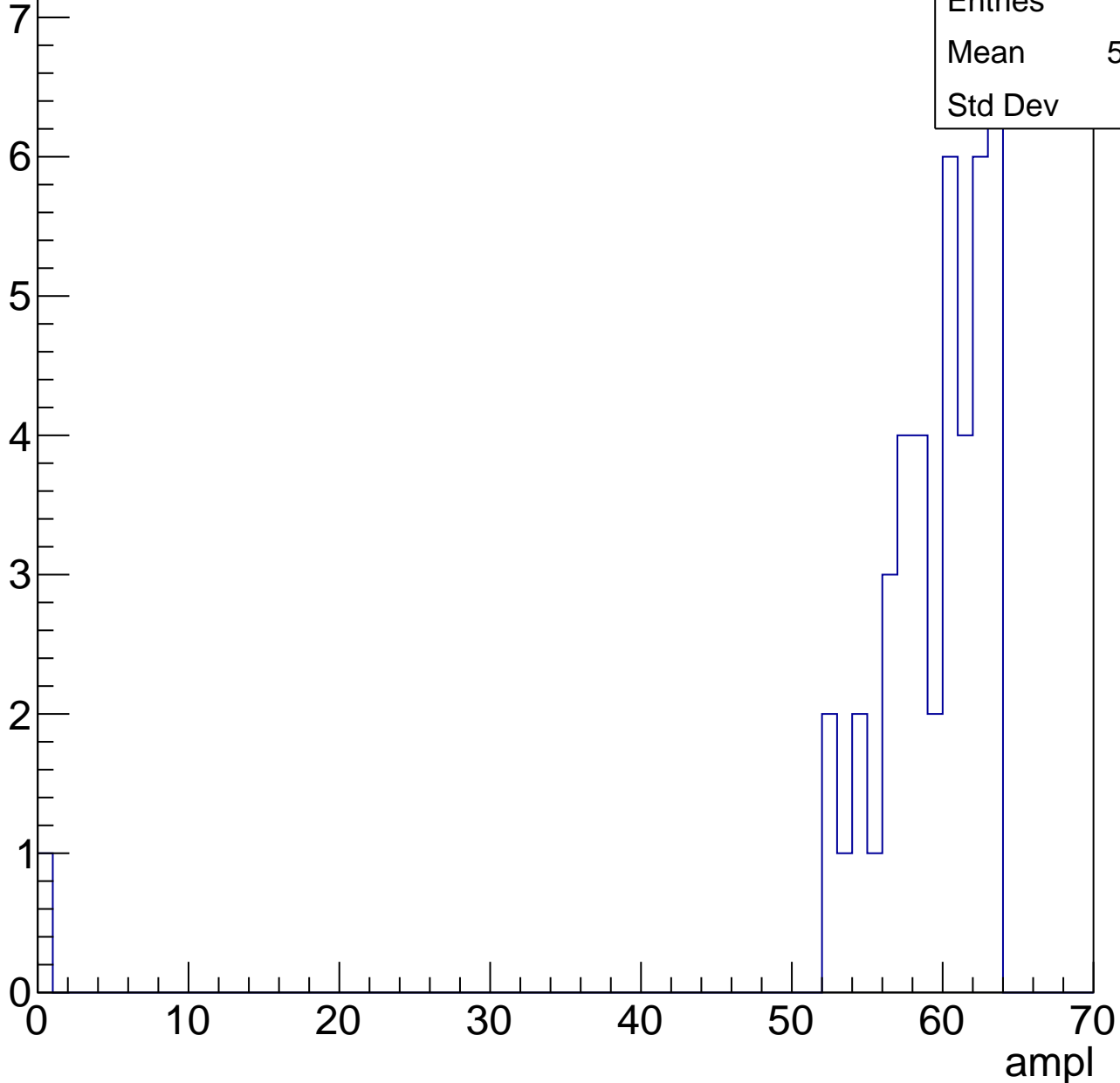


B1L103S, U20-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

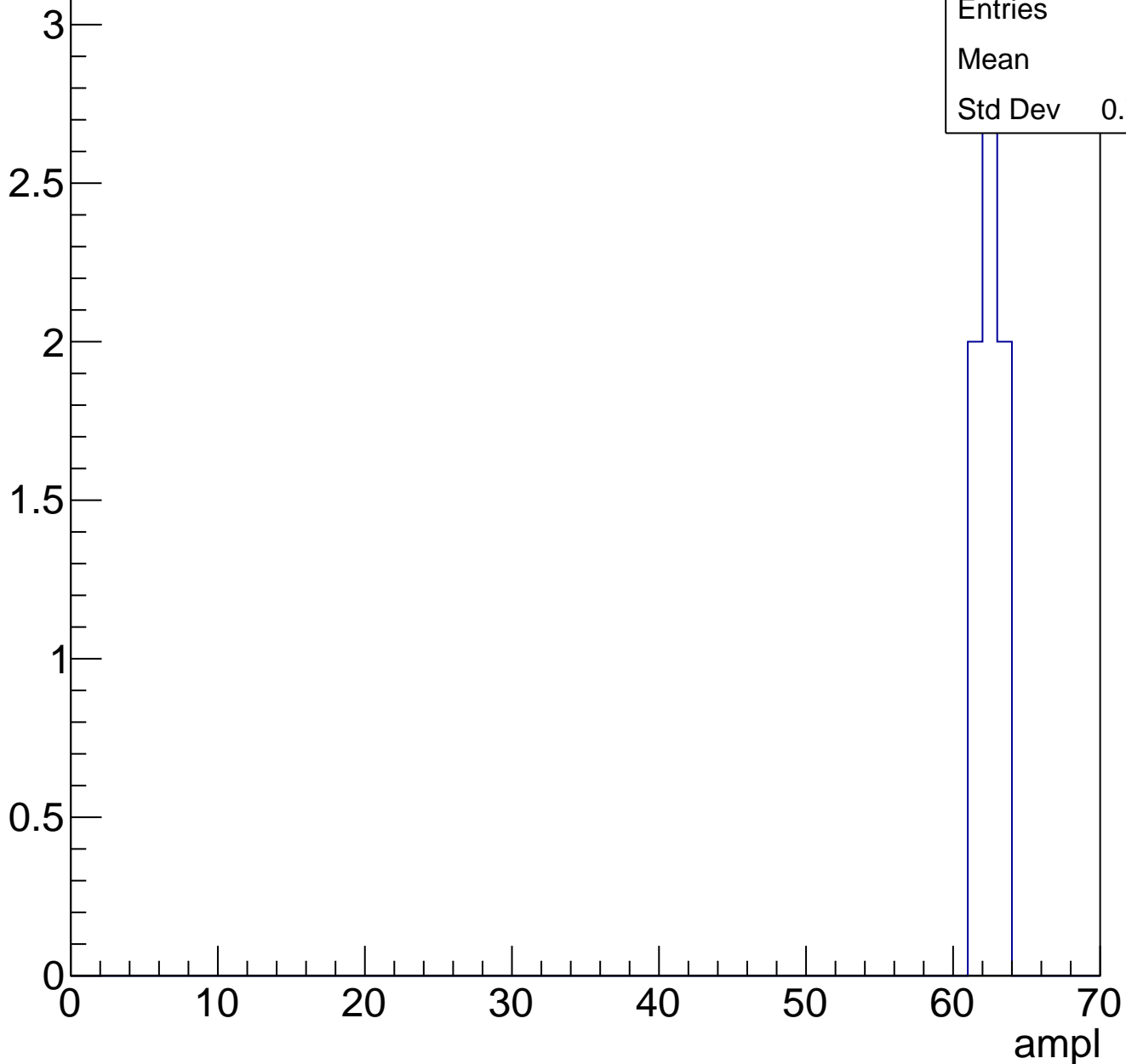
Entries	43
Mean	57.74
Std Dev	9.46



B1L103S, U20-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch33, adc0

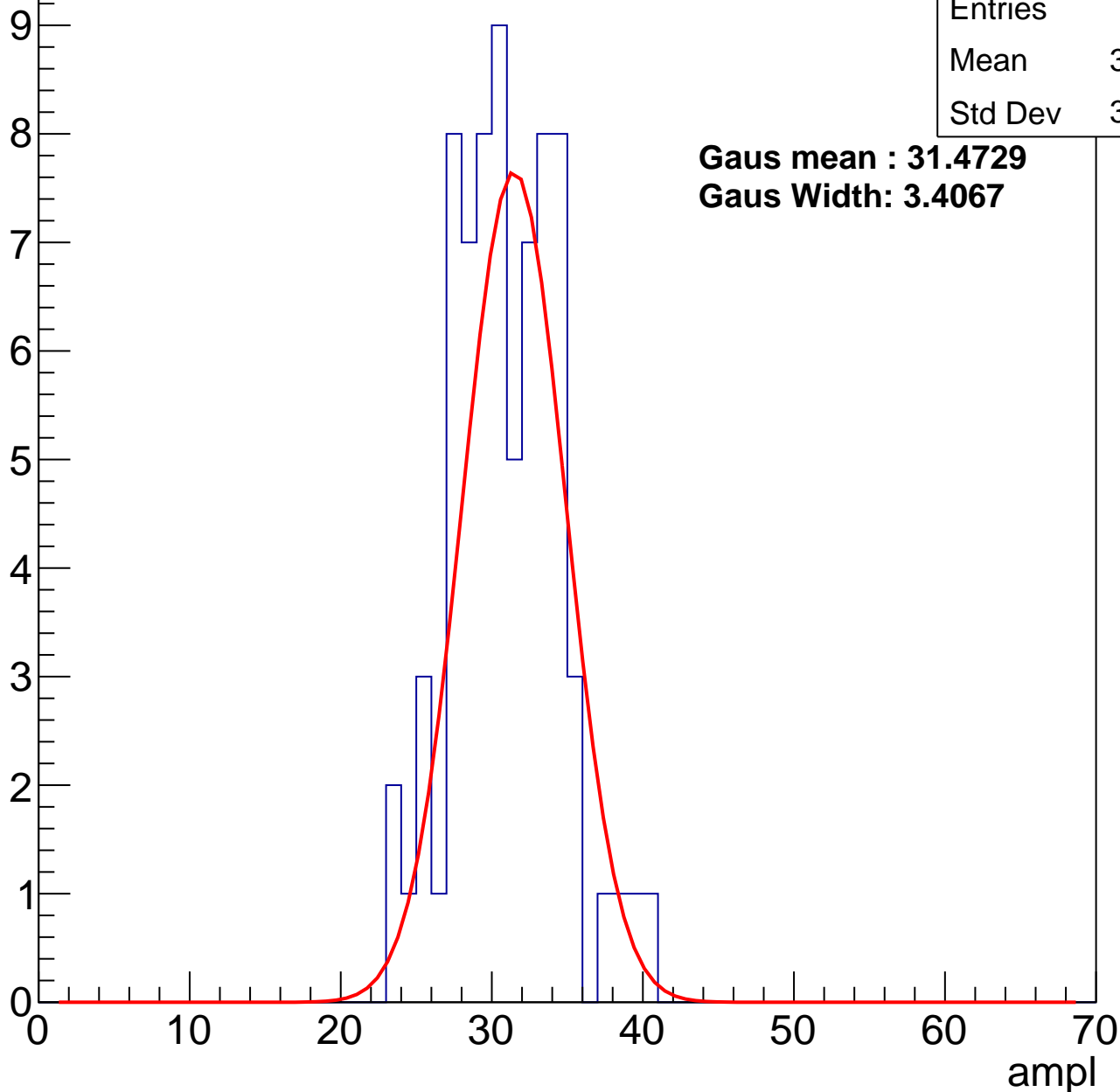
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.53
Std Dev	3.515

Gaus mean : 31.4729

Gaus Width: 3.4067

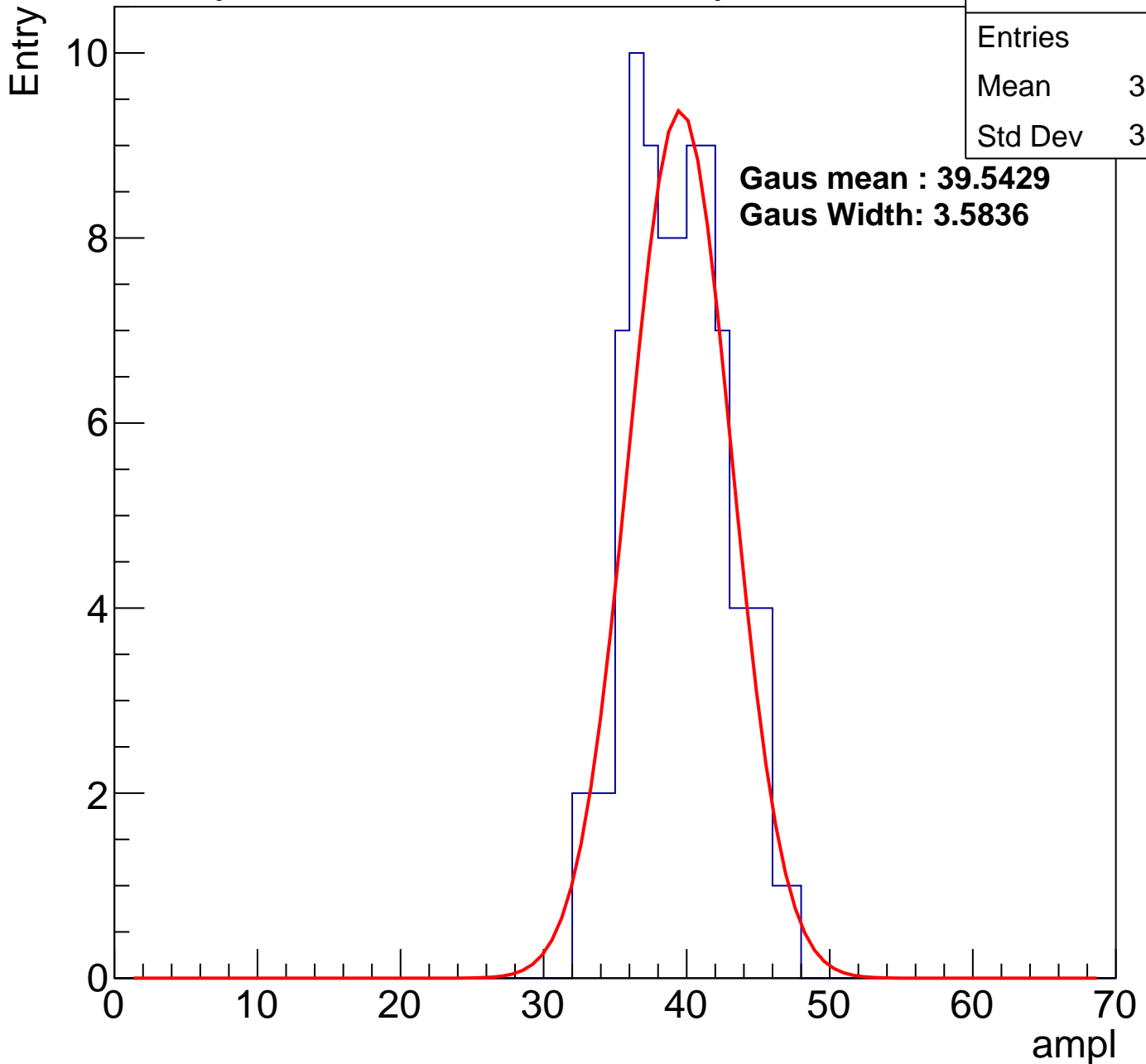


B1L103S, U20-ch33, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	39.03
Std Dev	3.382

Gaus mean : 39.5429
Gaus Width: 3.5836



B1L103S, U20-ch33, adc2

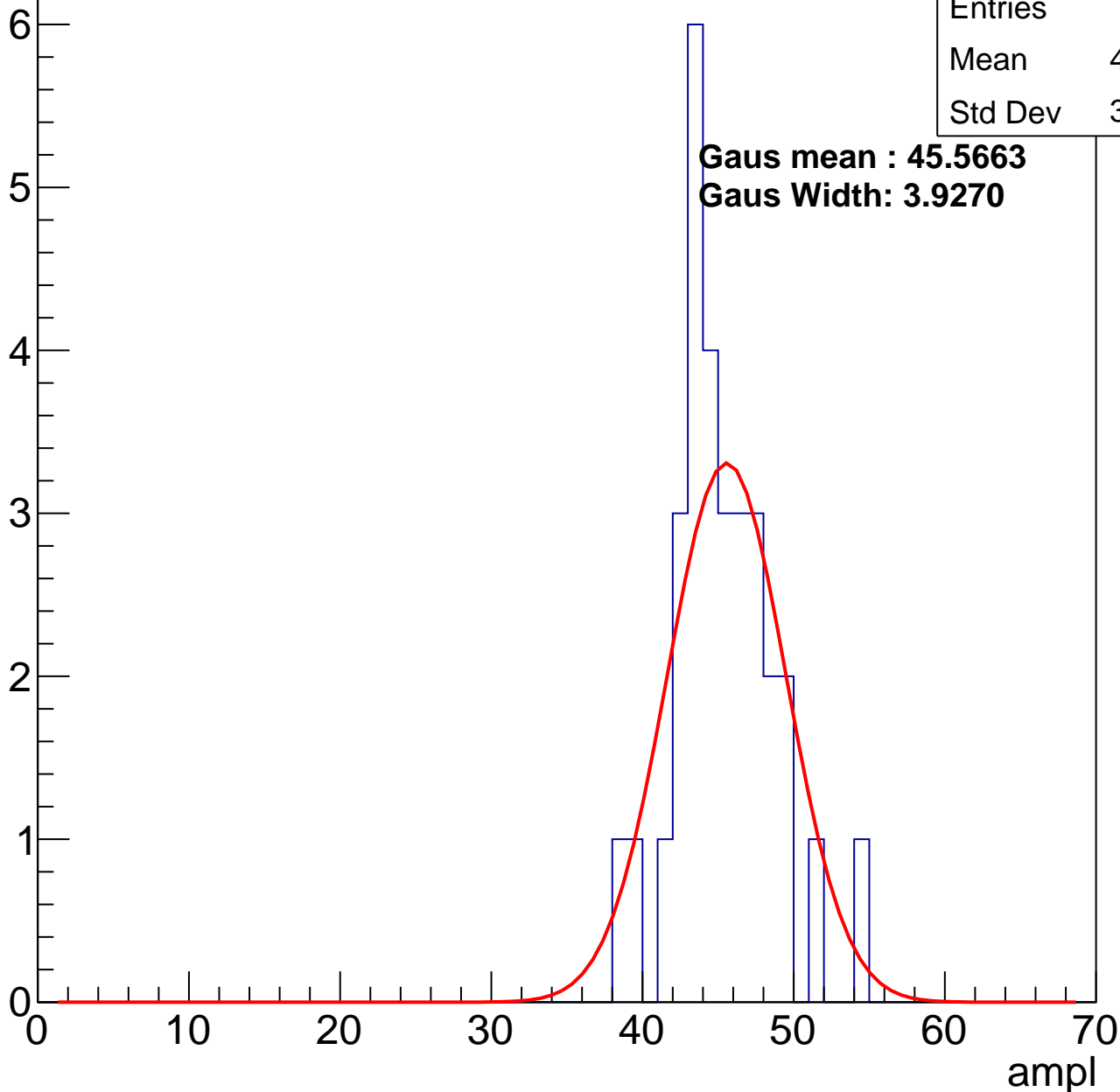
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	44.87
Std Dev	3.299

Gaus mean : 45.5663

Gaus Width: 3.9270

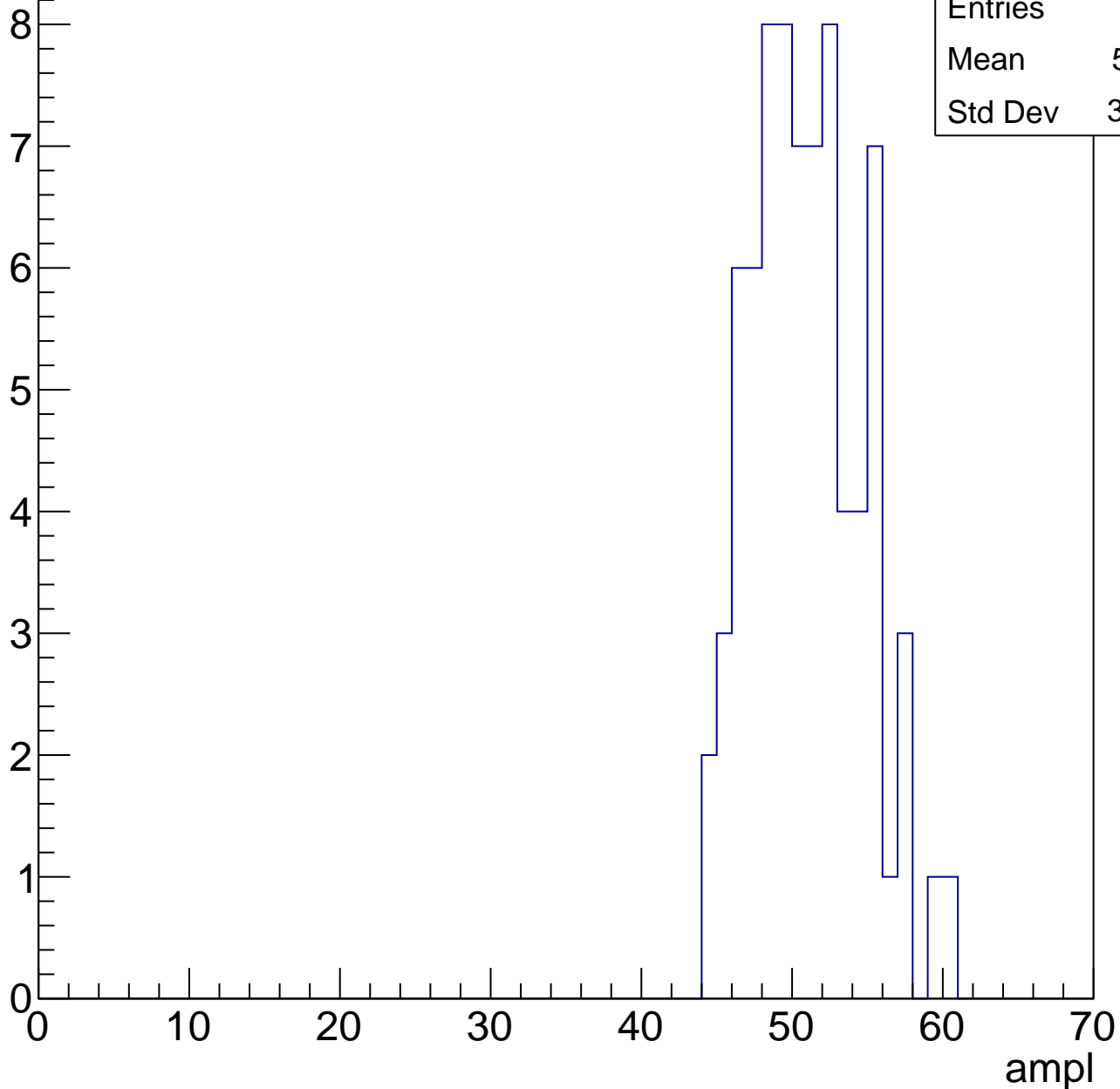


B1L103S, U20-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	50.51
Std Dev	3.618

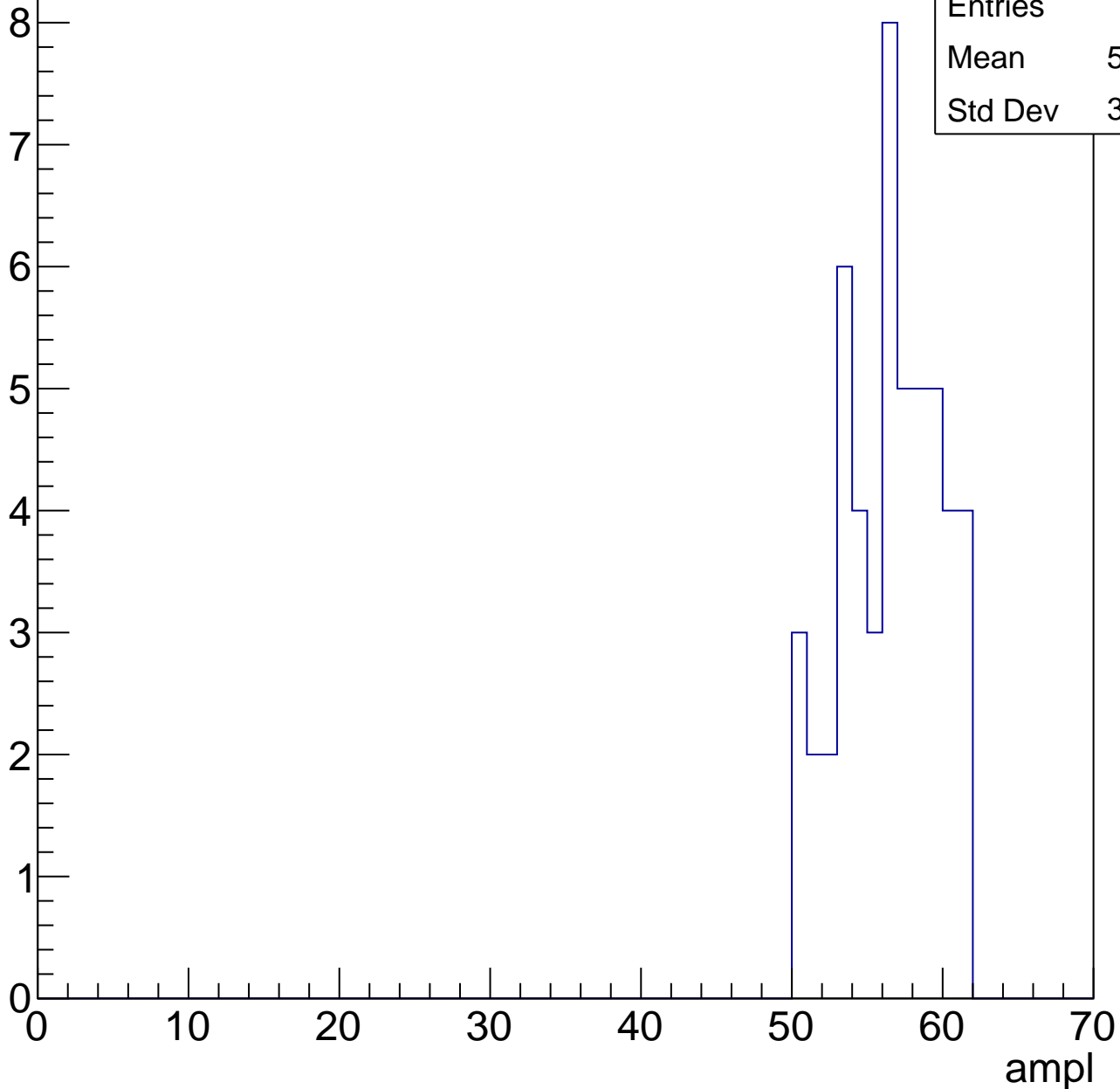


B1L103S, U20-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	56.02
Std Dev	3.122

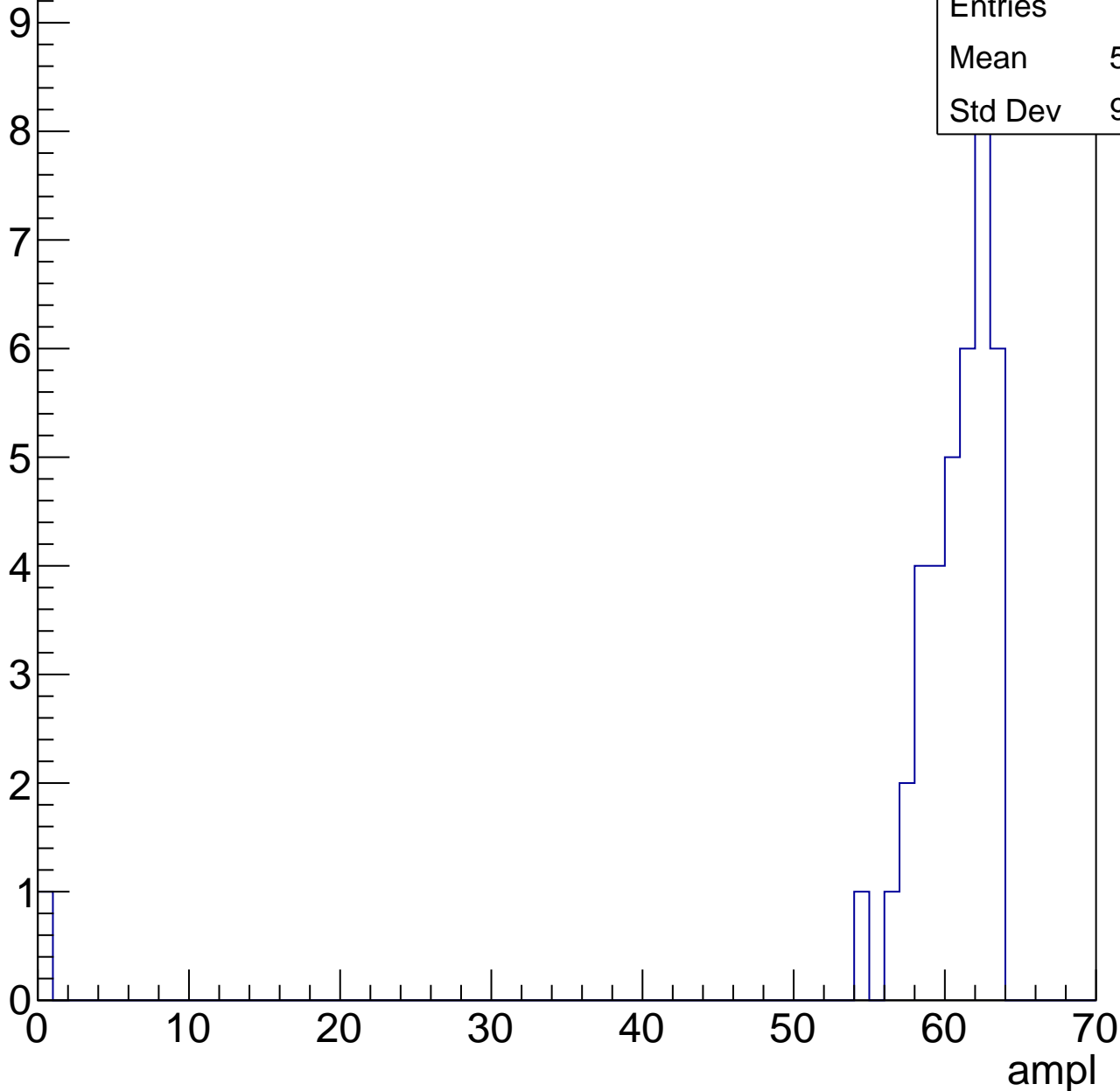


B1L103S, U20-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

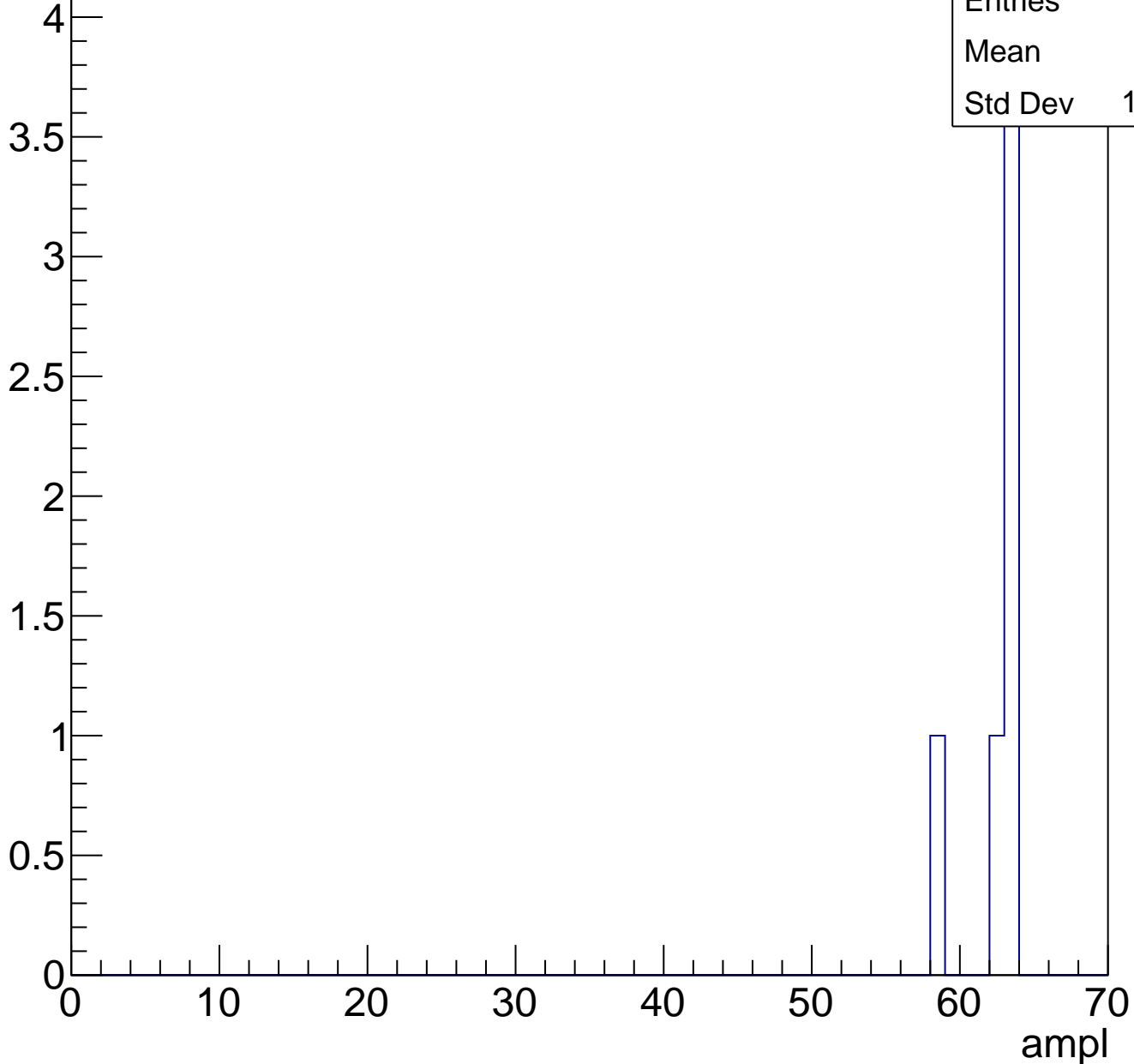
Entries	39
Mean	58.82
Std Dev	9.782



B1L103S, U20-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch34, adc0

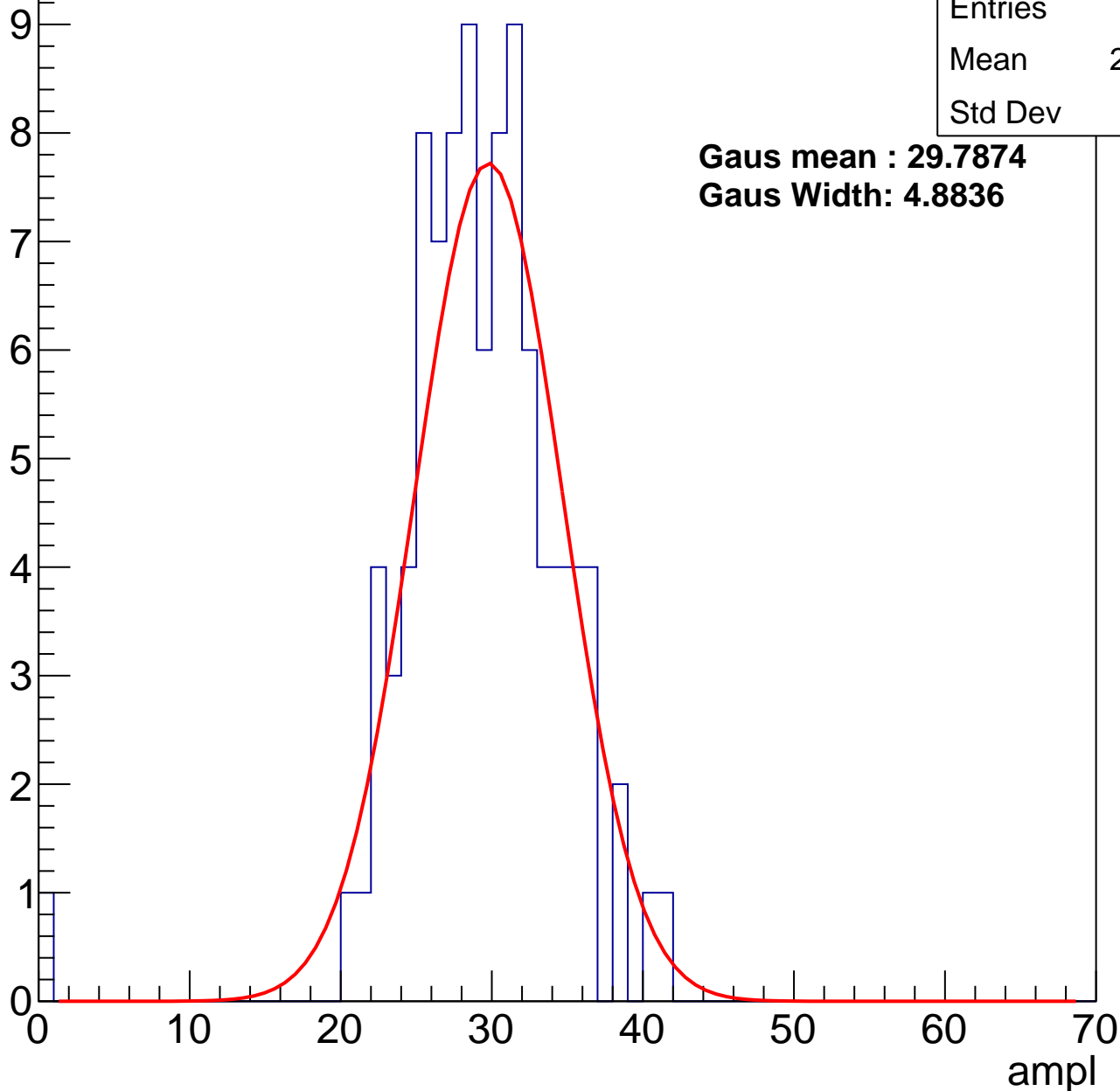
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	95
Mean	28.82
Std Dev	5.28

Gaus mean : 29.7874

Gaus Width: 4.8836



B1L103S, U20-ch34, adc1

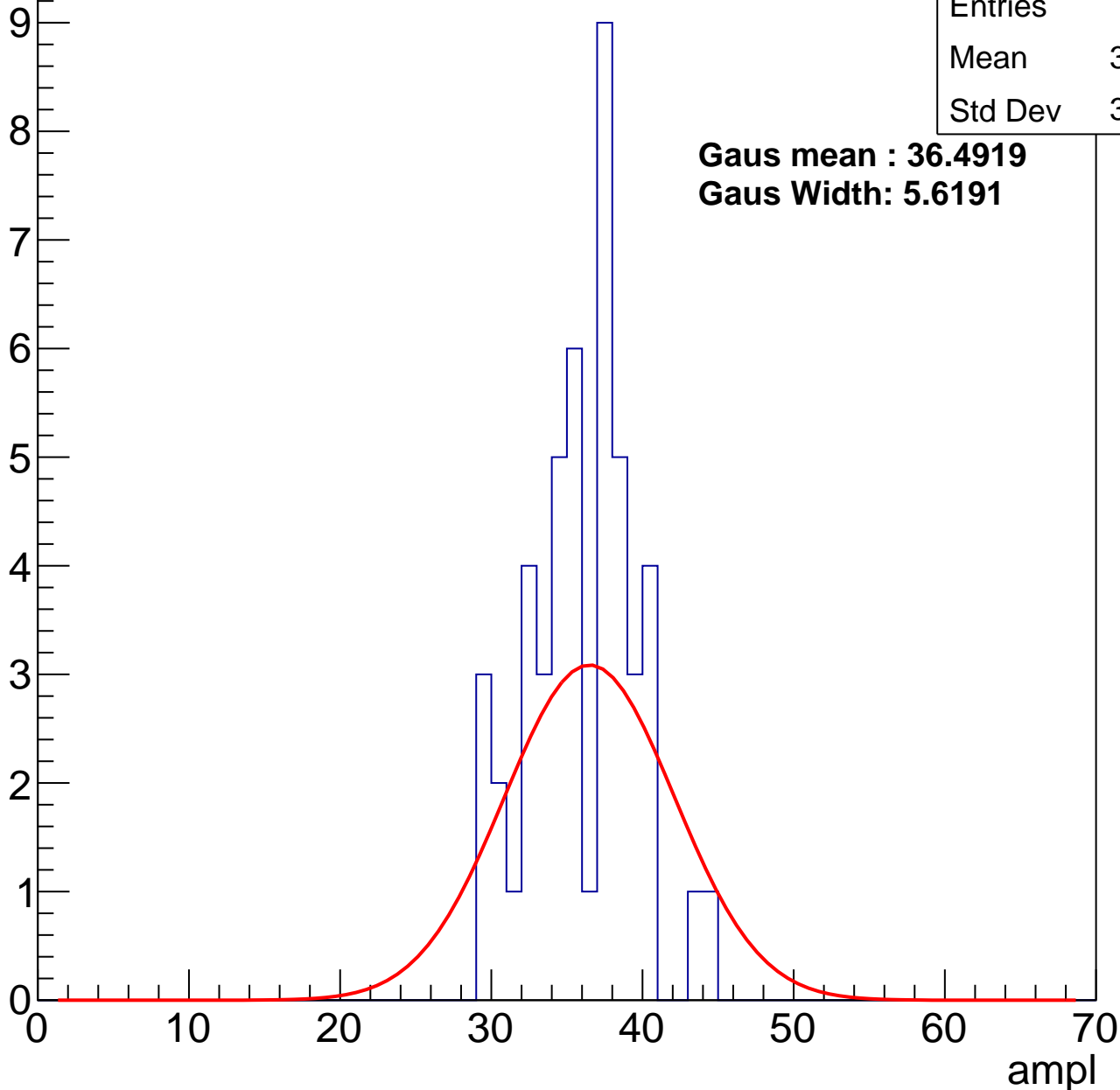
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	35.58
Std Dev	3.493

Gaus mean : 36.4919

Gaus Width: 5.6191



B1L103S, U20-ch34, adc2

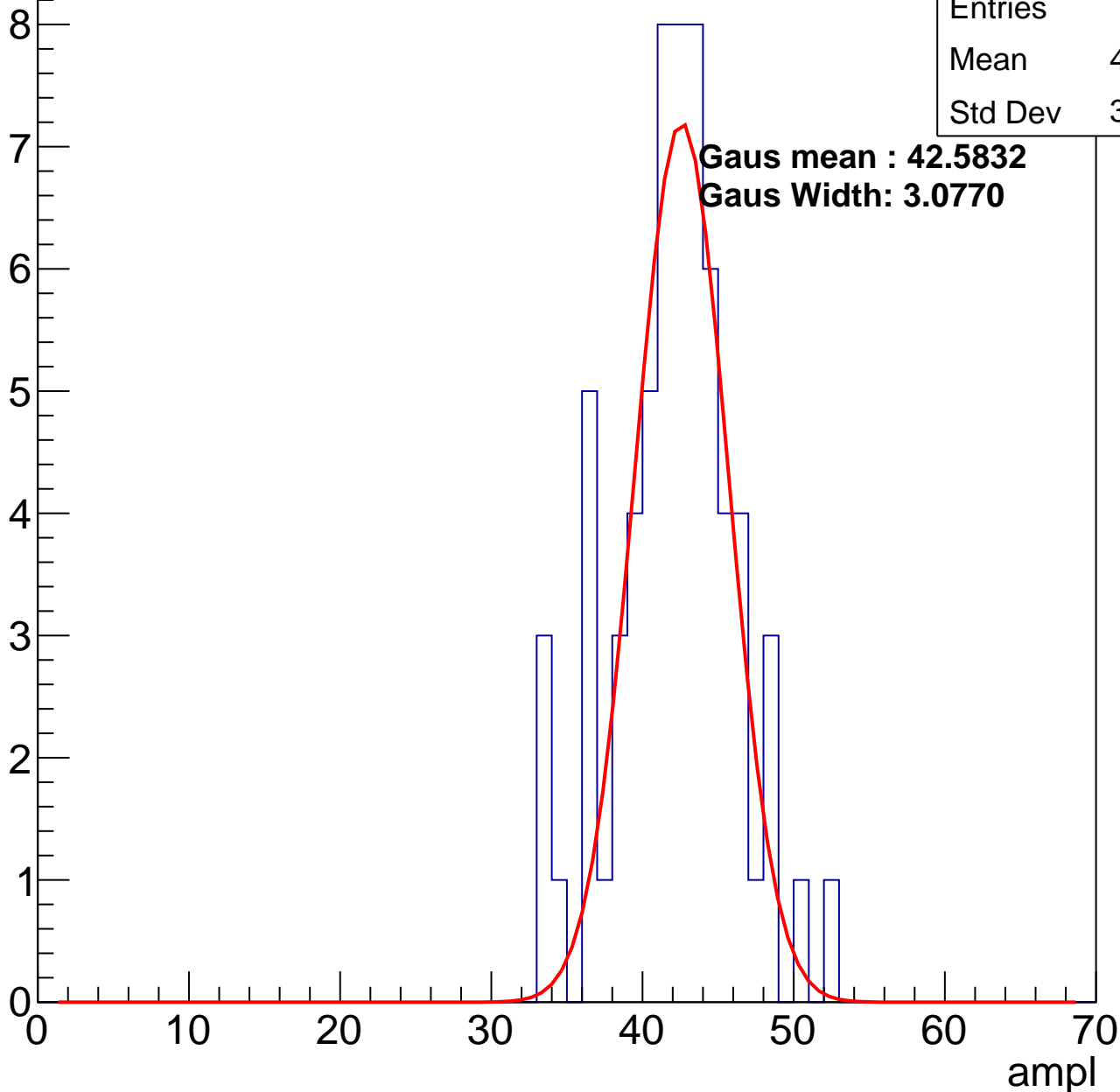
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.65
Std Dev	3.979

Gaus mean : 42.5832

Gaus Width: 3.0770

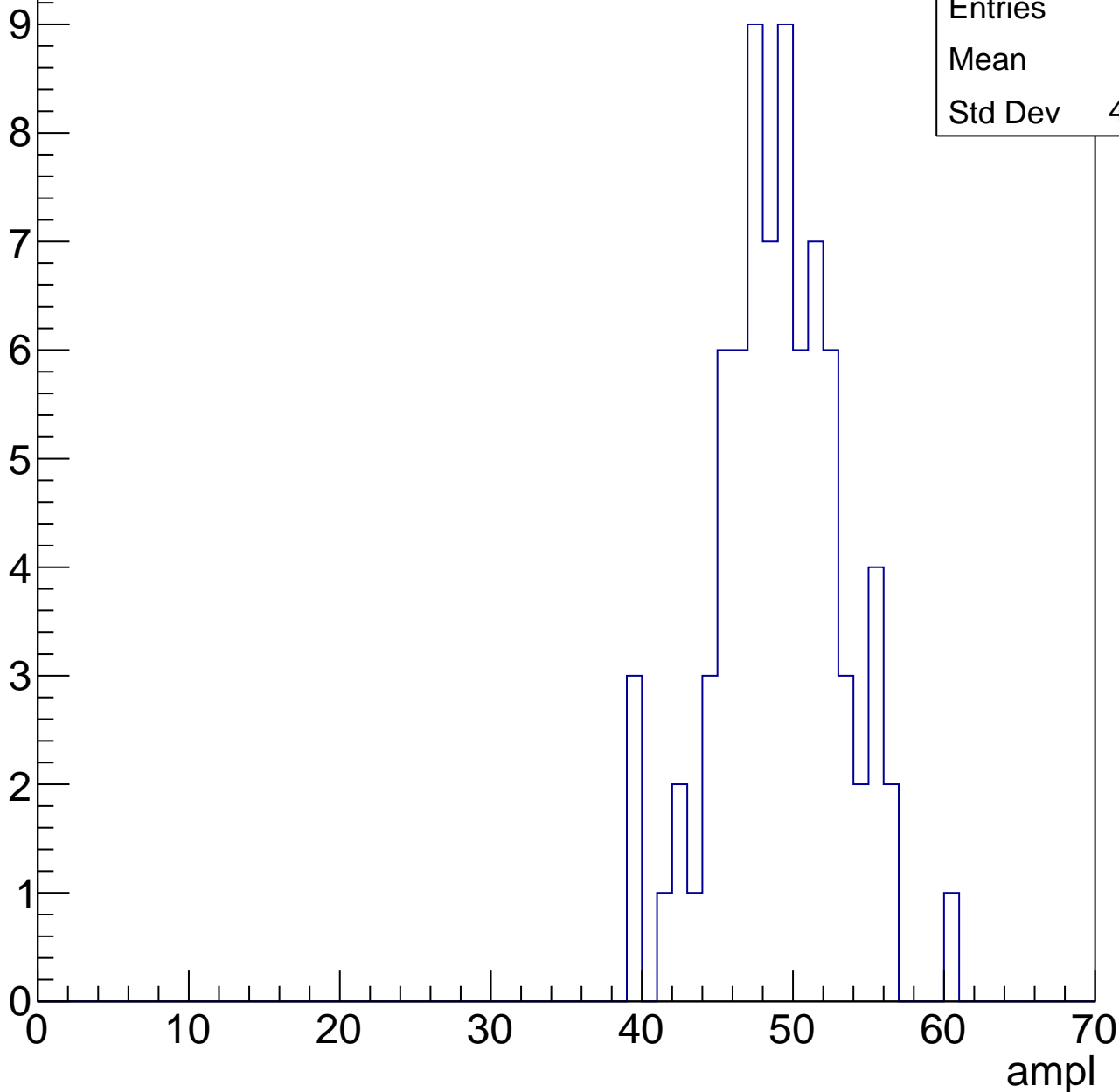


B1L103S, U20-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	48.6
Std Dev	4.087



B1L103S, U20-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	54.61
Std Dev	2.74

0

1

2

3

4

5

6

7

8

9

10

20

30

40

50

60

70

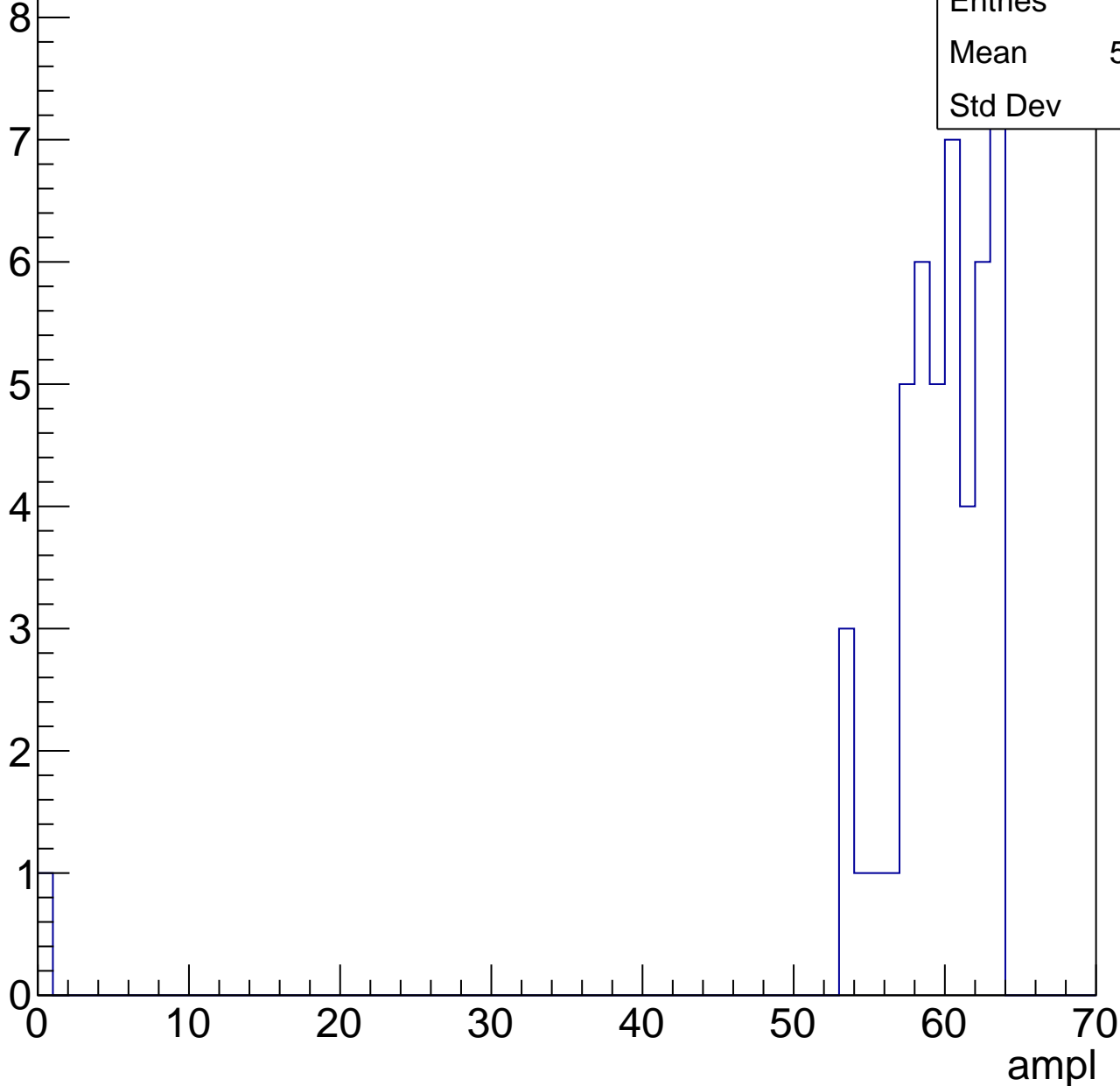
ampl

B1L103S, U20-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

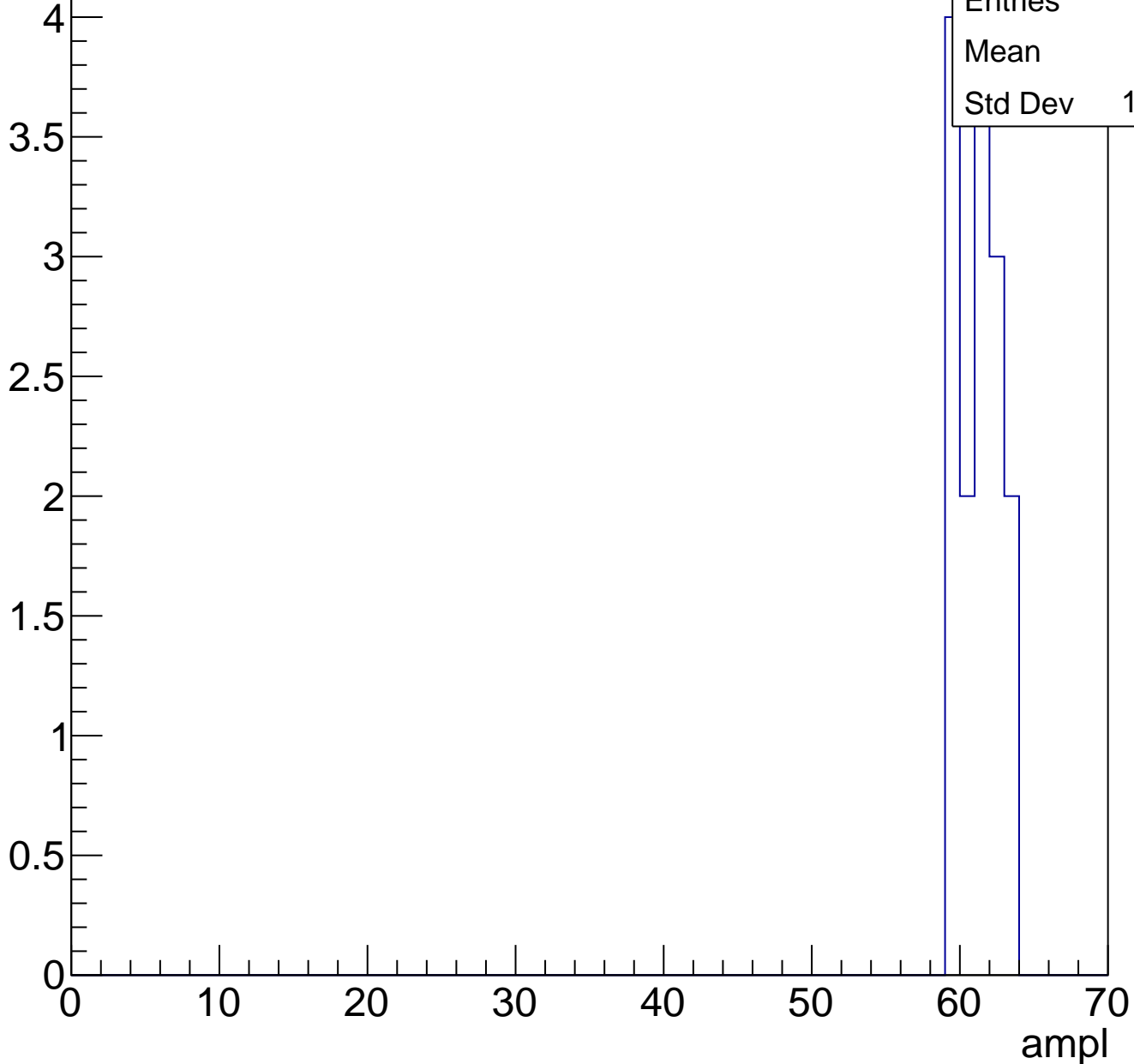
Entries	48
Mean	58.17
Std Dev	8.94



B1L103S, U20-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

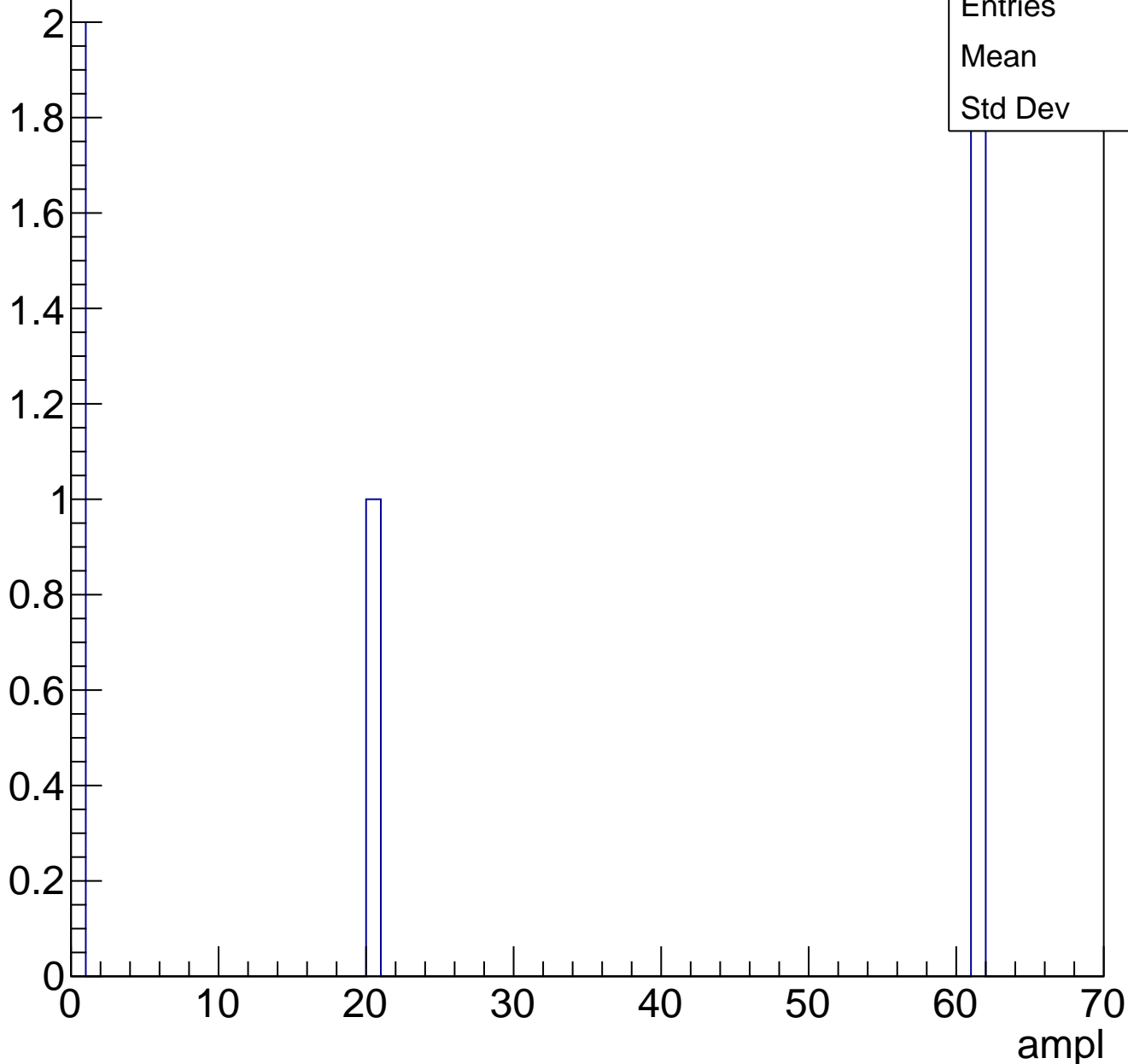
Entry



B1L103S, U20-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch35, adc0

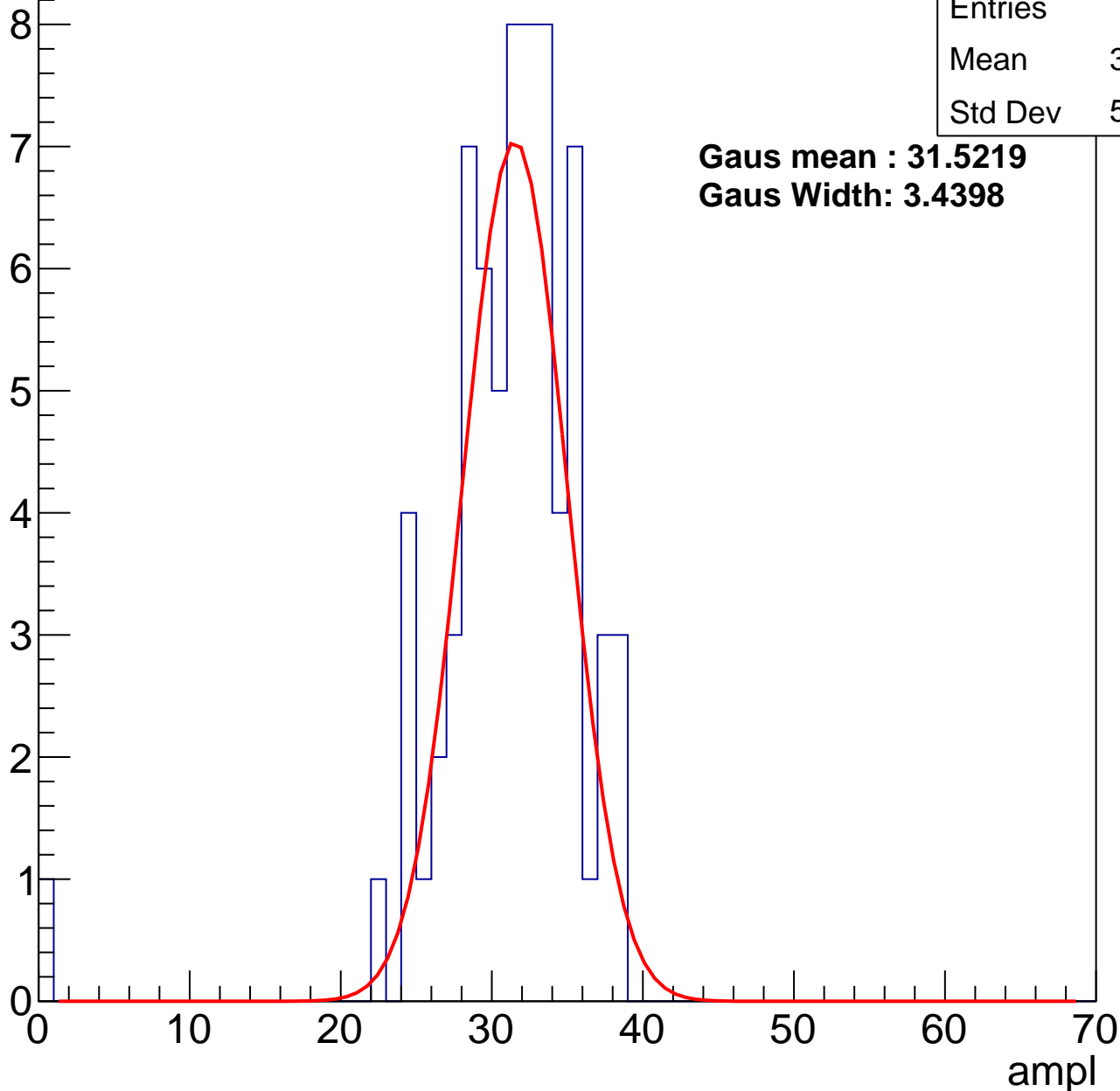
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.64
Std Dev	5.167

Gaus mean : 31.5219

Gaus Width: 3.4398



B1L103S, U20-ch35, adc1

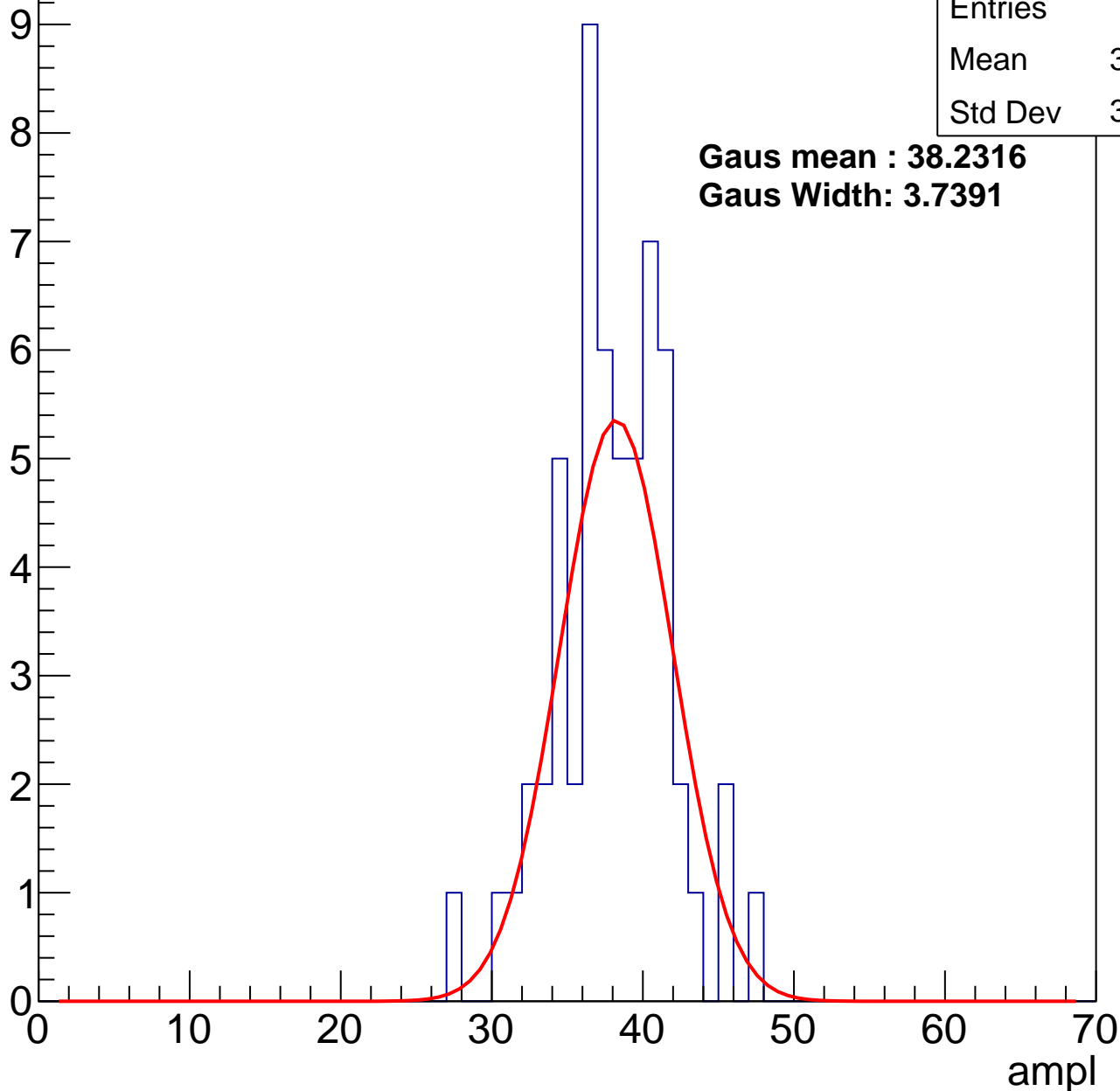
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.57
Std Dev	3.733

Gaus mean : 38.2316

Gaus Width: 3.7391



B1L103S, U20-ch35, adc2

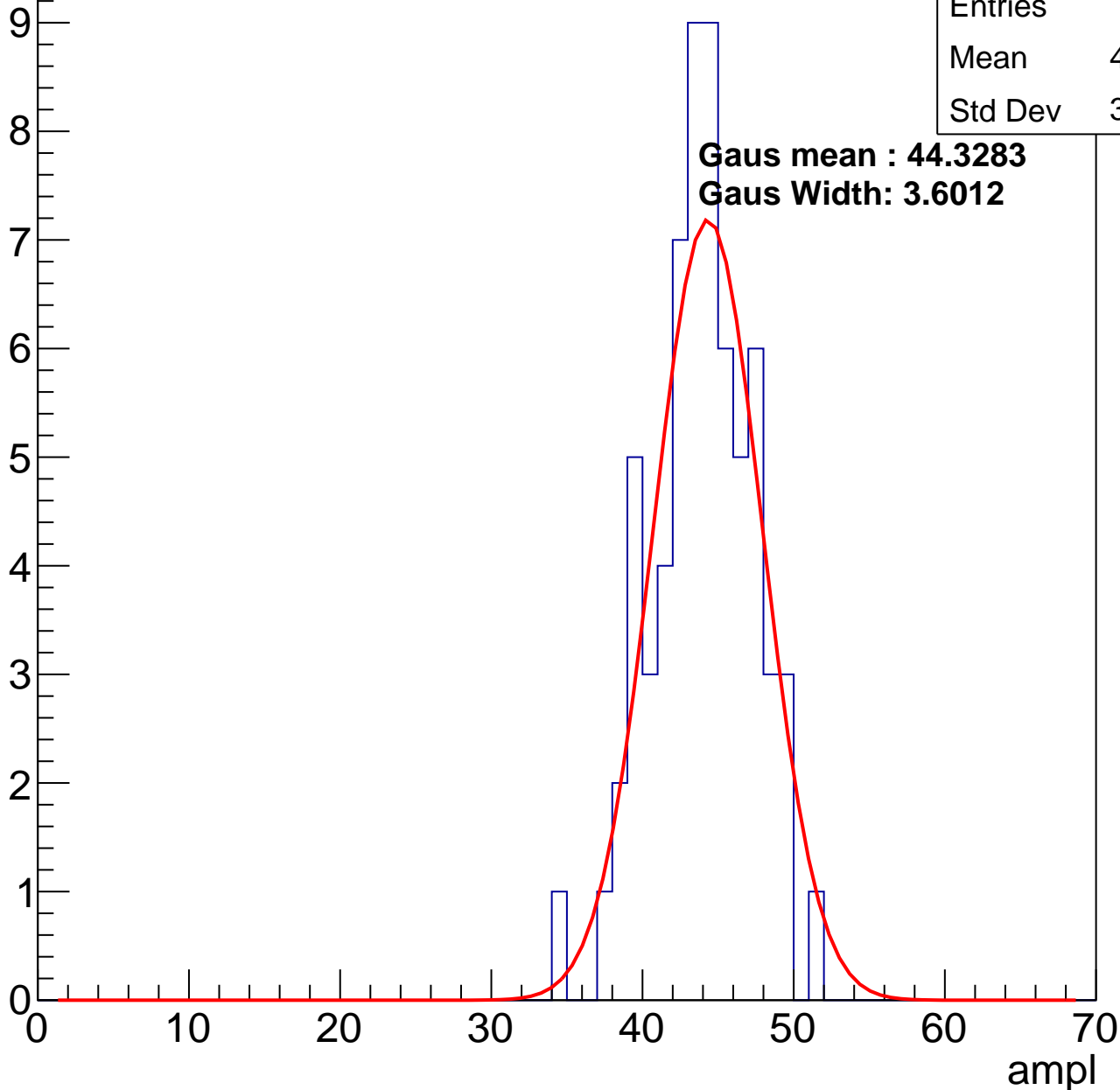
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.49
Std Dev	3.287

Gaus mean : 44.3283

Gaus Width: 3.6012

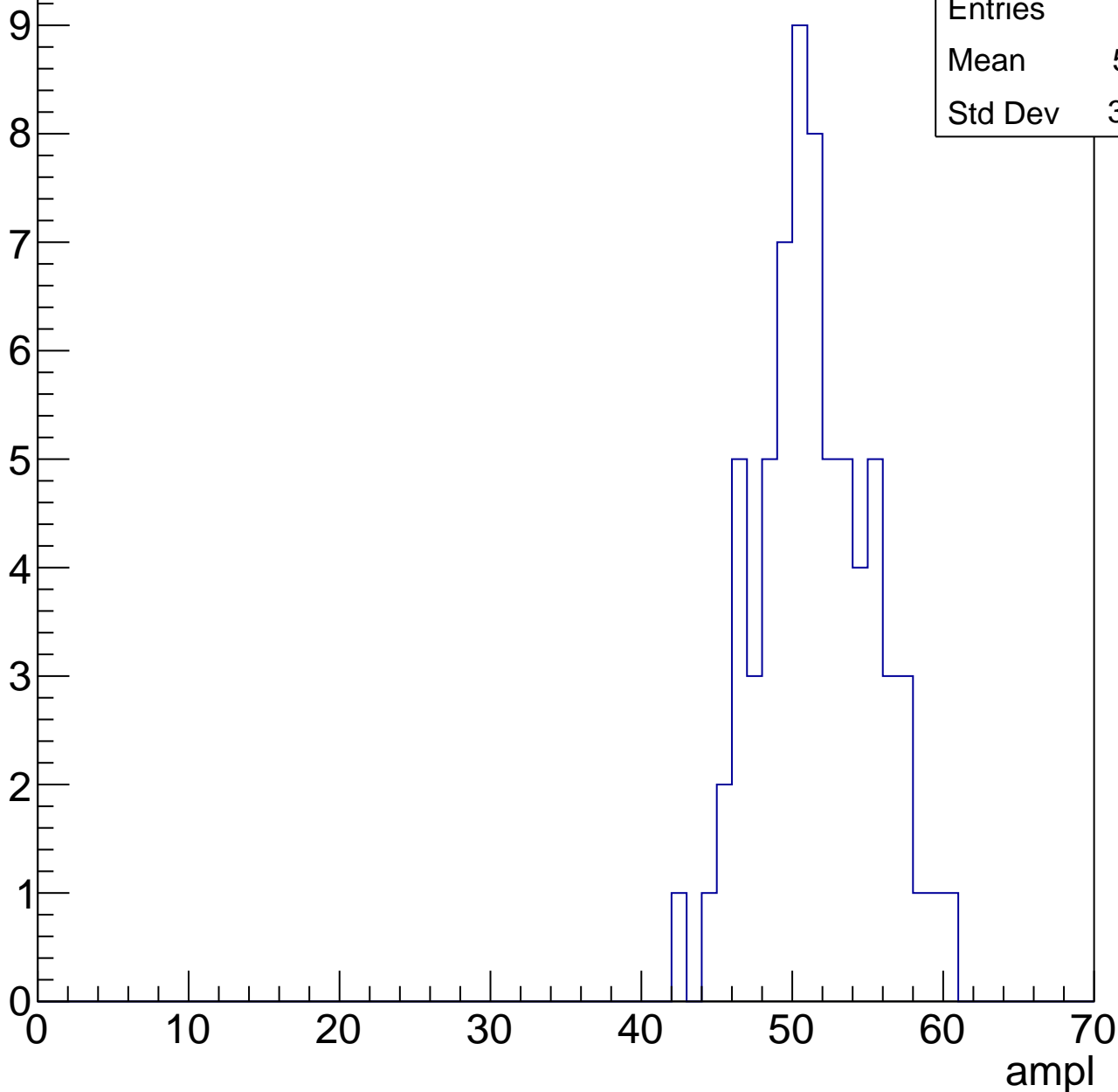


B1L103S, U20-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	51.01
Std Dev	3.759

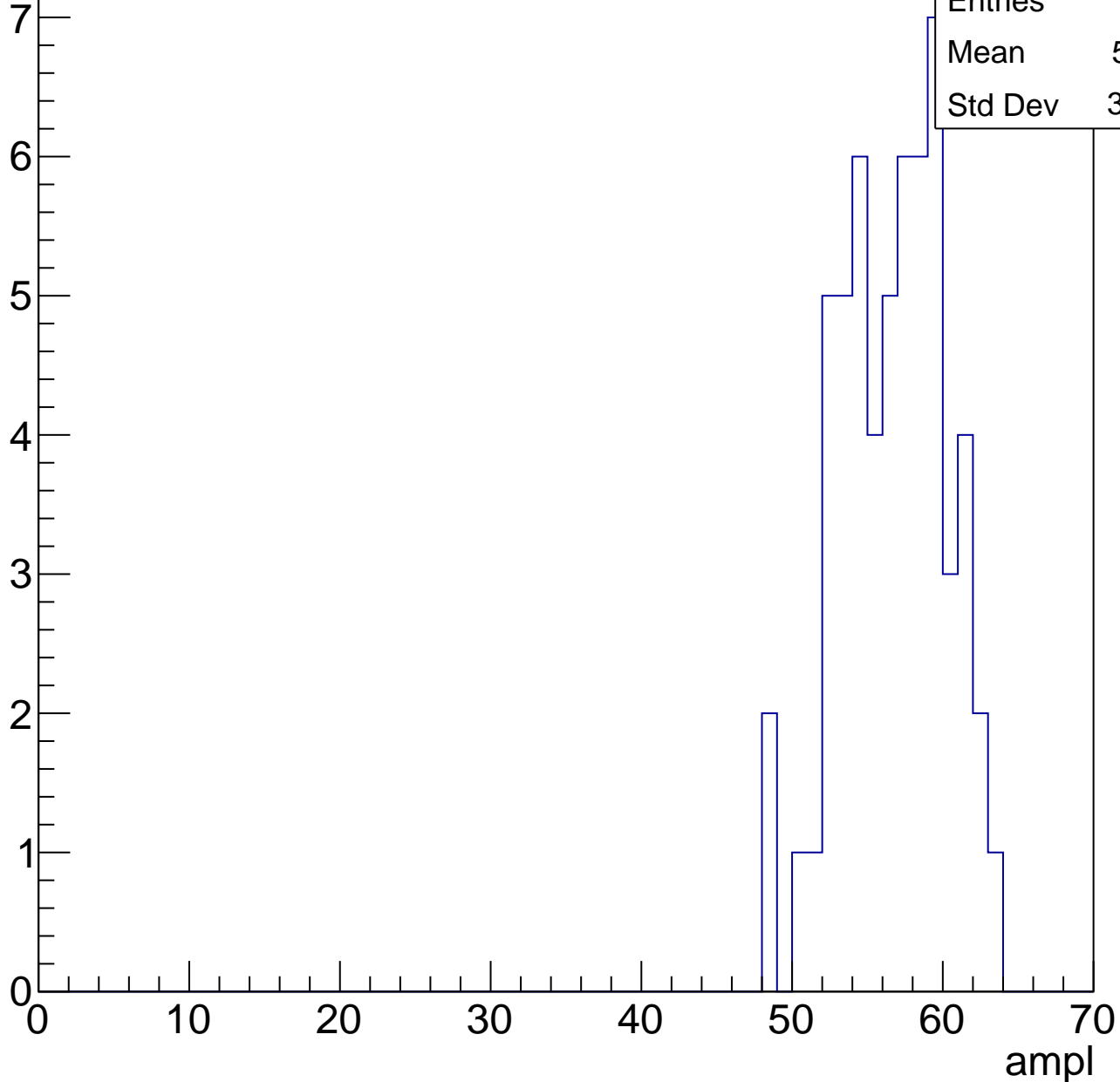


B1L103S, U20-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.21
Std Dev	3.478

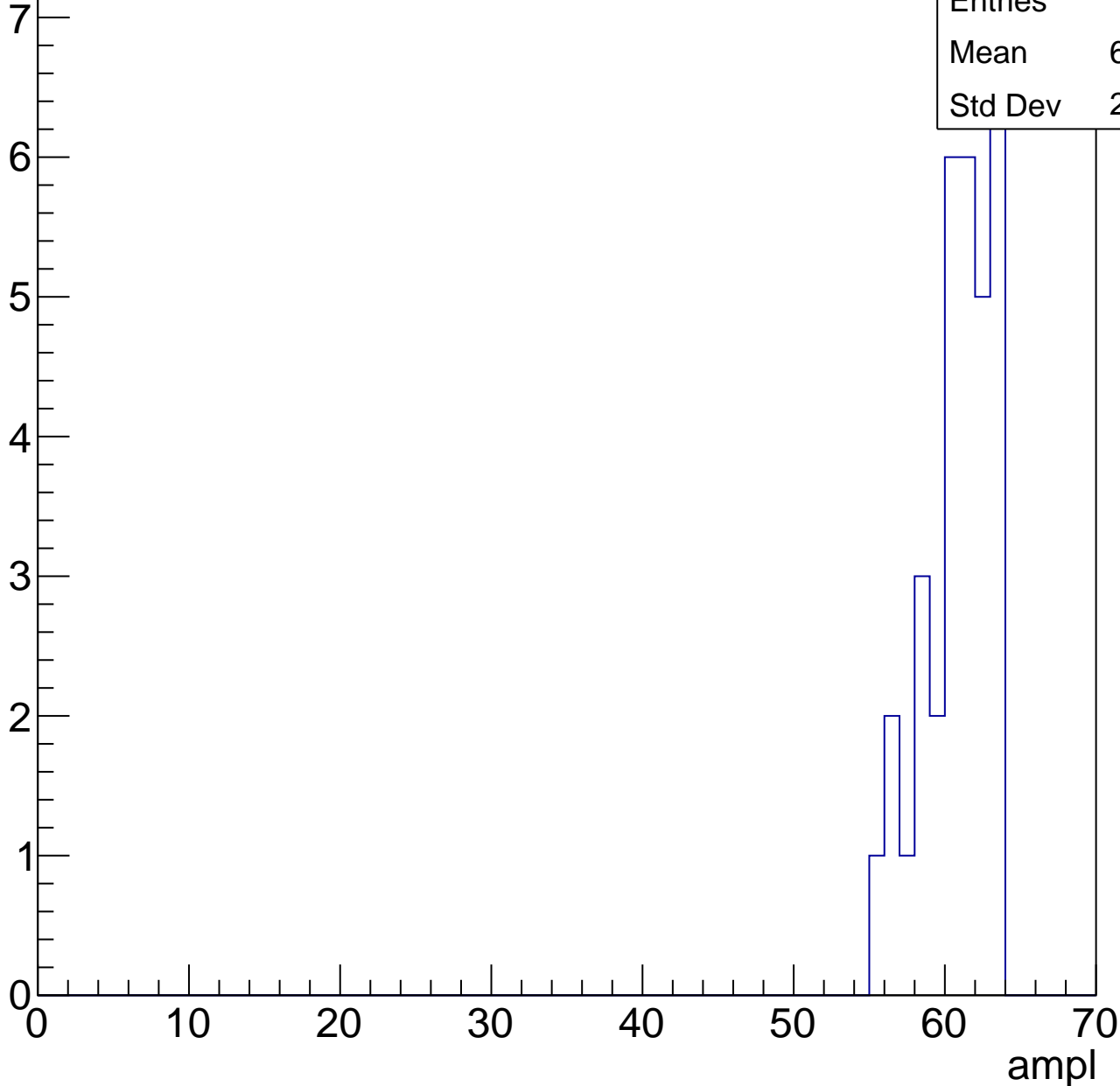


B1L103S, U20-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

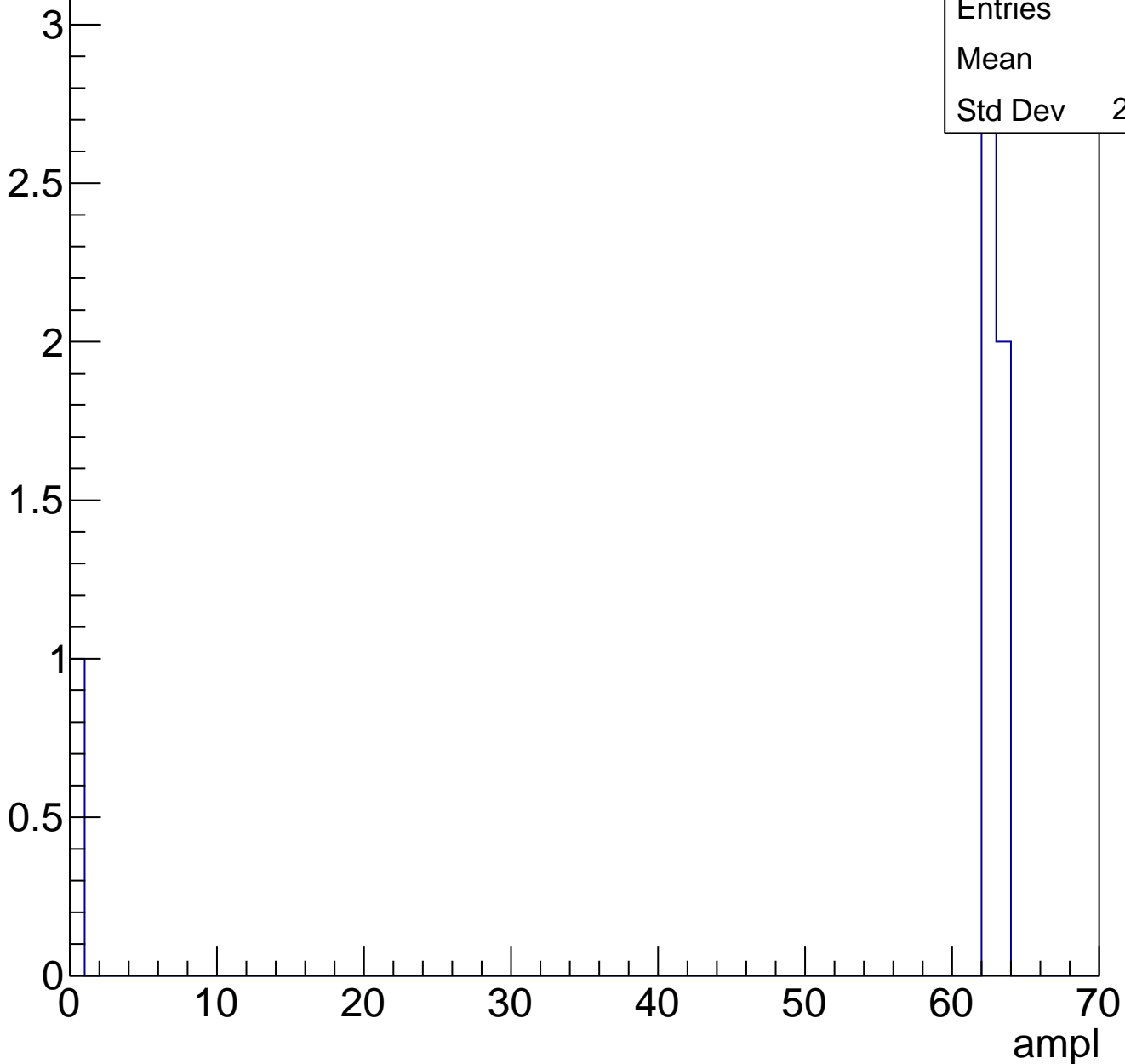
Entries	33
Mean	60.39
Std Dev	2.228



B1L103S, U20-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch36, adc0

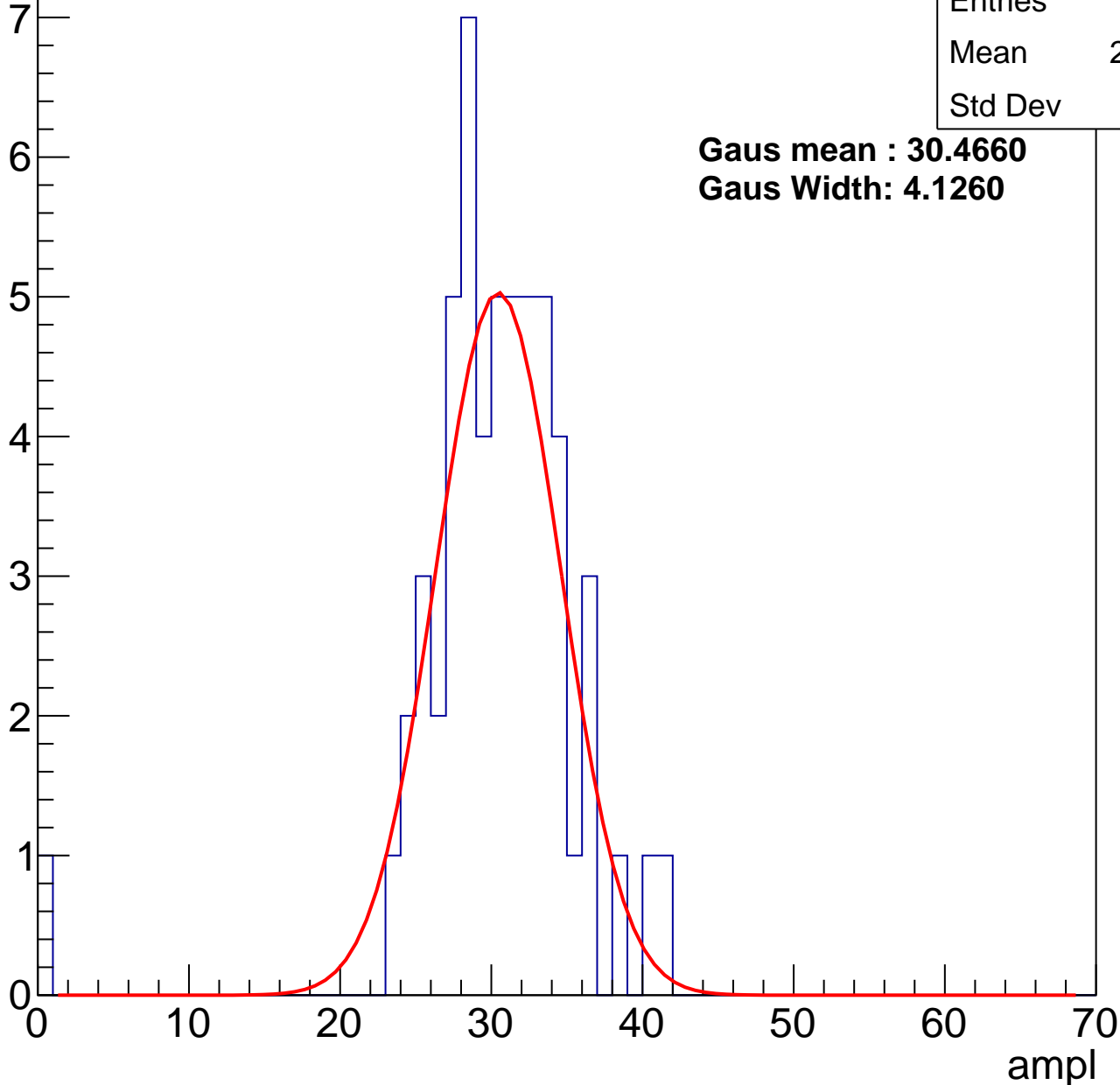
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.88
Std Dev	5.6

Gaus mean : 30.4660

Gaus Width: 4.1260



B1L103S, U20-ch36, adc1

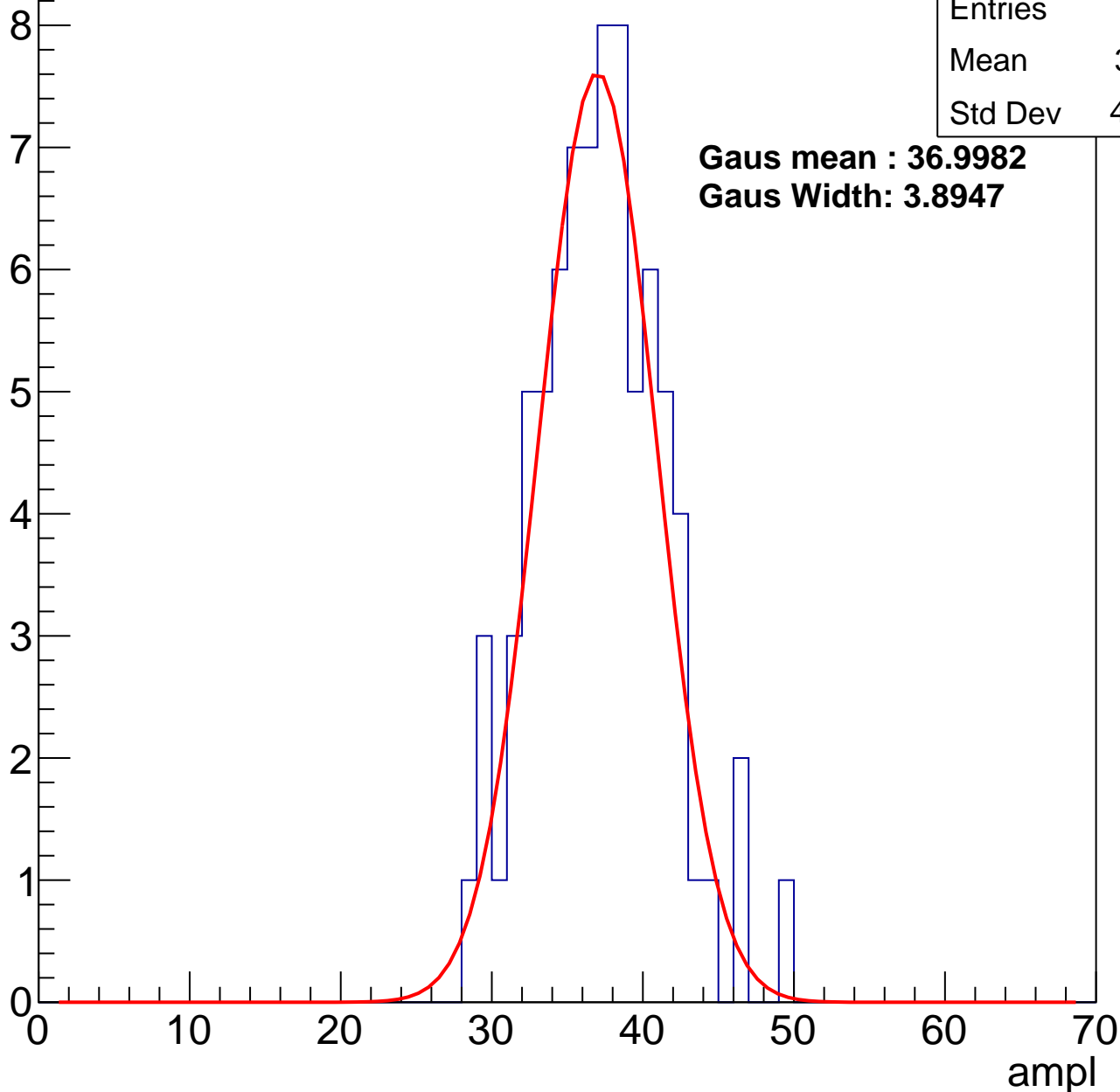
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.71
Std Dev	4.165

Gaus mean : 36.9982

Gaus Width: 3.8947



B1L103S, U20-ch36, adc2

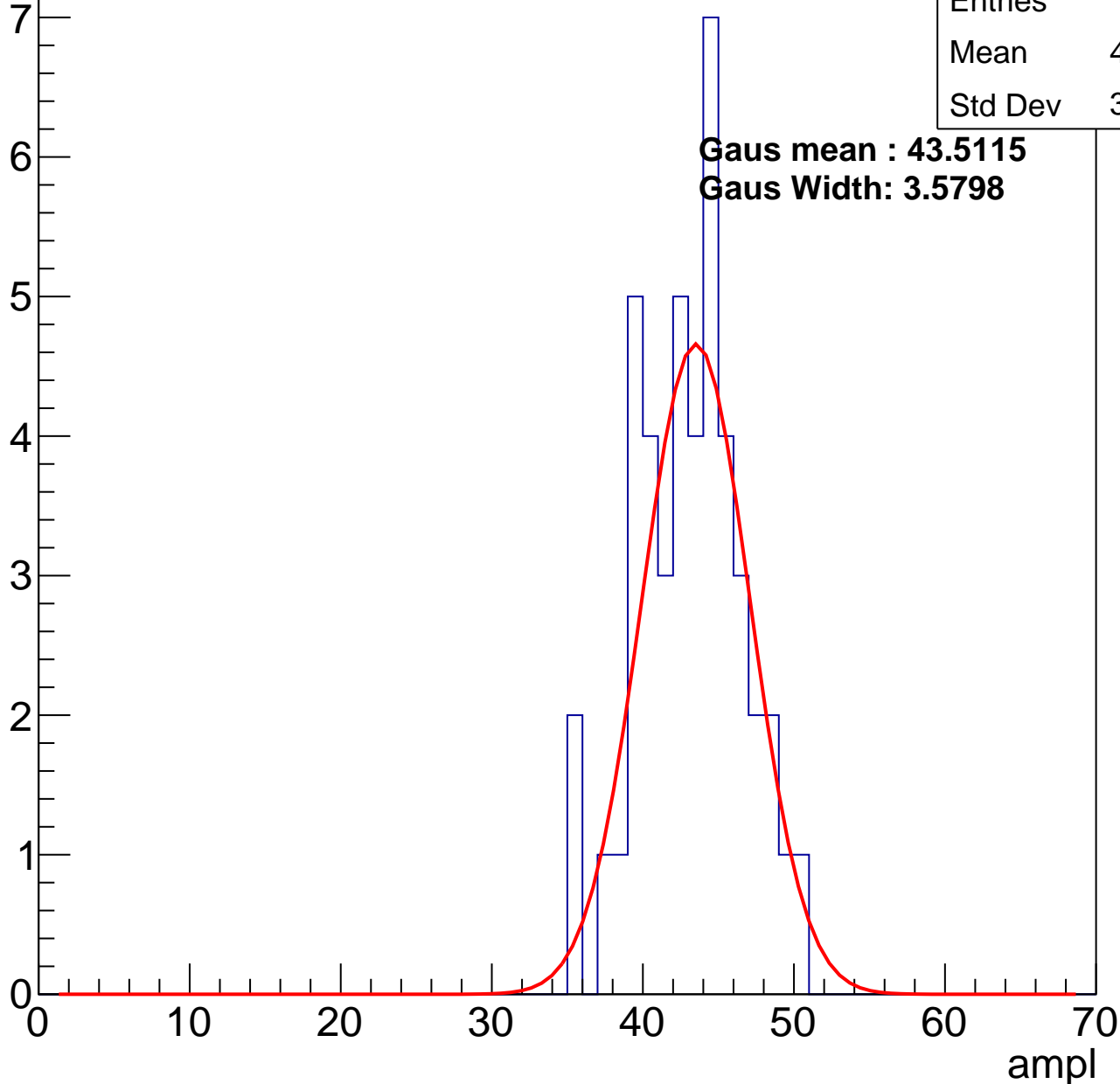
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	42.67
Std Dev	3.445

Gaus mean : 43.5115

Gaus Width: 3.5798

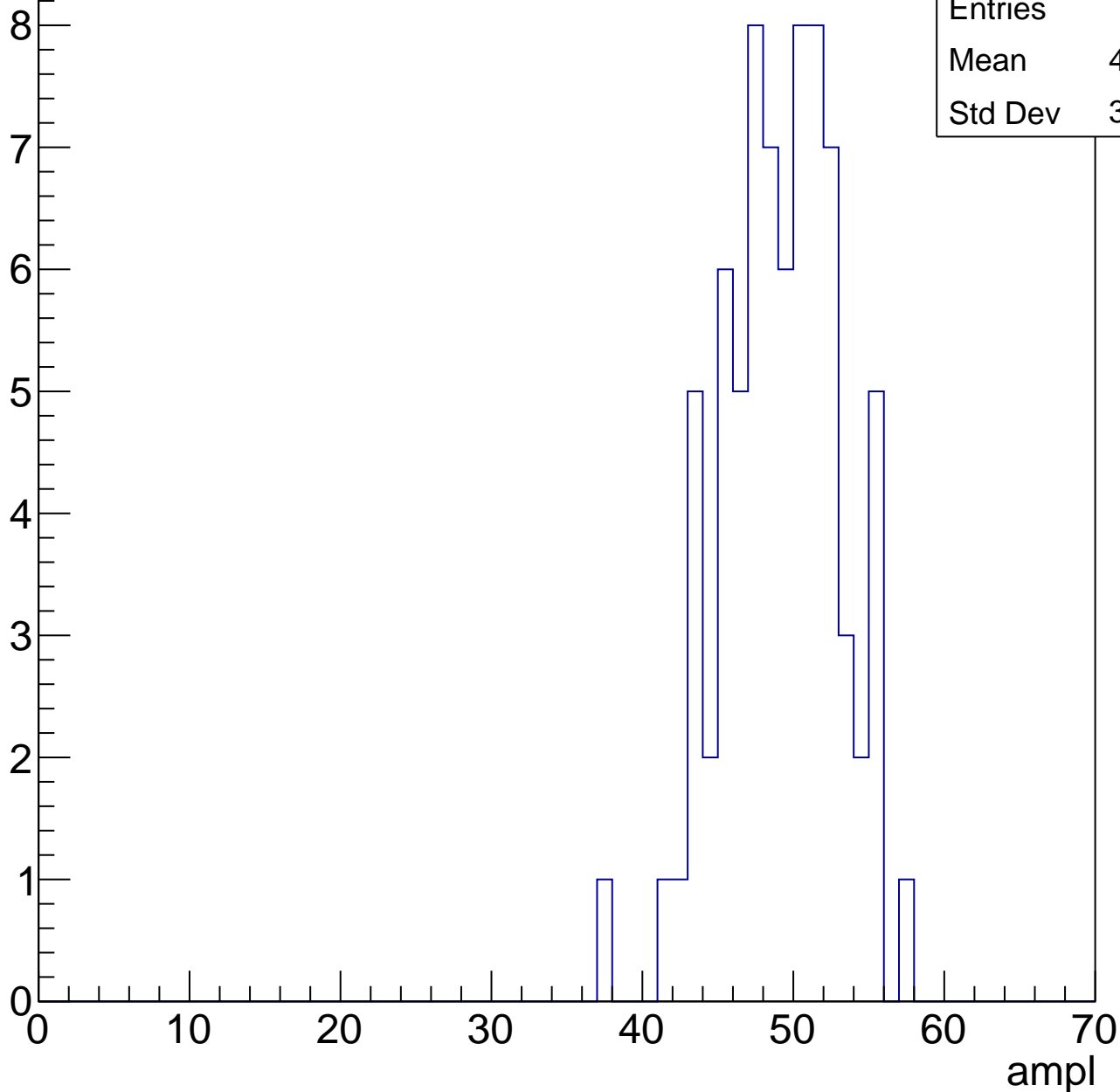


B1L103S, U20-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	48.68
Std Dev	3.812

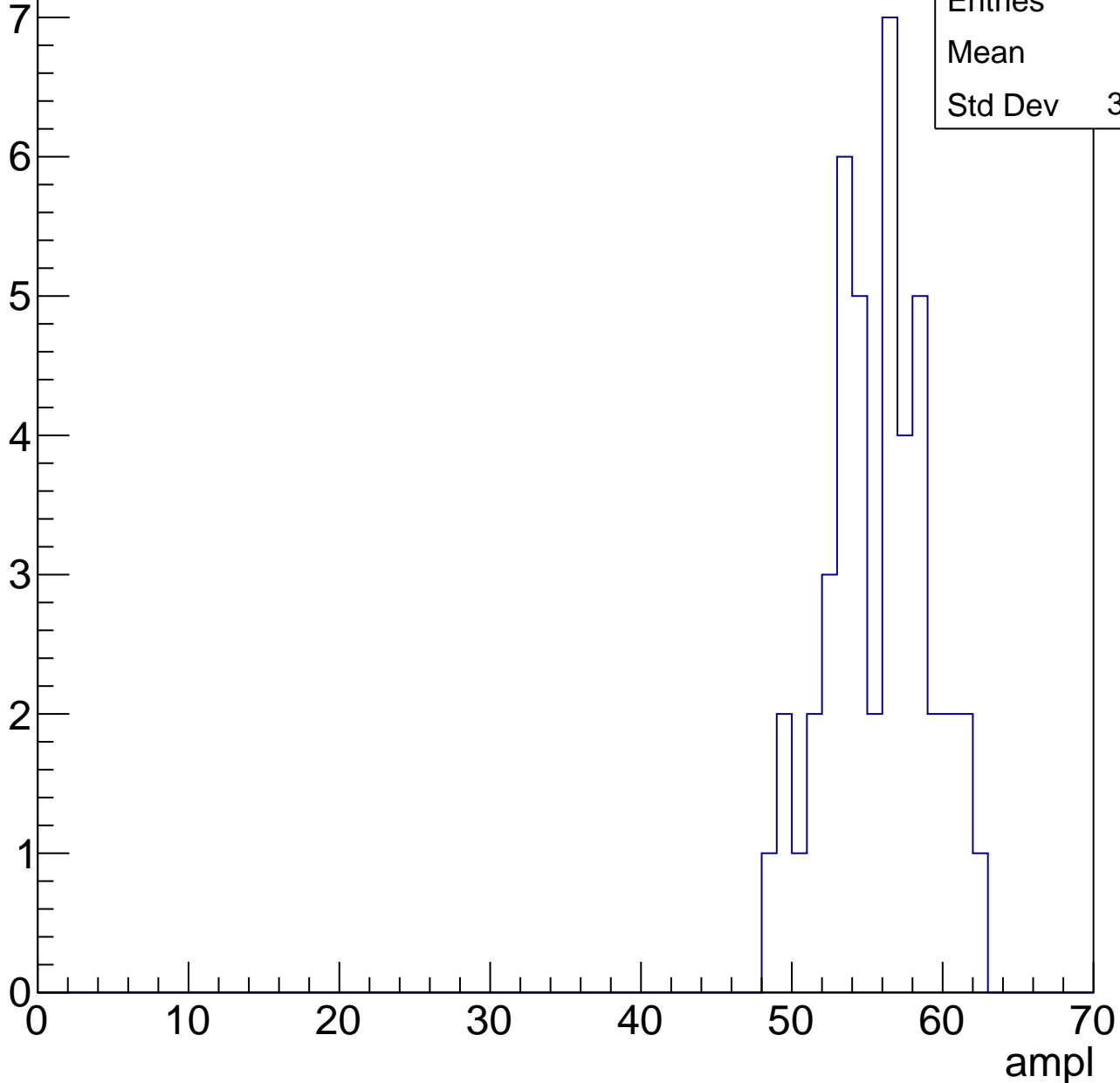


B1L103S, U20-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.2
Std Dev	3.344



B1L103S, U20-ch36, adc5

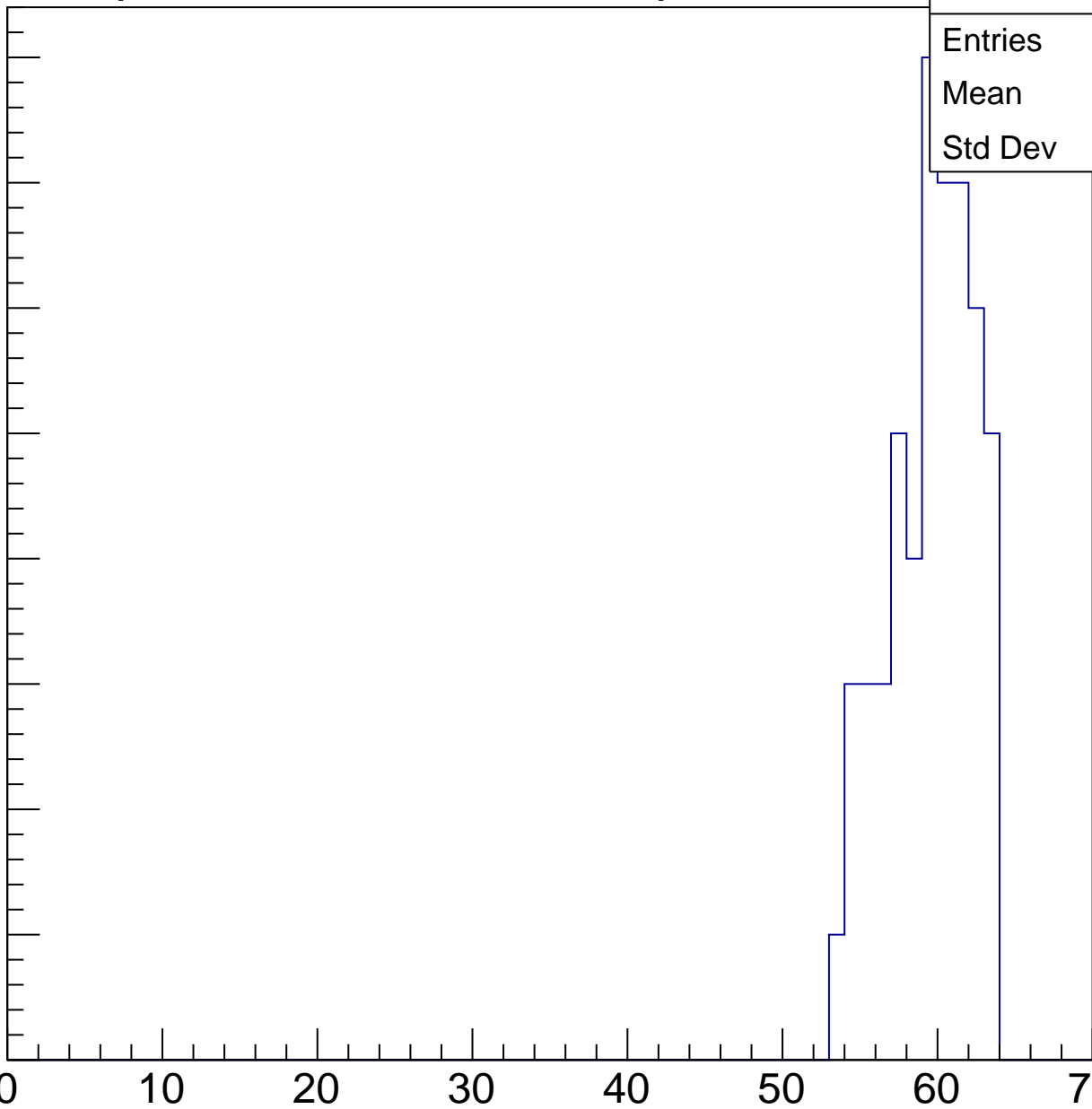
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.06
Std Dev	2.699

ampl

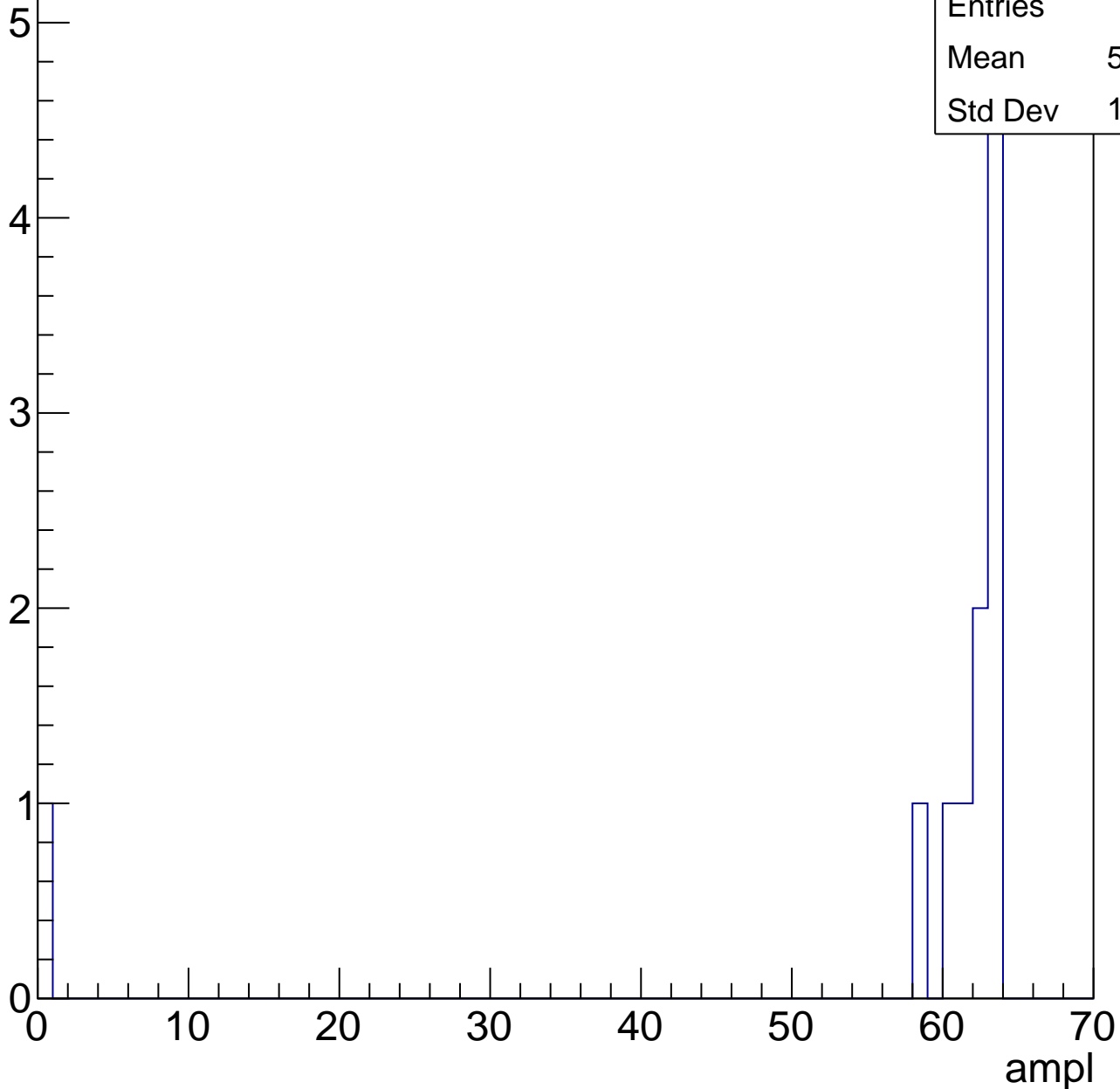


B1L103S, U20-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

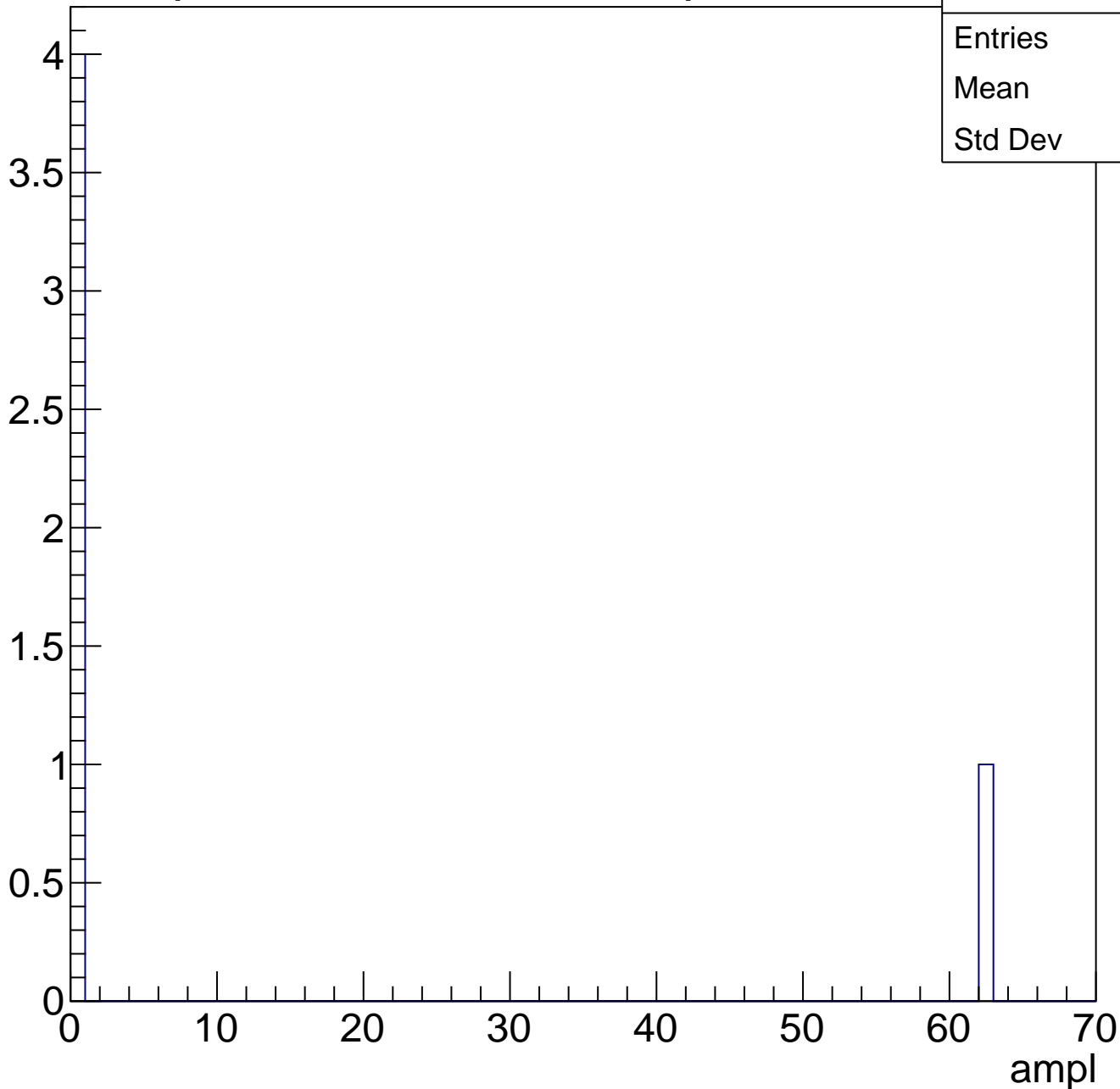
Entries	11
Mean	56.18
Std Dev	17.83



B1L103S, U20-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch37, adc0

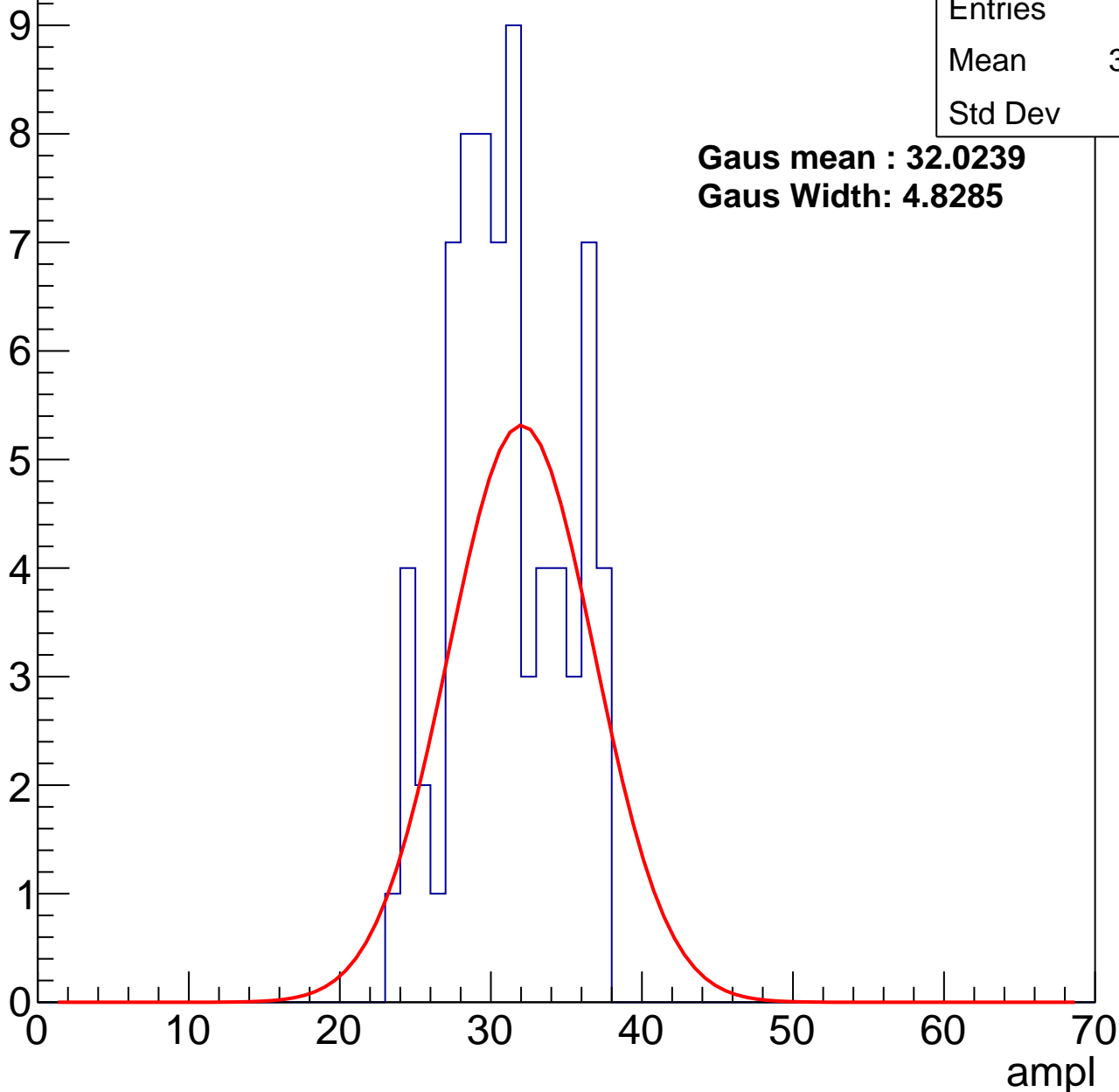
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.53
Std Dev	3.7

Gaus mean : 32.0239

Gaus Width: 4.8285



B1L103S, U20-ch37, adc1

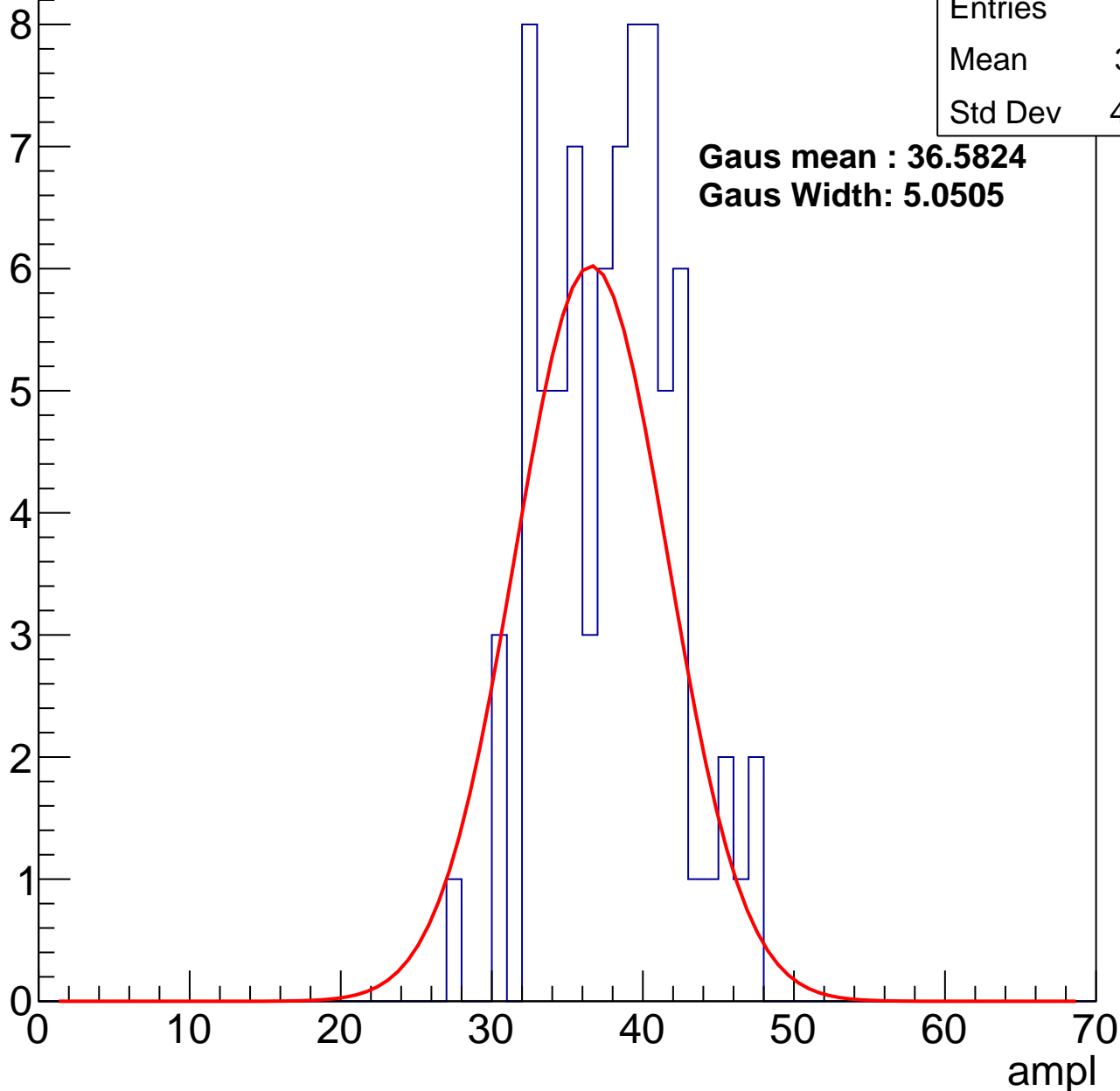
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	37.41
Std Dev	4.247

Gaus mean : 36.5824

Gaus Width: 5.0505



B1L103S, U20-ch37, adc2

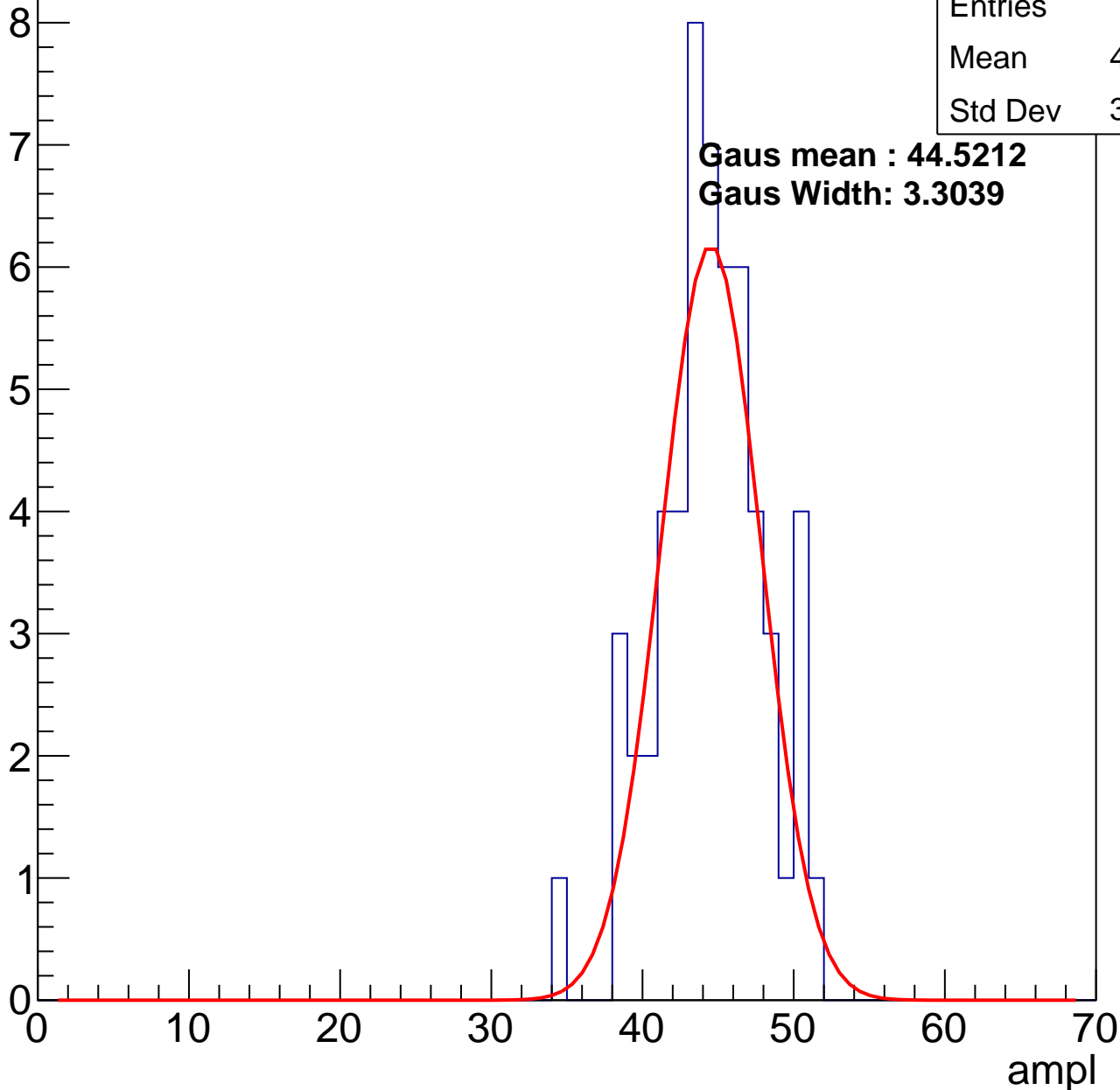
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	44.07
Std Dev	3.489

Gaus mean : 44.5212

Gaus Width: 3.3039

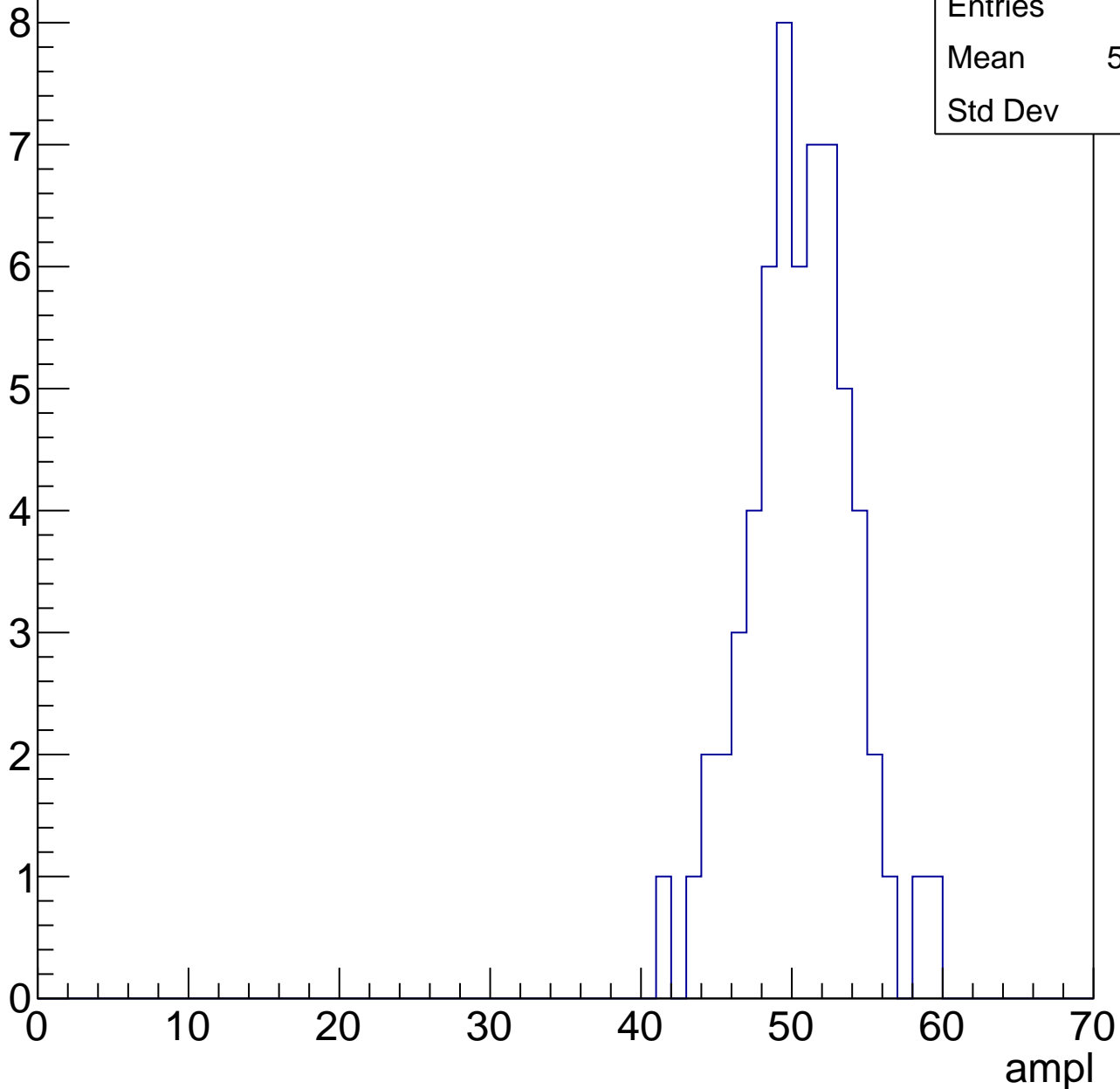


B1L103S, U20-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

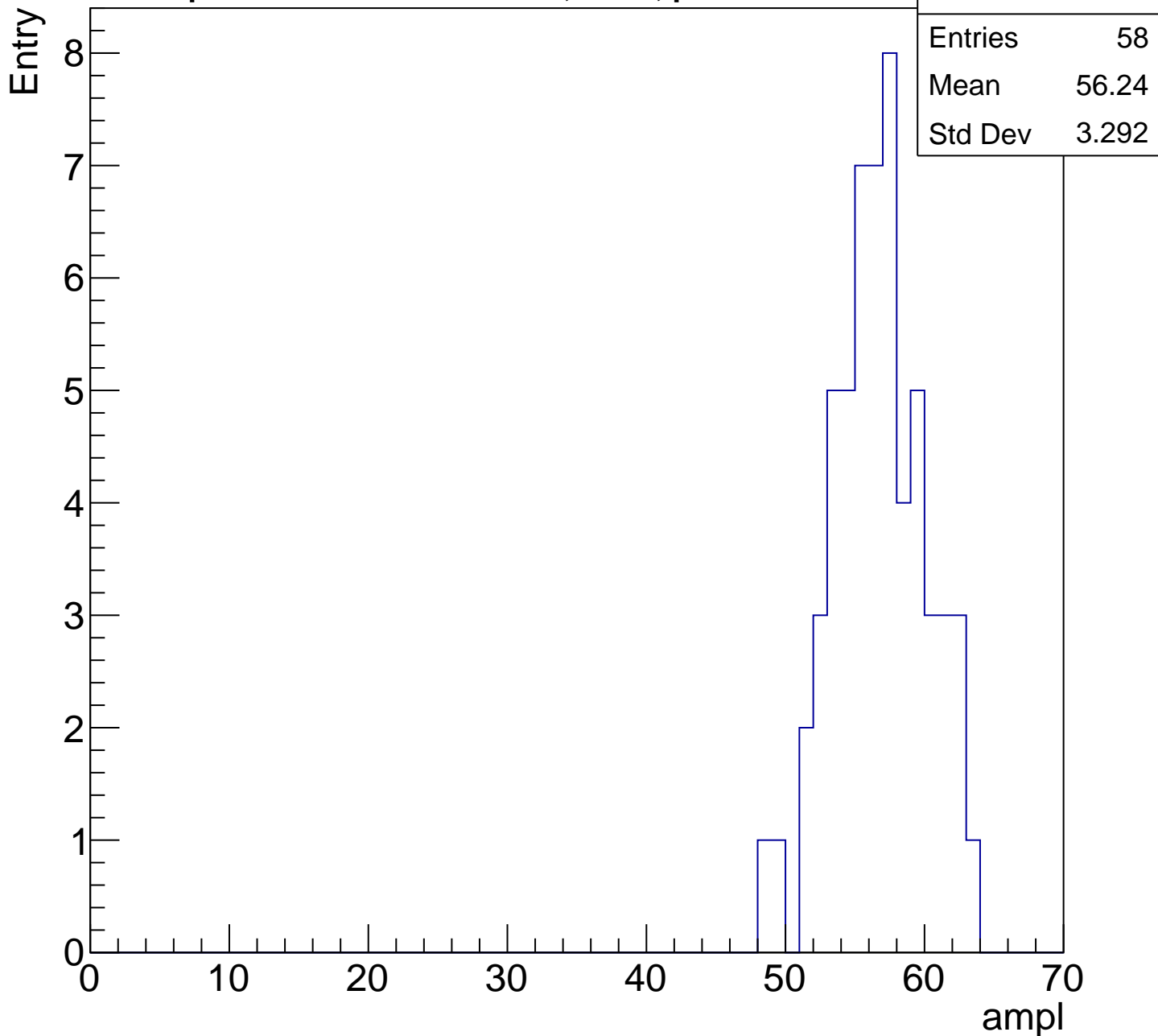
Entry

Entries	61
Mean	50.05
Std Dev	3.49



B1L103S, U20-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U20-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

41

Mean

60.02

Std Dev

2.363

ampl

0

10

20

30

40

50

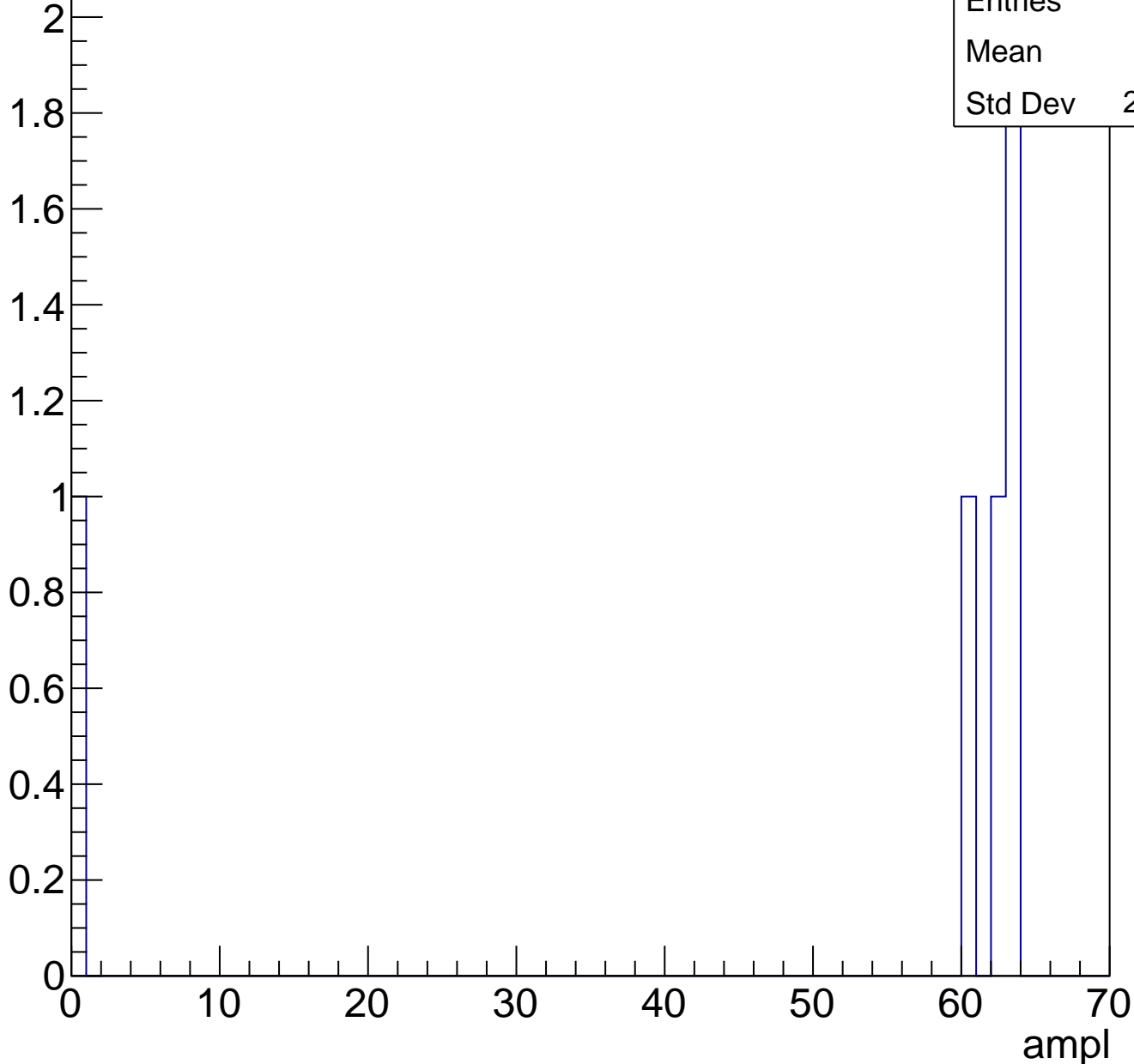
60

70

B1L103S, U20-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L103S, U20-ch38, adc0

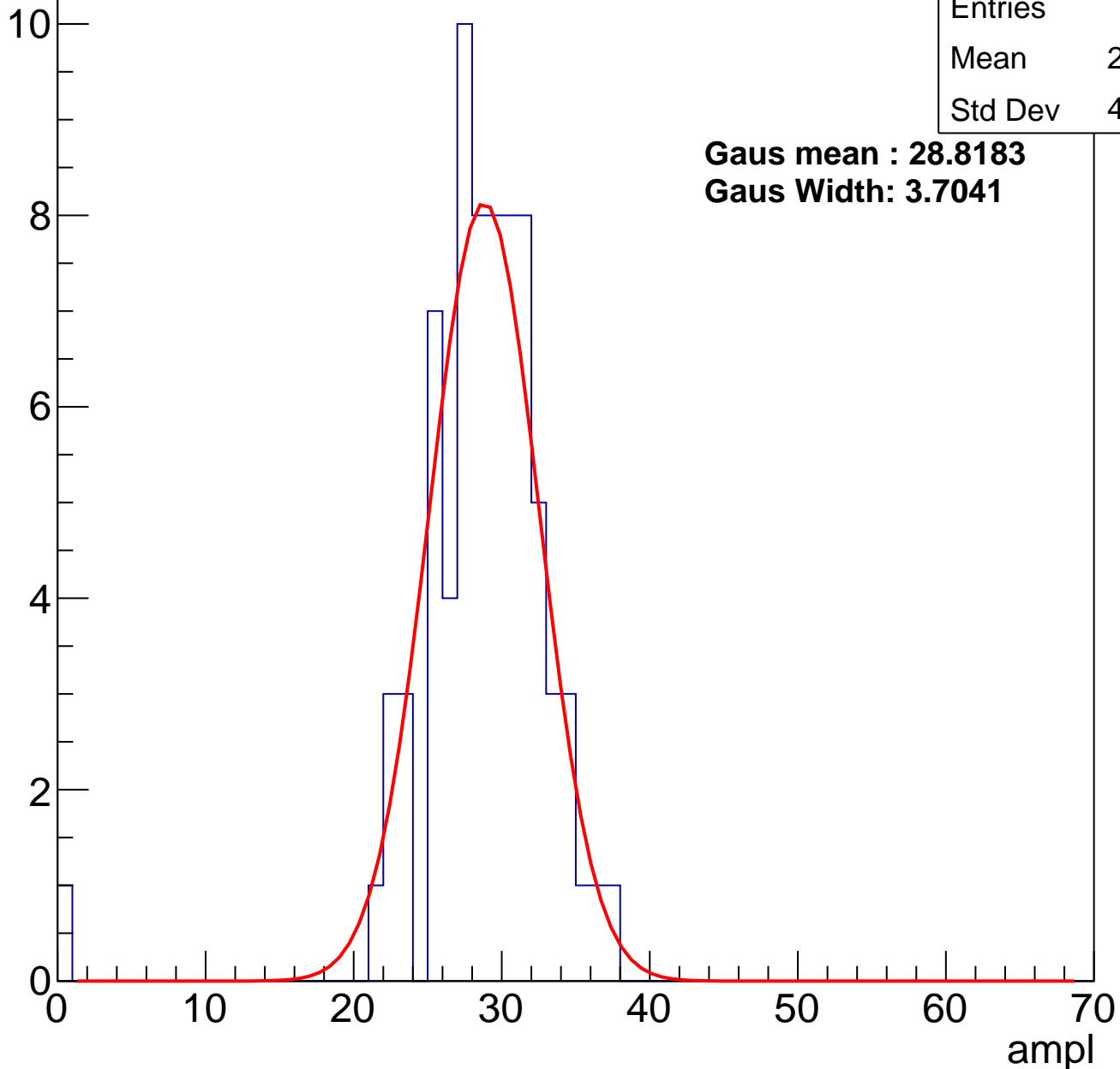
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	28.24
Std Dev	4.718

Gaus mean : 28.8183

Gaus Width: 3.7041

Entry



B1L103S, U20-ch38, adc1

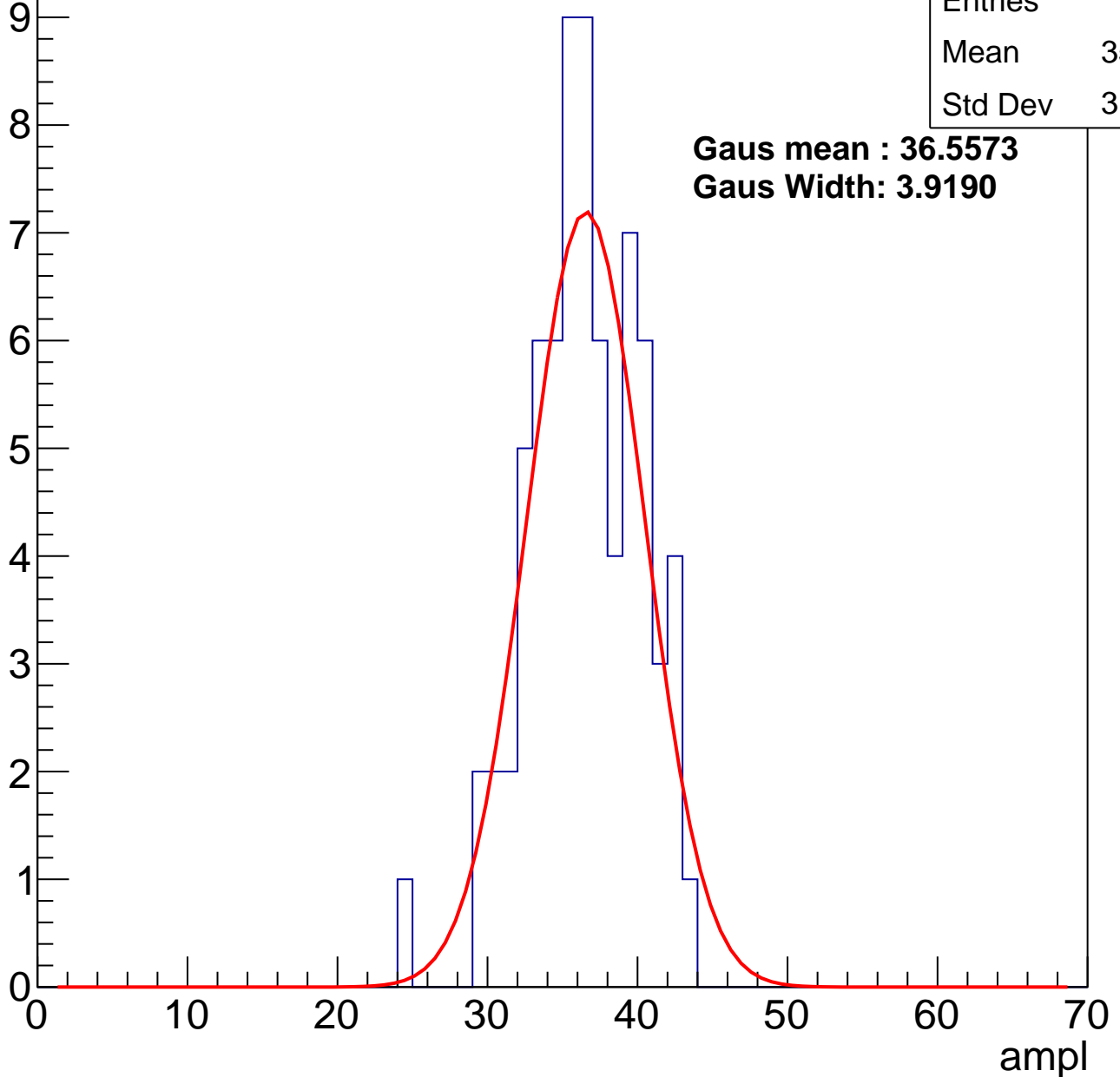
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.97
Std Dev	3.664

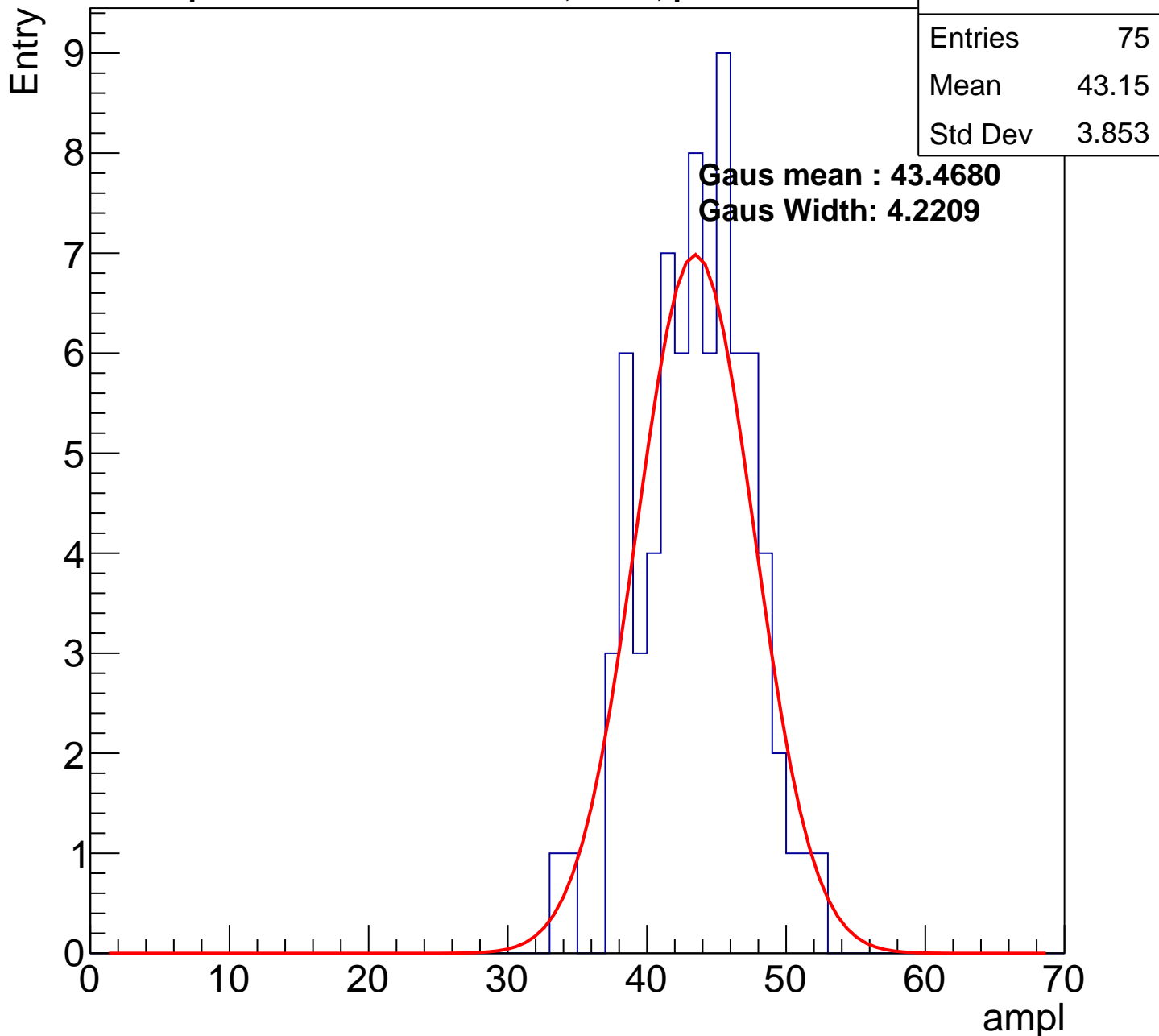
Gaus mean : 36.5573

Gaus Width: 3.9190



B1L103S, U20-ch38, adc2

calib_packv5_042523_0143.root, FC#7, port C2

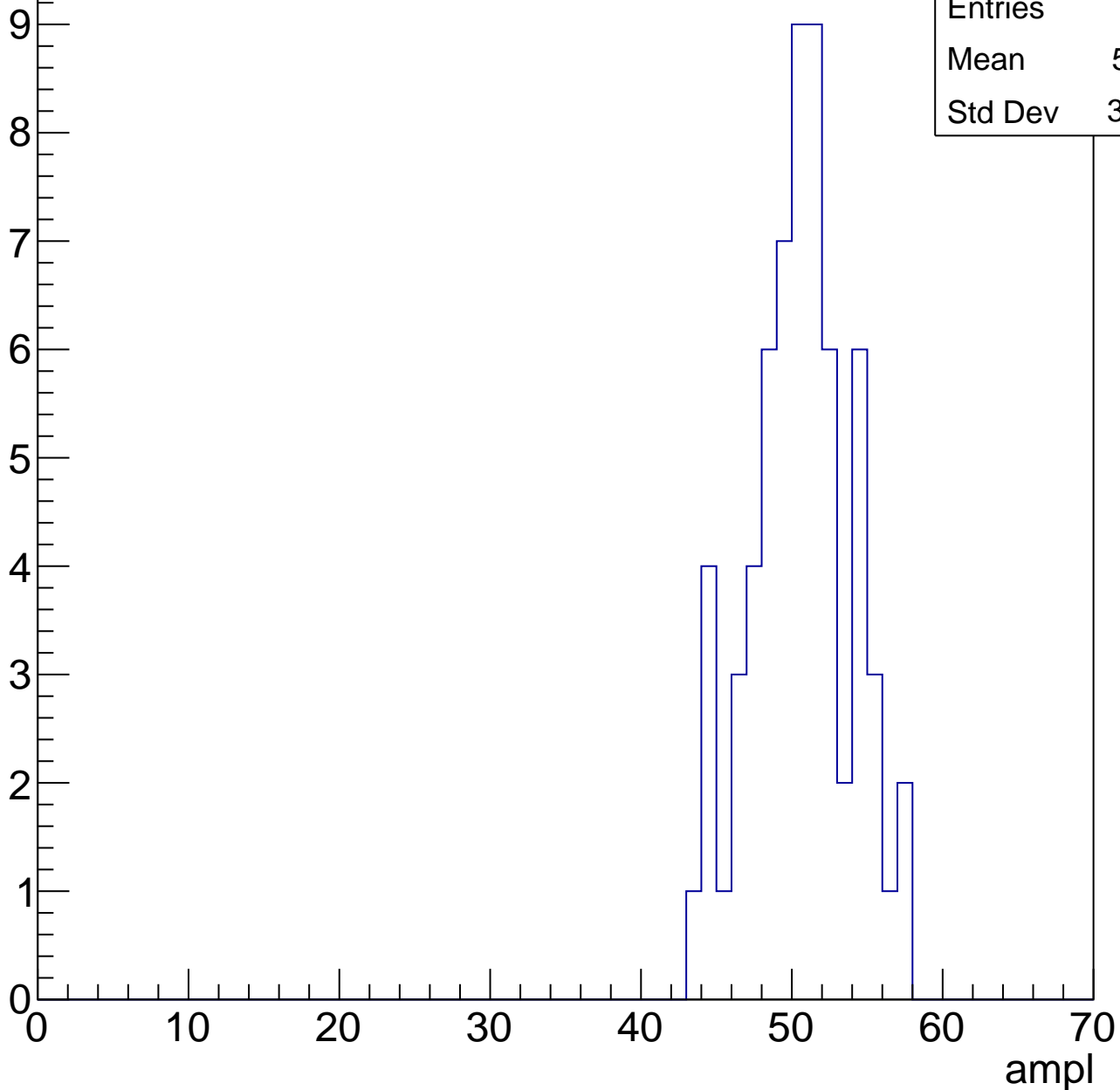


B1L103S, U20-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.11
Std Dev	3.279

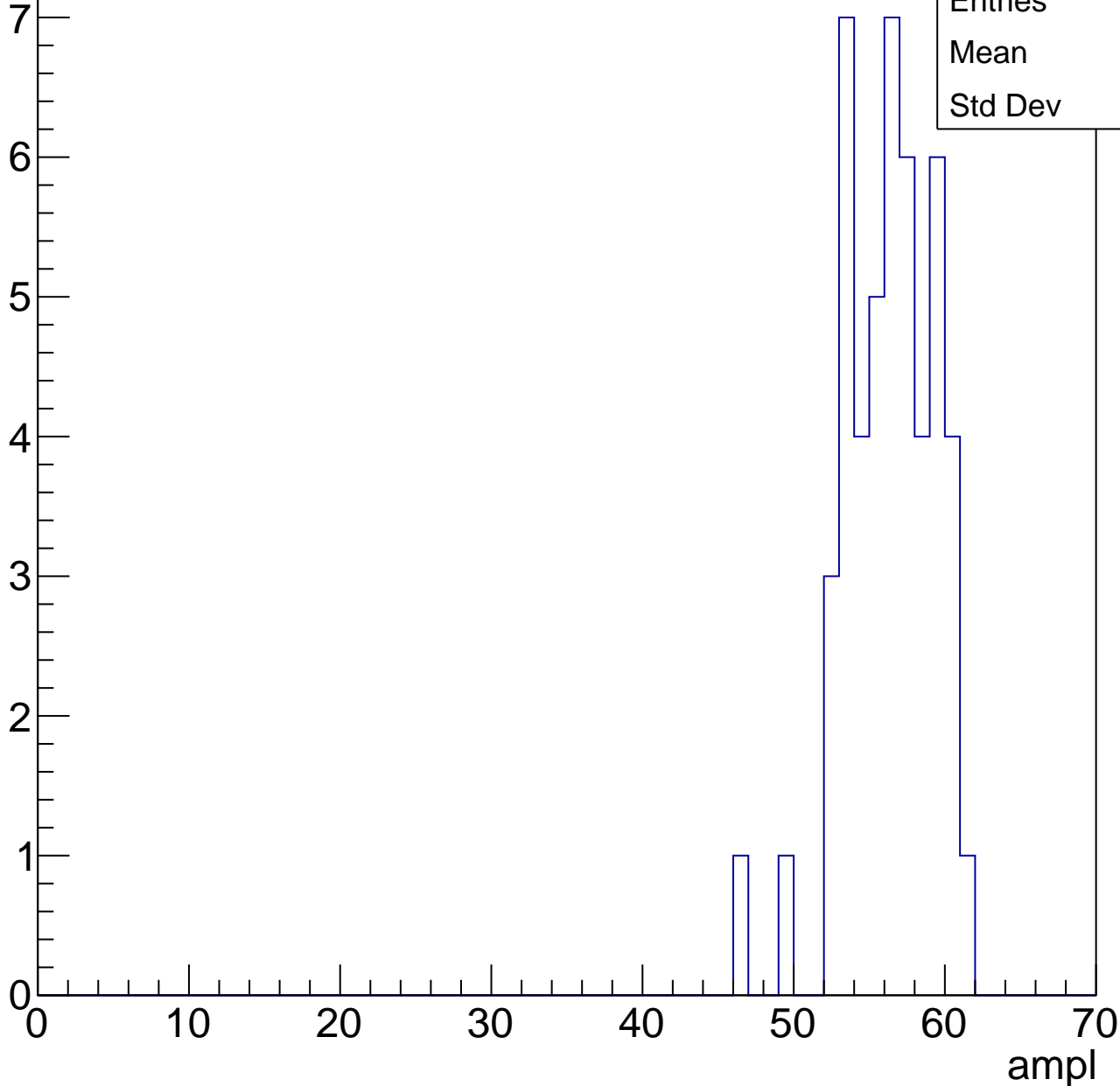


B1L103S, U20-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.8
Std Dev	3.01

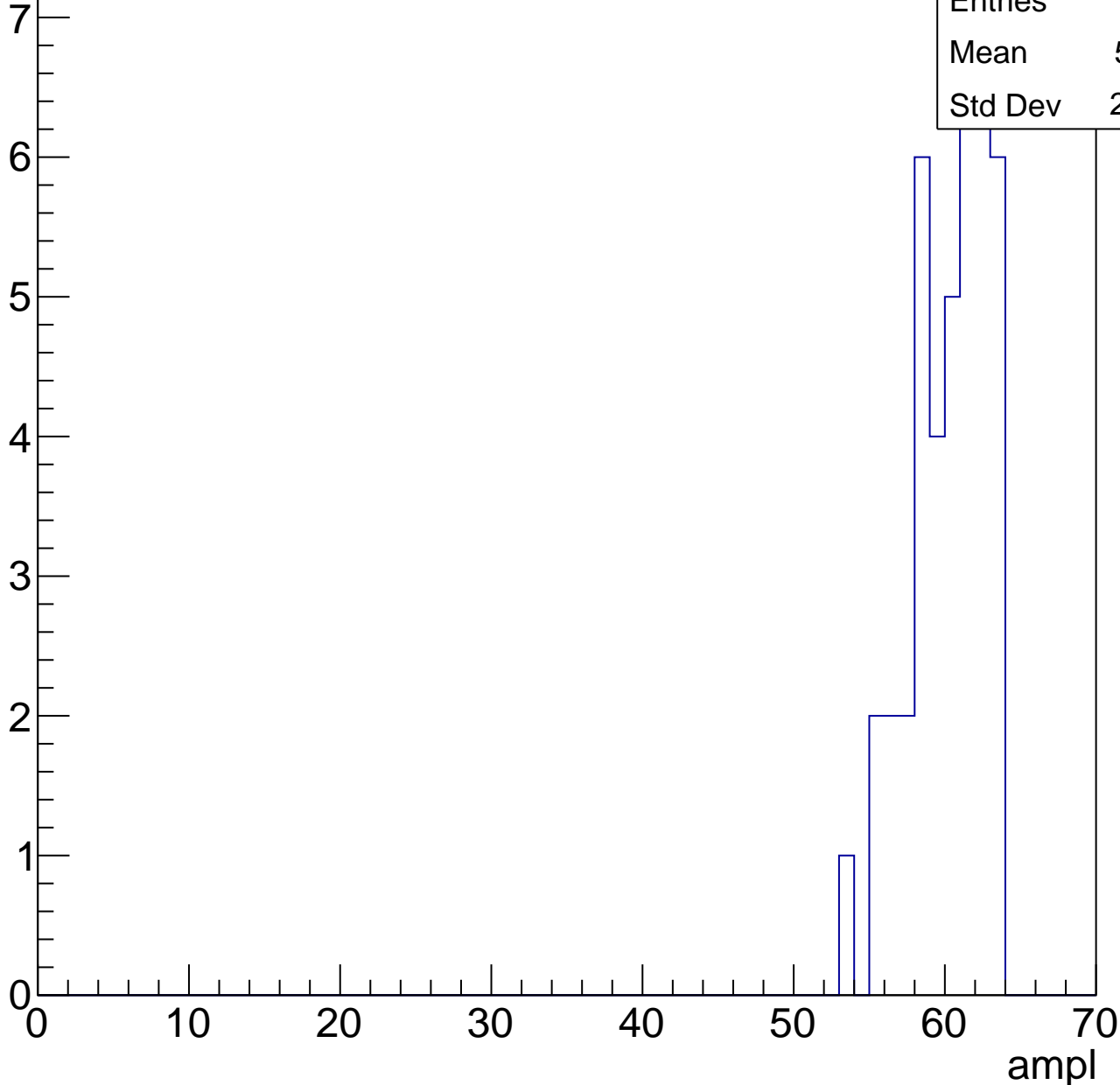


B1L103S, U20-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

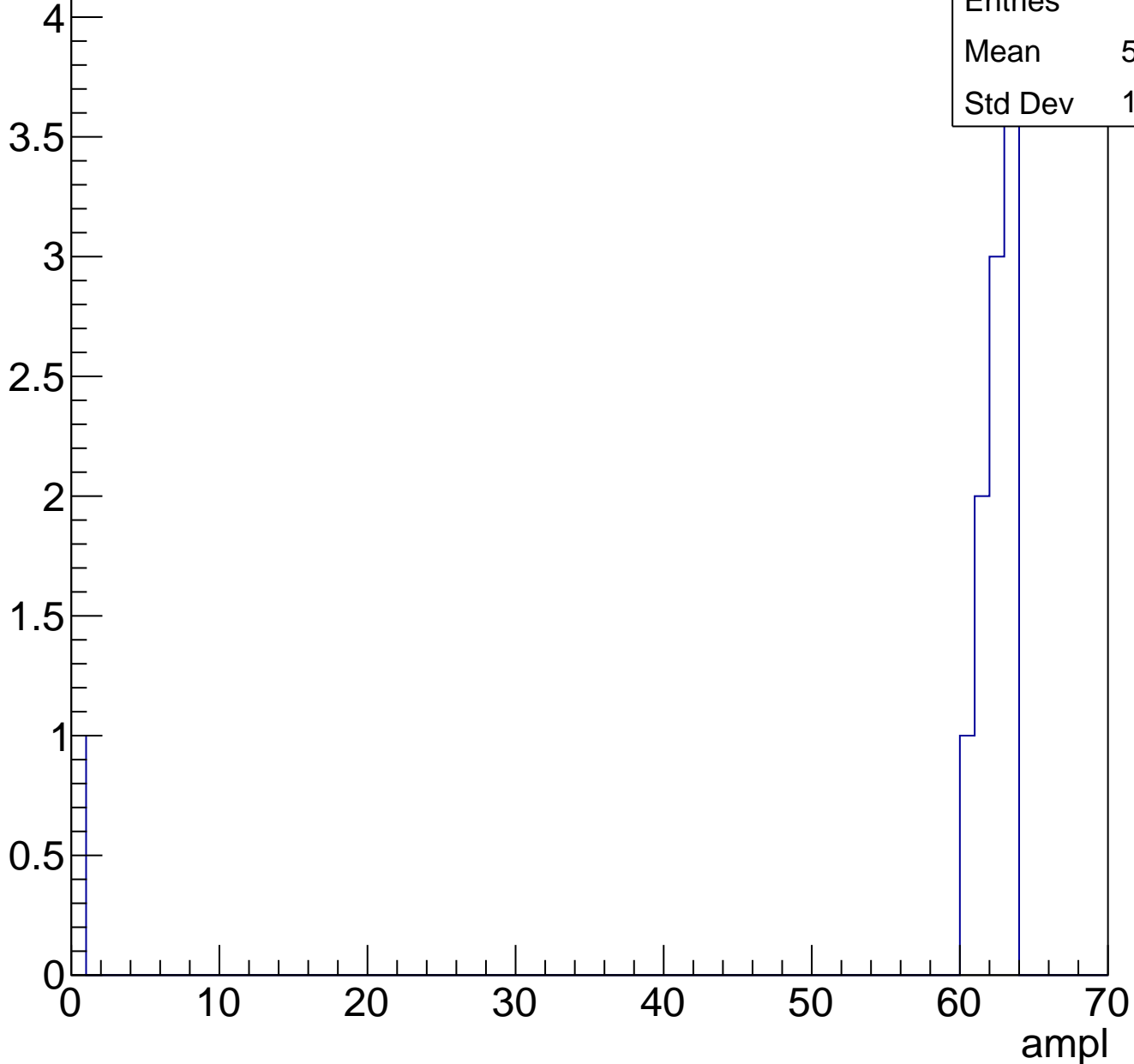
Entries	42
Mean	59.81
Std Dev	2.509



B1L103S, U20-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch39, adc0

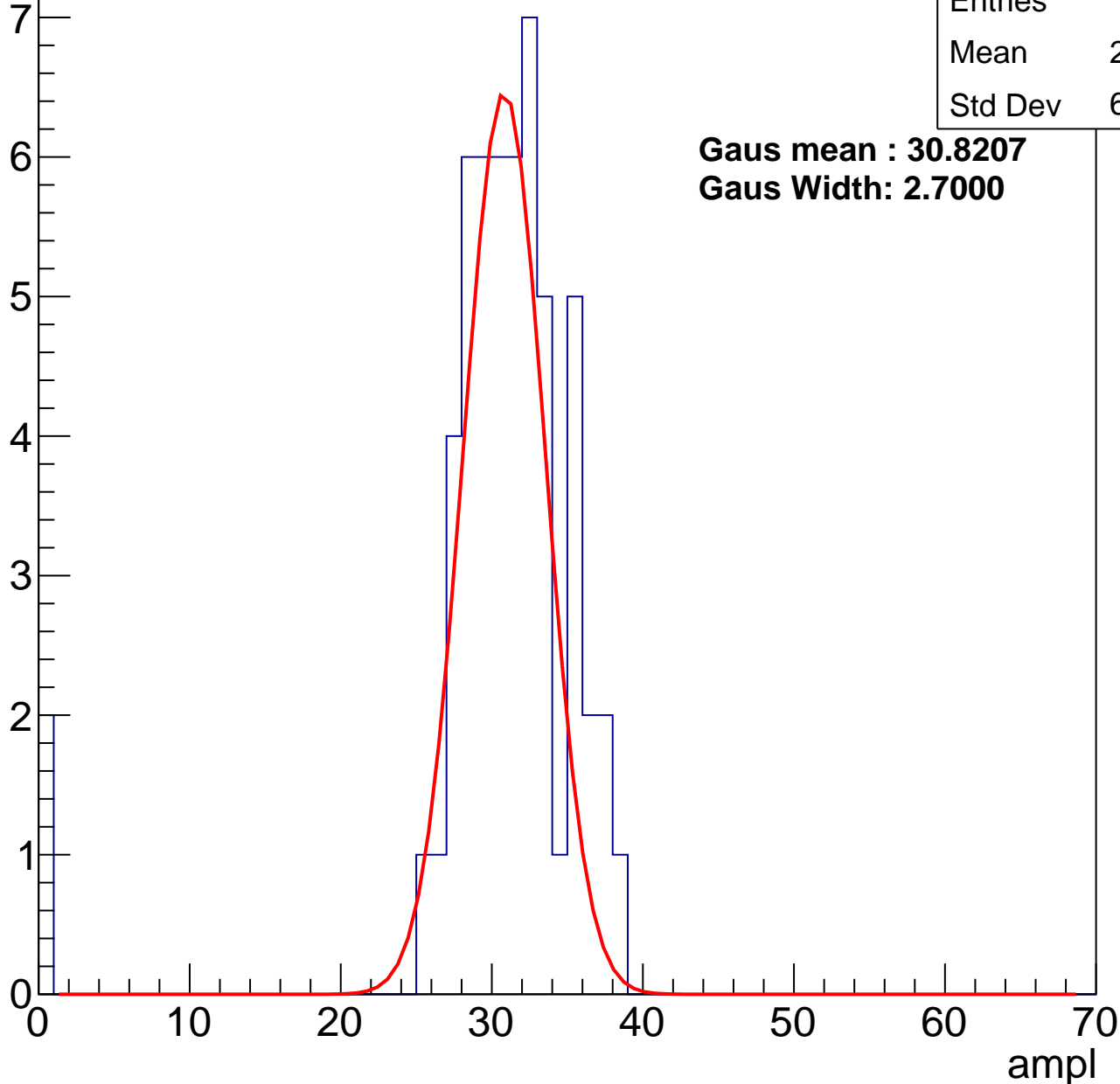
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	29.98
Std Dev	6.552

Gaus mean : 30.8207

Gaus Width: 2.7000



B1L103S, U20-ch39, adc1

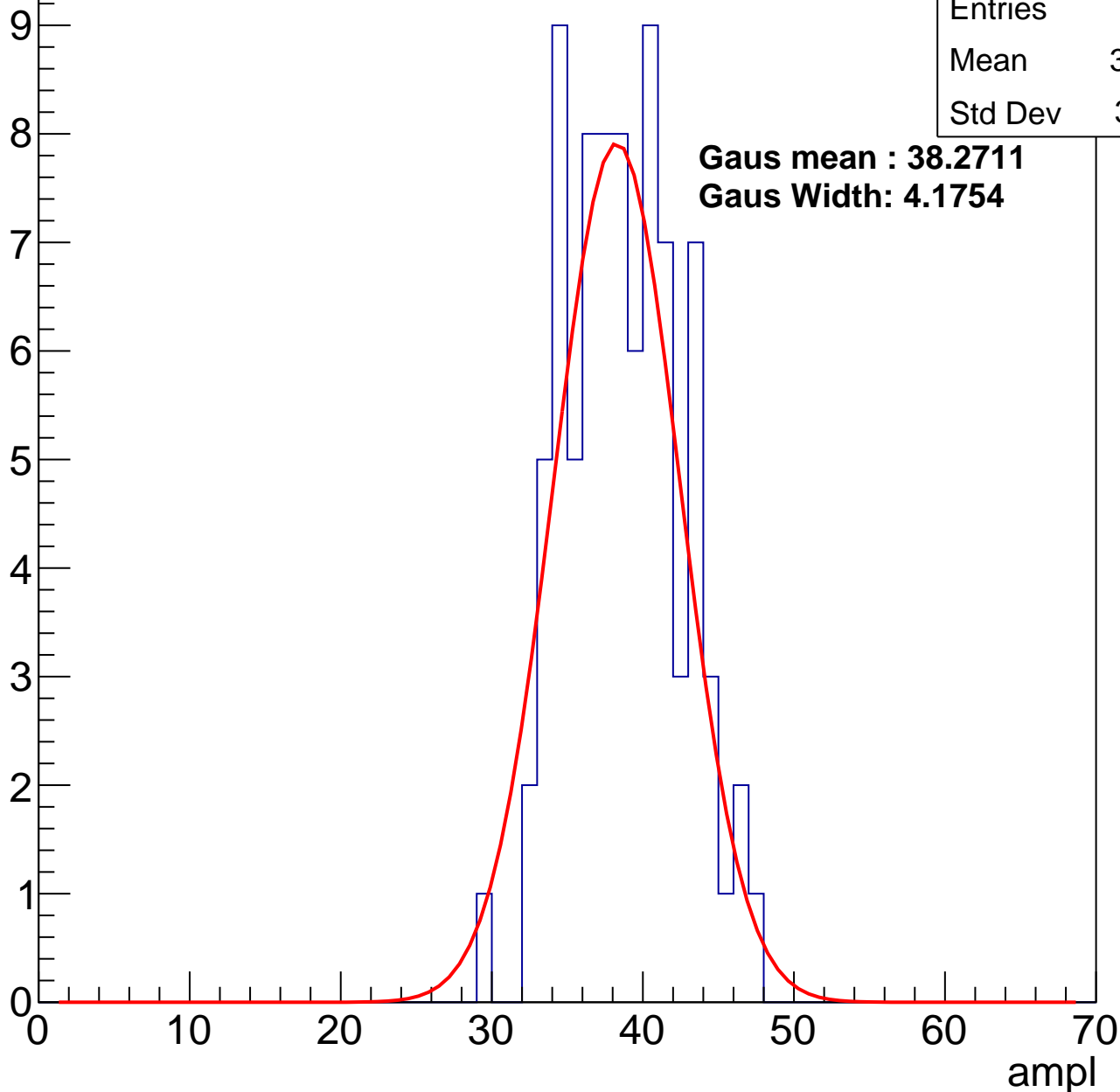
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	38.25
Std Dev	3.751

Gaus mean : 38.2711

Gaus Width: 4.1754



B1L103S, U20-ch39, adc2

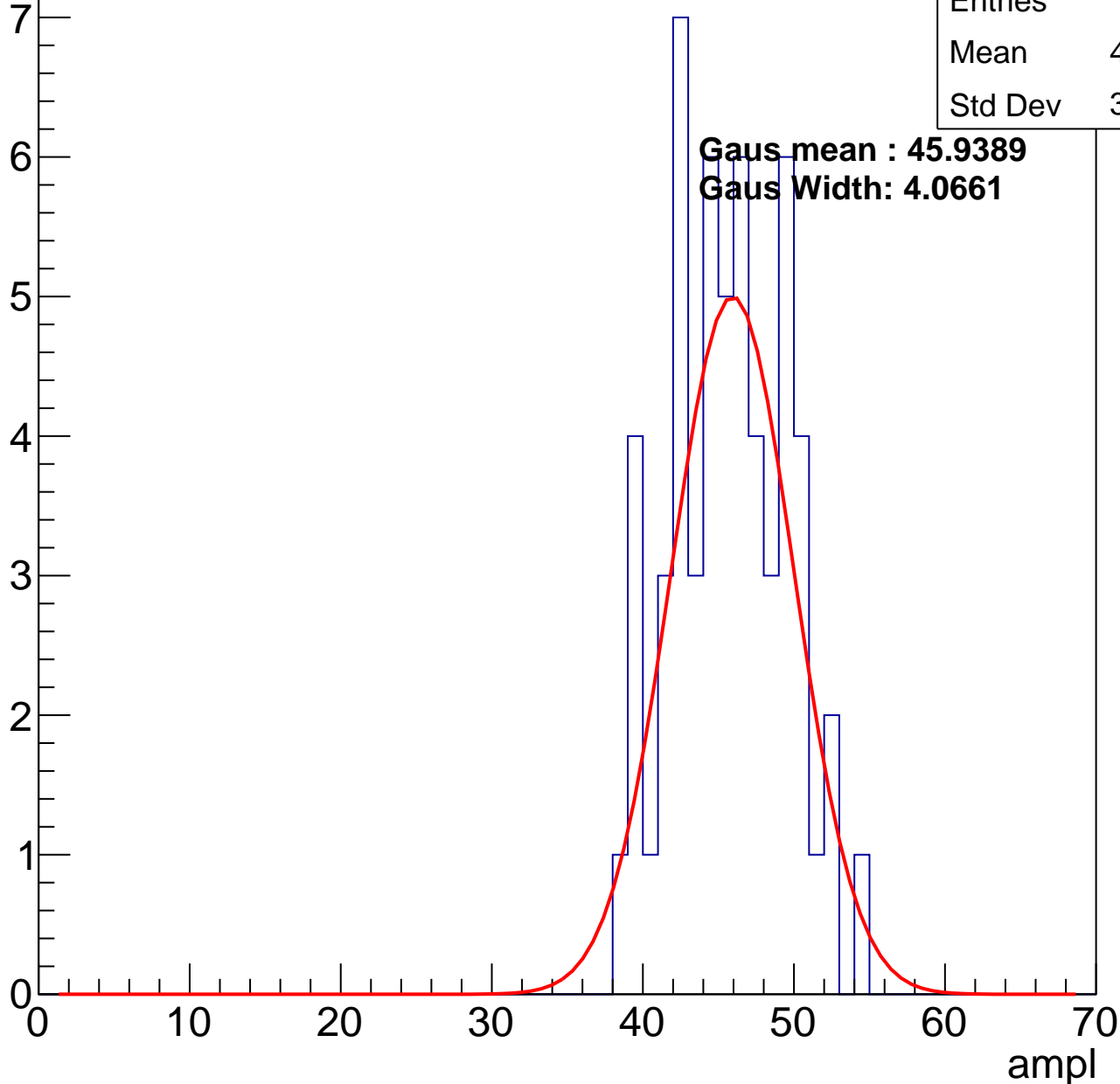
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	45.26
Std Dev	3.753

Gaus mean : 45.9389

Gaus Width: 4.0661

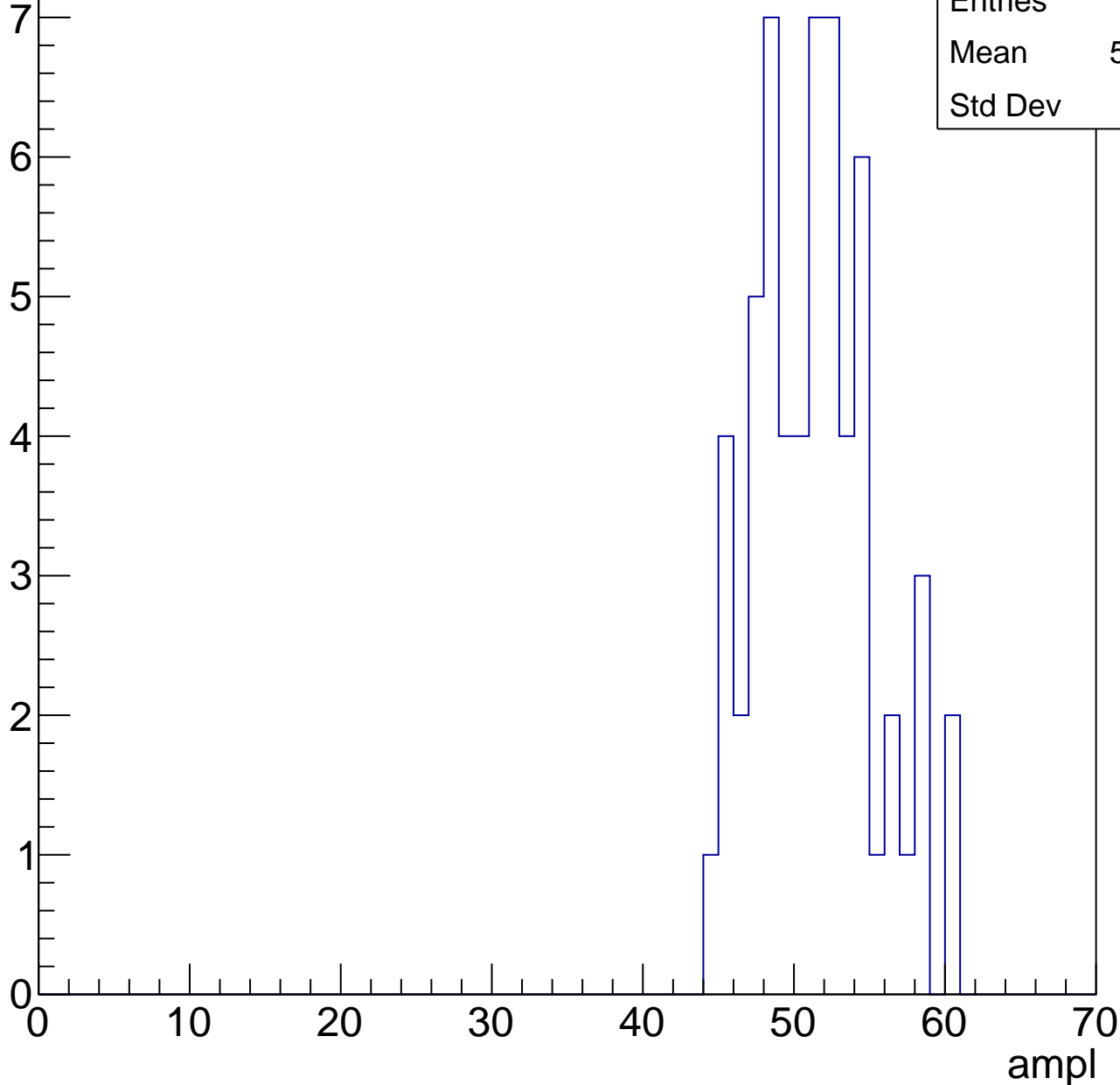


B1L103S, U20-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

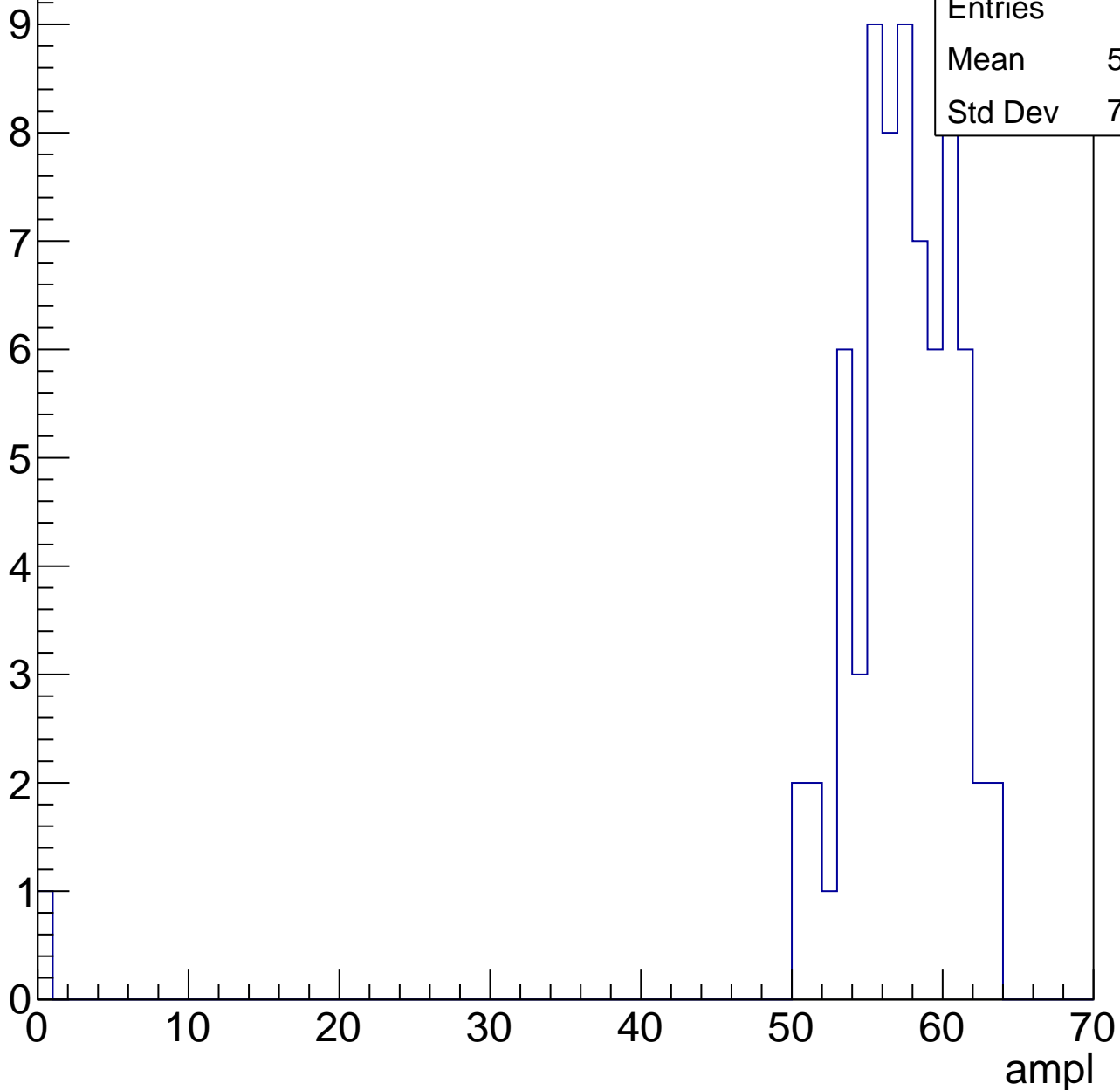
Entries	60
Mean	50.97
Std Dev	3.86



B1L103S, U20-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

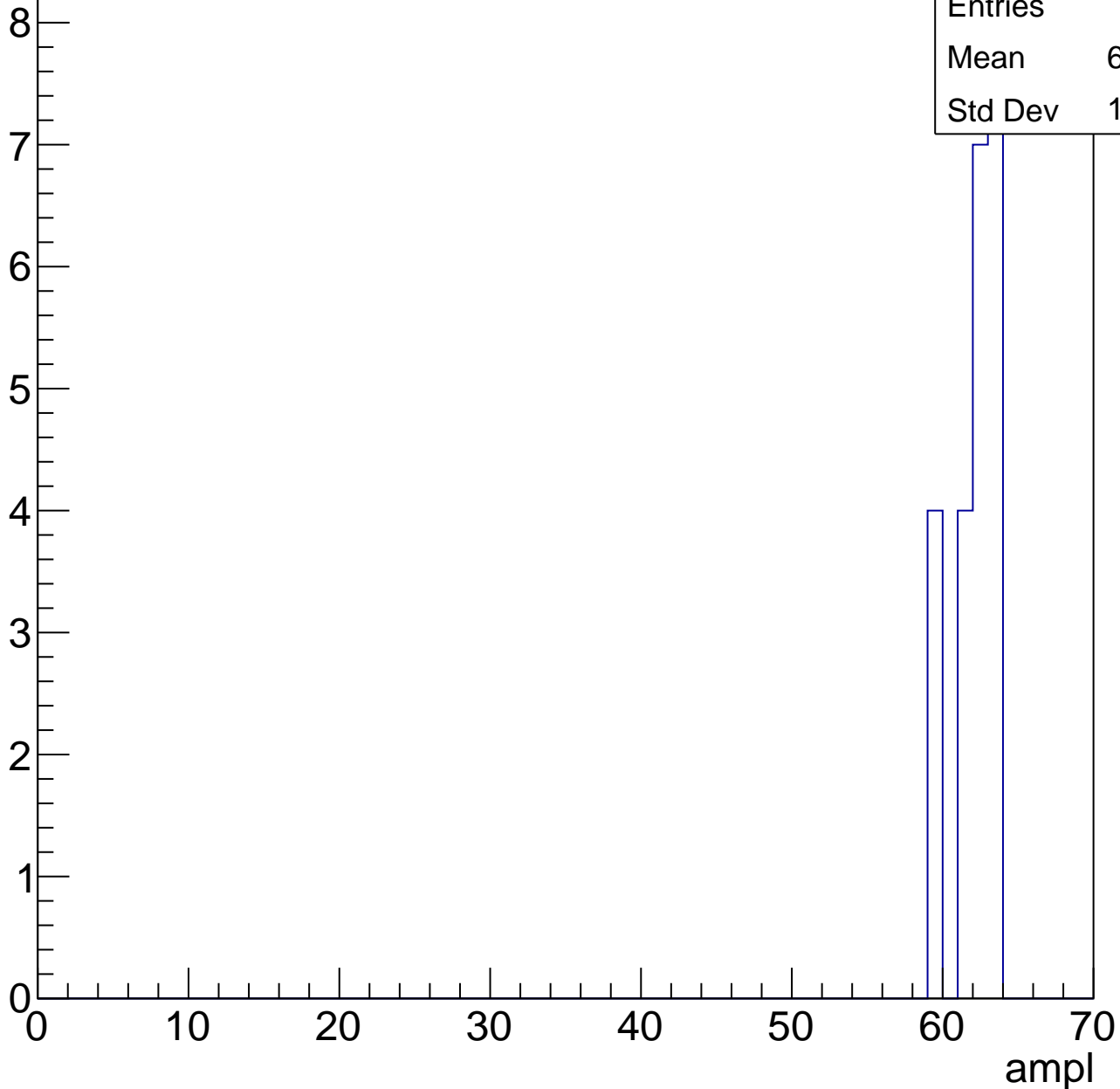


B1L103S, U20-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	61.65
Std Dev	1.402



B1L103S, U20-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch40, adc0

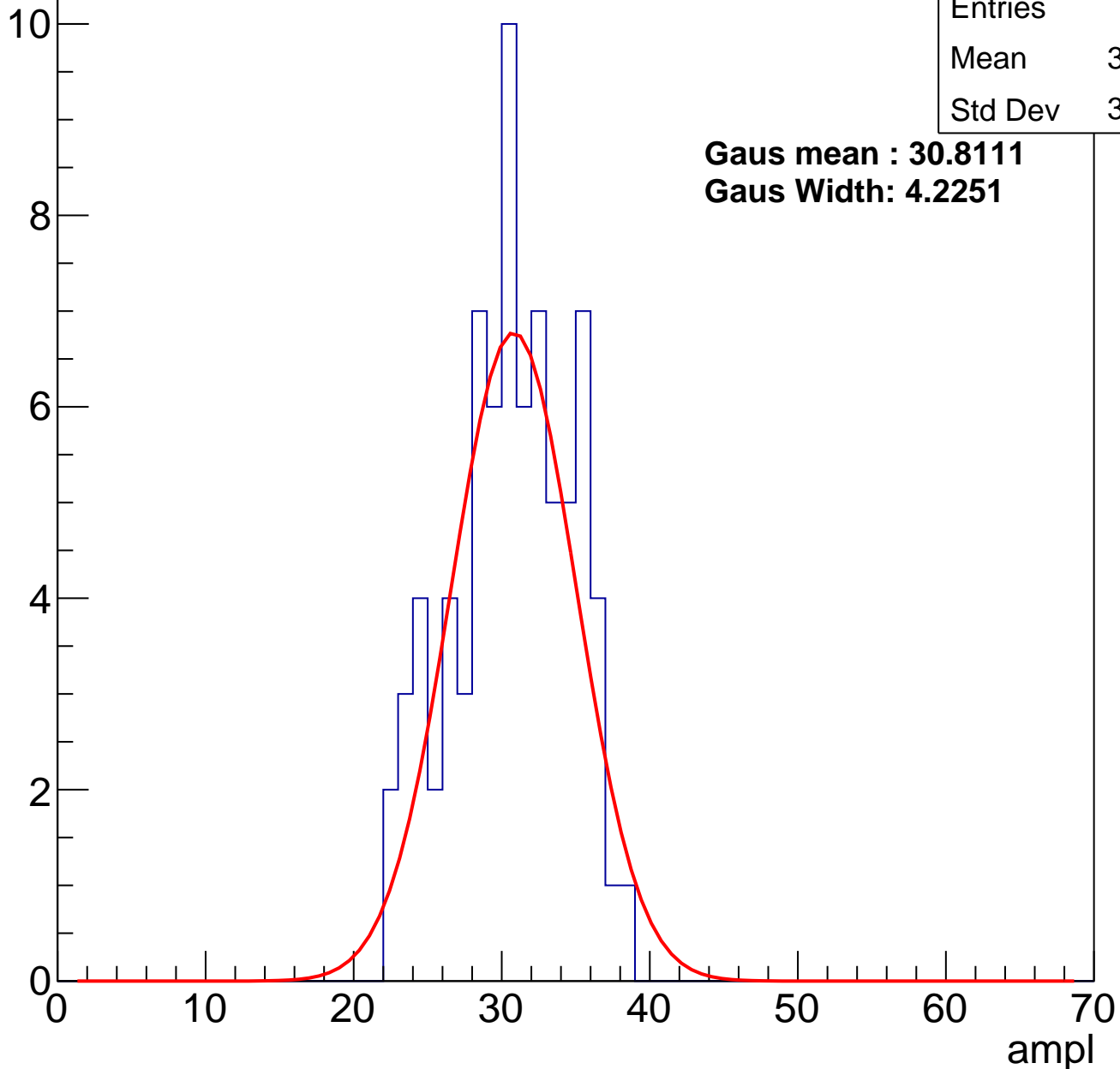
calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	30.17
Std Dev	3.919

Gaus mean : 30.8111

Gaus Width: 4.2251

Entry



B1L103S, U20-ch40, adc1

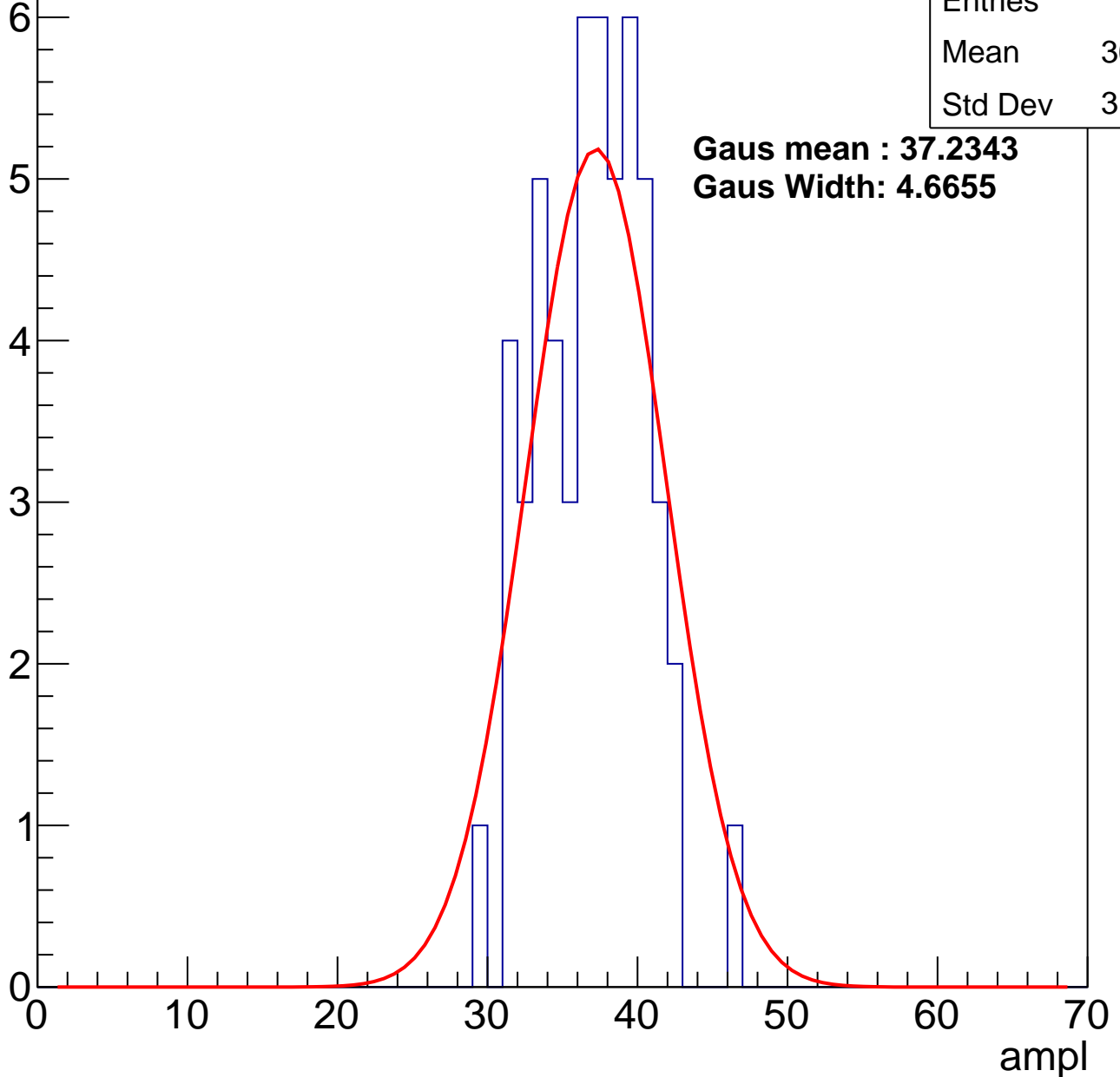
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.48
Std Dev	3.484

Gaus mean : 37.2343

Gaus Width: 4.6655



B1L103S, U20-ch40, adc2

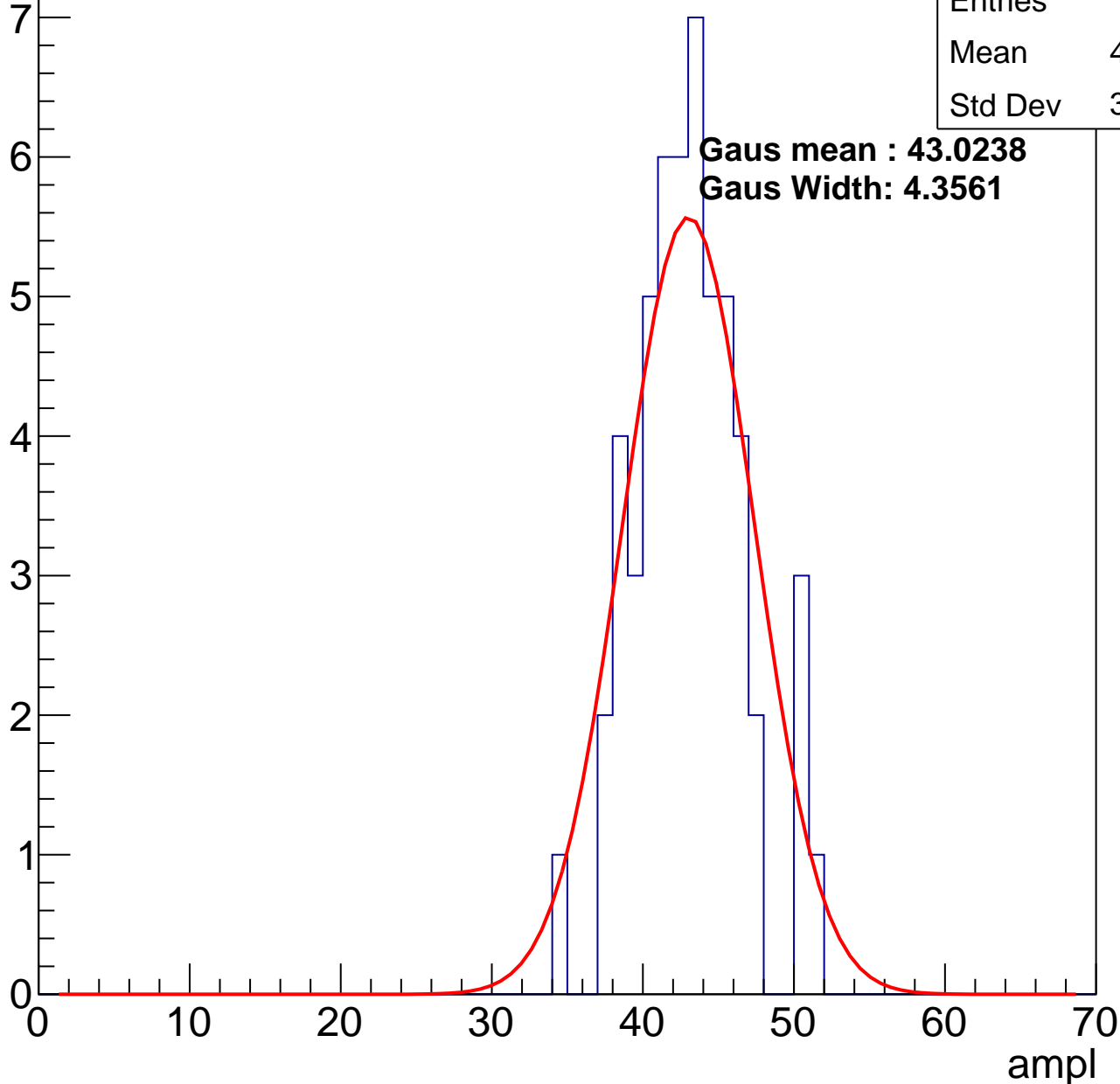
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	42.59
Std Dev	3.525

Gaus mean : 43.0238

Gaus Width: 4.3561

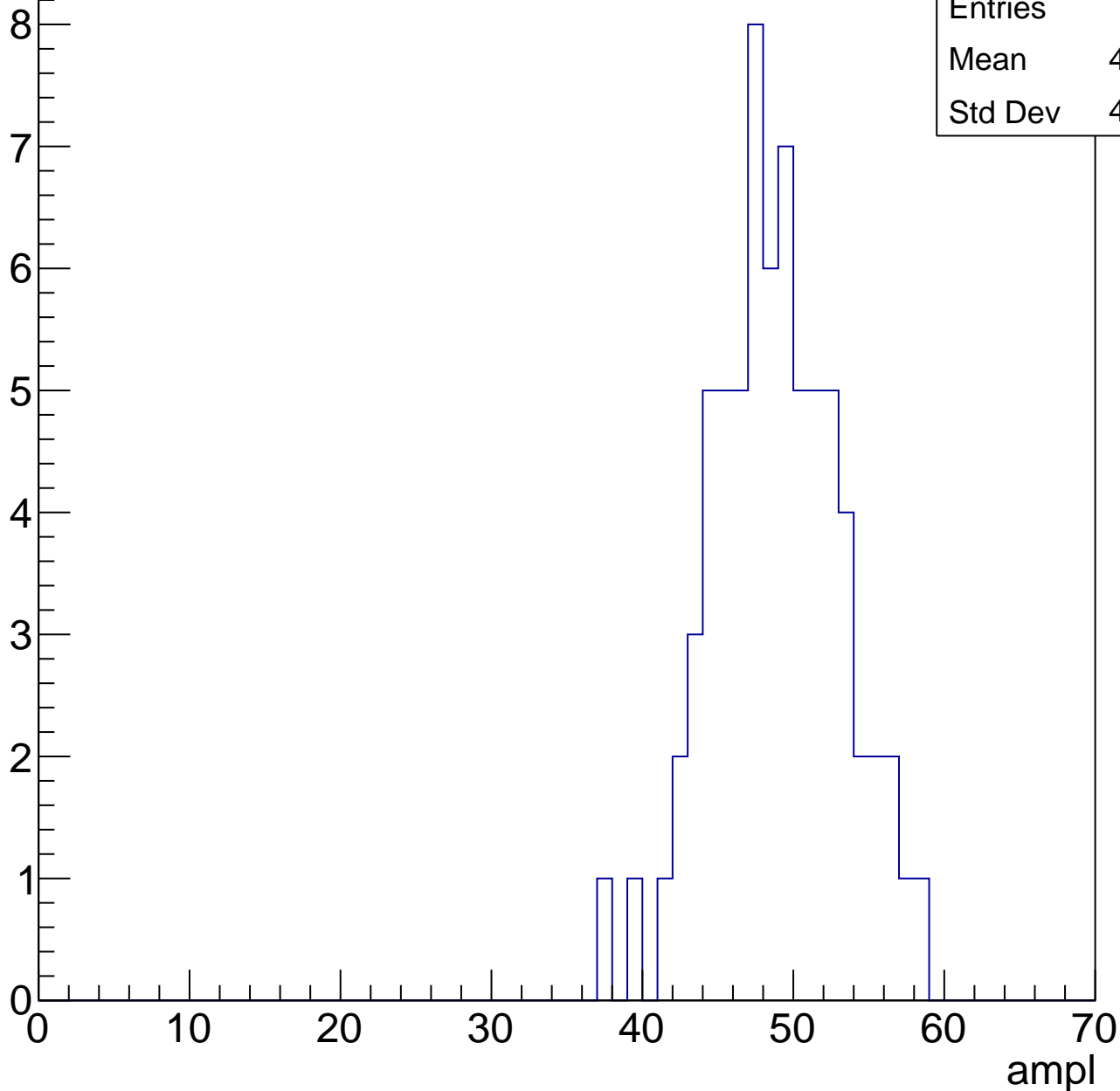


B1L103S, U20-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.37
Std Dev	4.233

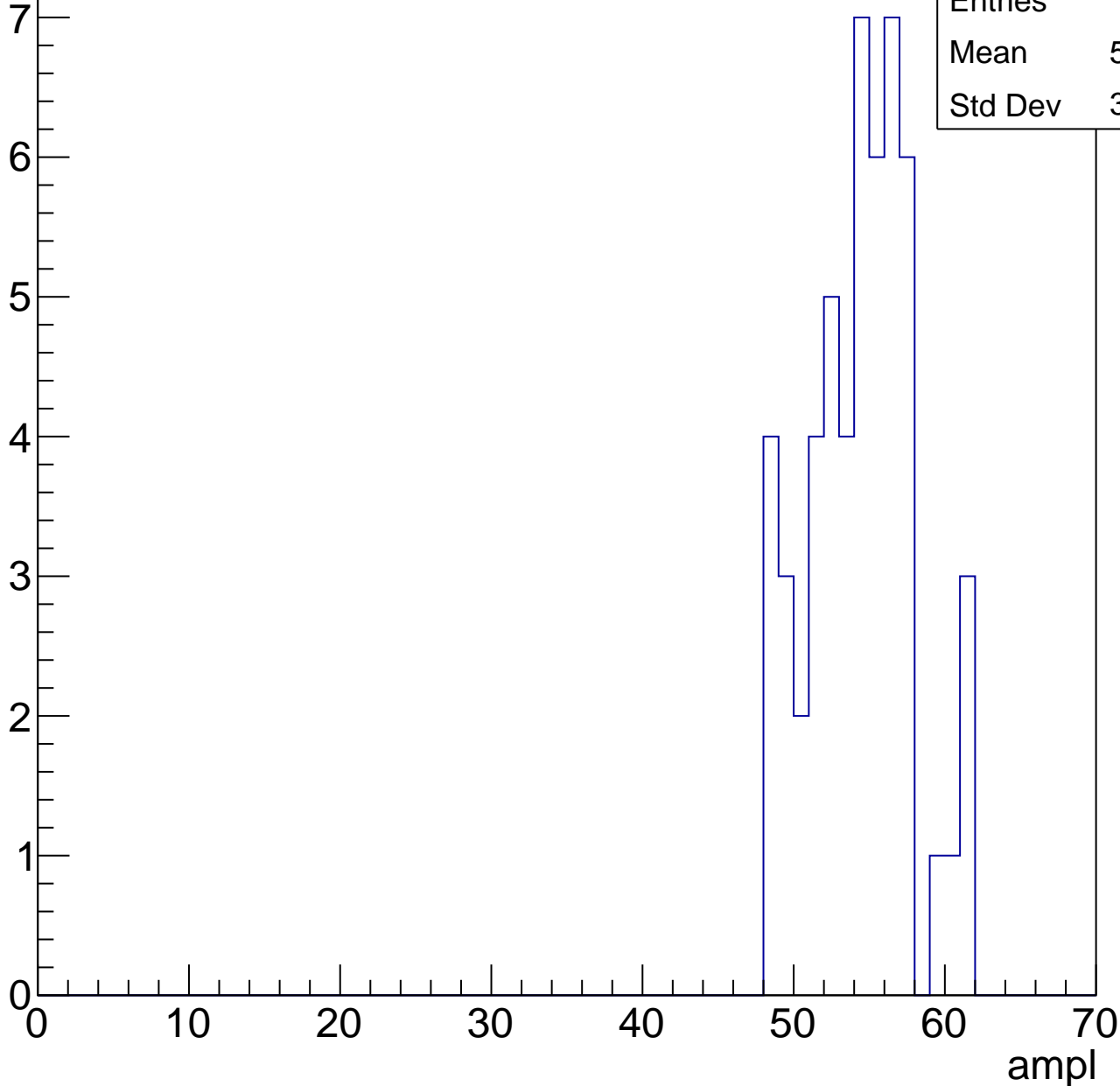


B1L103S, U20-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

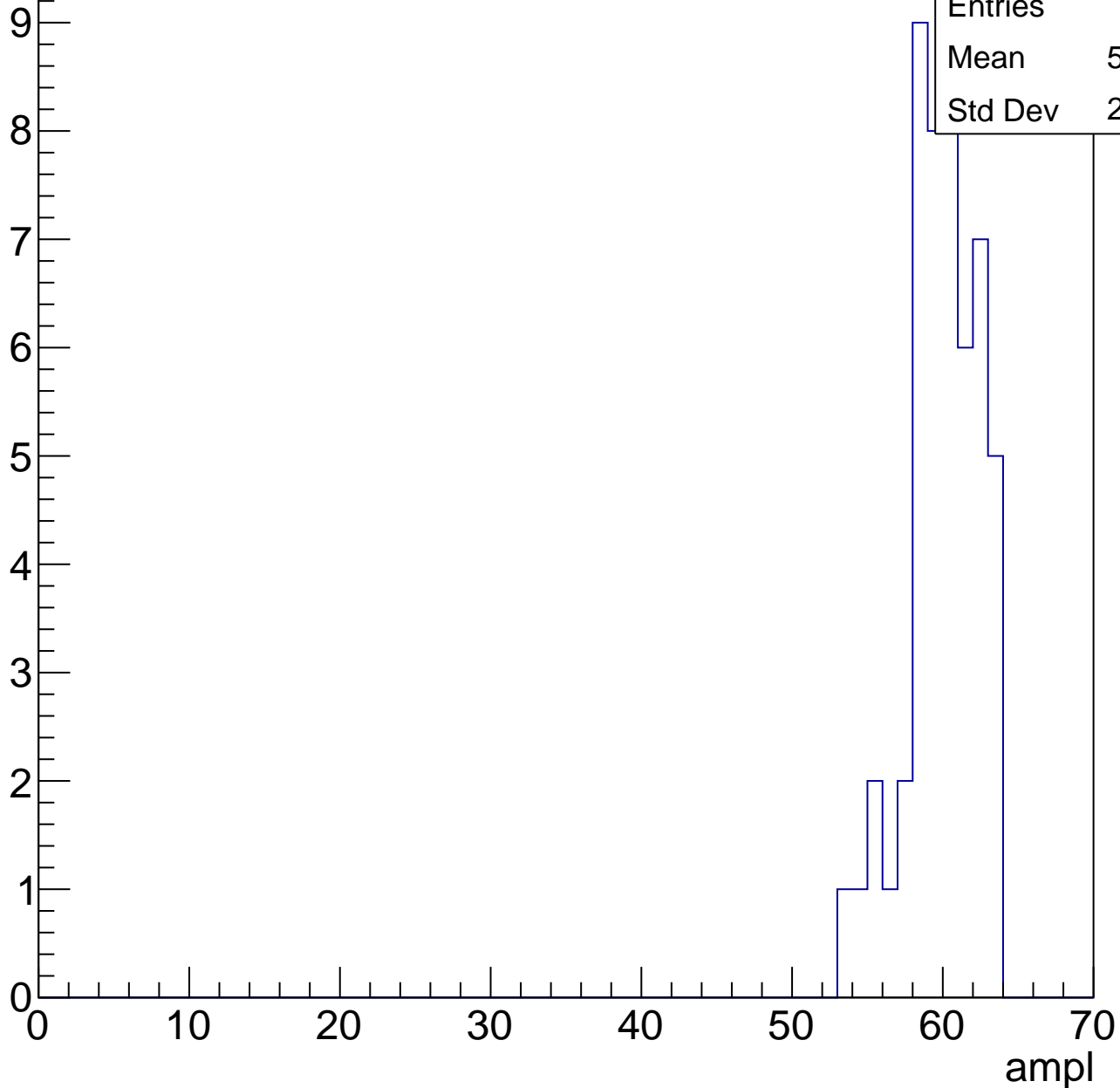
Entries	53
Mean	53.94
Std Dev	3.384



B1L103S, U20-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



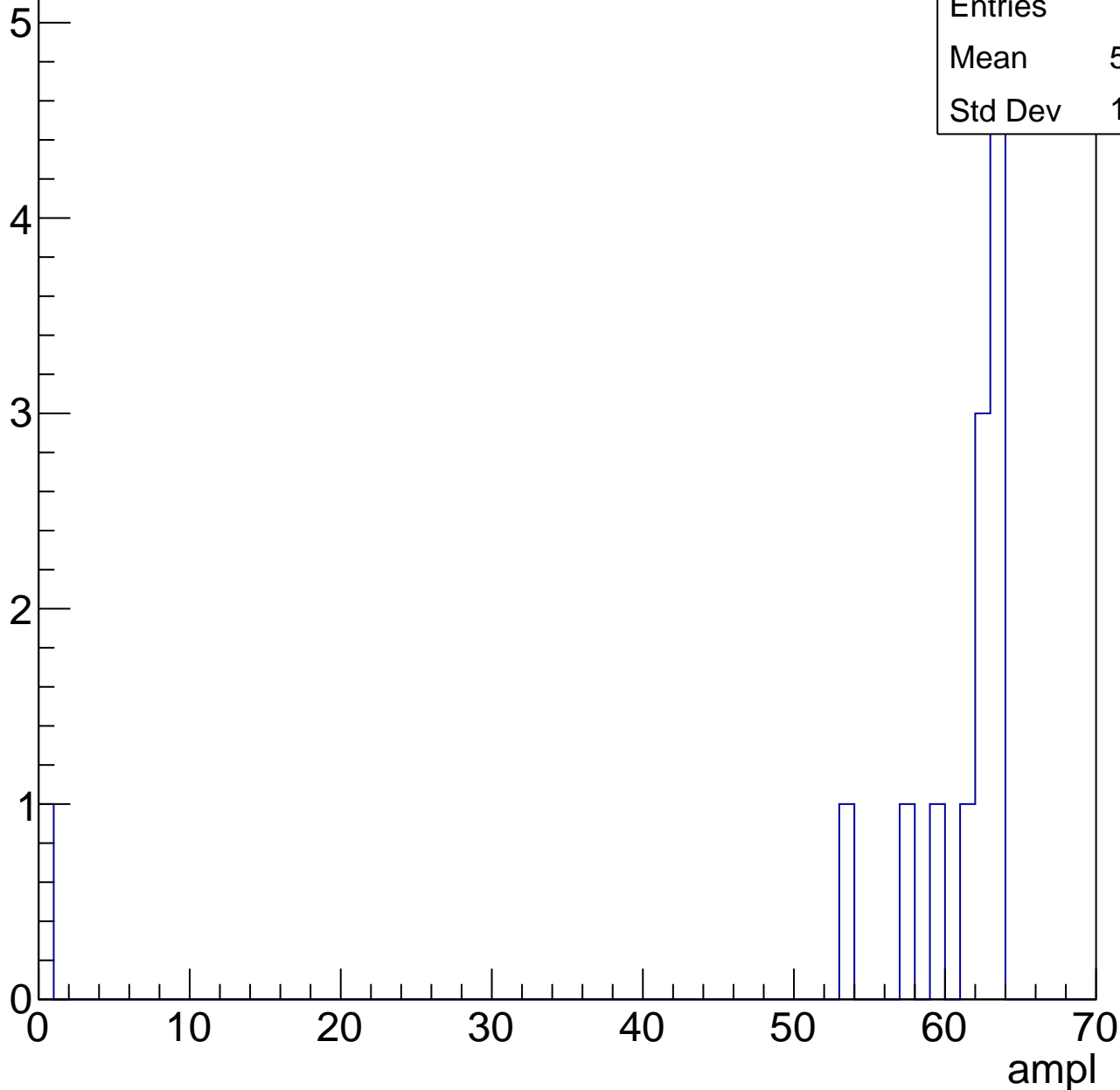
Entries	51
Mean	59.53
Std Dev	2.346

B1L103S, U20-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	56.23
Std Dev	16.48



B1L103S, U20-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch41, adc0

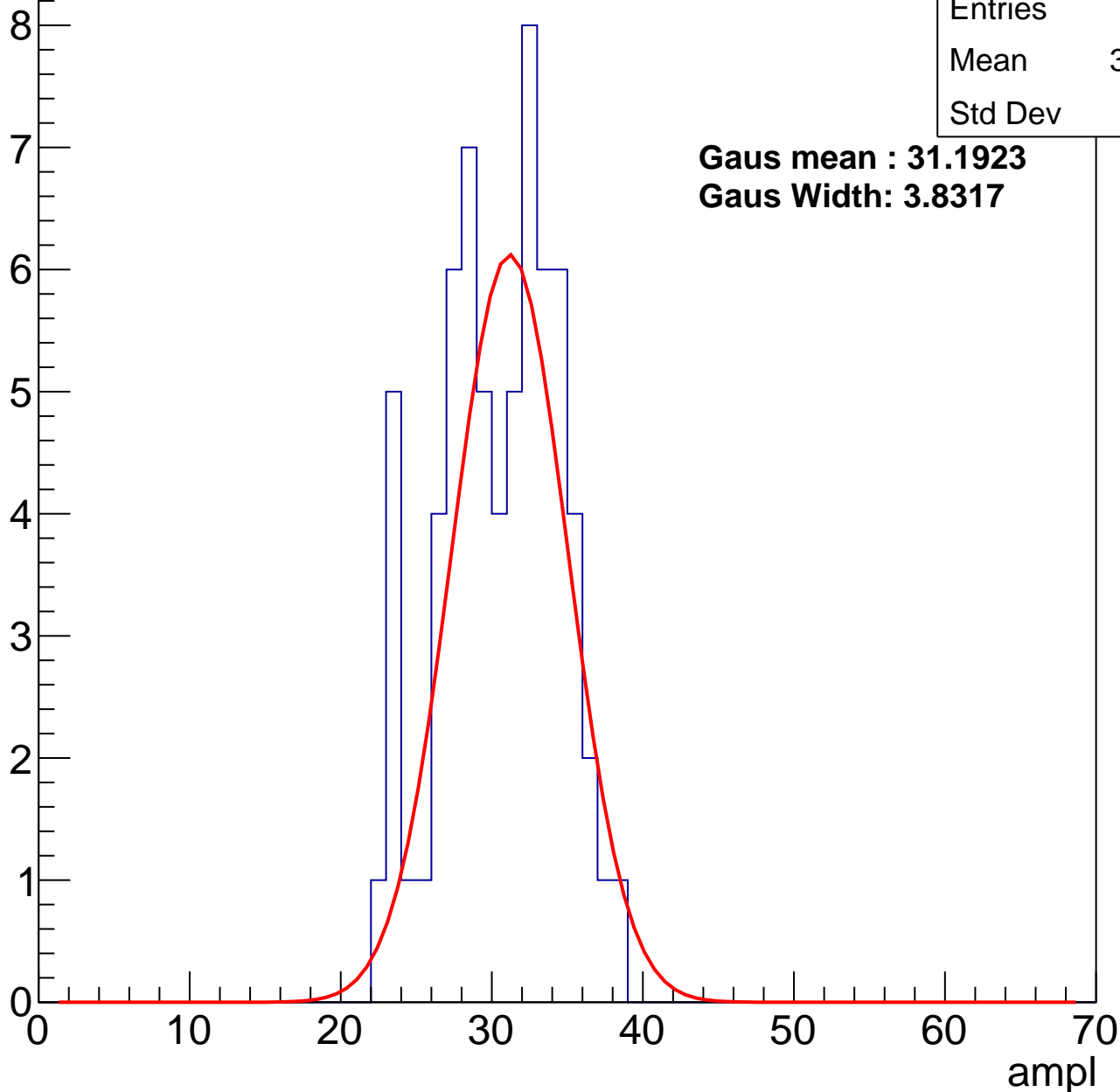
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.04
Std Dev	3.85

Gaus mean : 31.1923

Gaus Width: 3.8317



B1L103S, U20-ch41, adc1

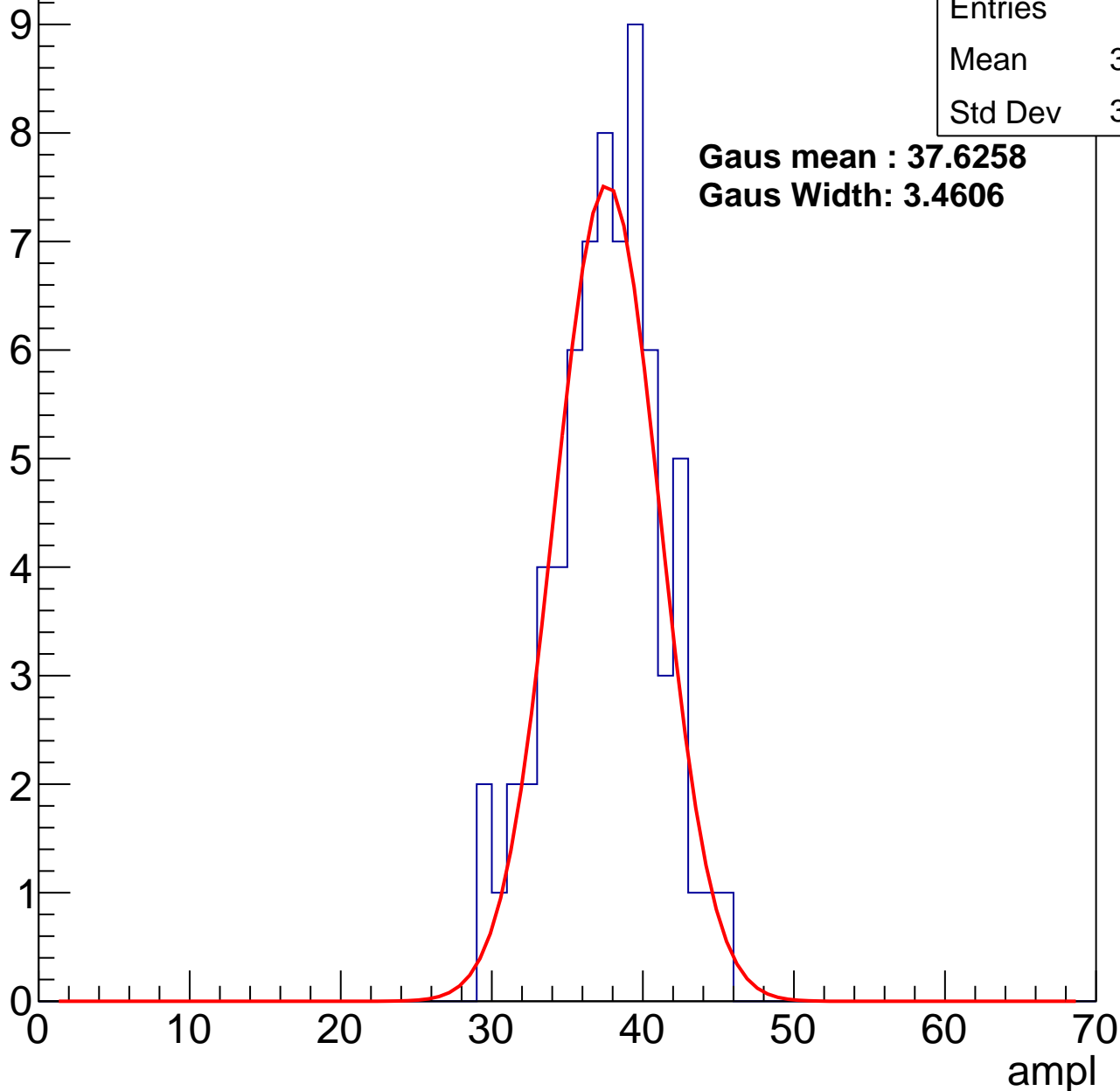
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.13
Std Dev	3.505

Gaus mean : 37.6258

Gaus Width: 3.4606



B1L103S, U20-ch41, adc2

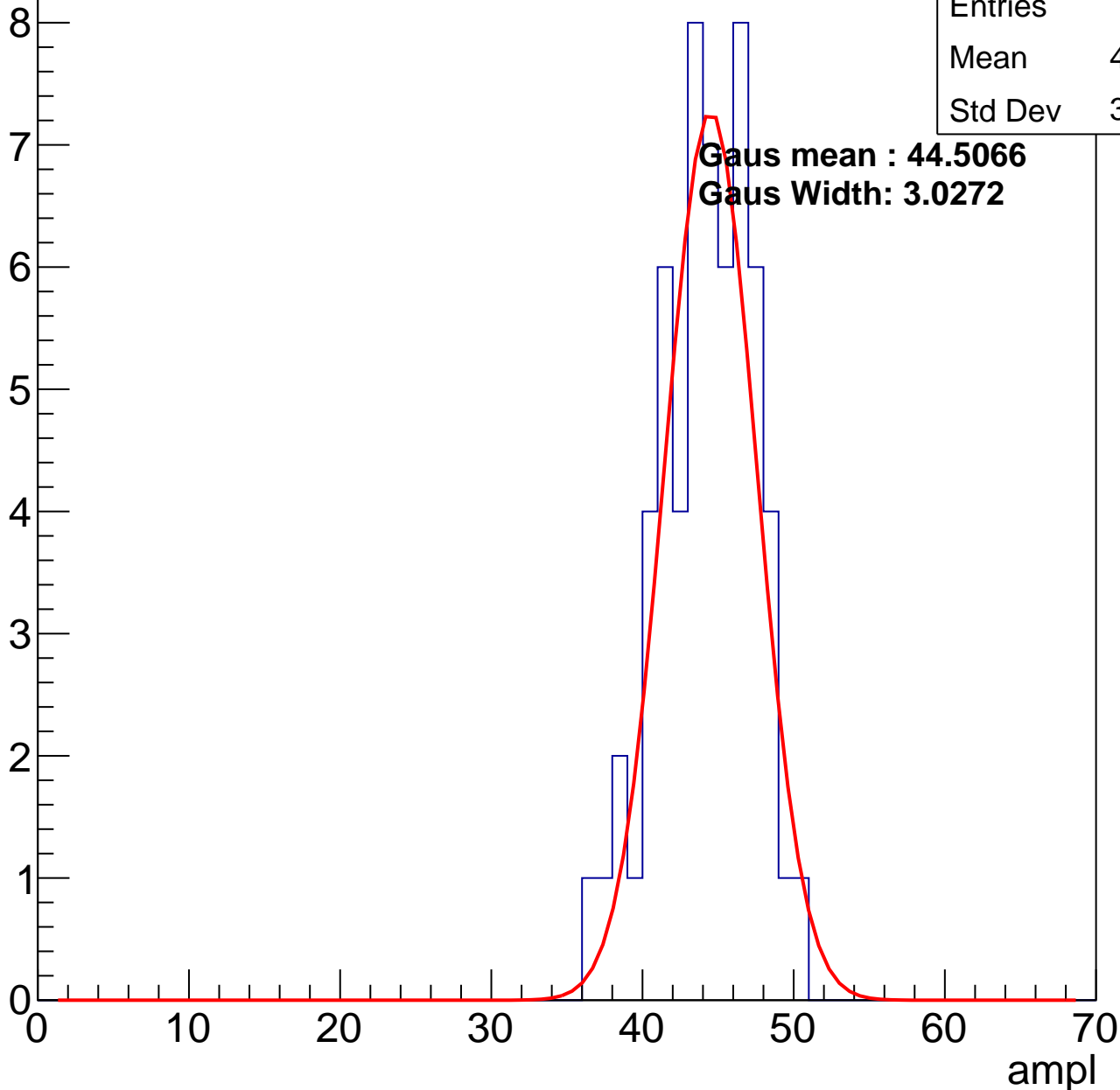
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.75
Std Dev	3.069

Gaus mean : 44.5066

Gaus Width: 3.0272

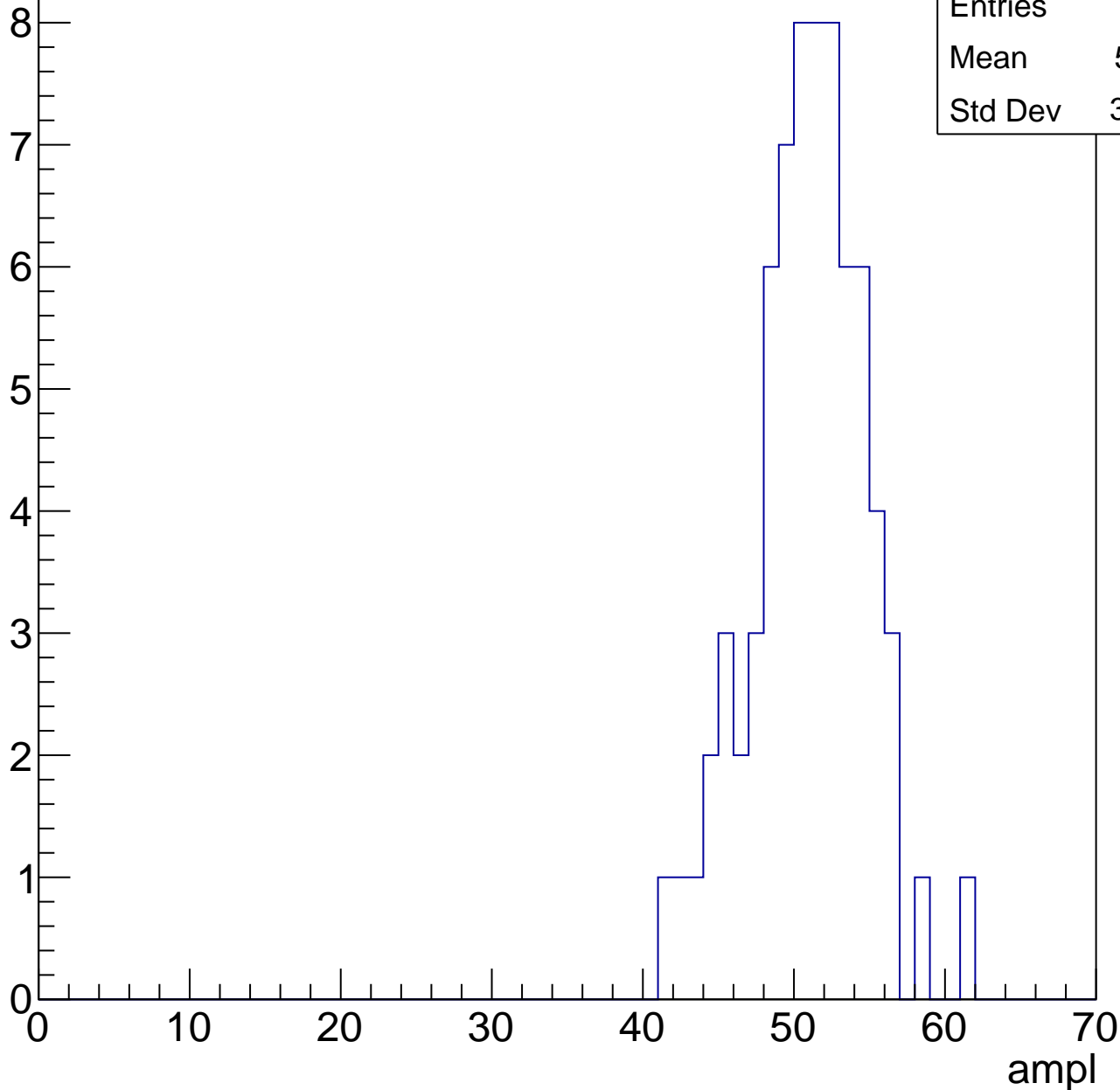


B1L103S, U20-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.51
Std Dev	3.745

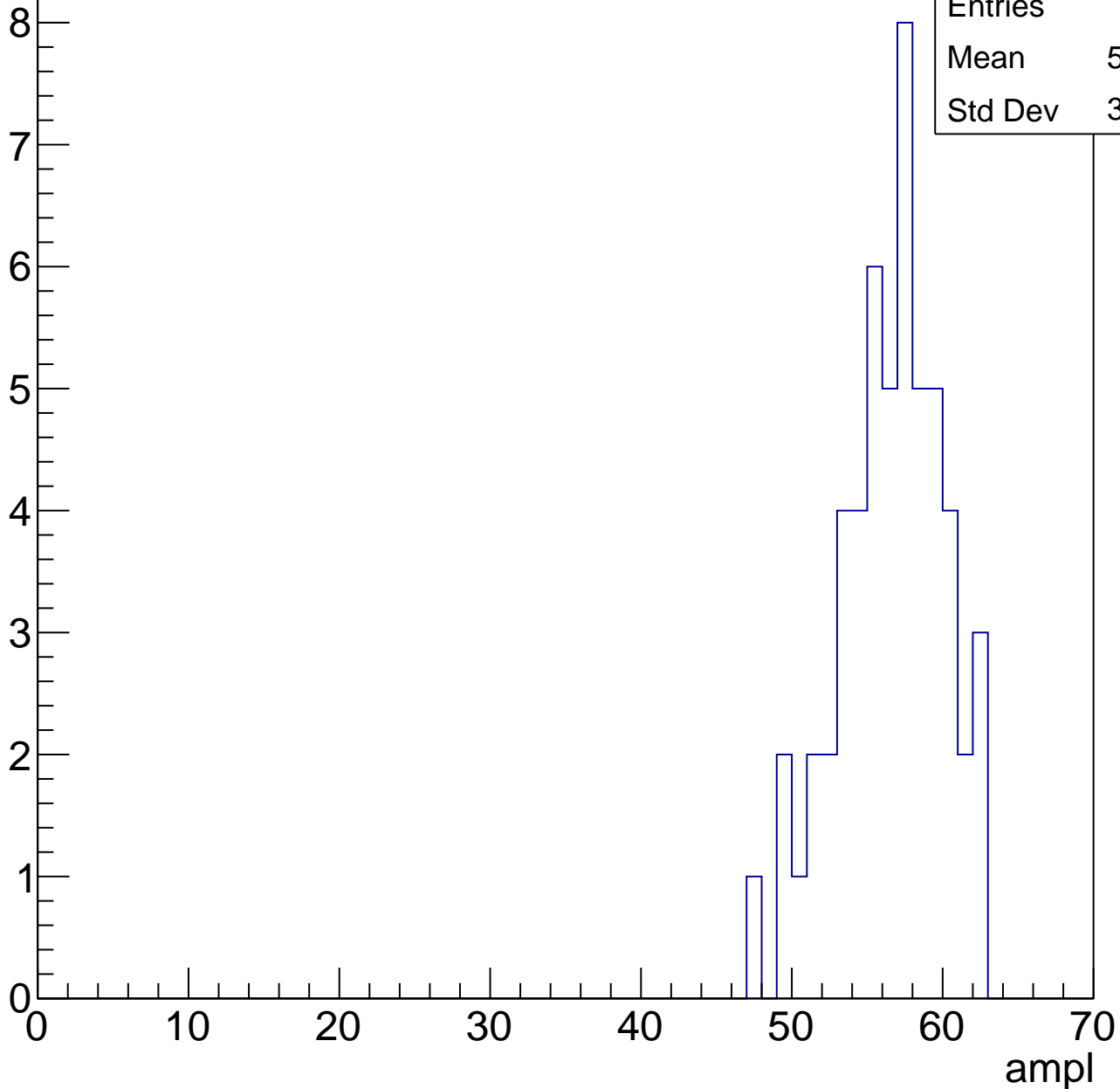


B1L103S, U20-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

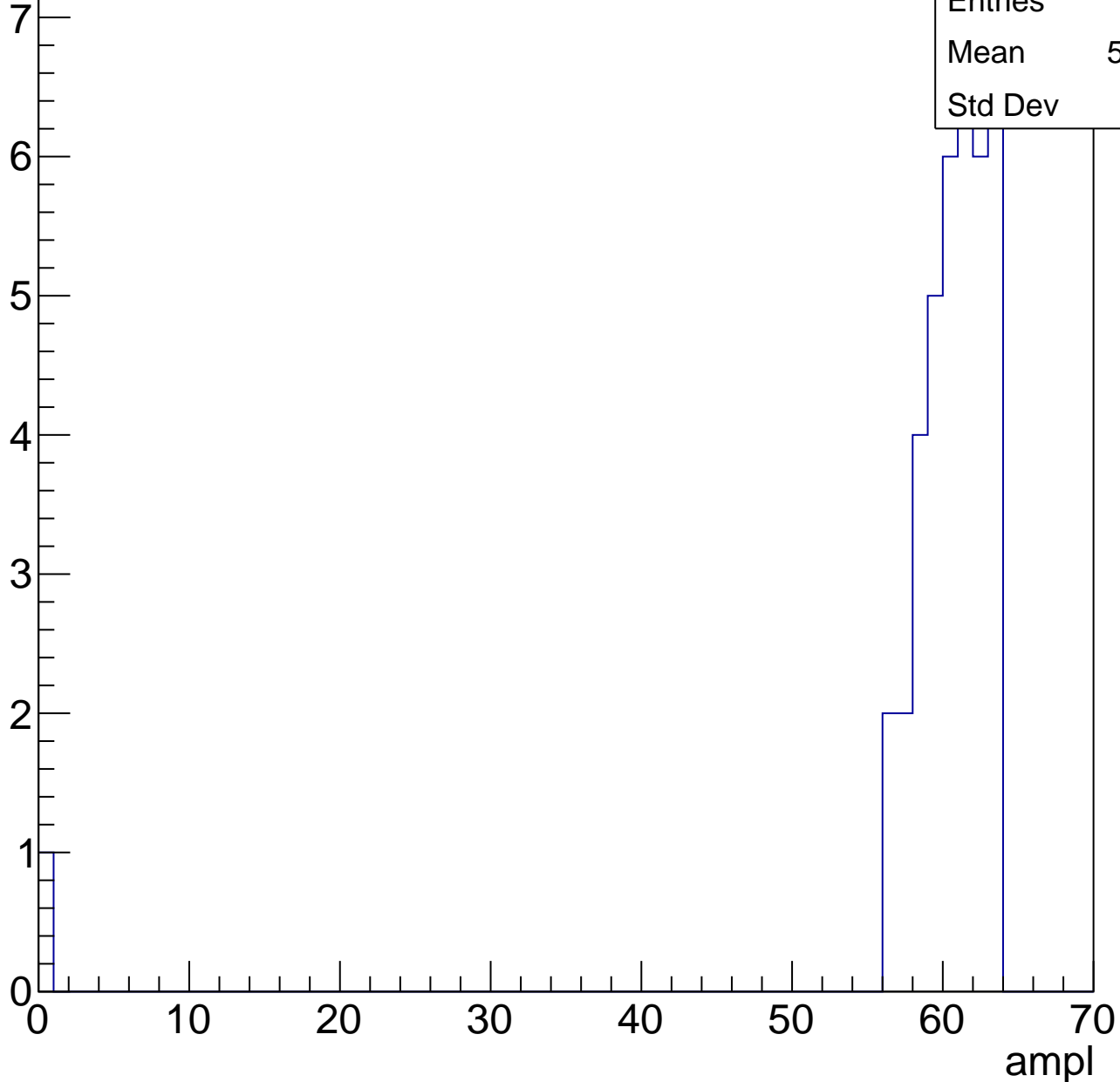
Entries	54
Mean	56.07
Std Dev	3.469



B1L103S, U20-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

B1L103S, U20-ch42, adc0

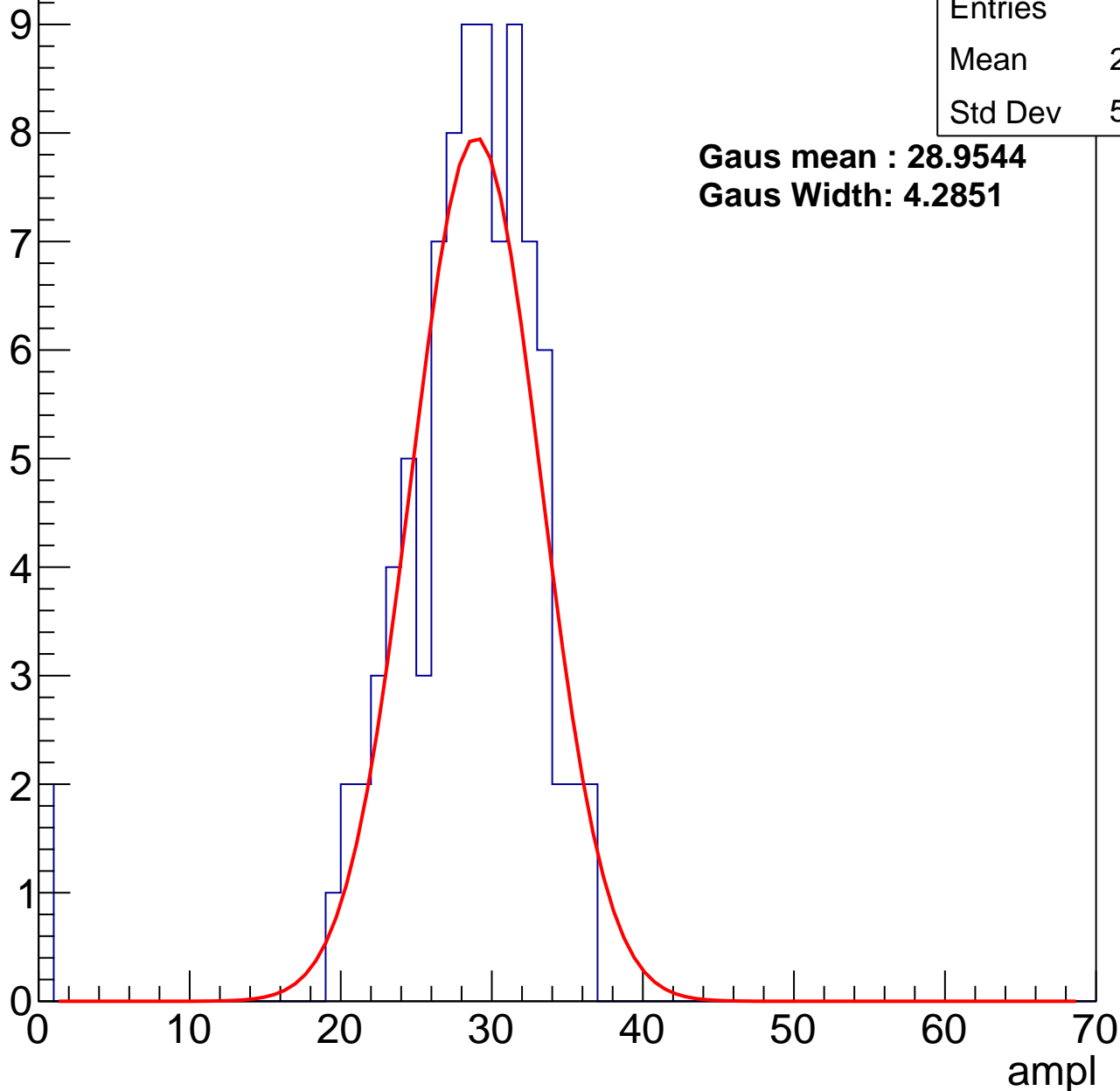
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	27.62
Std Dev	5.668

Gaus mean : 28.9544

Gaus Width: 4.2851



B1L103S, U20-ch42, adc1

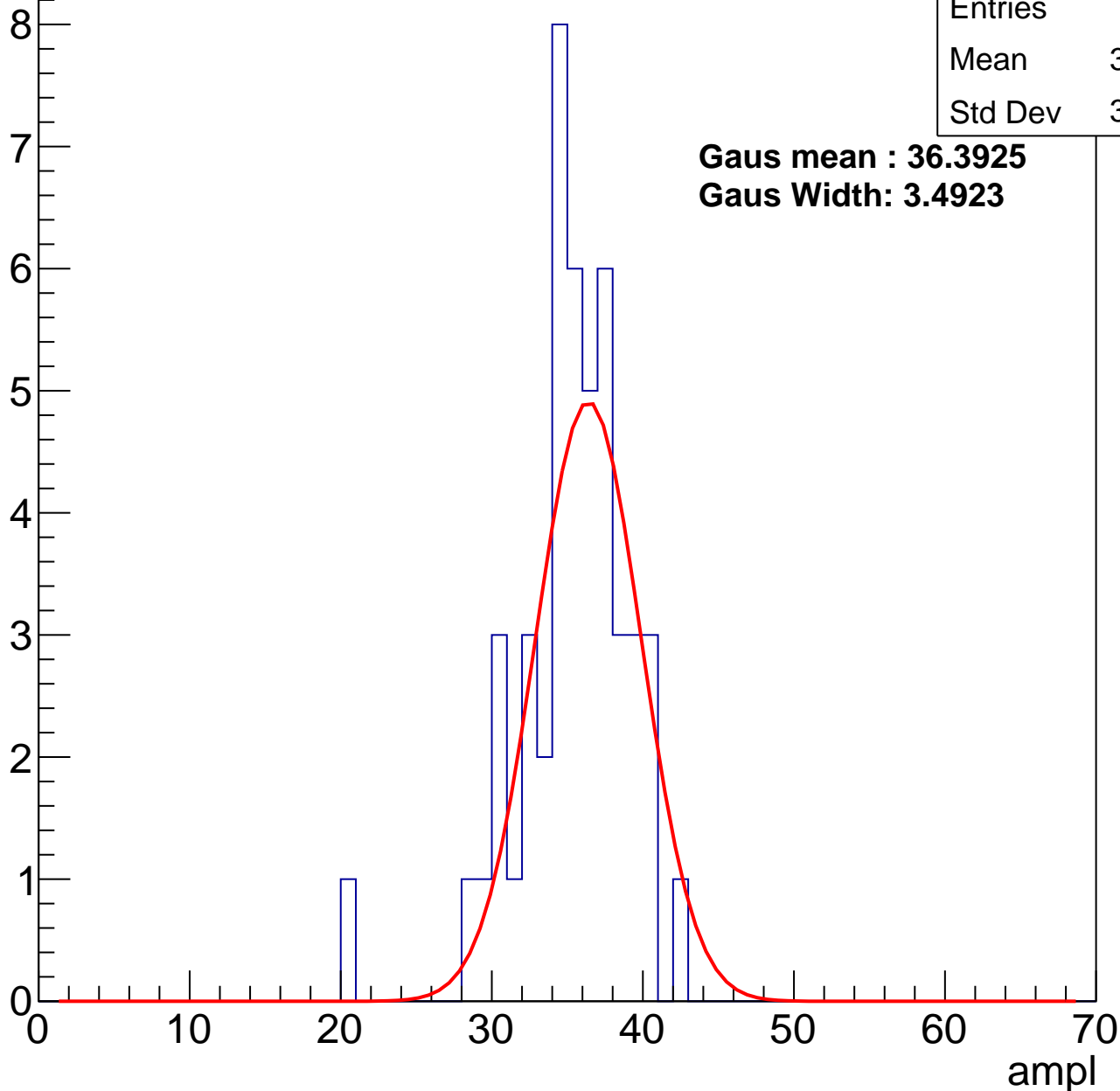
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	34.83
Std Dev	3.777

Gaus mean : 36.3925

Gaus Width: 3.4923



B1L103S, U20-ch42, adc2

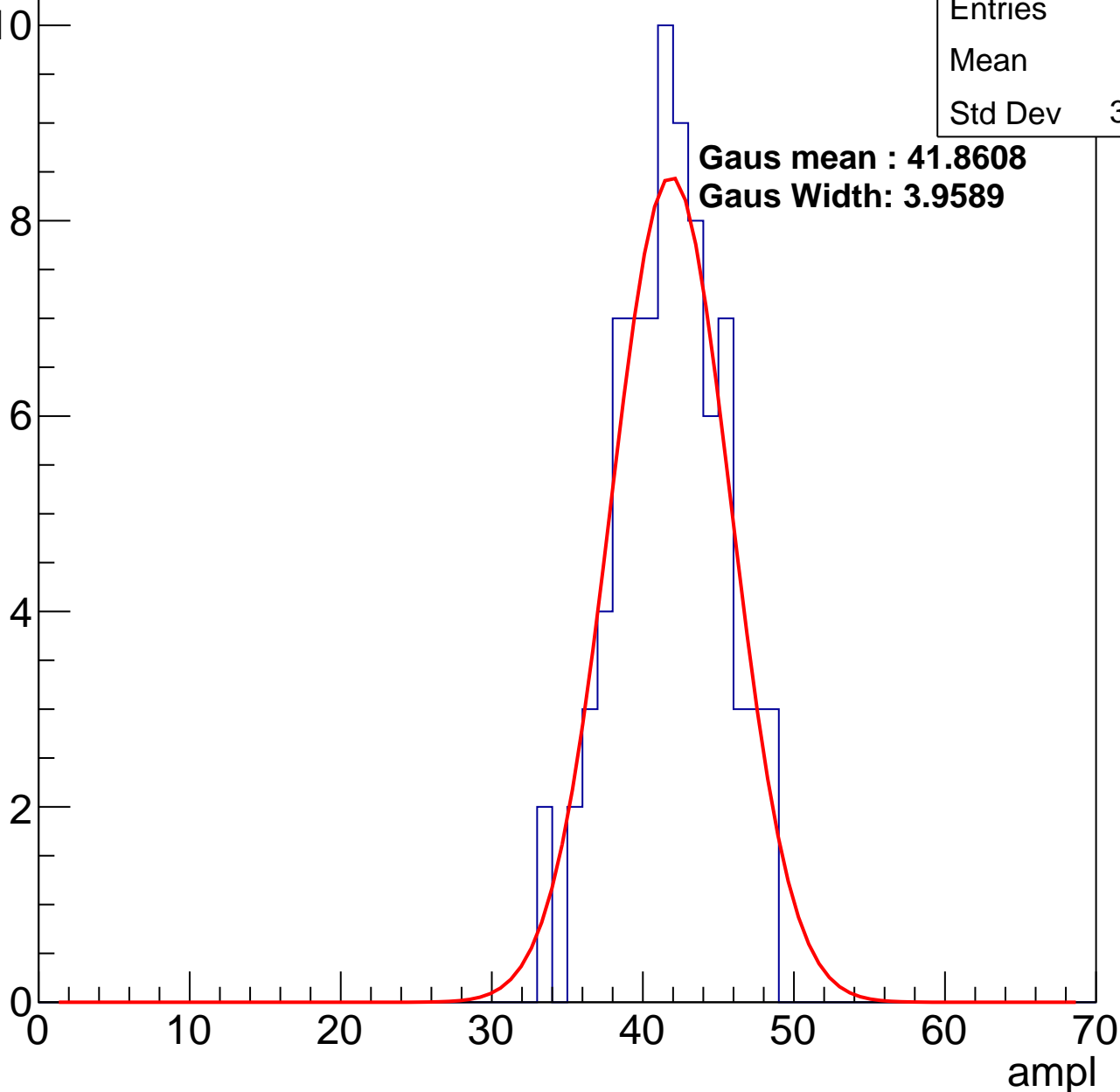
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	41.3
Std Dev	3.455

Gaus mean : 41.8608

Gaus Width: 3.9589

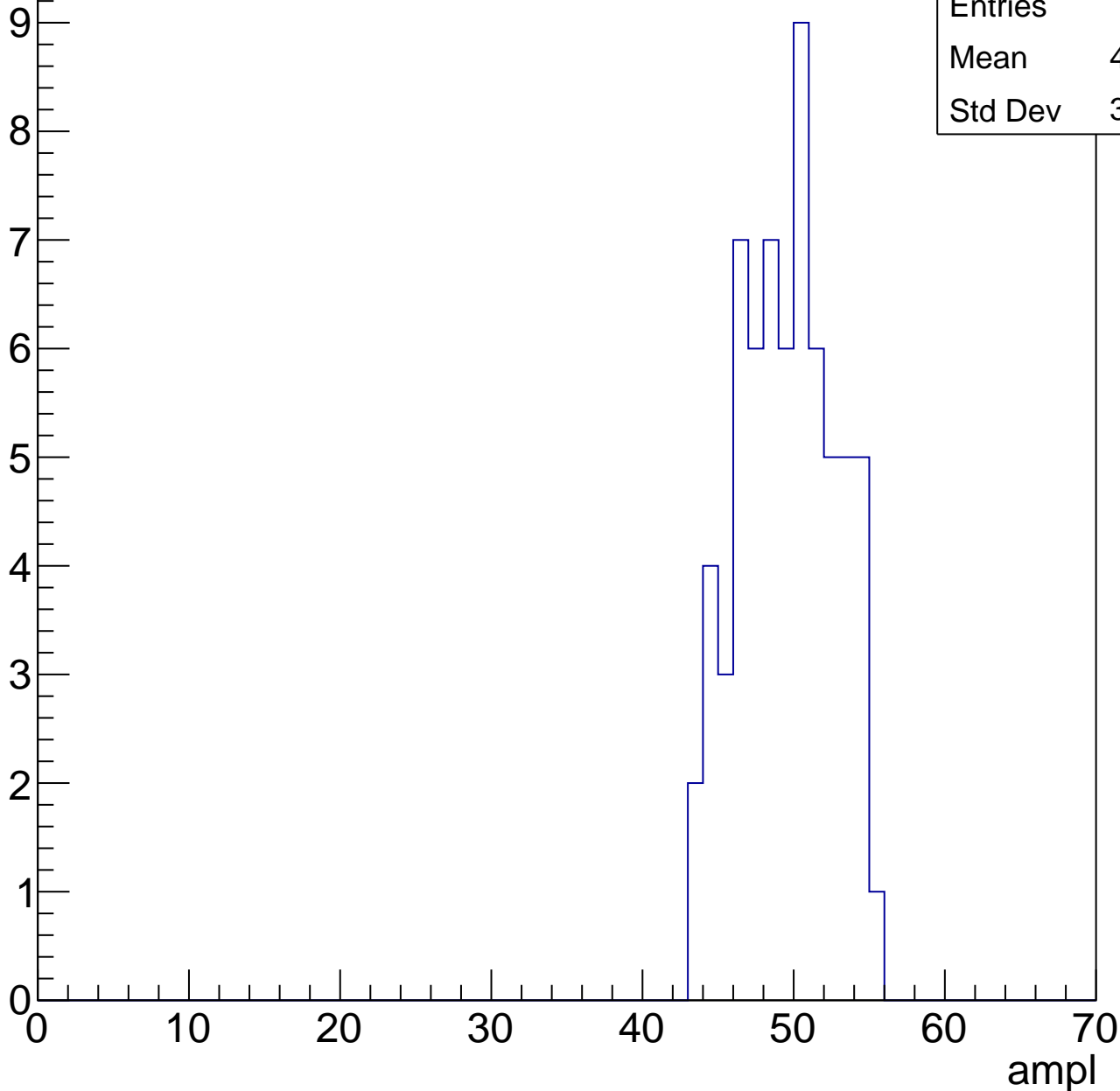


B1L103S, U20-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.05
Std Dev	3.097

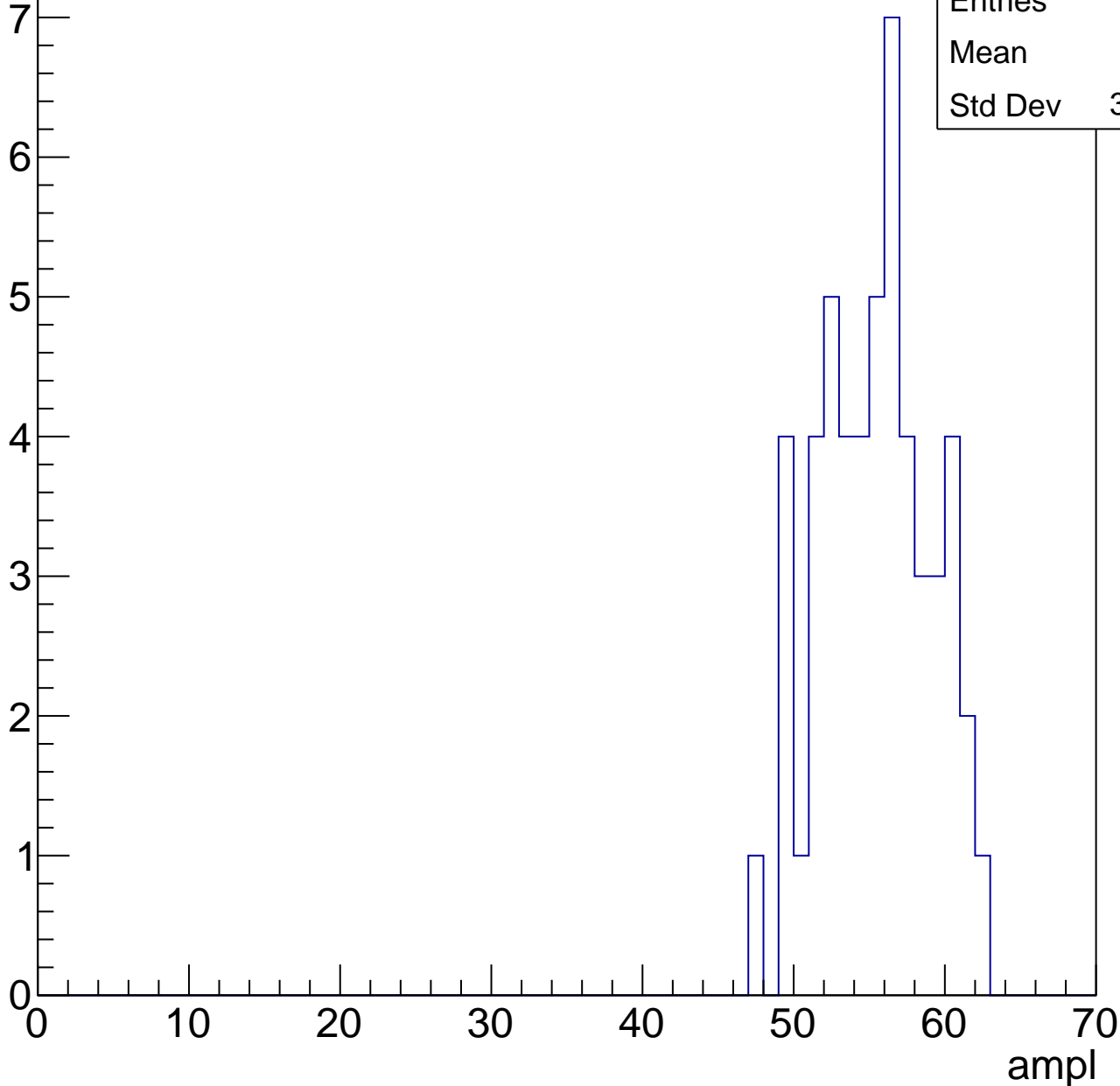


B1L103S, U20-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.9
Std Dev	3.618

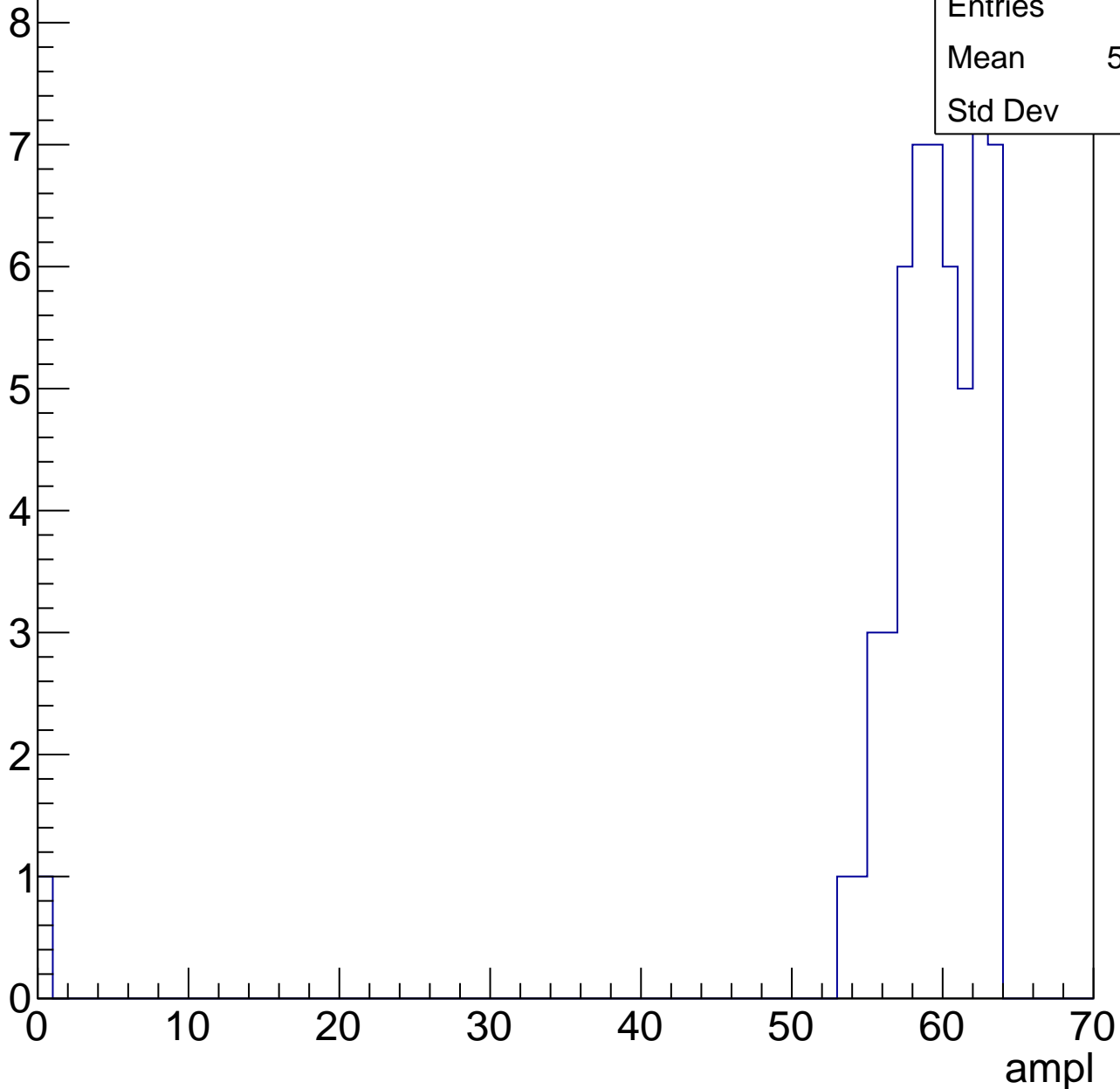


B1L103S, U20-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

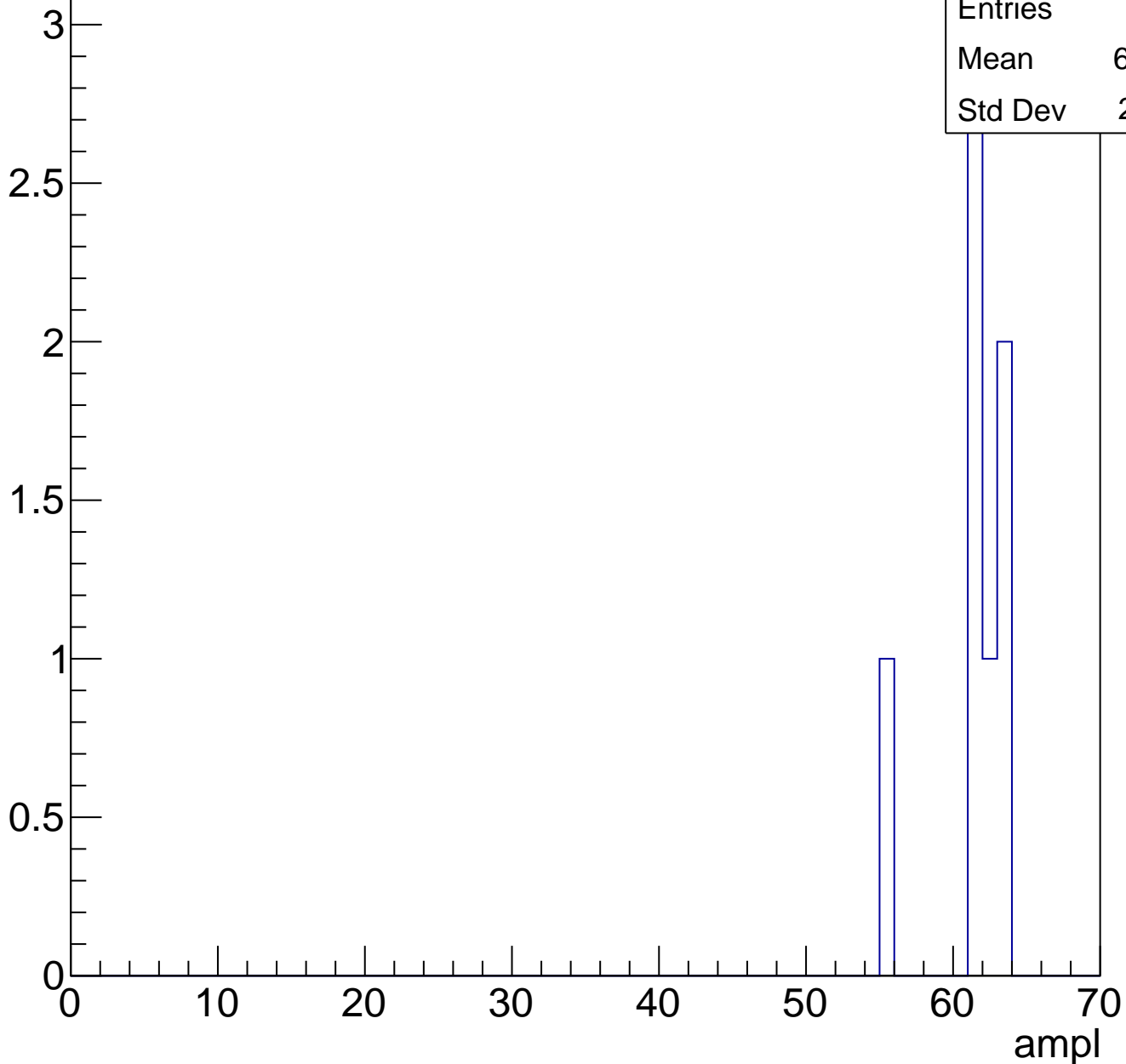
Entries	55
Mean	58.24
Std Dev	8.34



B1L103S, U20-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch43, adc0

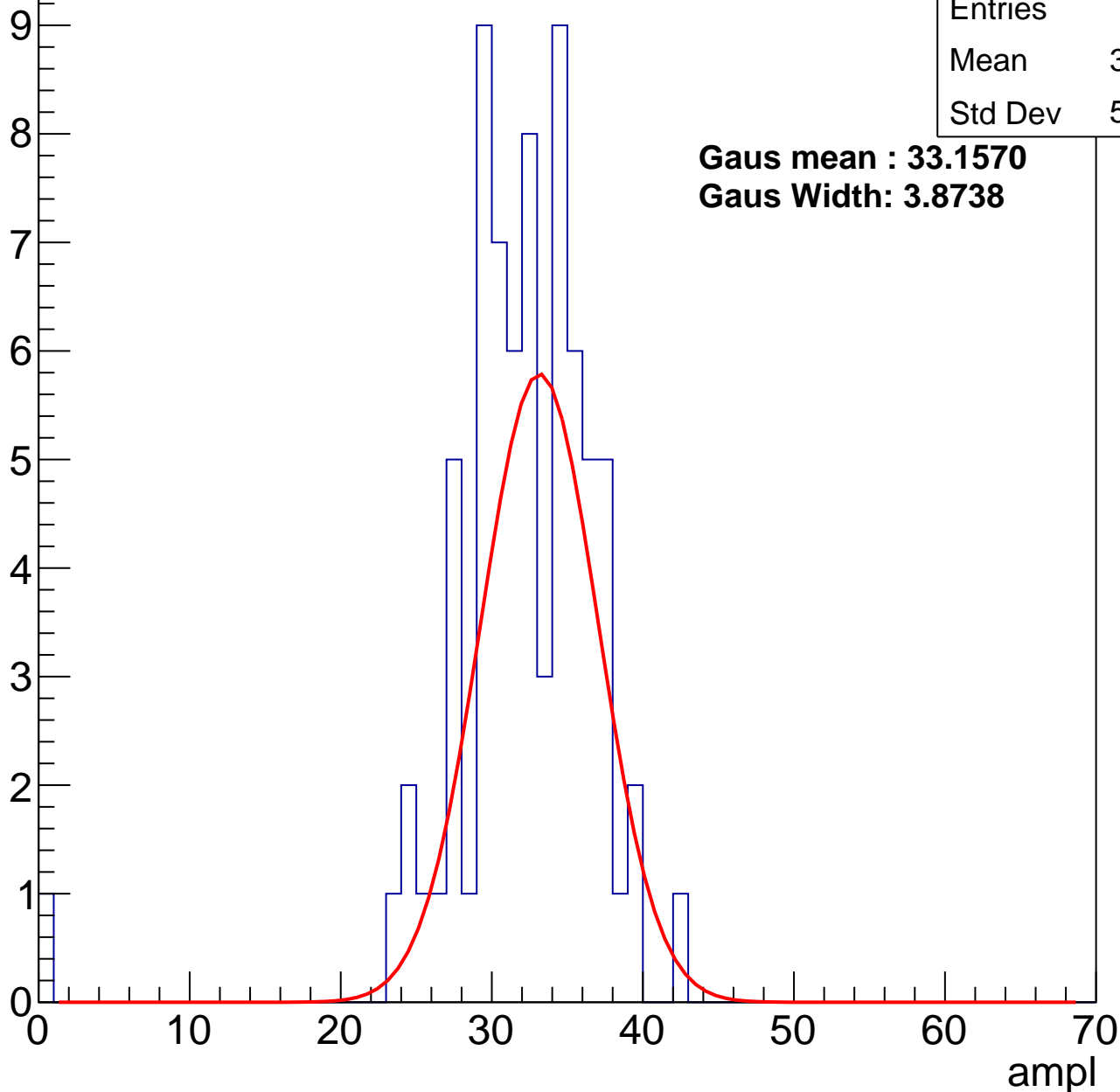
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	31.57
Std Dev	5.317

Gaus mean : 33.1570

Gaus Width: 3.8738



B1L103S, U20-ch43, adc1

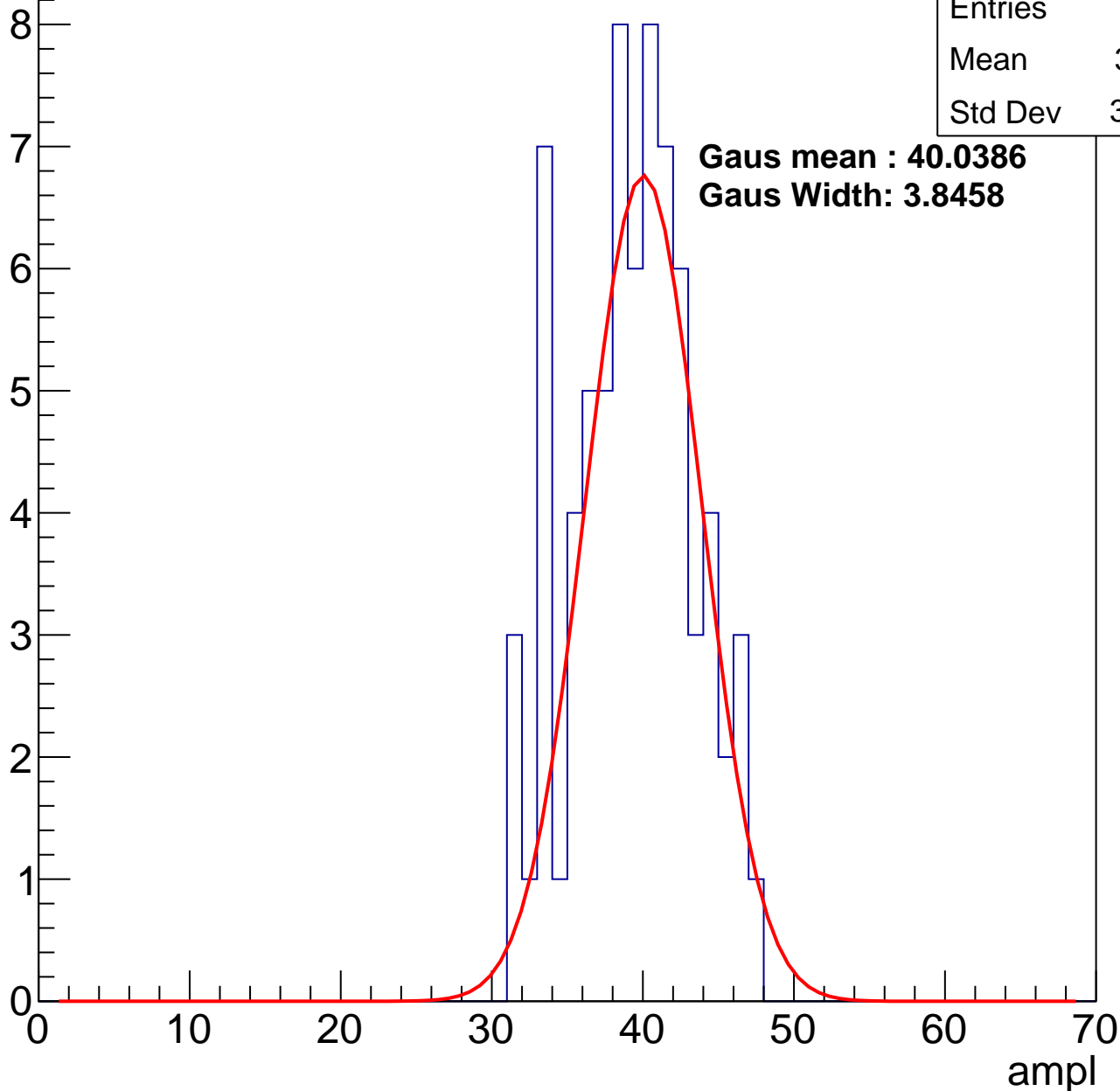
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	38.81
Std Dev	3.982

Gaus mean : 40.0386

Gaus Width: 3.8458



B1L103S, U20-ch43, adc2

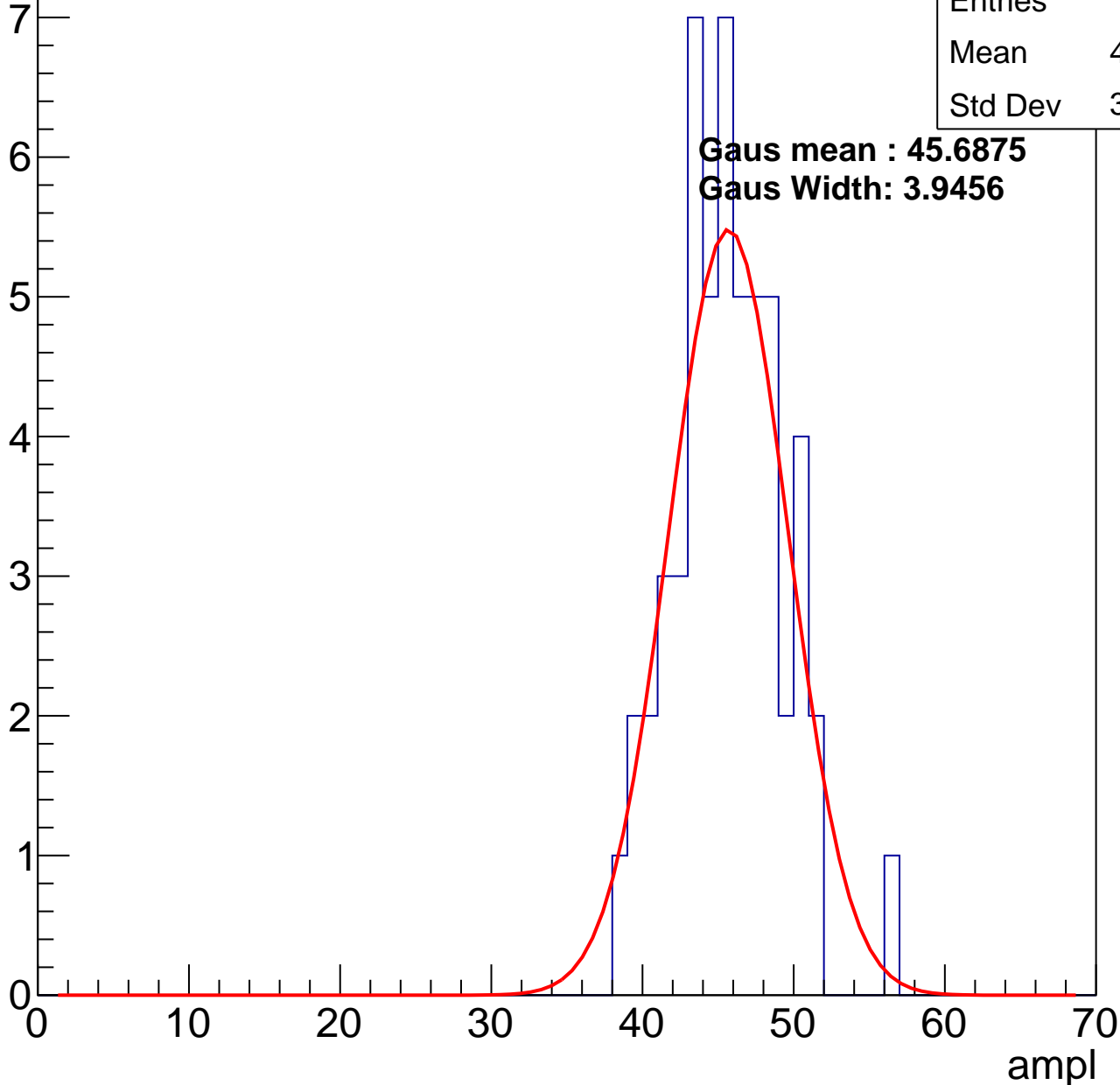
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	45.22
Std Dev	3.526

Gaus mean : 45.6875

Gaus Width: 3.9456

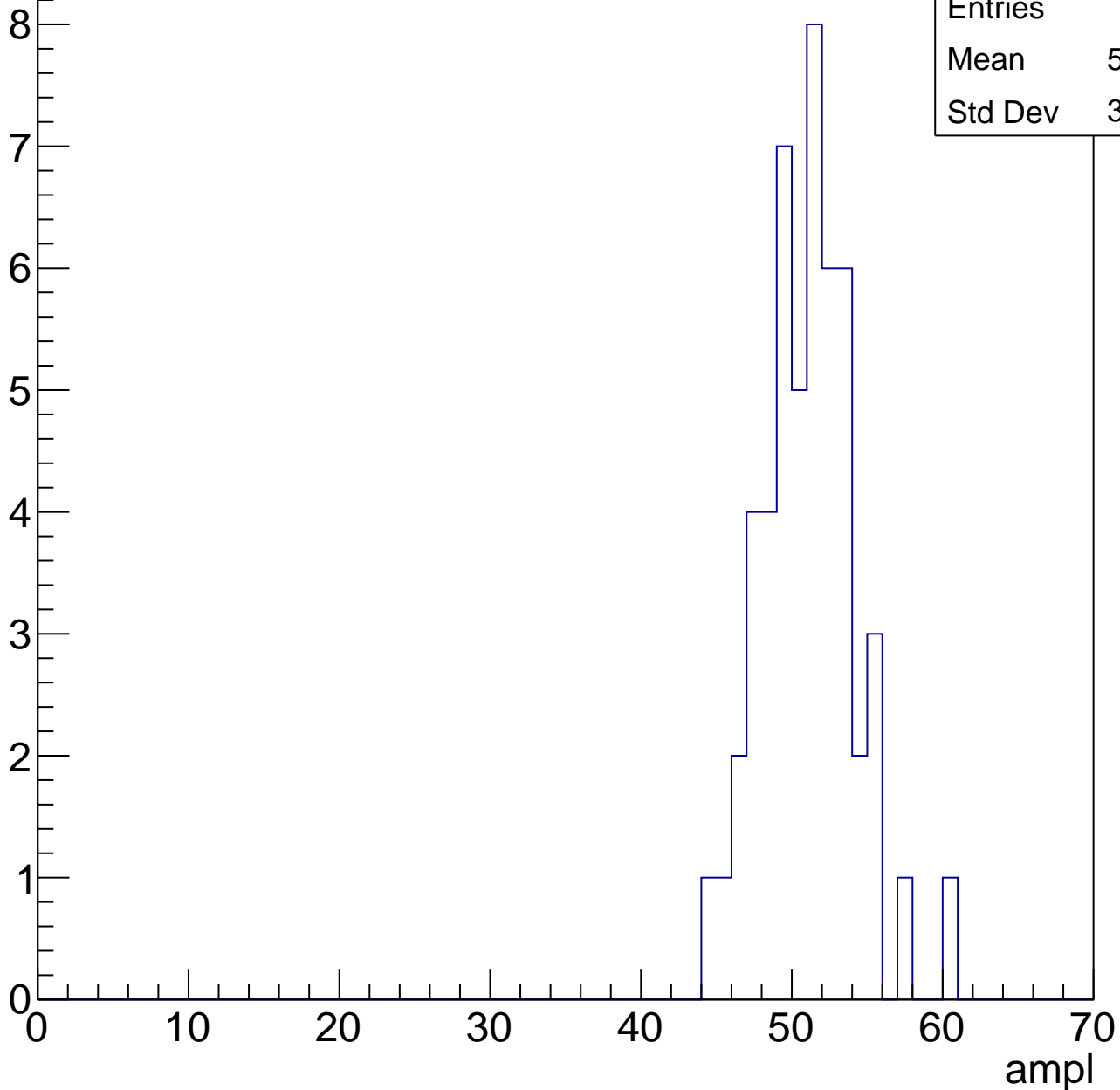


B1L103S, U20-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

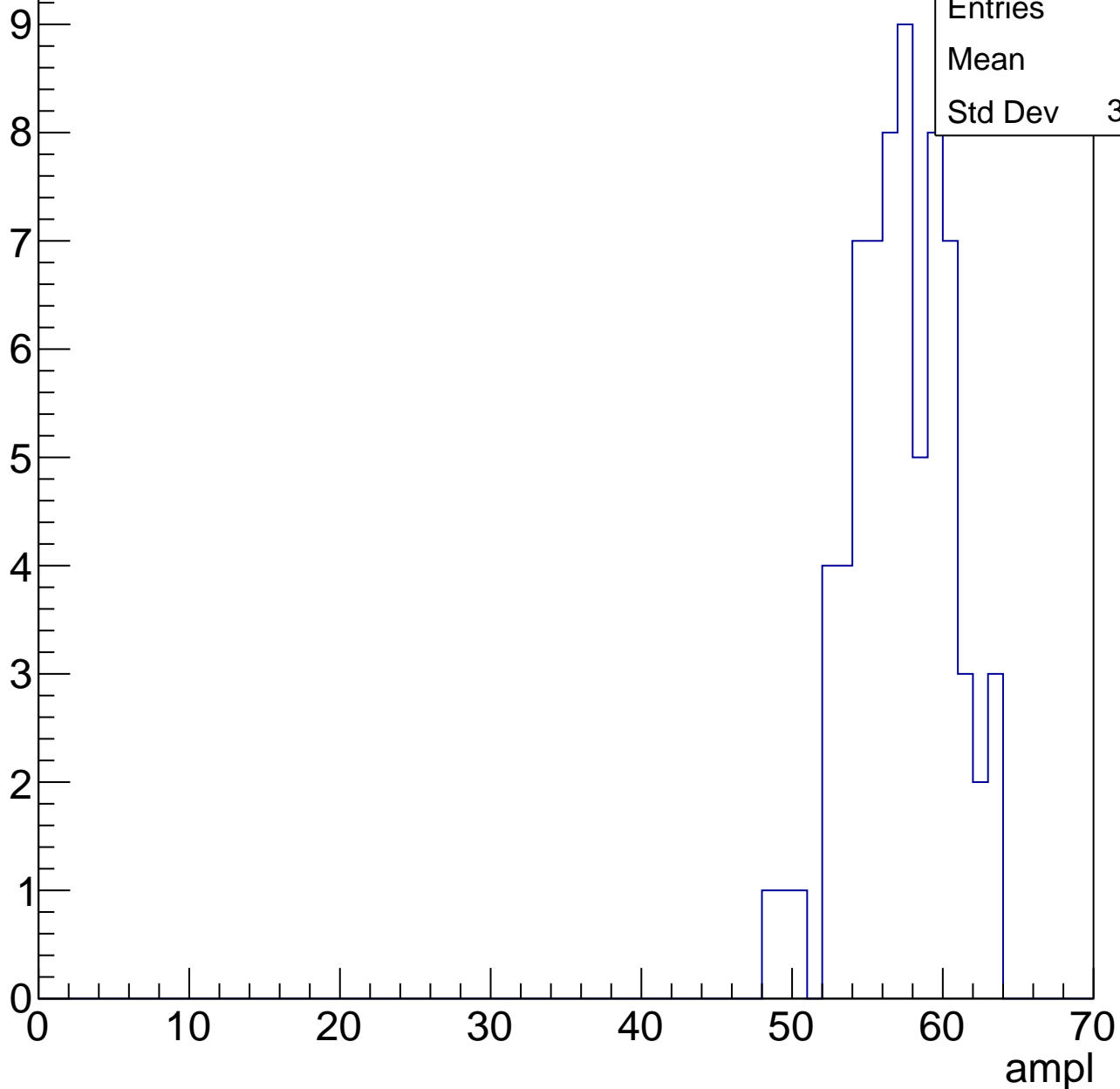
Entries	51
Mean	50.63
Std Dev	3.048



B1L103S, U20-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

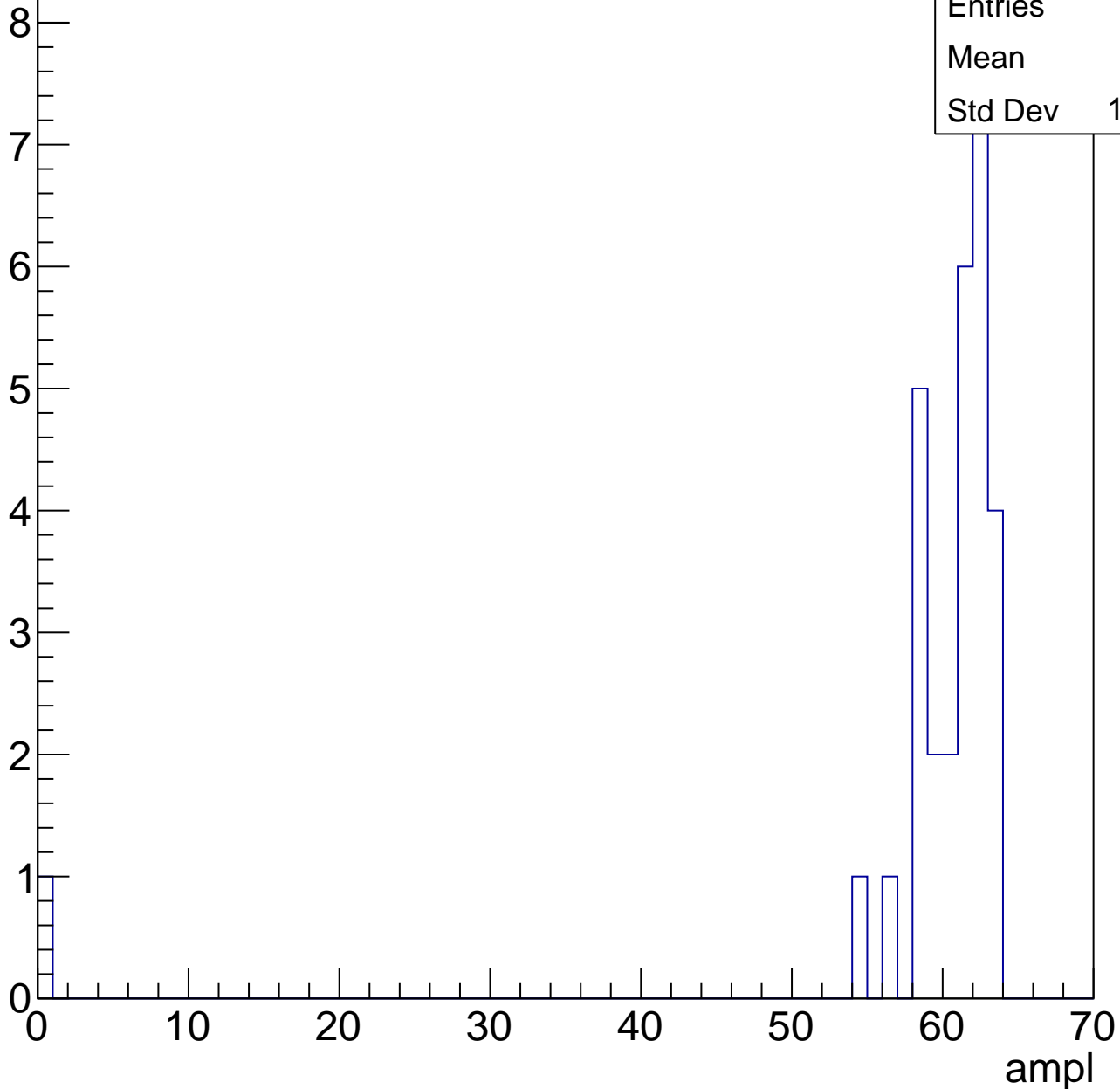


B1L103S, U20-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	58.4
Std Dev	11.06



B1L103S, U20-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch44, adc0

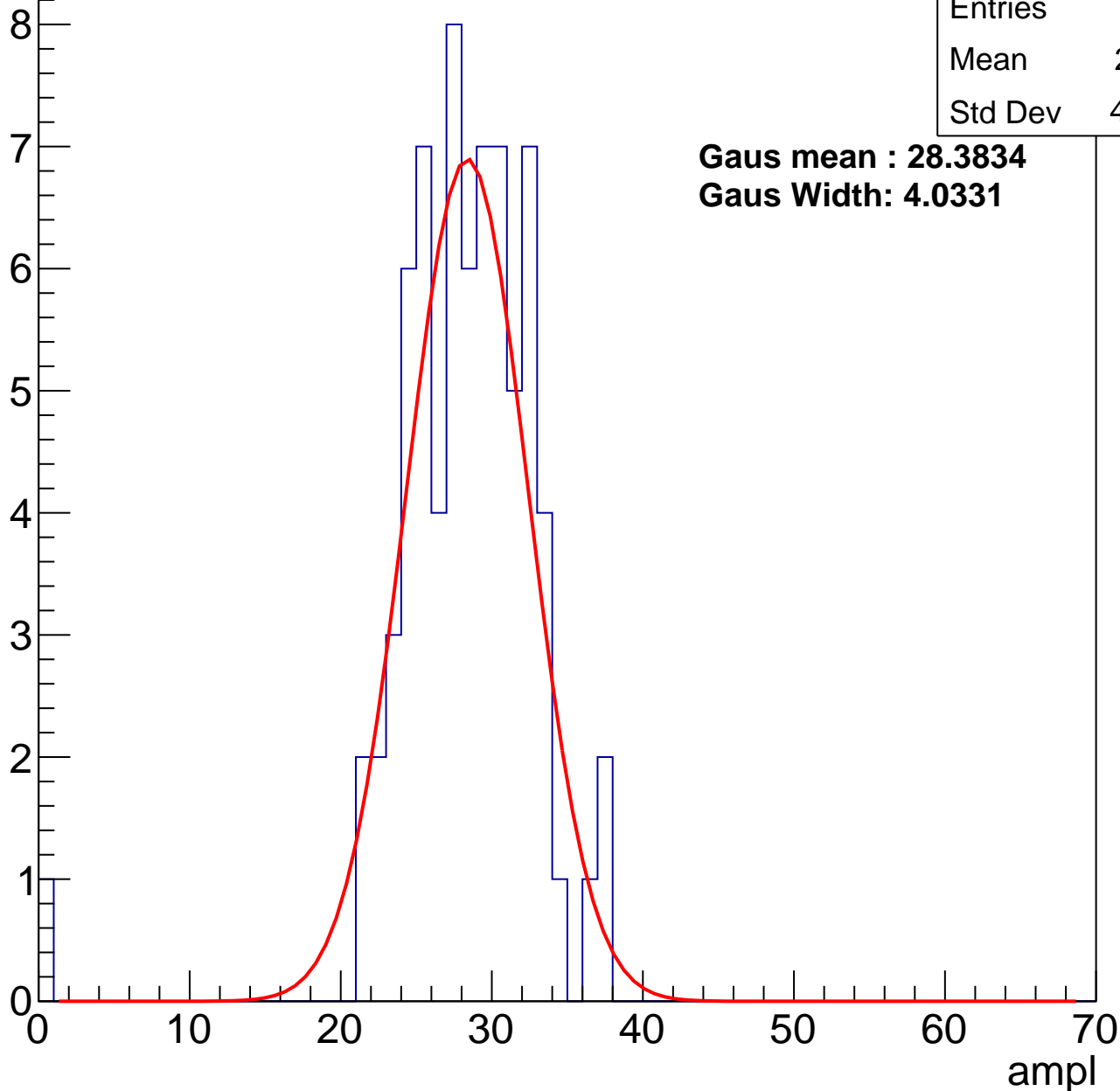
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.81
Std Dev	4.915

Gaus mean : 28.3834

Gaus Width: 4.0331



B1L103S, U20-ch44, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	34.94
Std Dev	3.532

Gaus mean : 36.2444

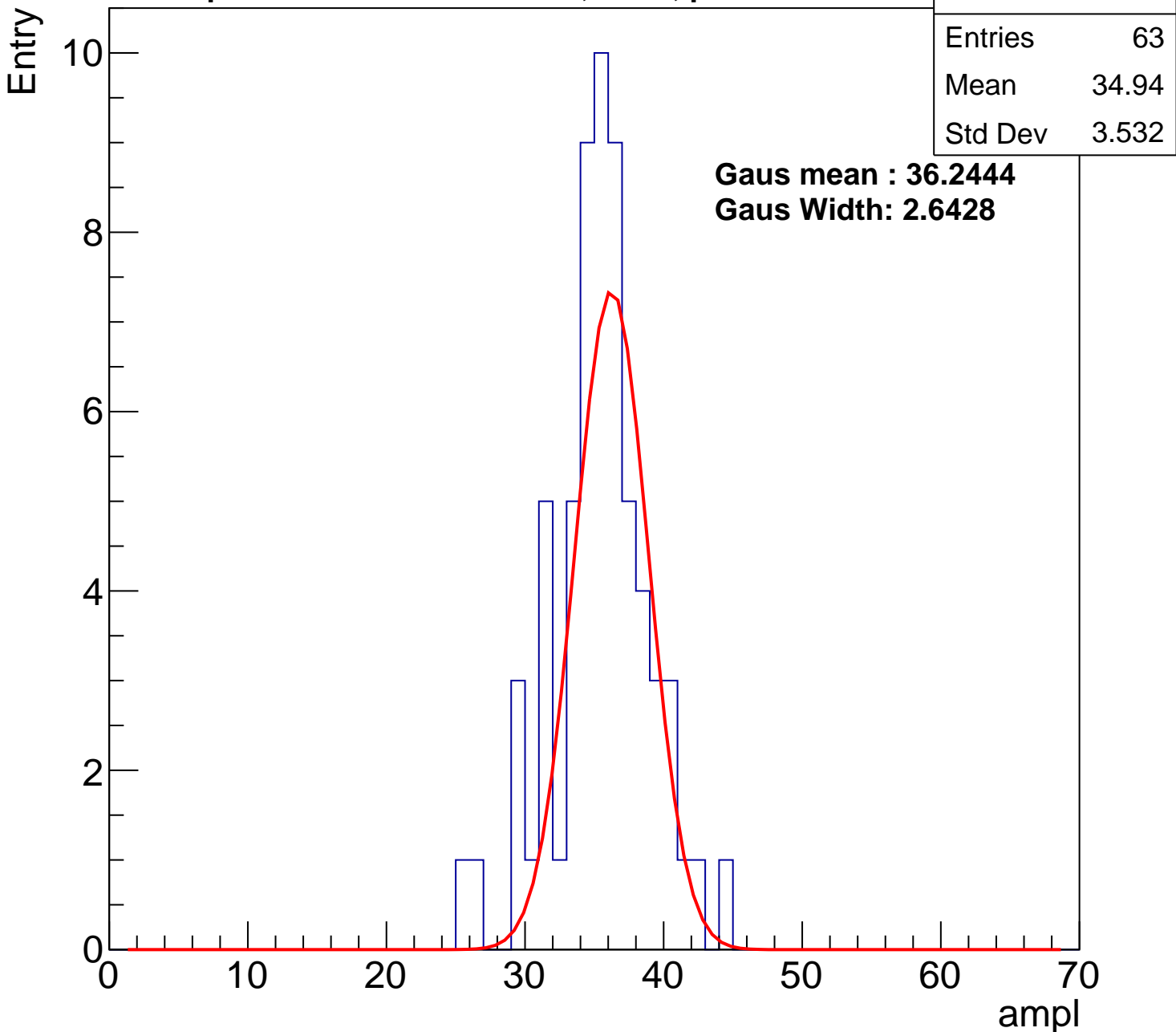
Gaus Width: 2.6428

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch44, adc2

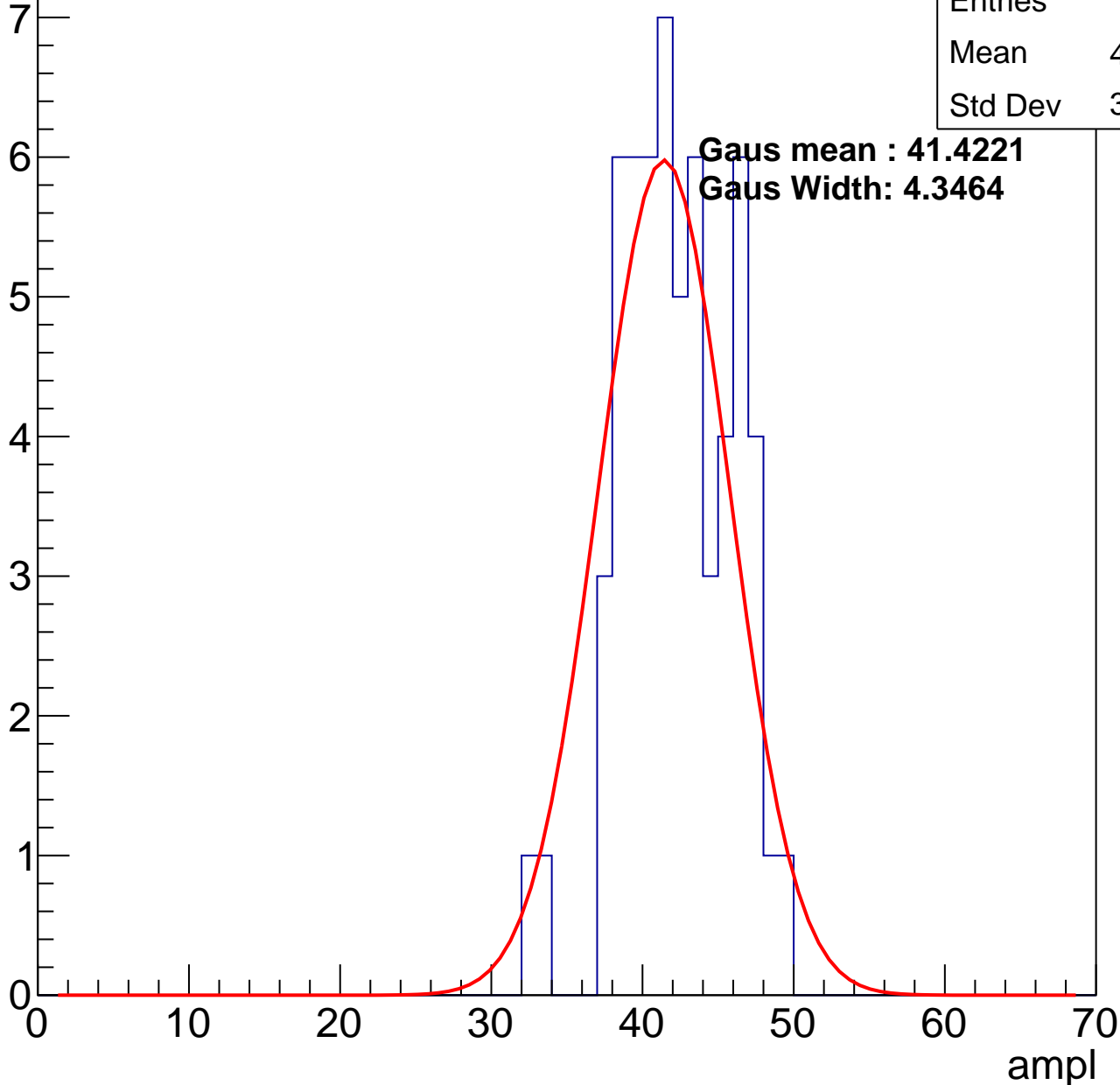
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	41.77
Std Dev	3.579

Gaus mean : 41.4221

Gaus Width: 4.3464

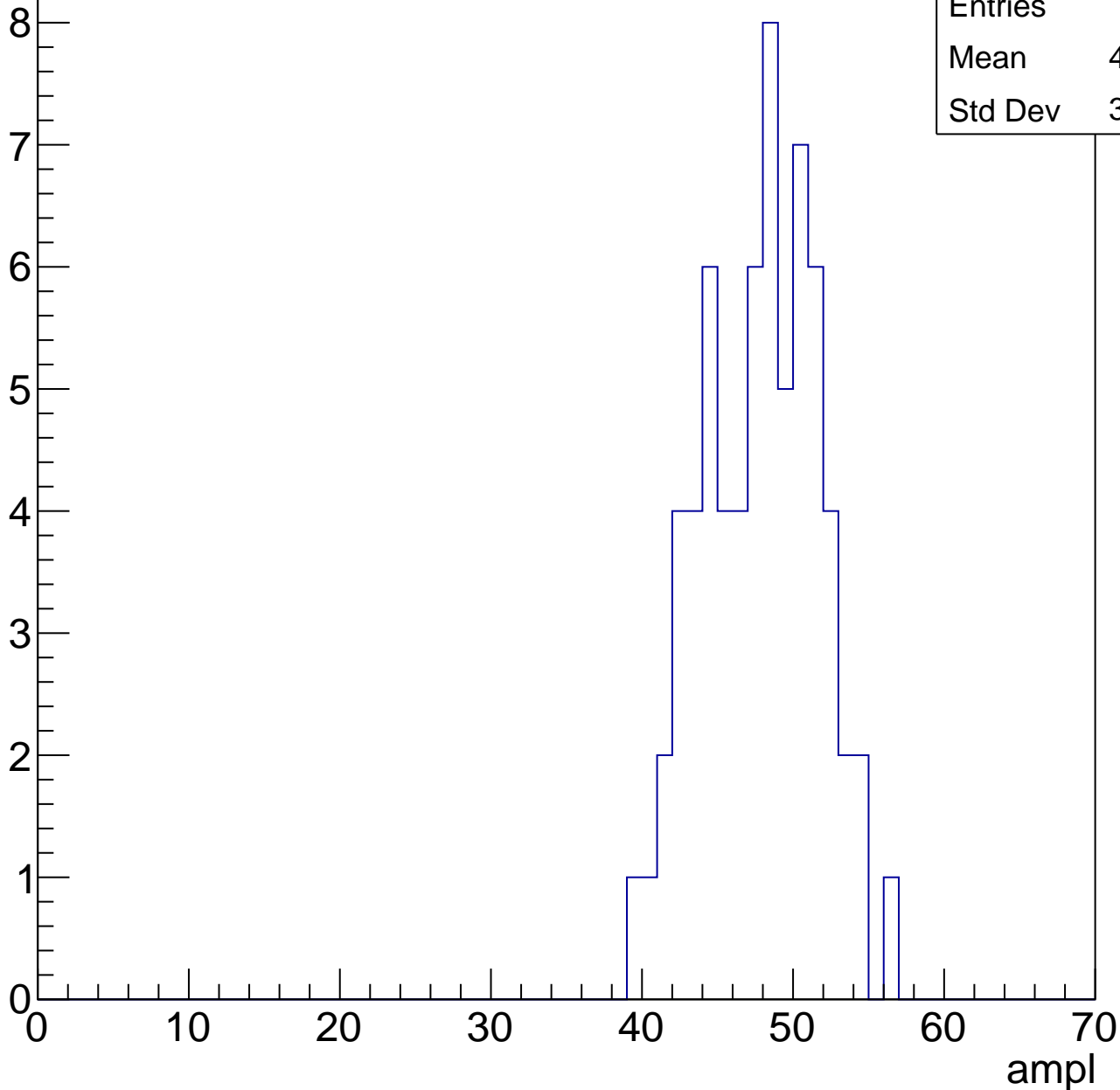


B1L103S, U20-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	47.37
Std Dev	3.769



B1L103S, U20-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	54.95
Std Dev	3.721

Entry

10

8

6

4

2

0

0

10

20

30

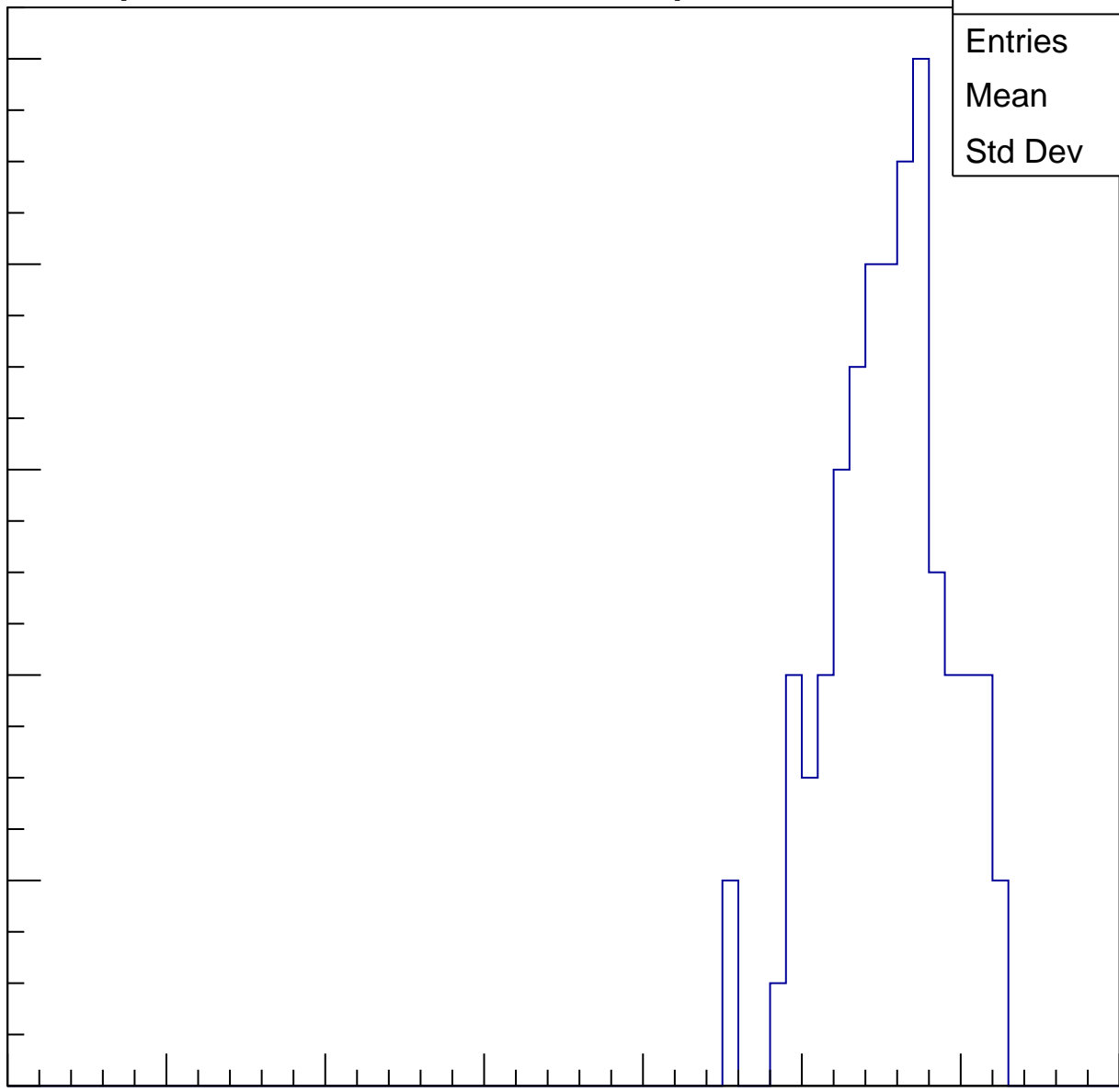
40

50

60

70

ampl

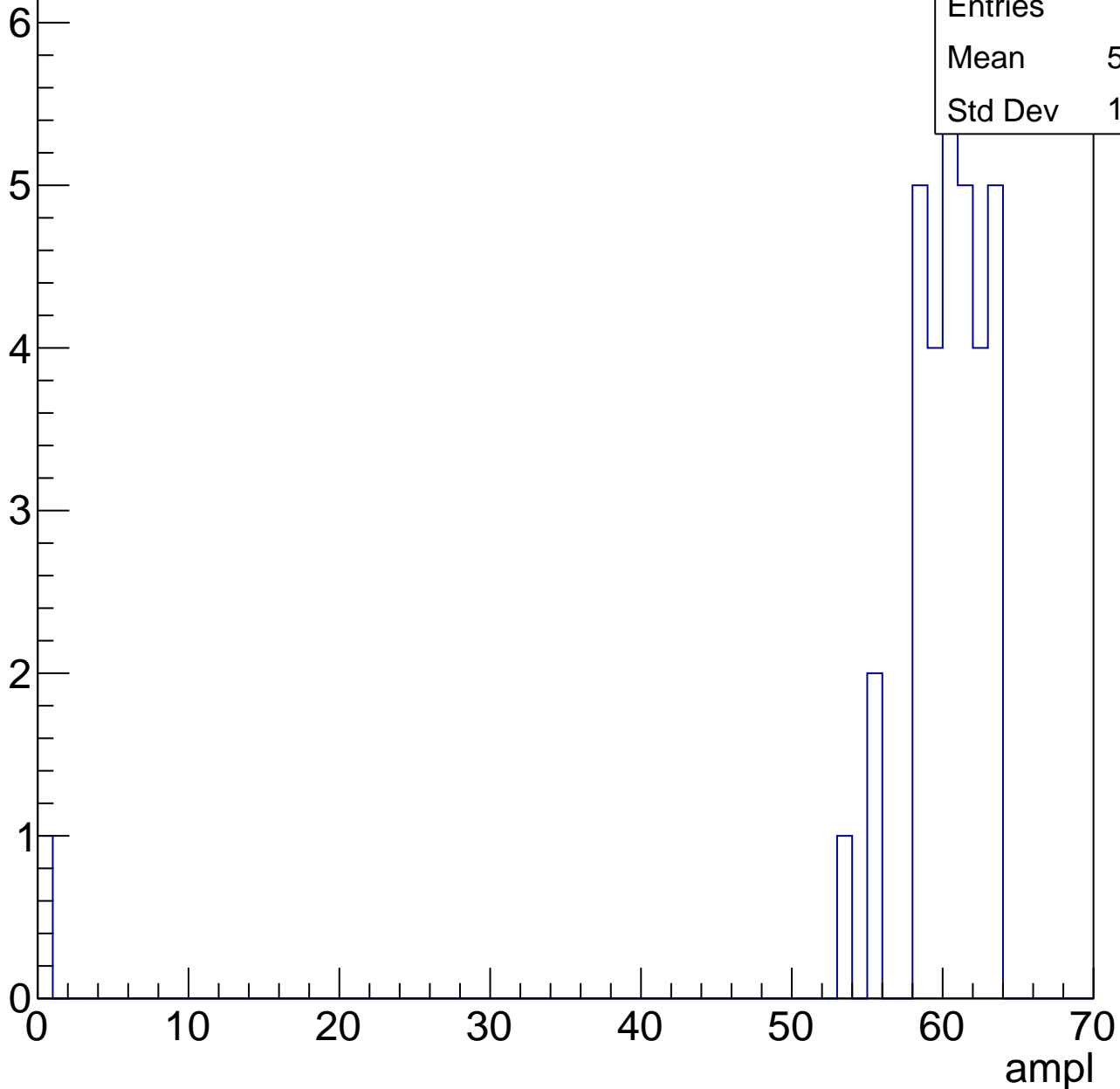


B1L103S, U20-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

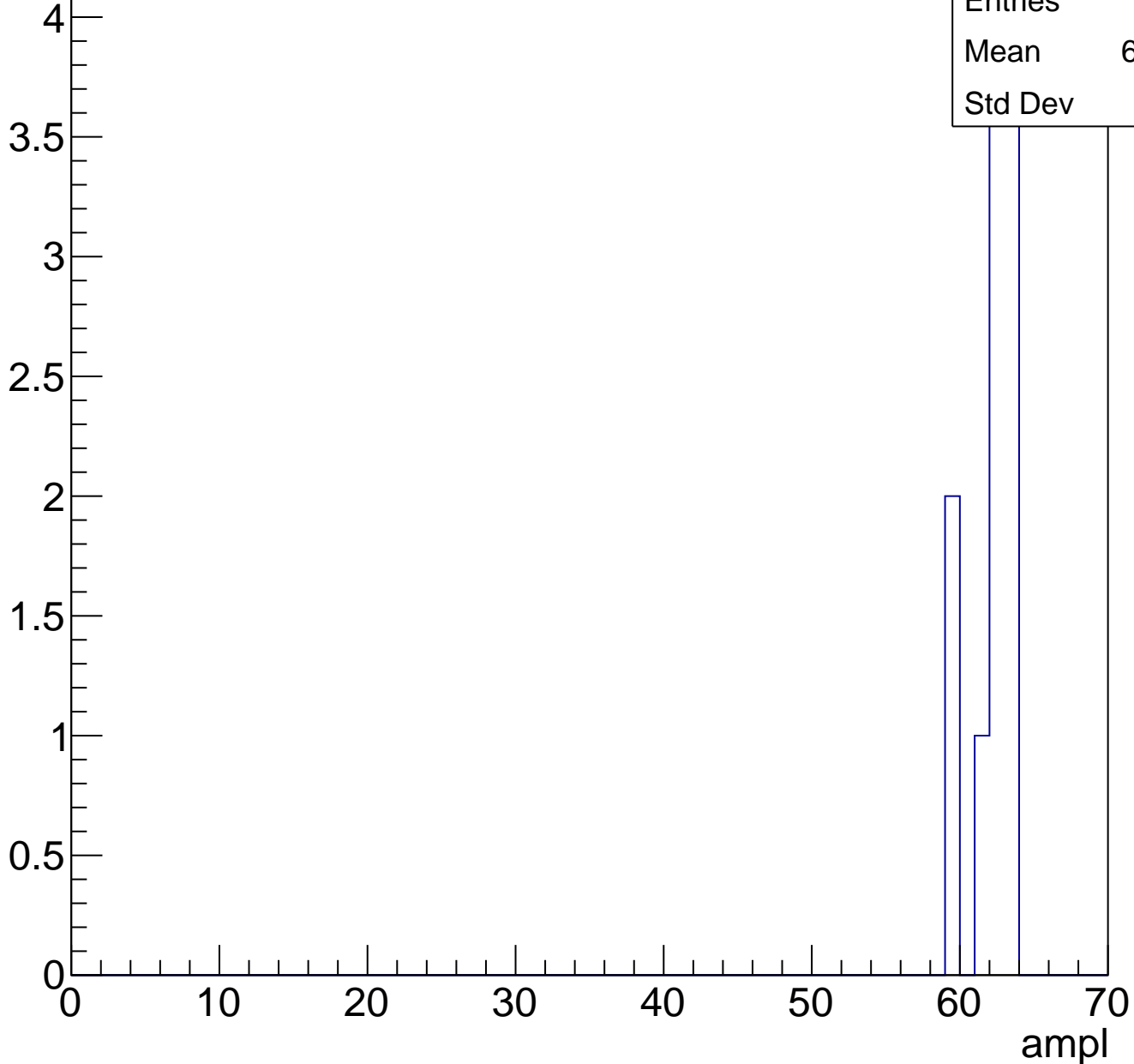
Entries	33
Mean	58.09
Std Dev	10.54



B1L103S, U20-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch45, adc0

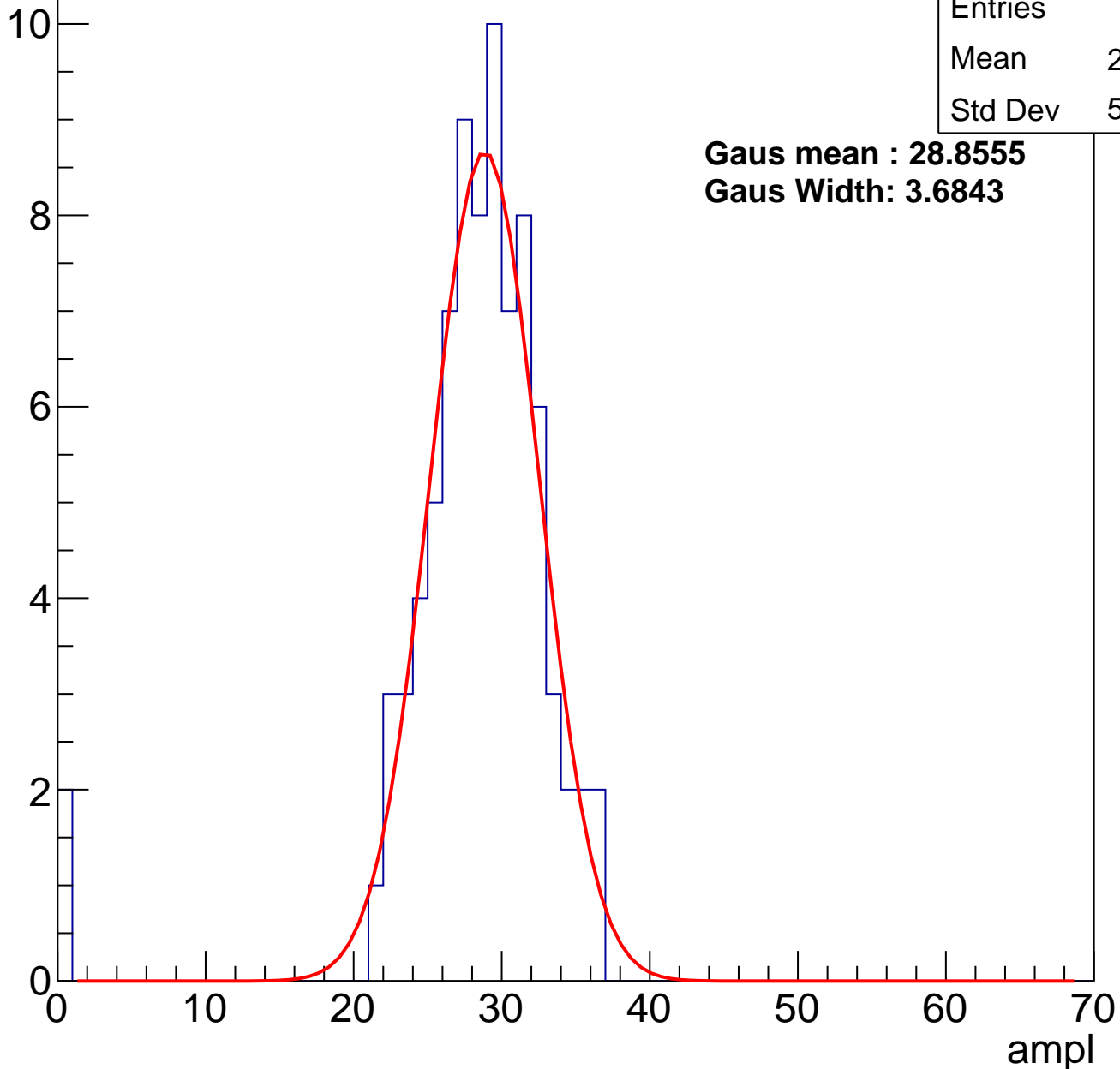
calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	27.74
Std Dev	5.543

Gaus mean : 28.8555

Gaus Width: 3.6843

Entry



B1L103S, U20-ch45, adc1

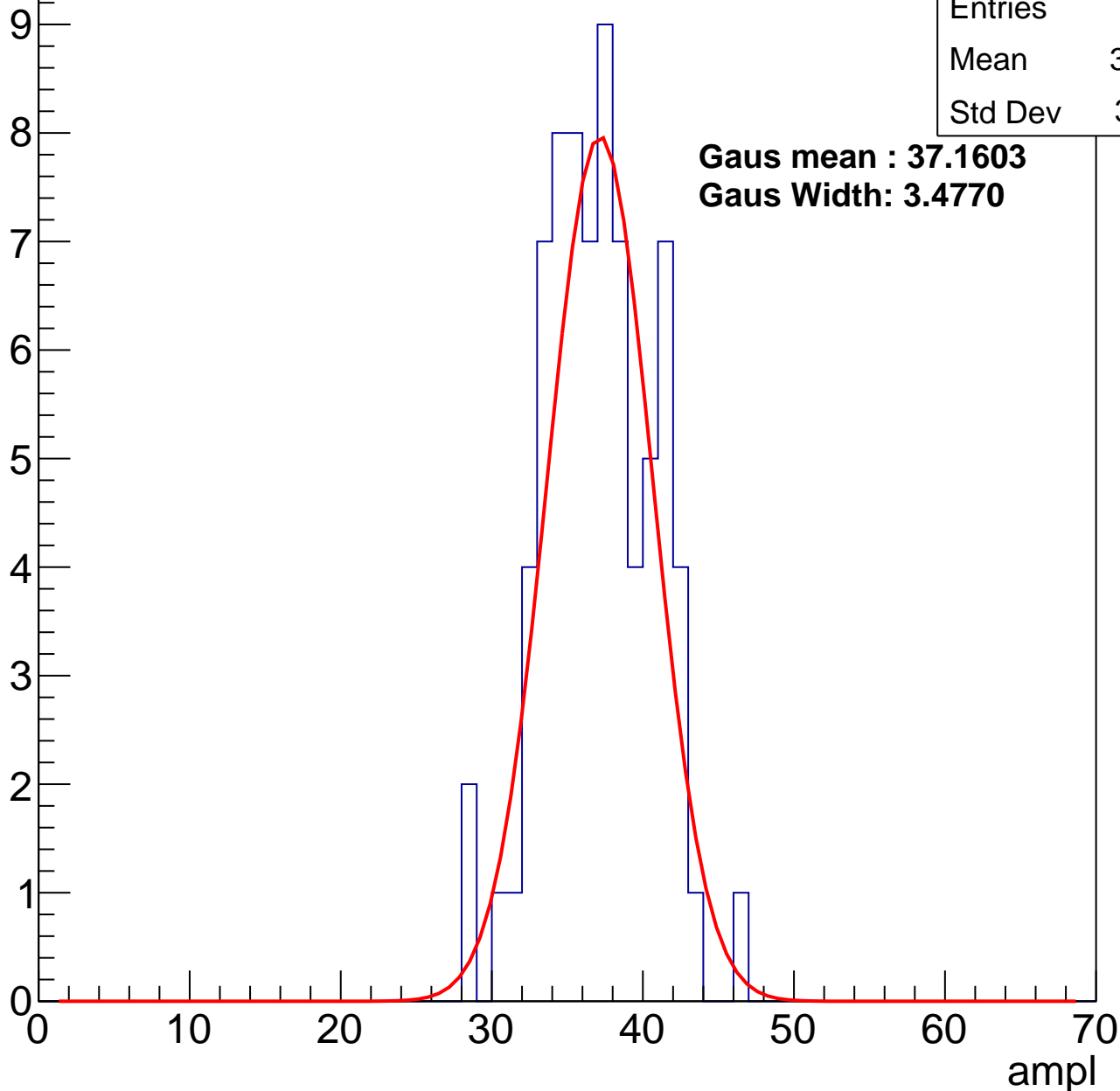
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.57
Std Dev	3.541

Gaus mean : 37.1603

Gaus Width: 3.4770



B1L103S, U20-ch45, adc2

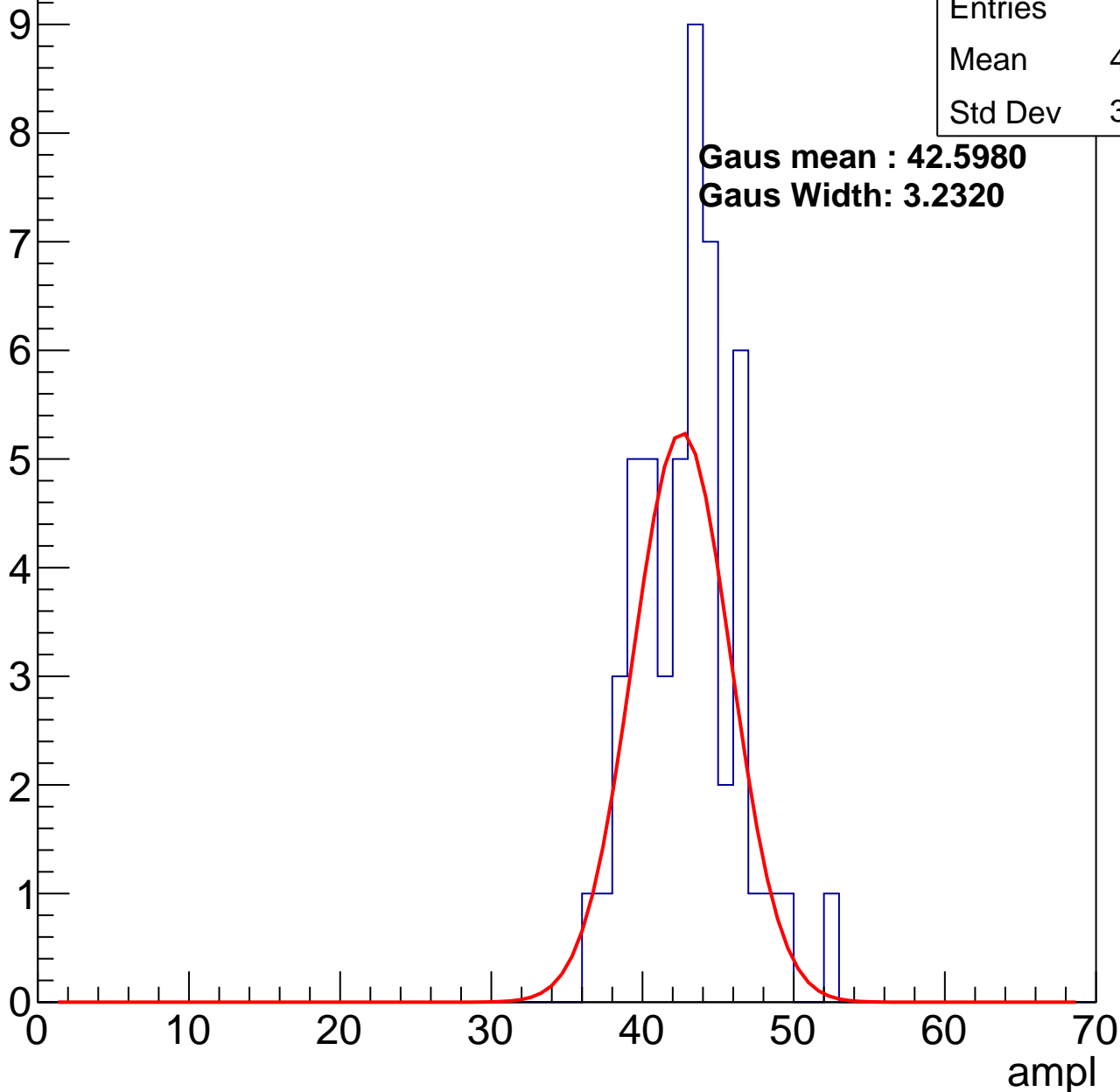
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	42.59
Std Dev	3.194

Gaus mean : 42.5980

Gaus Width: 3.2320

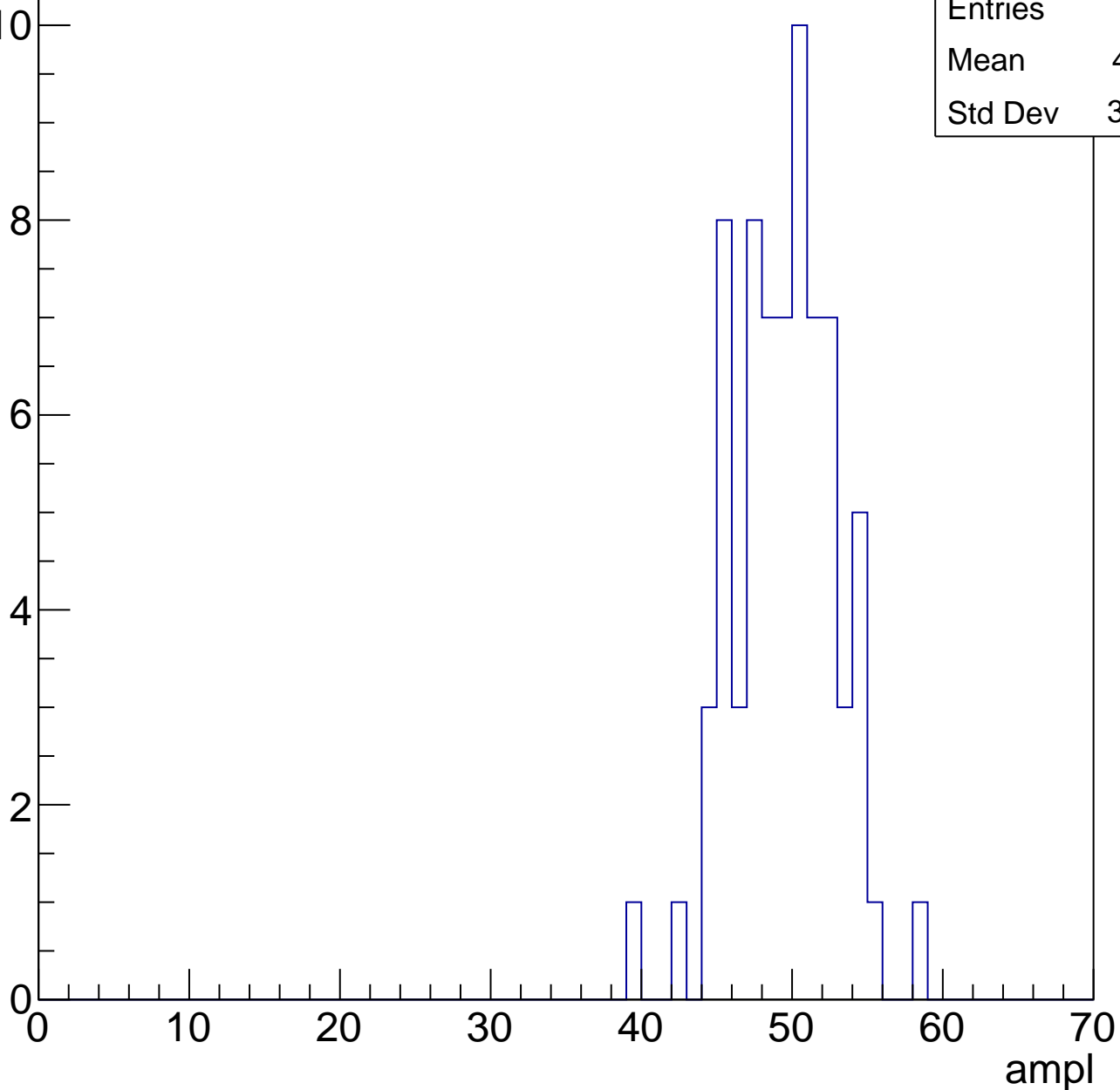


B1L103S, U20-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.01
Std Dev	3.352

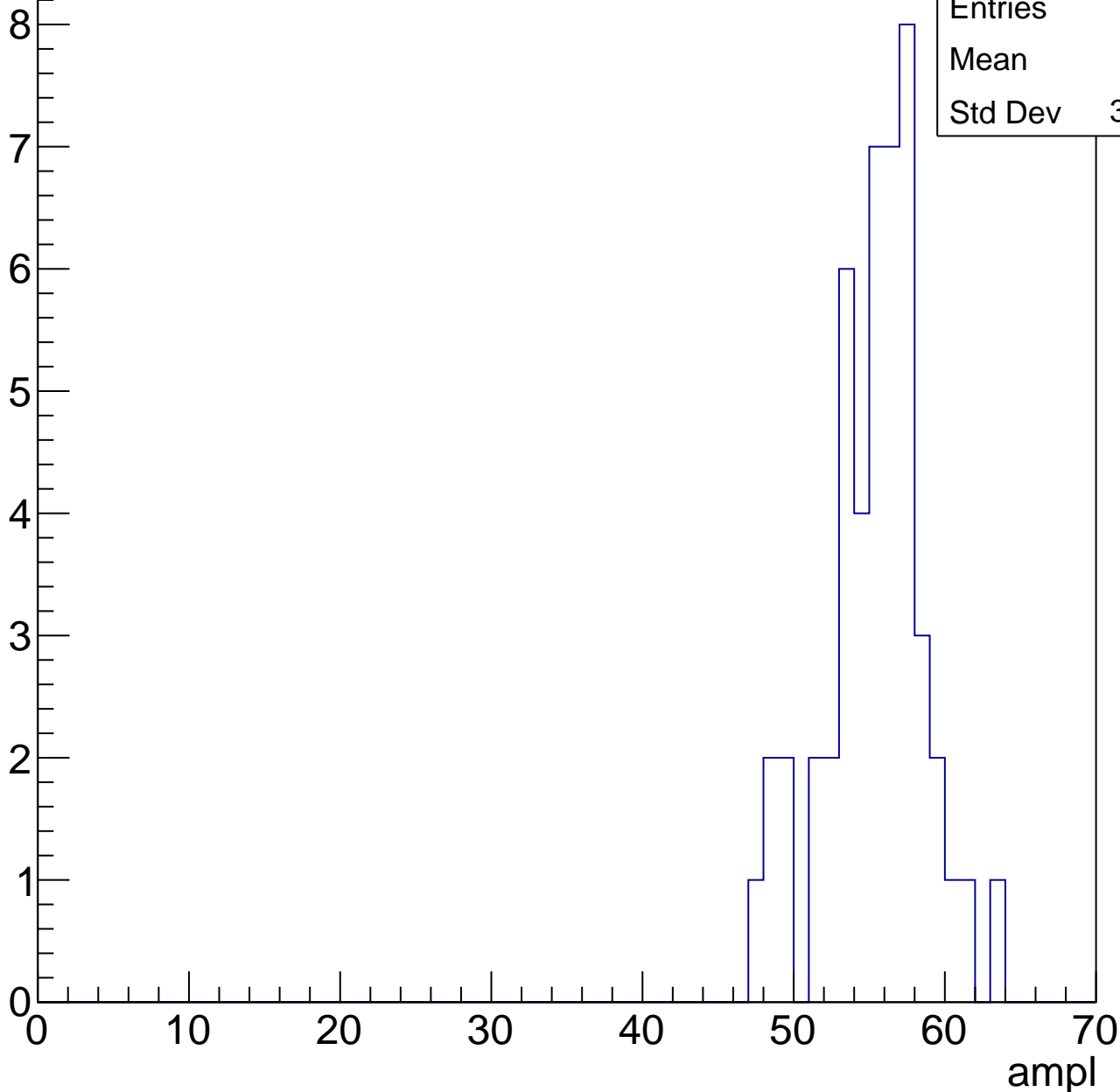


B1L103S, U20-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.9
Std Dev	3.303



B1L103S, U20-ch45, adc5

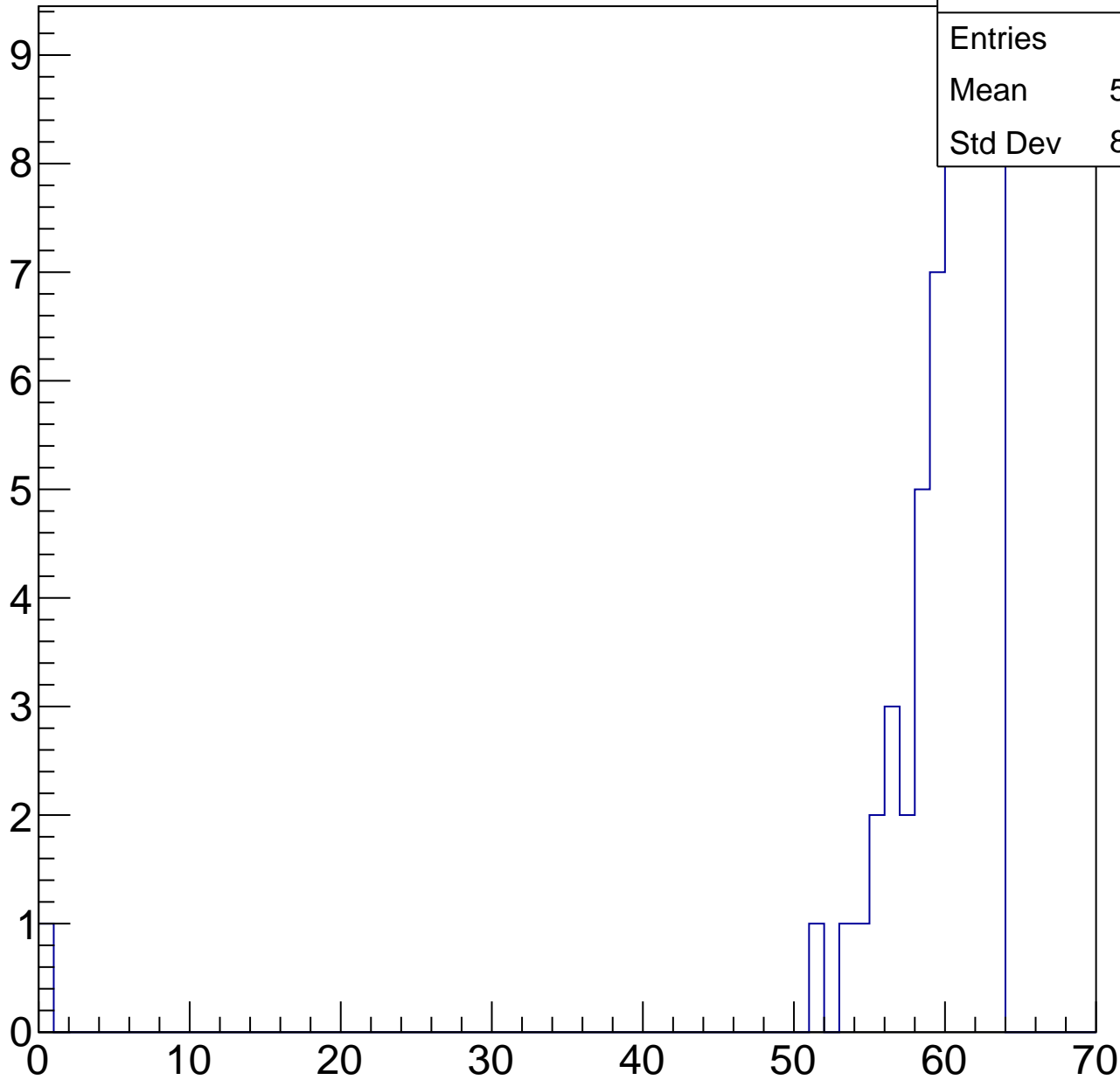
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.69
Std Dev	8.228

ampl



B1L103S, U20-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	4
Mean	60.5
Std Dev	2.062

B1L103S, U20-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch46, adc0

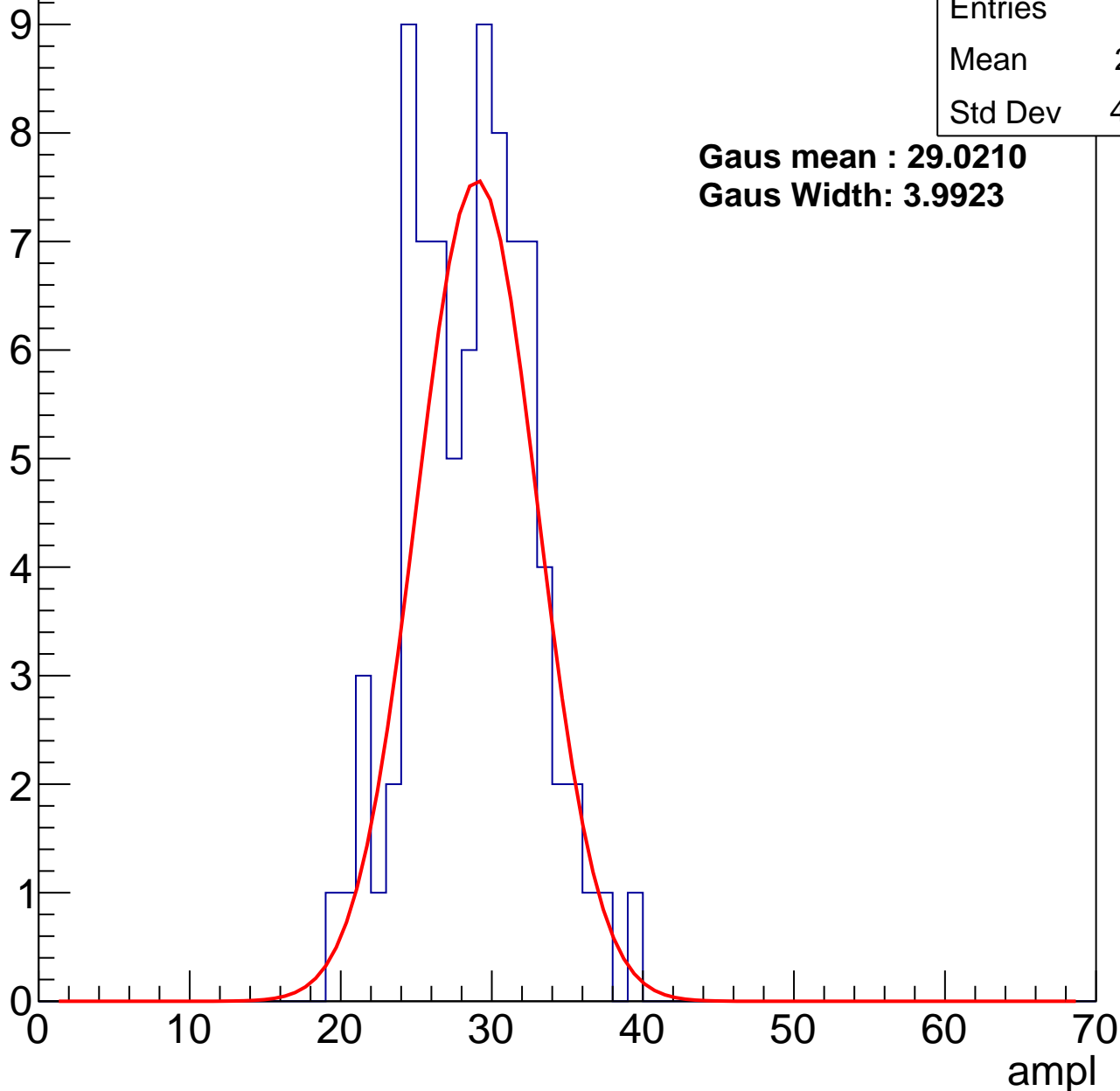
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	28.21
Std Dev	4.033

Gaus mean : 29.0210

Gaus Width: 3.9923



B1L103S, U20-ch46, adc1

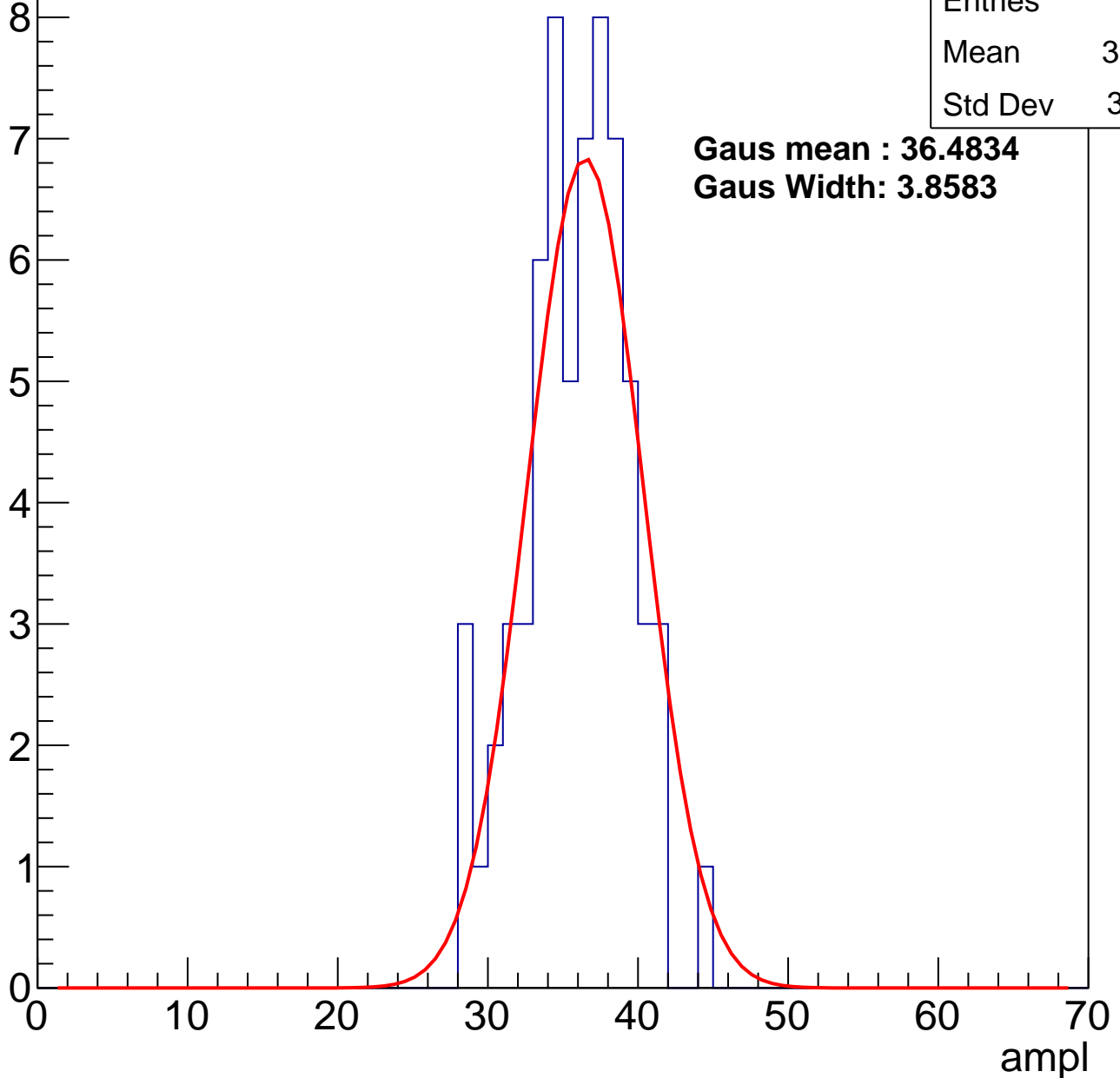
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.43
Std Dev	3.451

Gaus mean : 36.4834

Gaus Width: 3.8583



B1L103S, U20-ch46, adc2

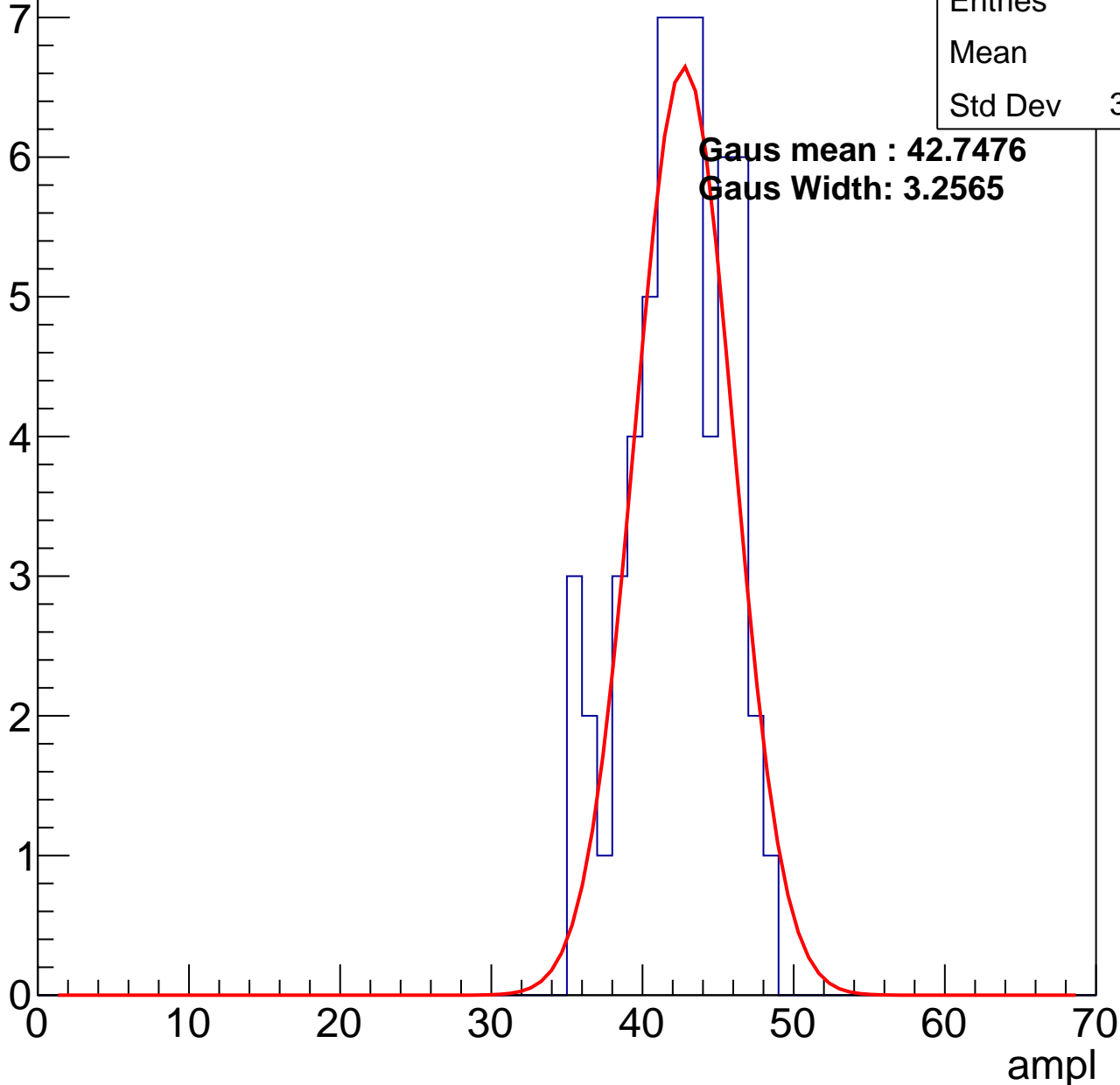
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.9
Std Dev	3.252

Gaus mean : 42.7476

Gaus Width: 3.2565

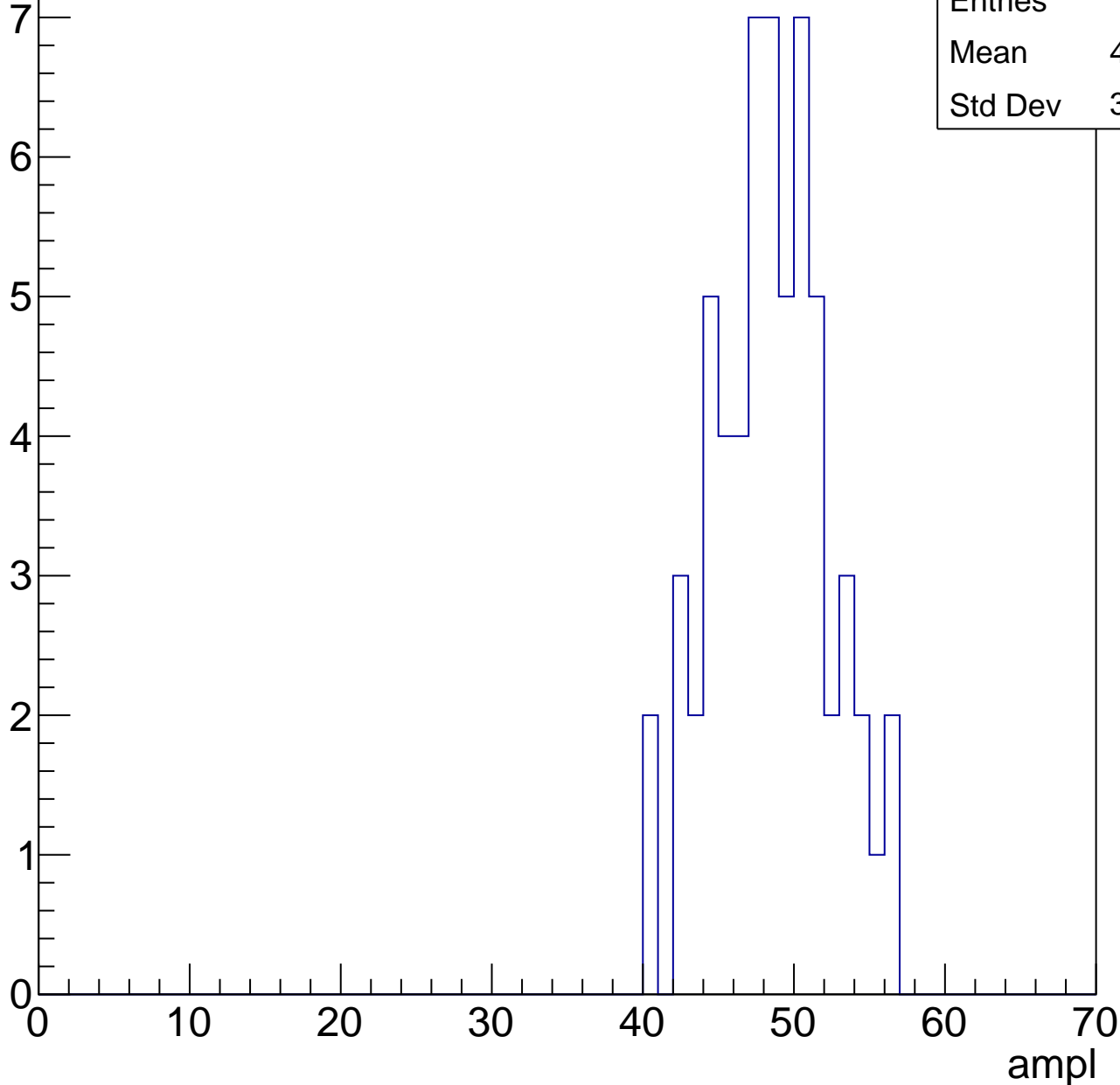


B1L103S, U20-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.02
Std Dev	3.753

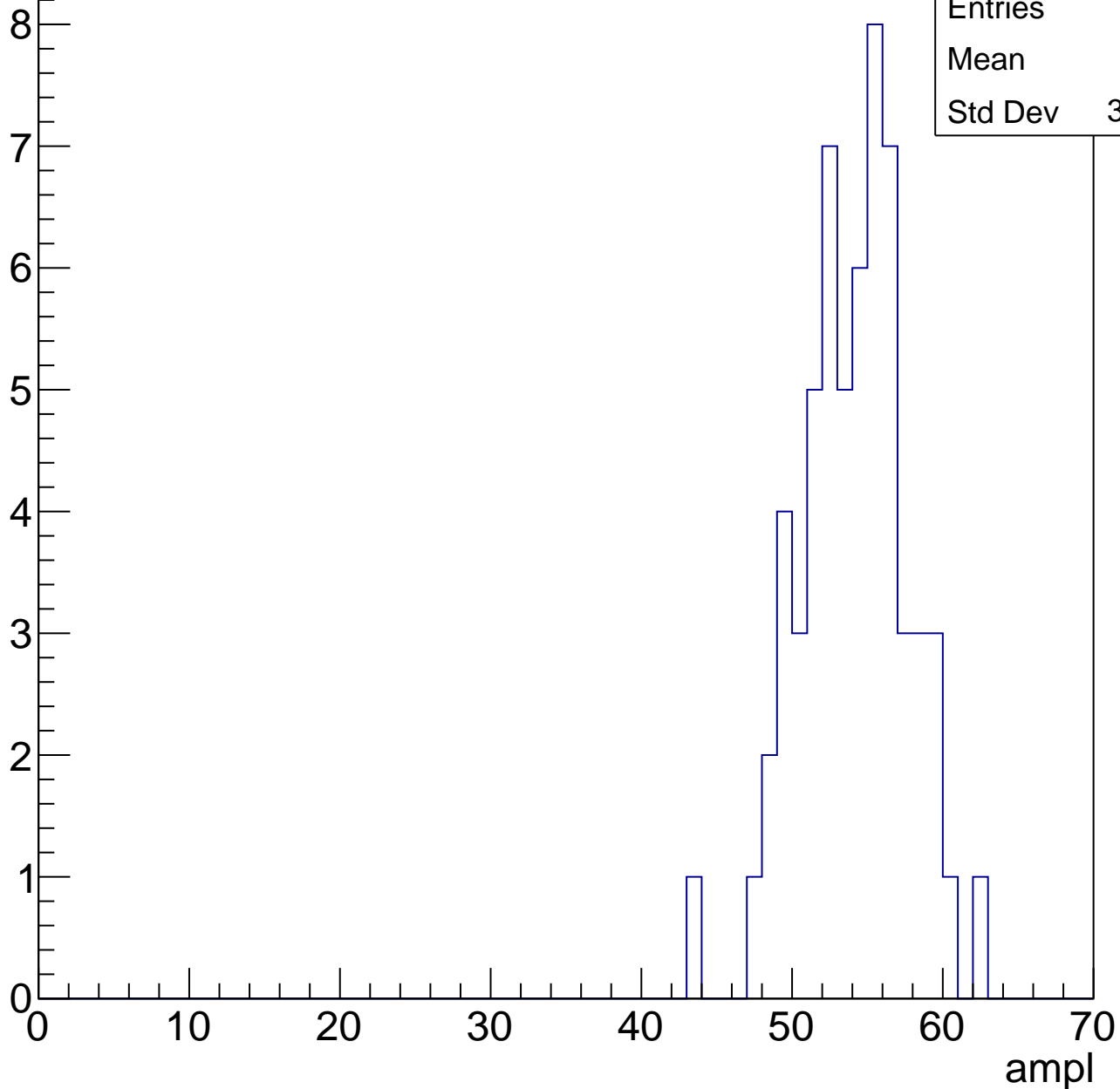


B1L103S, U20-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	53.6
Std Dev	3.522



B1L103S, U20-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

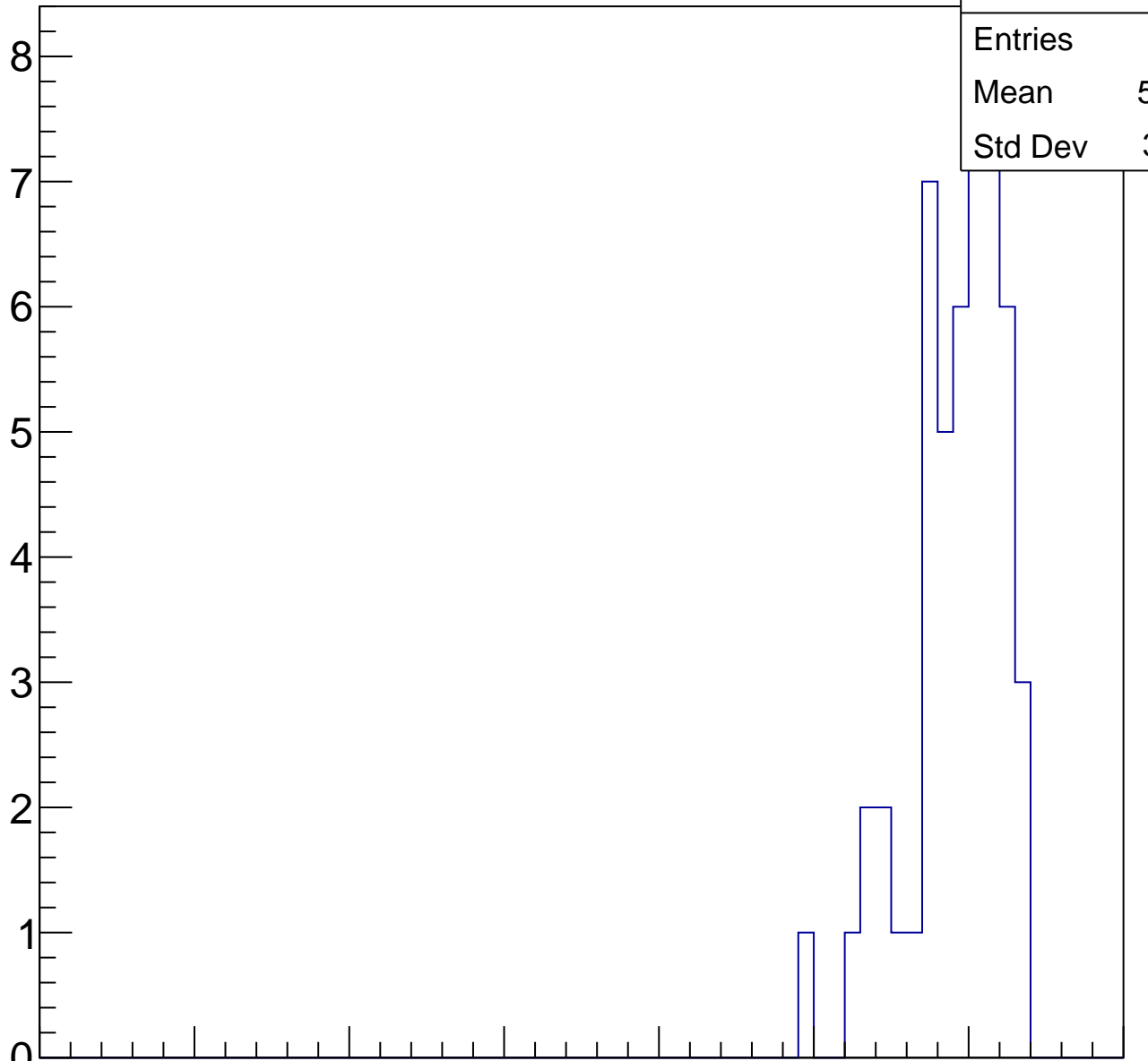
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.78
Std Dev	3.031

ampl

0 10 20 30 40 50 60 70

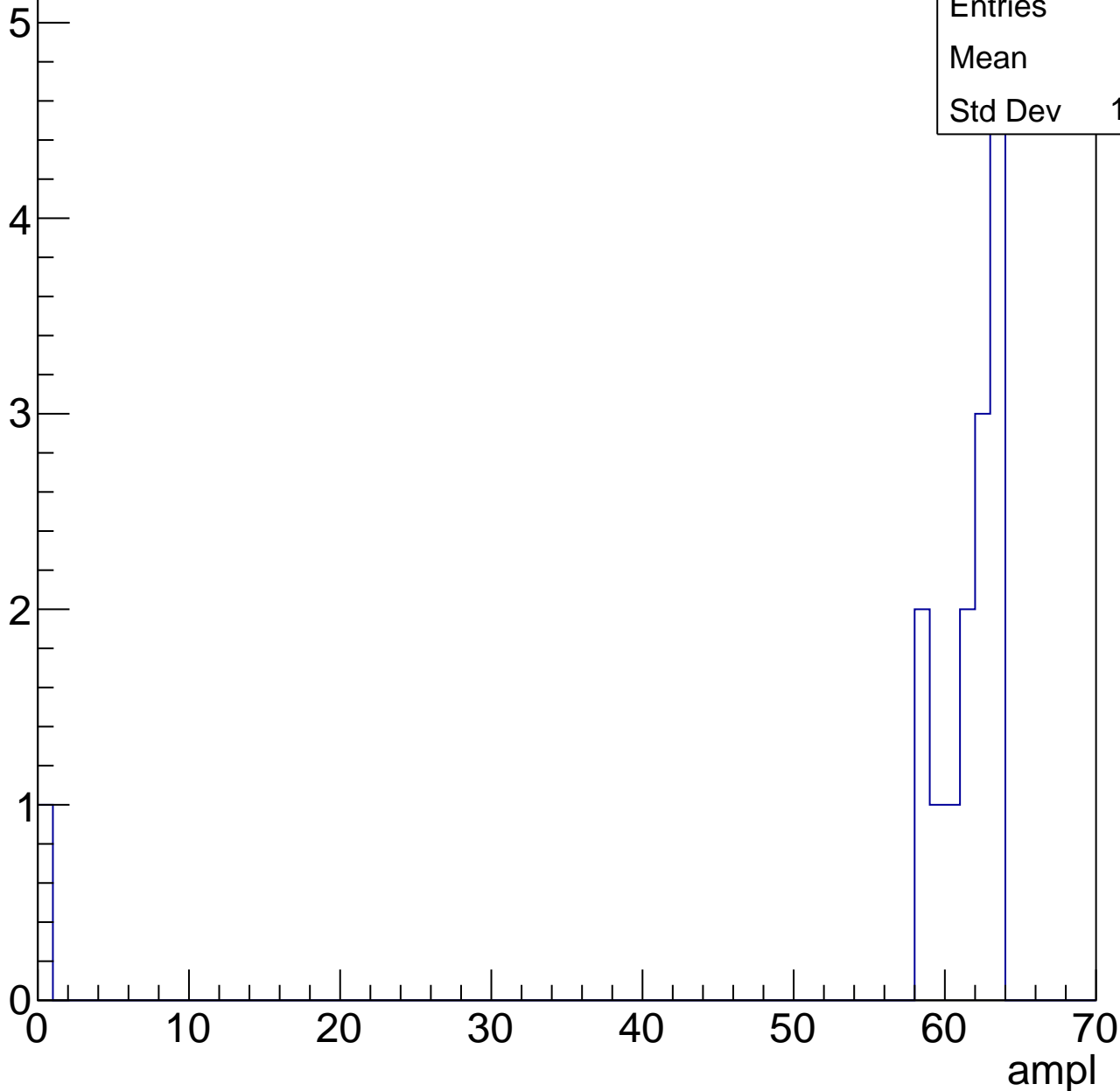


B1L103S, U20-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	57.2
Std Dev	15.38



B1L103S, U20-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch47, adc0

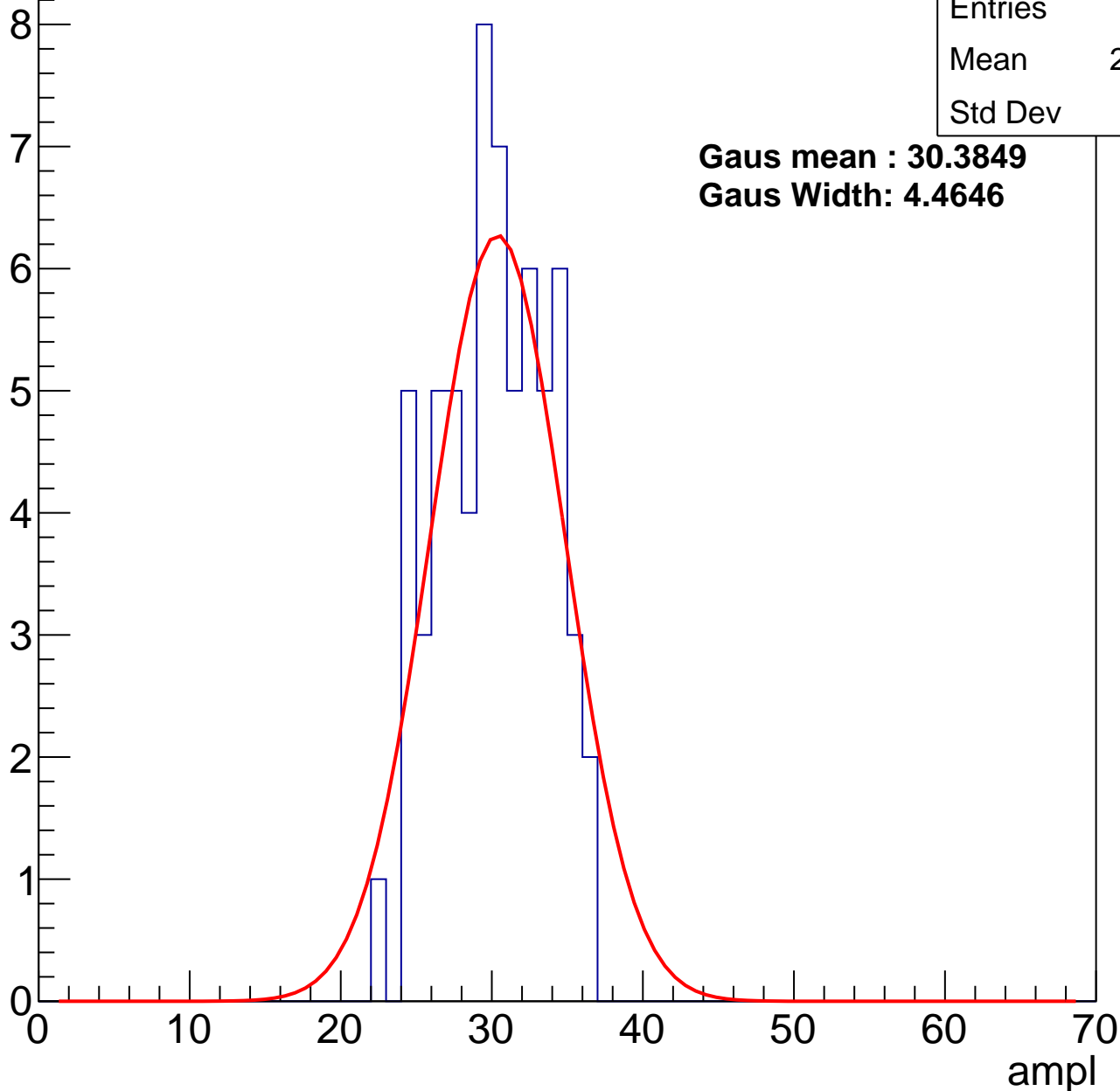
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.68
Std Dev	3.46

Gaus mean : 30.3849

Gaus Width: 4.4646



B1L103S, U20-ch47, adc1

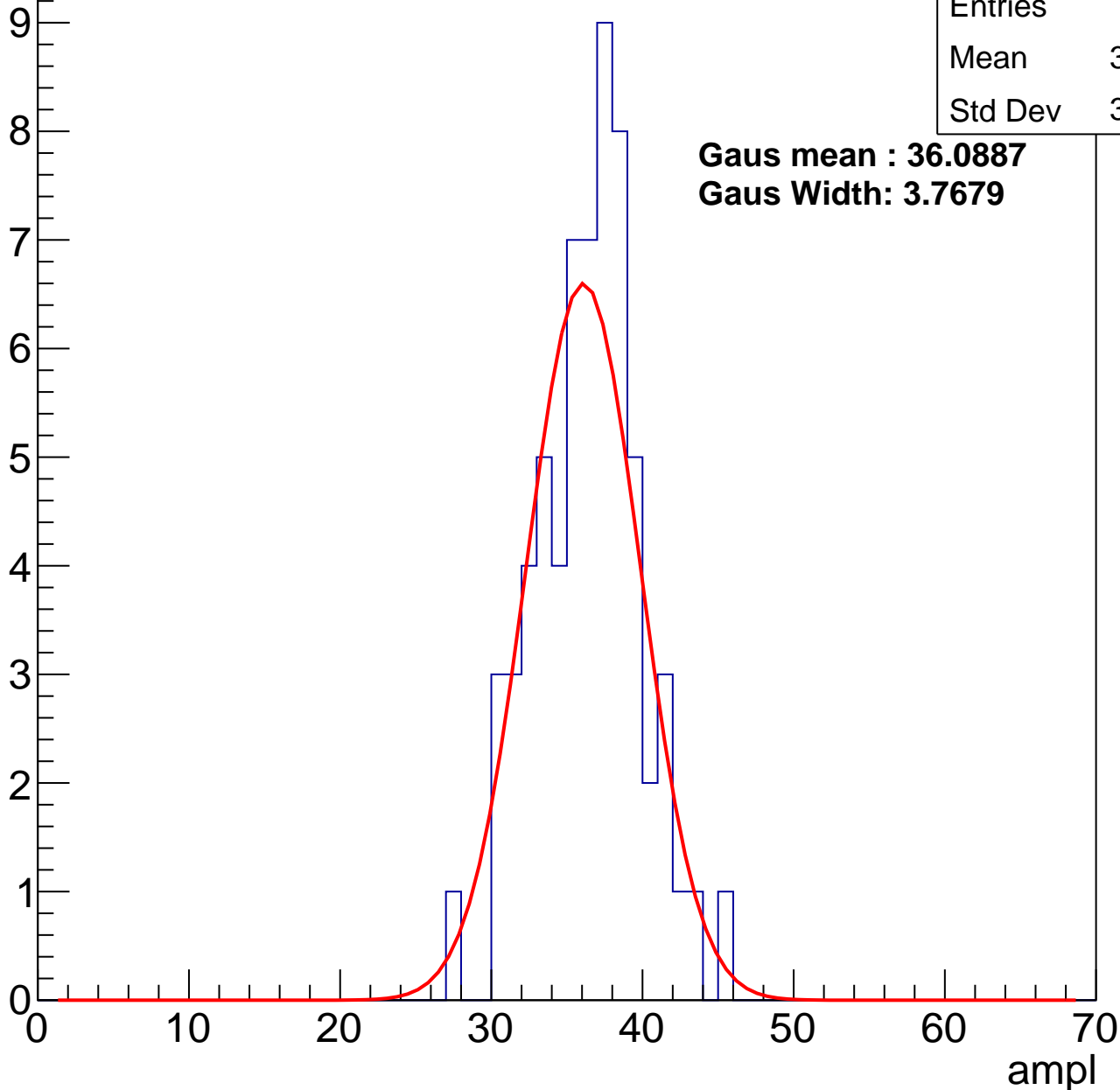
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.95
Std Dev	3.434

Gaus mean : 36.0887

Gaus Width: 3.7679



B1L103S, U20-ch47, adc2

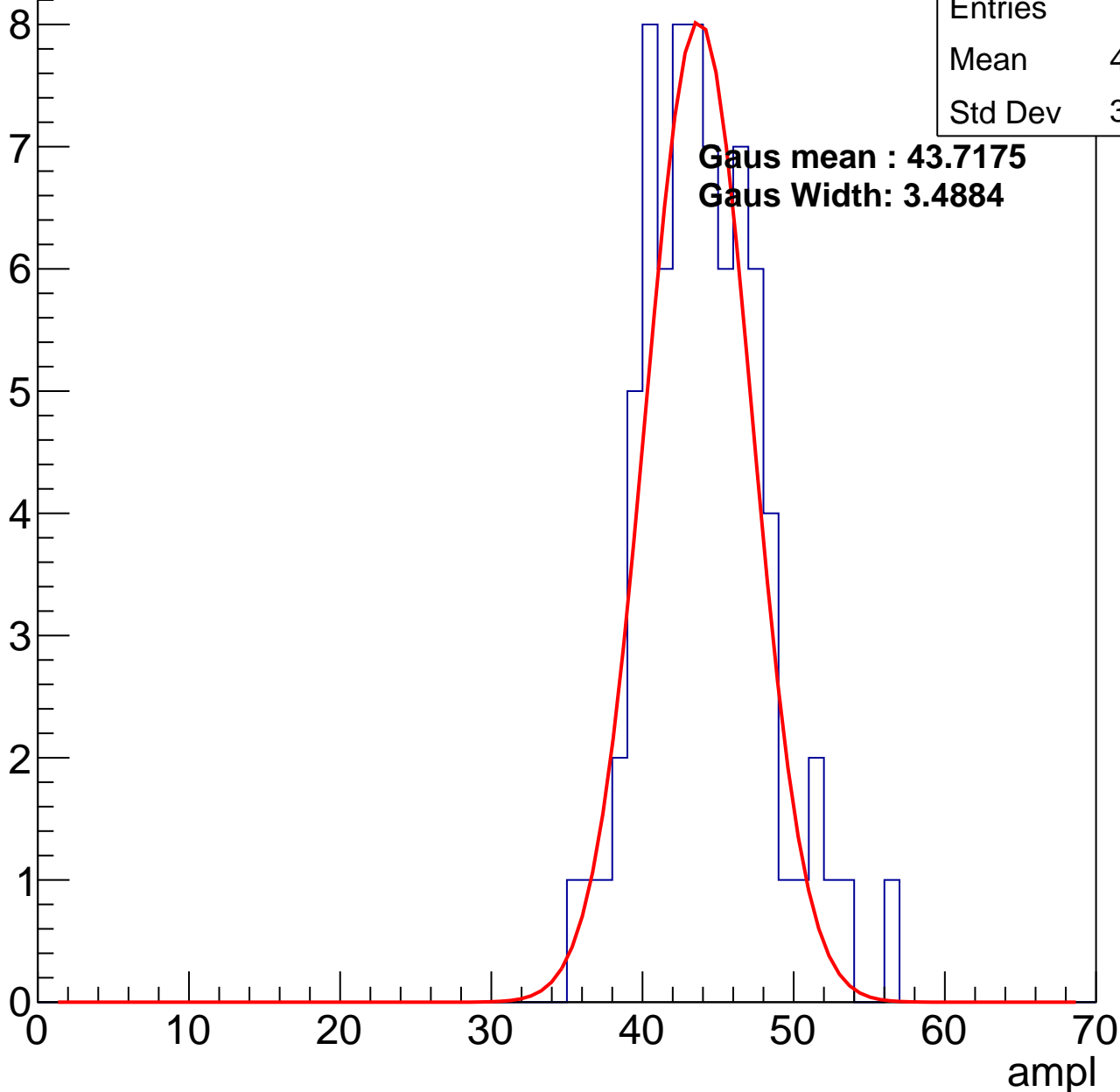
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	43.65
Std Dev	3.954

Gaus mean : 43.7175

Gaus Width: 3.4884

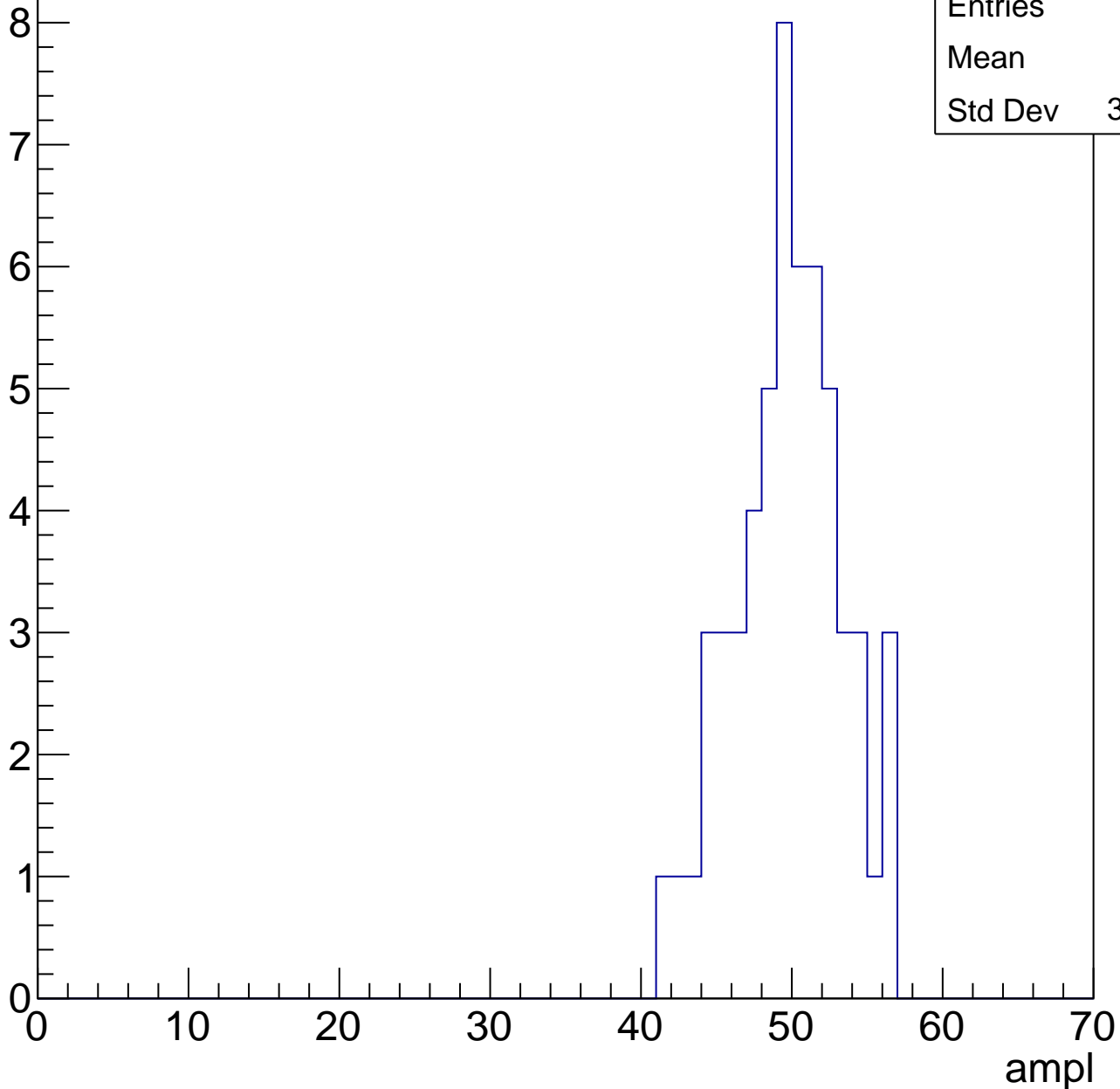


B1L103S, U20-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.3
Std Dev	3.525

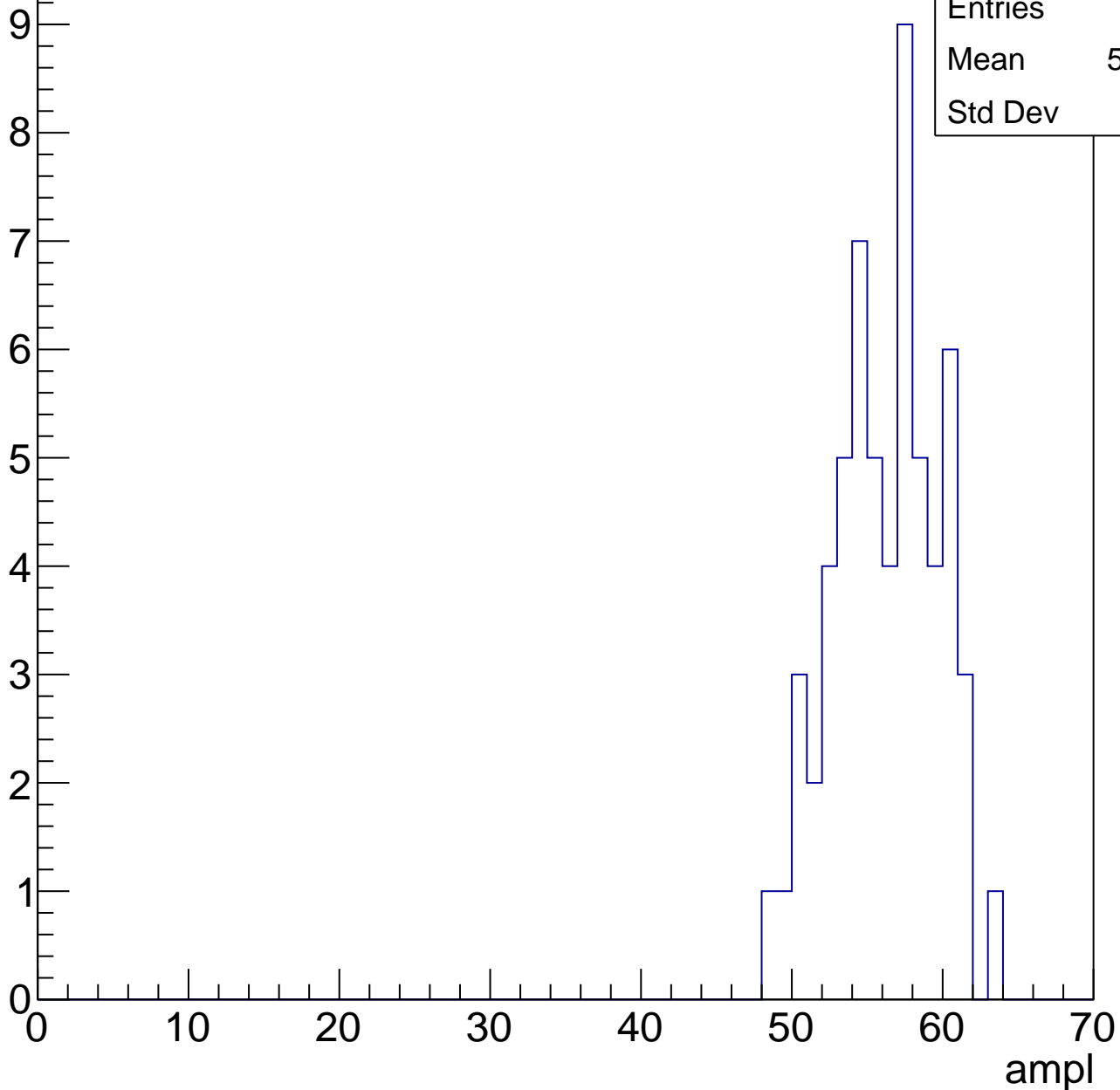


B1L103S, U20-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.73
Std Dev	3.4

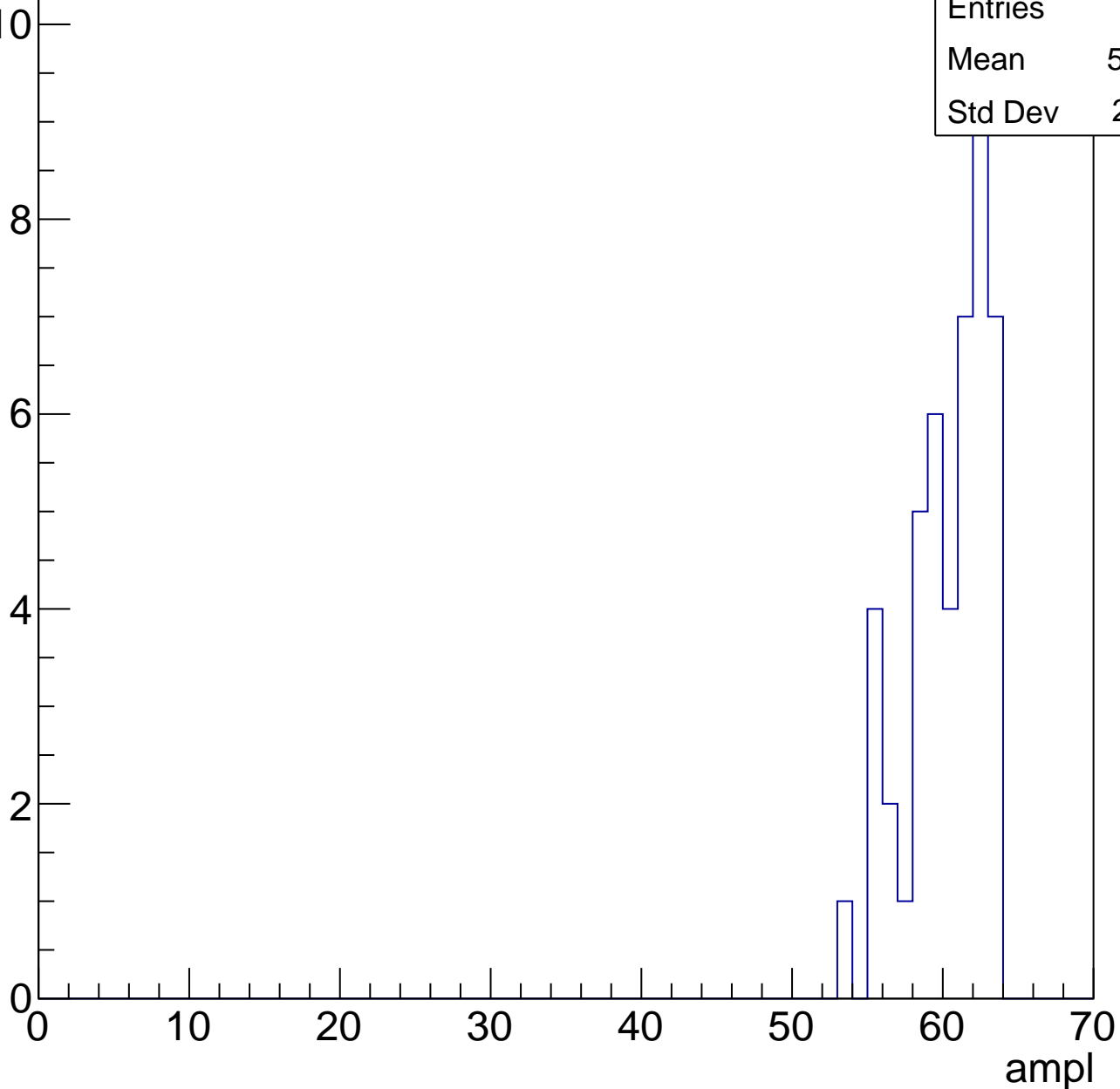


B1L103S, U20-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	59.87
Std Dev	2.631



B1L103S, U20-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U20-ch48, adc0

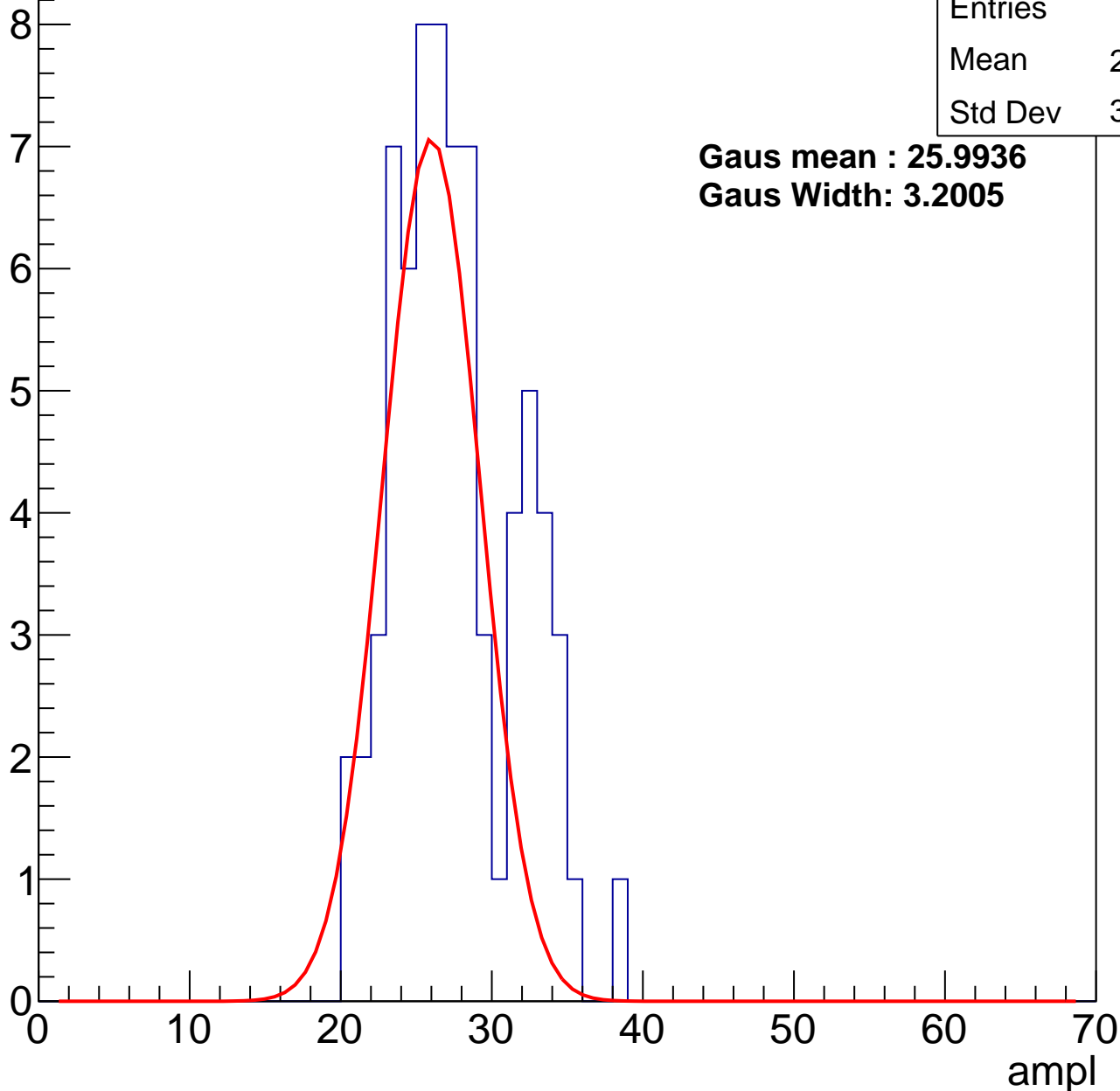
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.14
Std Dev	3.984

Gaus mean : 25.9936

Gaus Width: 3.2005



B1L103S, U20-ch48, adc1

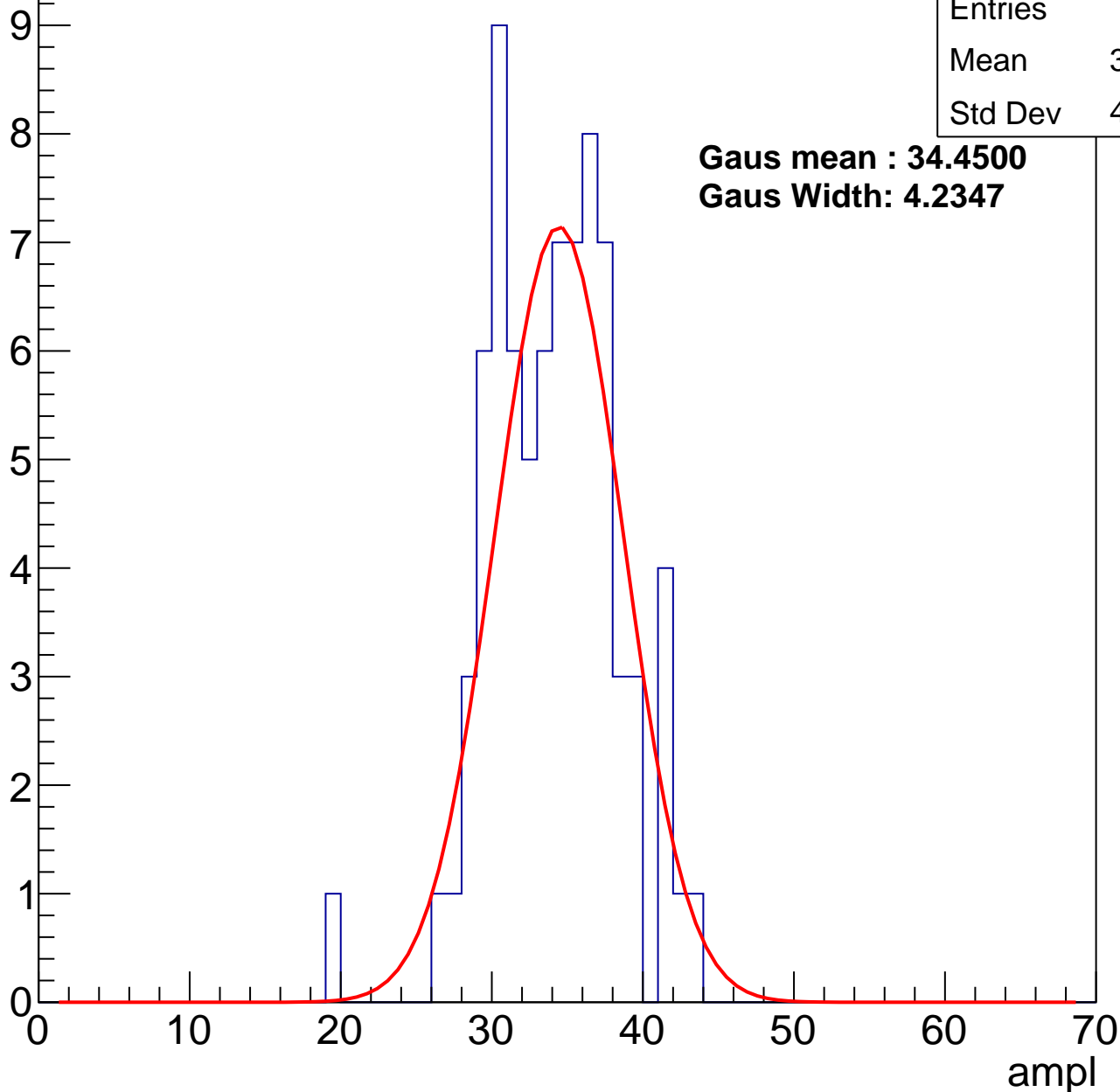
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	33.59
Std Dev	4.169

Gaus mean : 34.4500

Gaus Width: 4.2347



B1L103S, U20-ch48, adc2

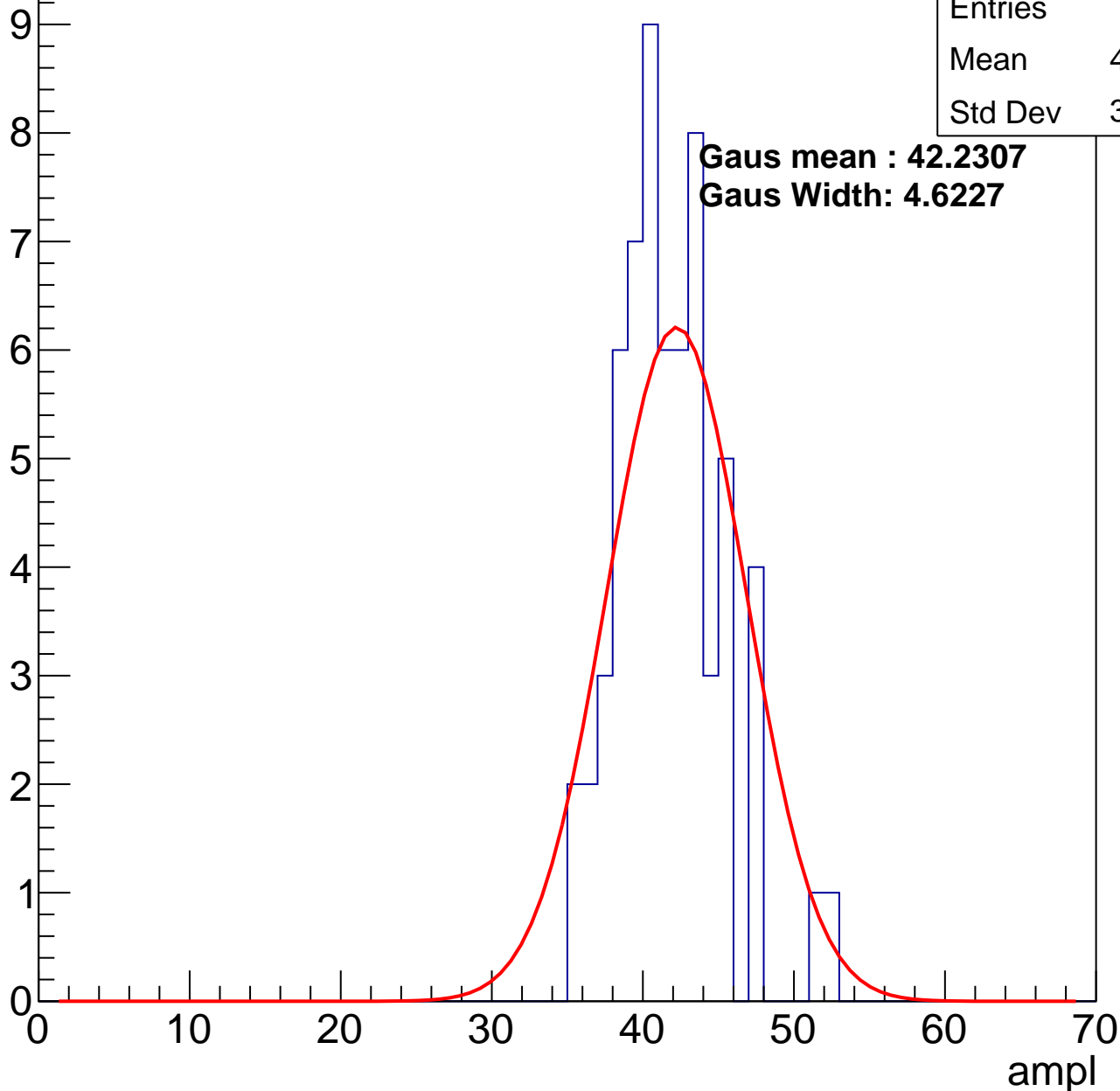
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.33
Std Dev	3.482

Gaus mean : 42.2307

Gaus Width: 4.6227



B1L103S, U20-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

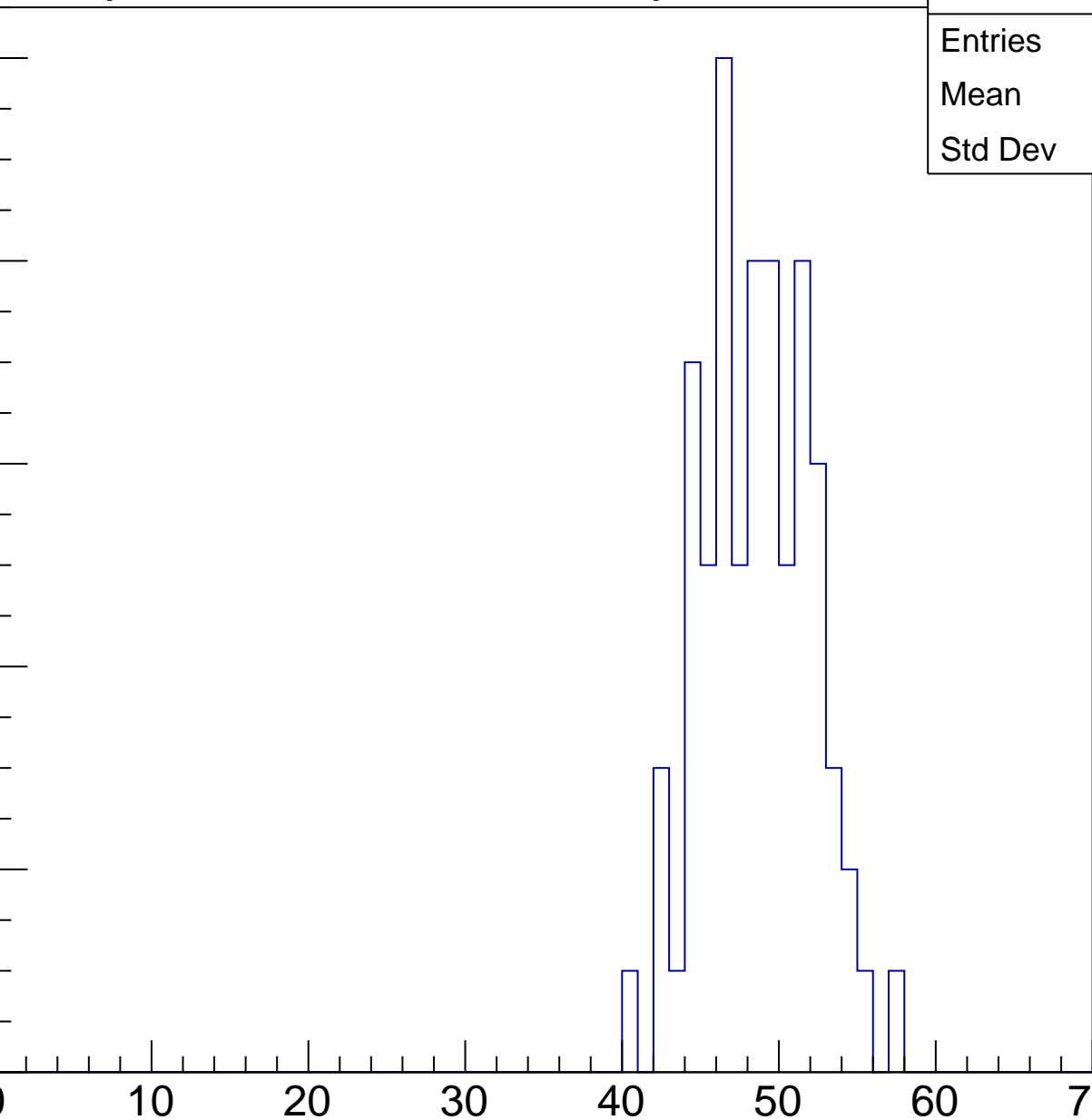
Entries	74
Mean	48.14
Std Dev	3.43

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

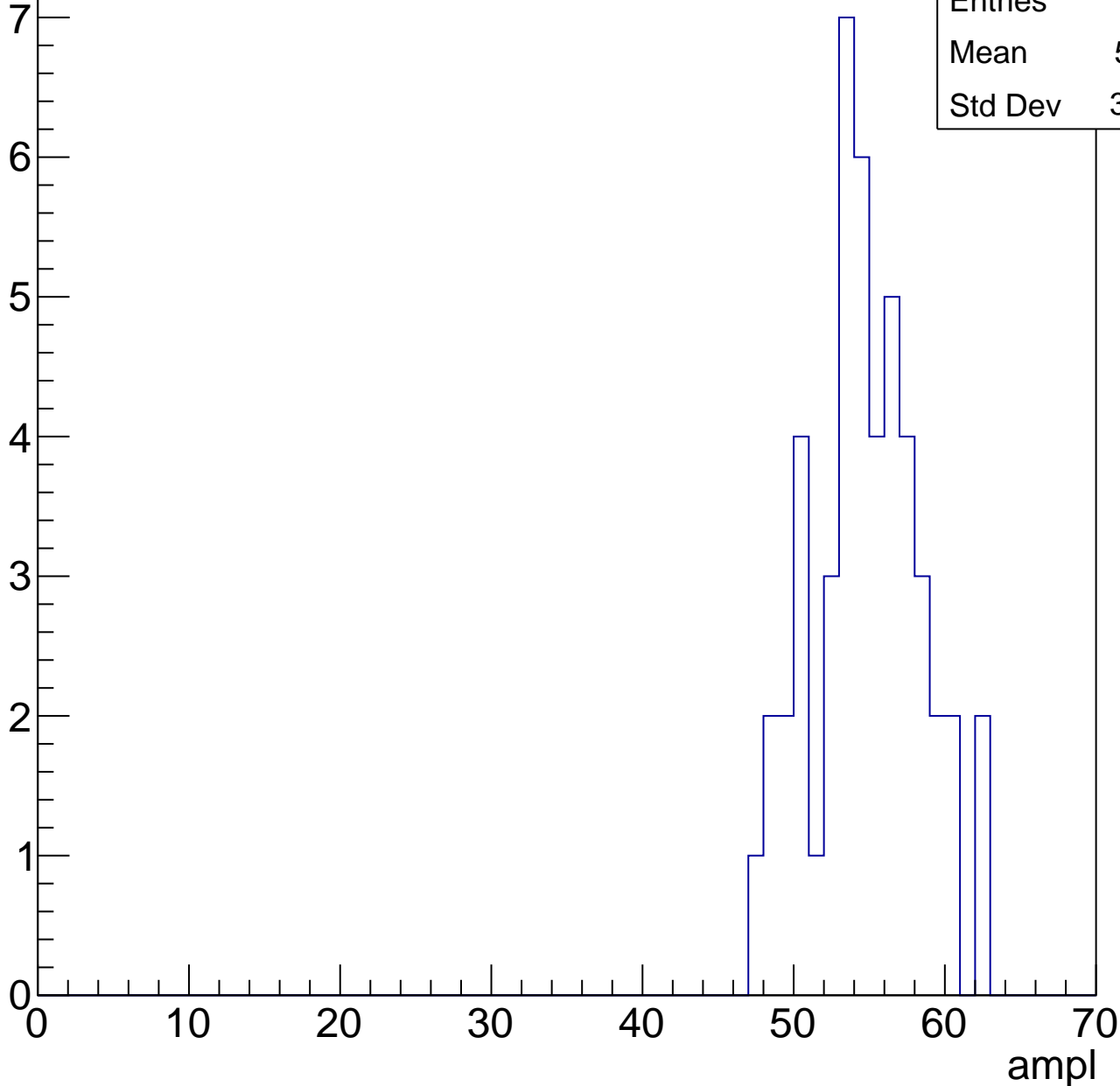


B1L103S, U20-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	54.31
Std Dev	3.583

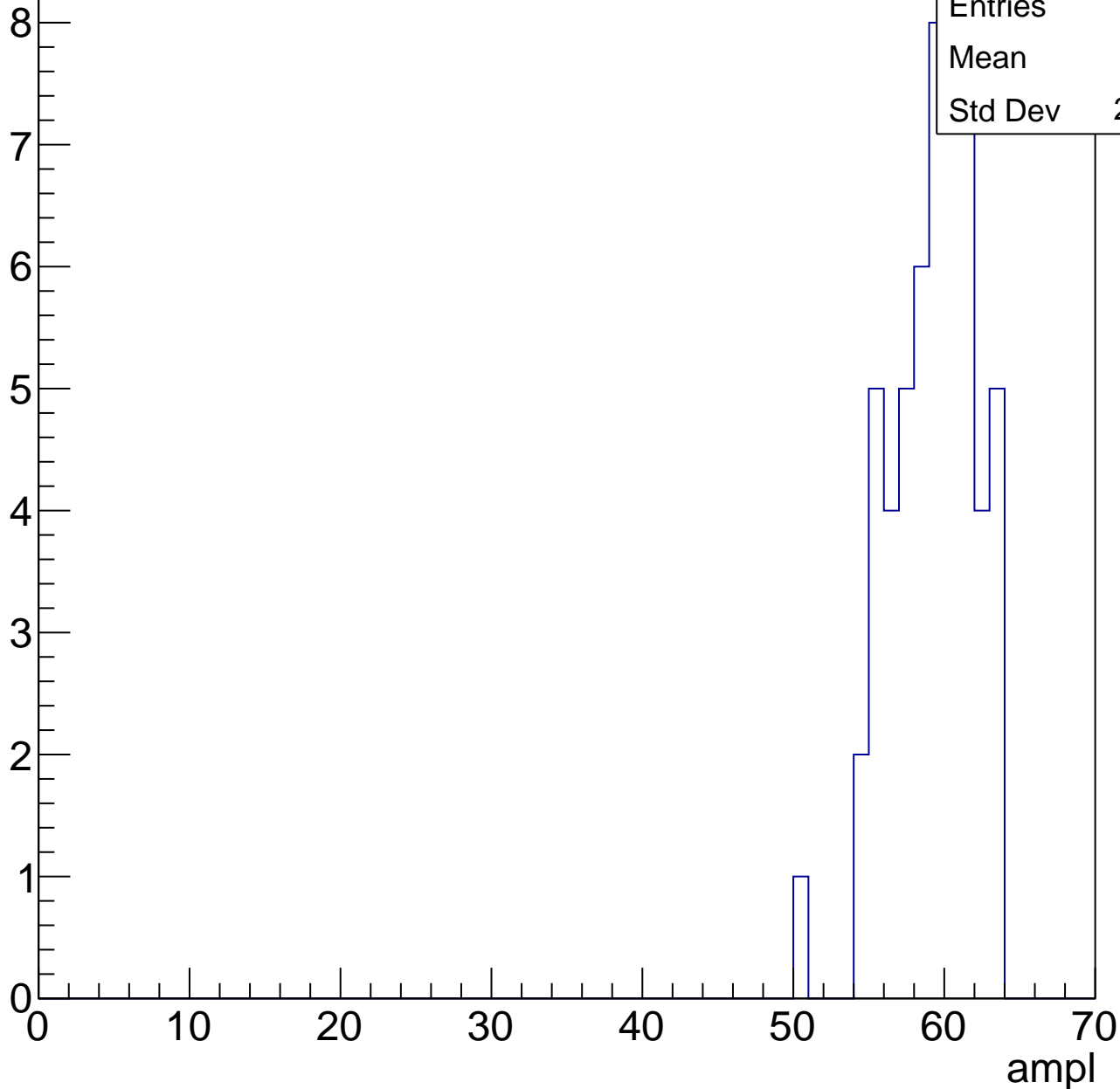


B1L103S, U20-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

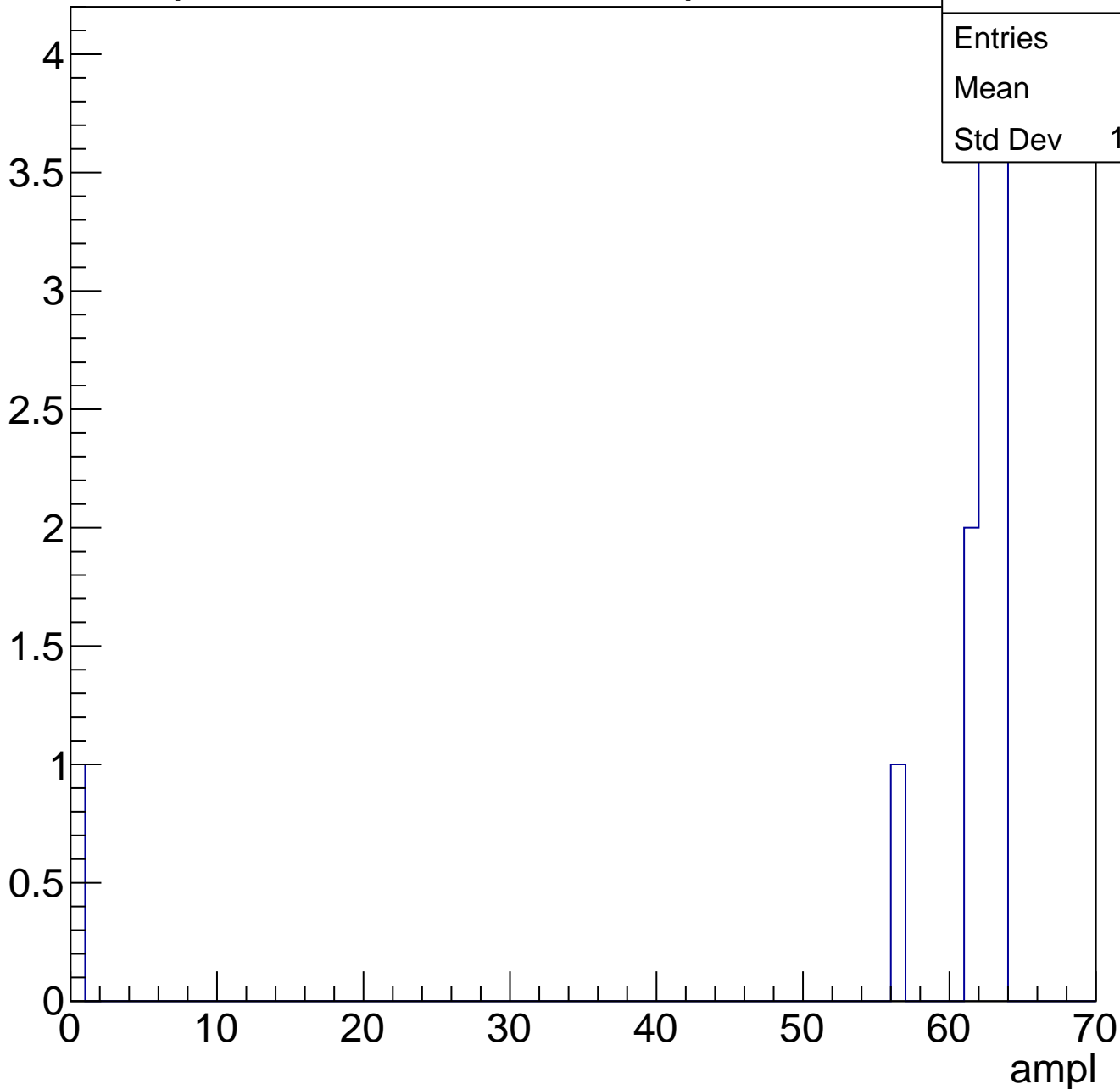
Entries	56
Mean	58.8
Std Dev	2.761



B1L103S, U20-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

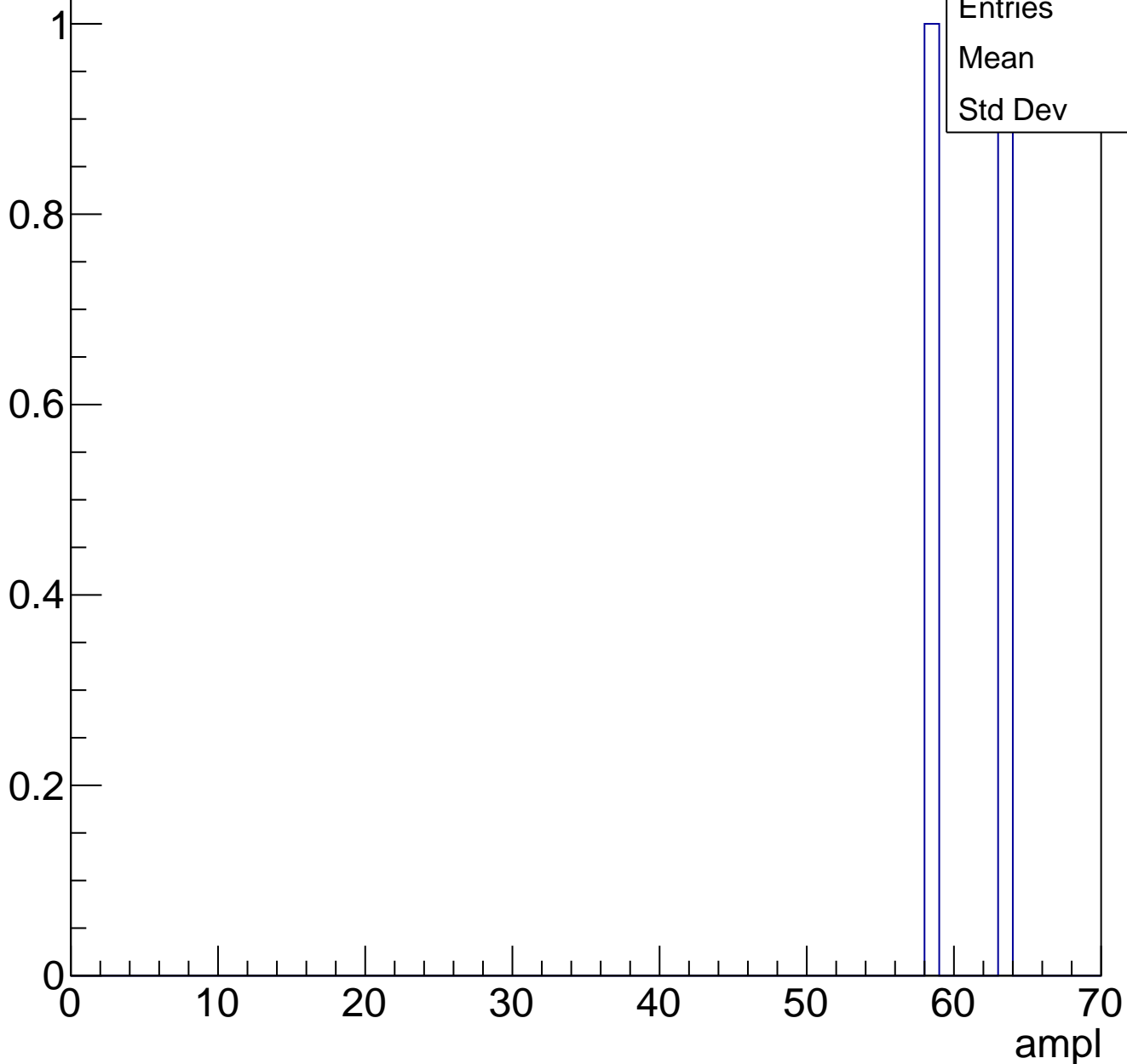
Entry



B1L103S, U20-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	60.5
Std Dev	2.5

B1L103S, U20-ch49, adc0

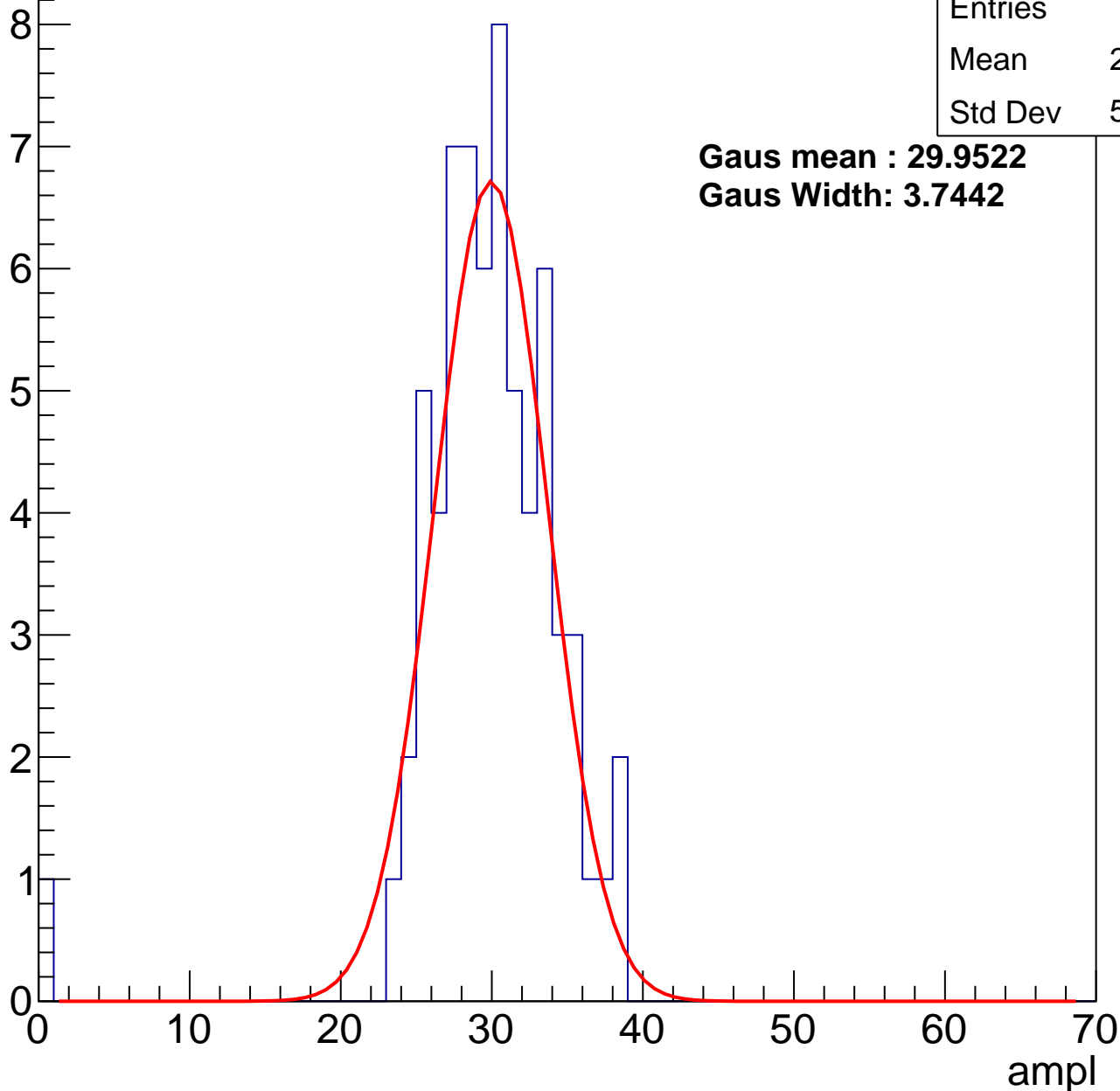
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.33
Std Dev	5.058

Gaus mean : 29.9522

Gaus Width: 3.7442



B1L103S, U20-ch49, adc1

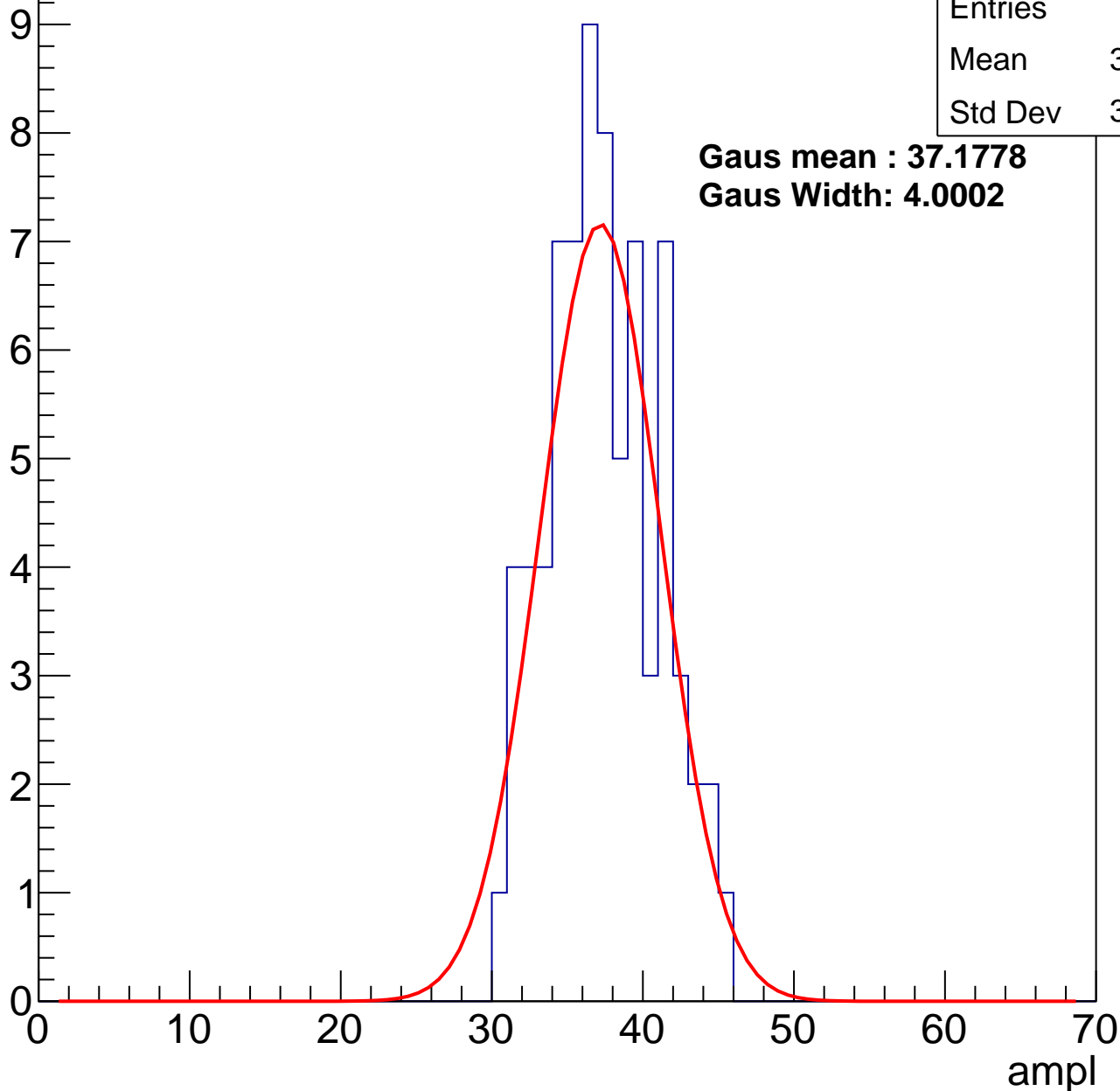
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.92
Std Dev	3.559

Gaus mean : 37.1778

Gaus Width: 4.0002

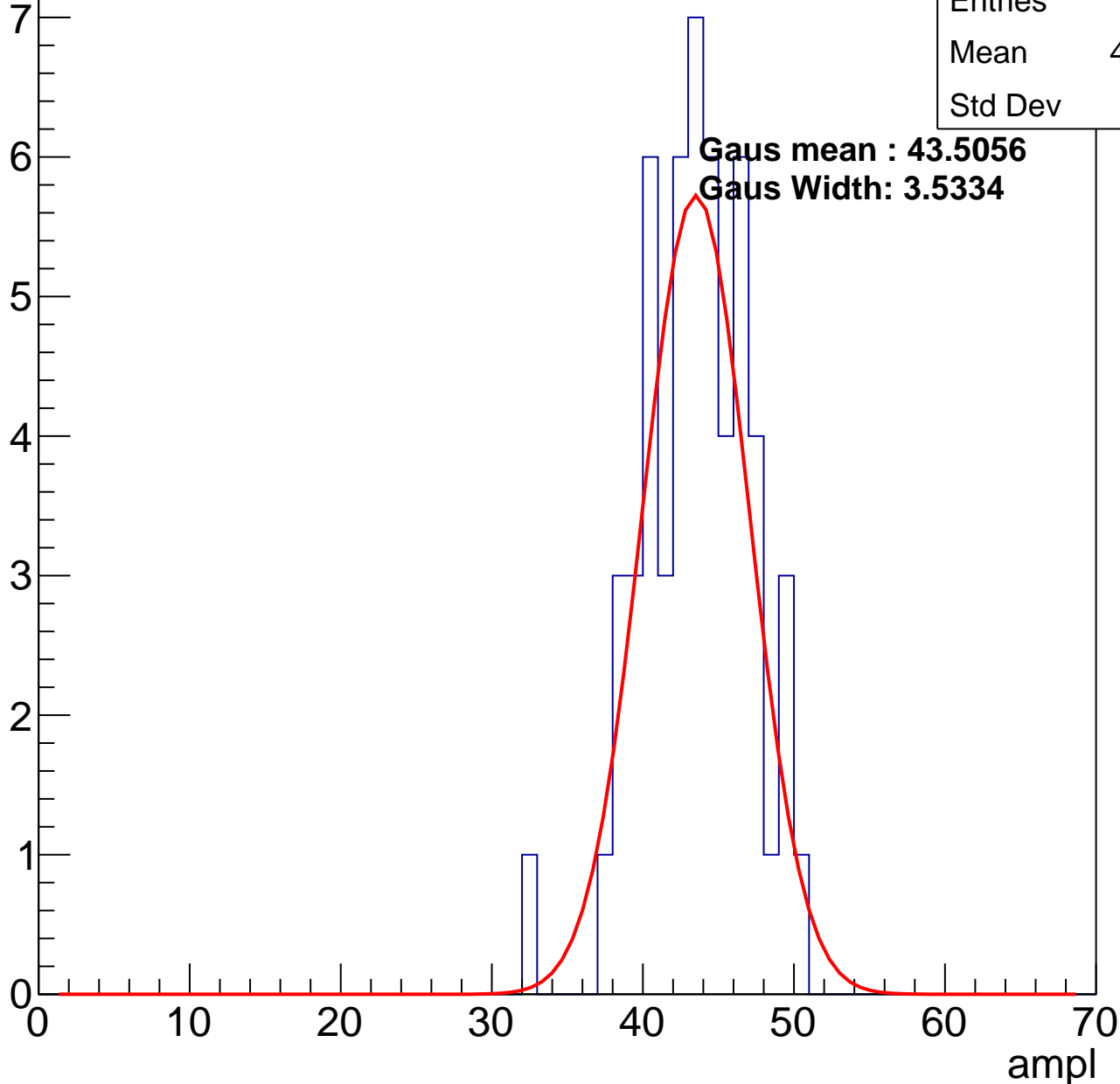


B1L103S, U20-ch49, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.07
Std Dev	3.5

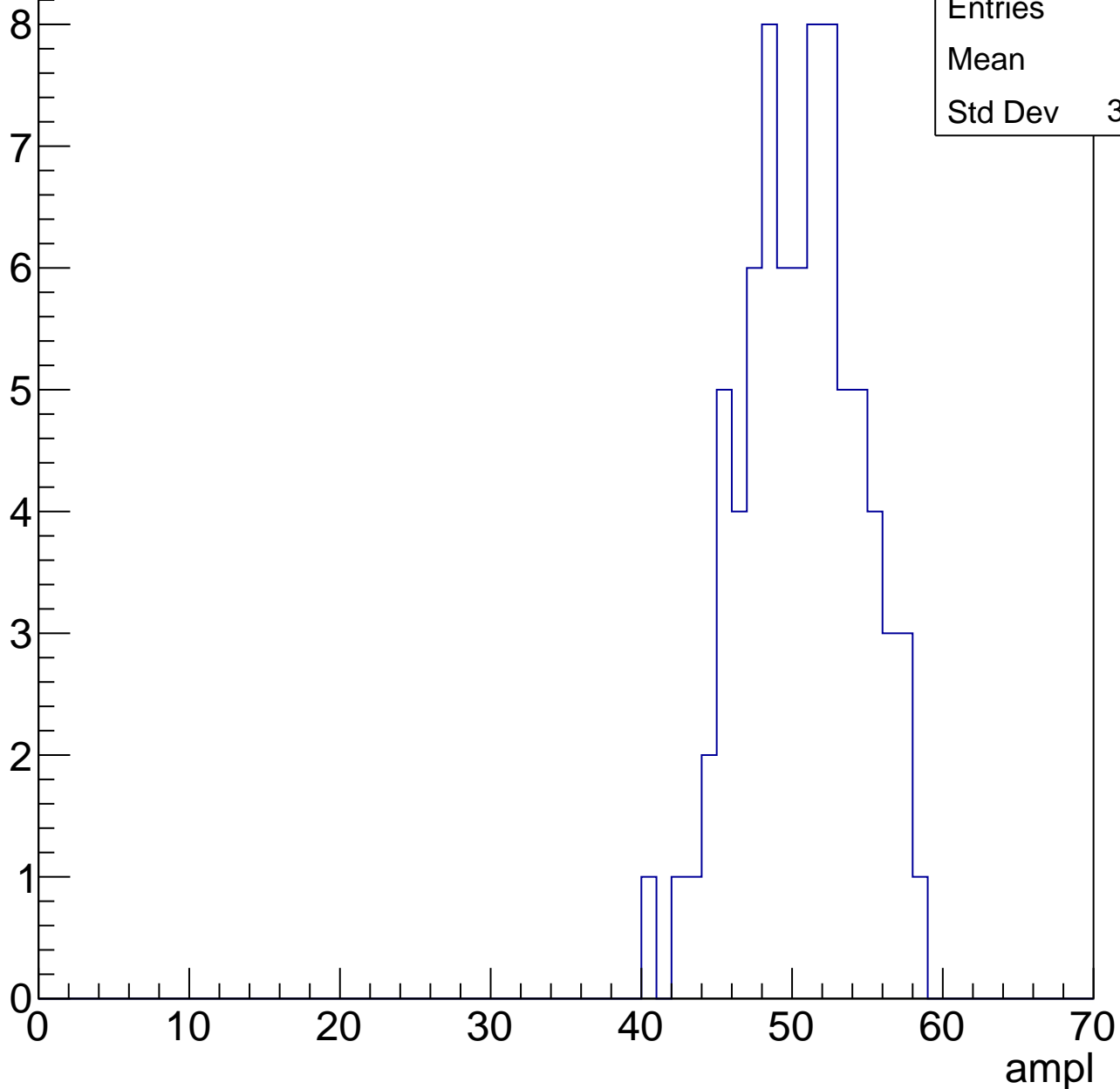


B1L103S, U20-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	50.1
Std Dev	3.863

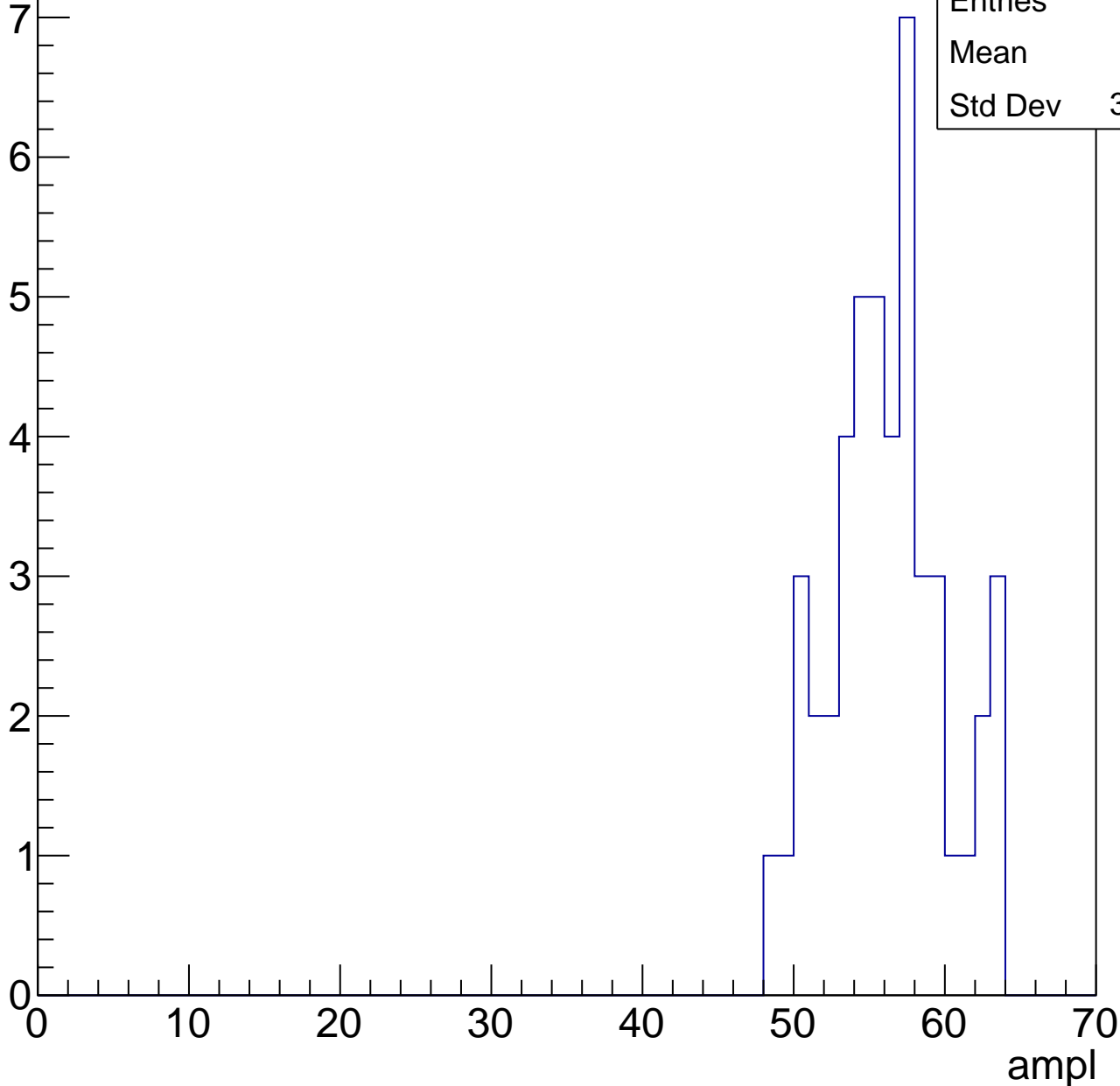


B1L103S, U20-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

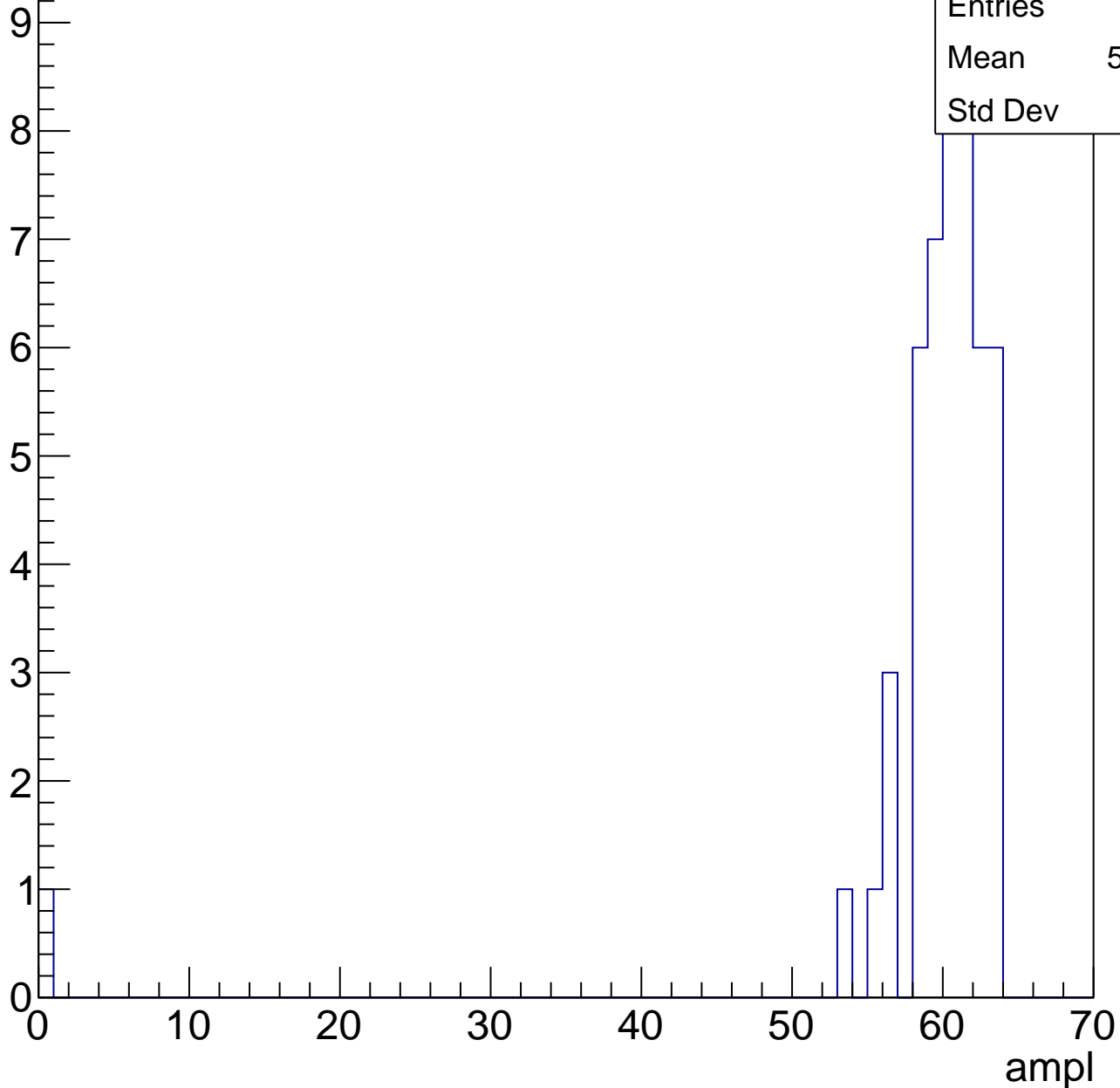
Entries	47
Mean	55.7
Std Dev	3.764



B1L103S, U20-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

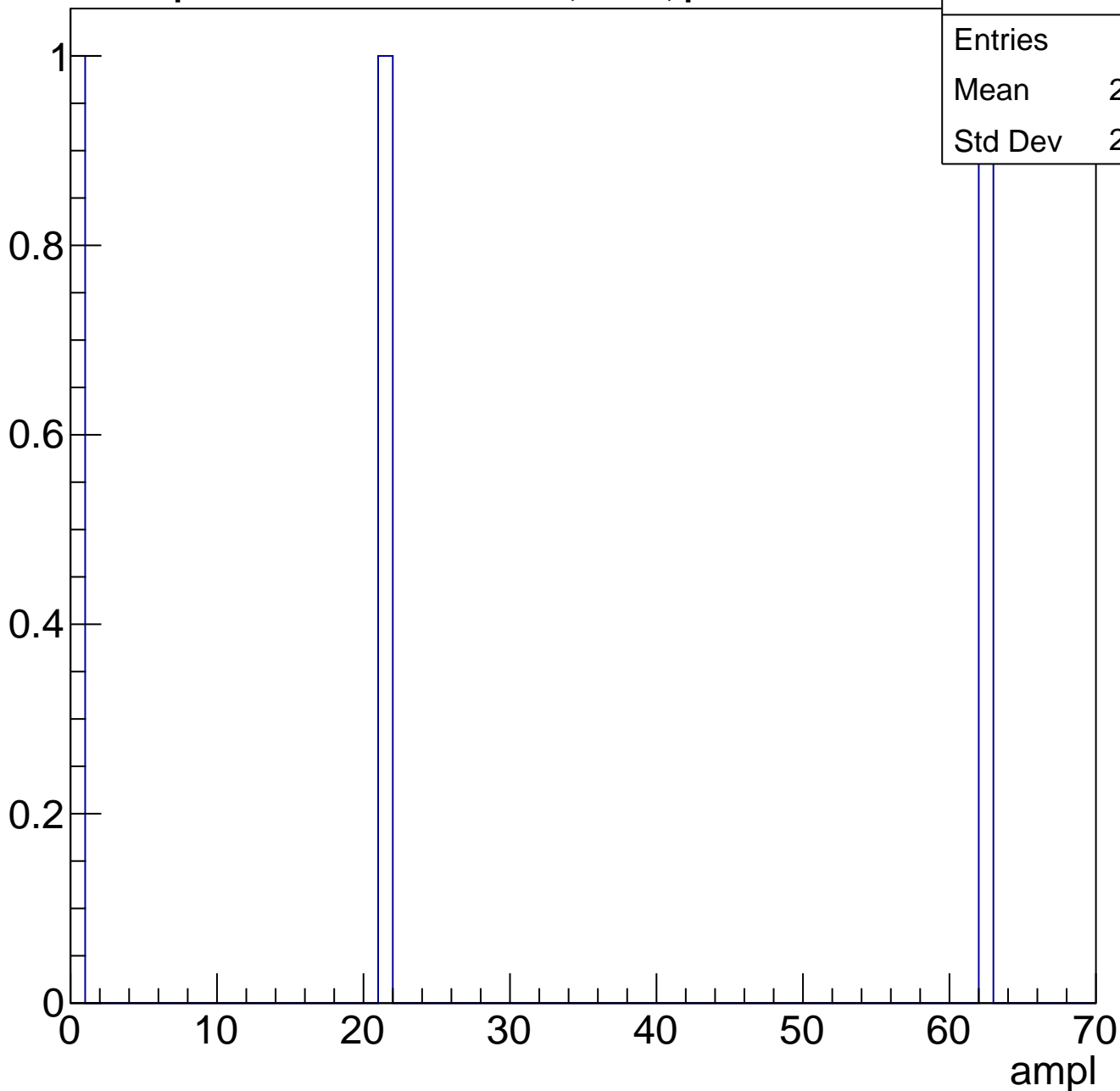
Entry



B1L103S, U20-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B1L103S, U20-ch50, adc0

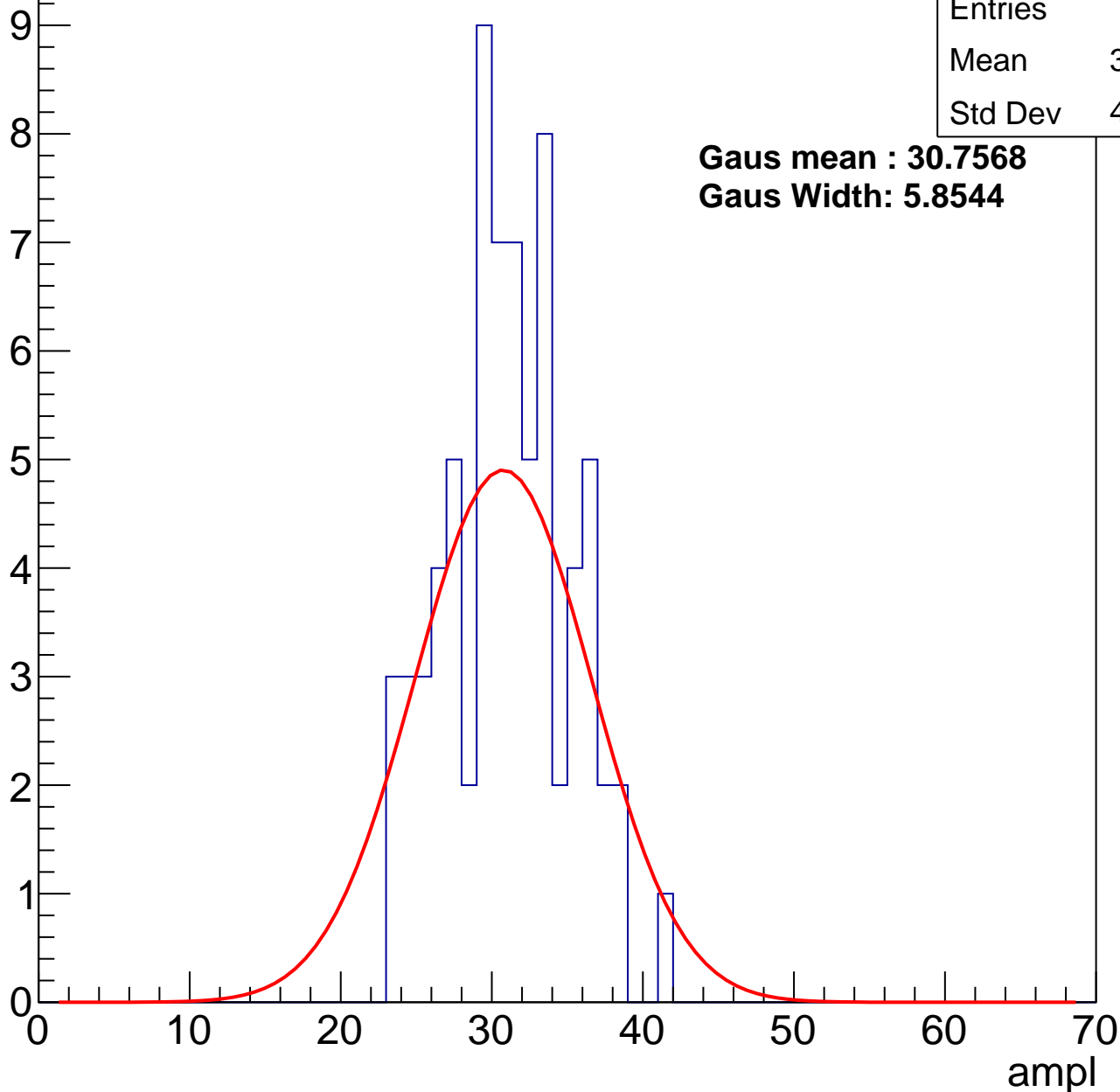
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.58
Std Dev	4.065

Gaus mean : 30.7568

Gaus Width: 5.8544



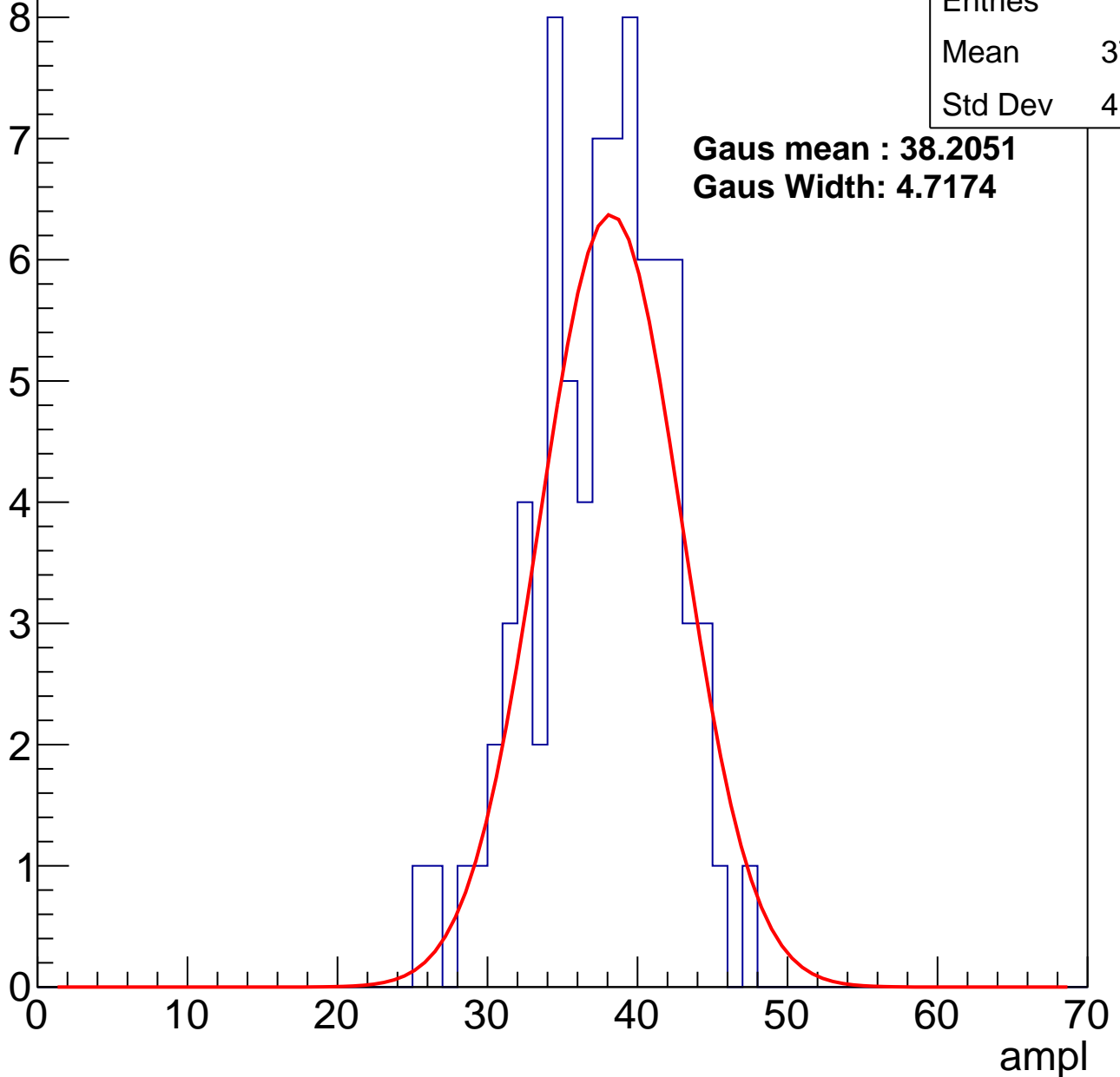
B1L103S, U20-ch50, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	37.17
Std Dev	4.472

Gaus mean : 38.2051
Gaus Width: 4.7174



B1L103S, U20-ch50, adc2

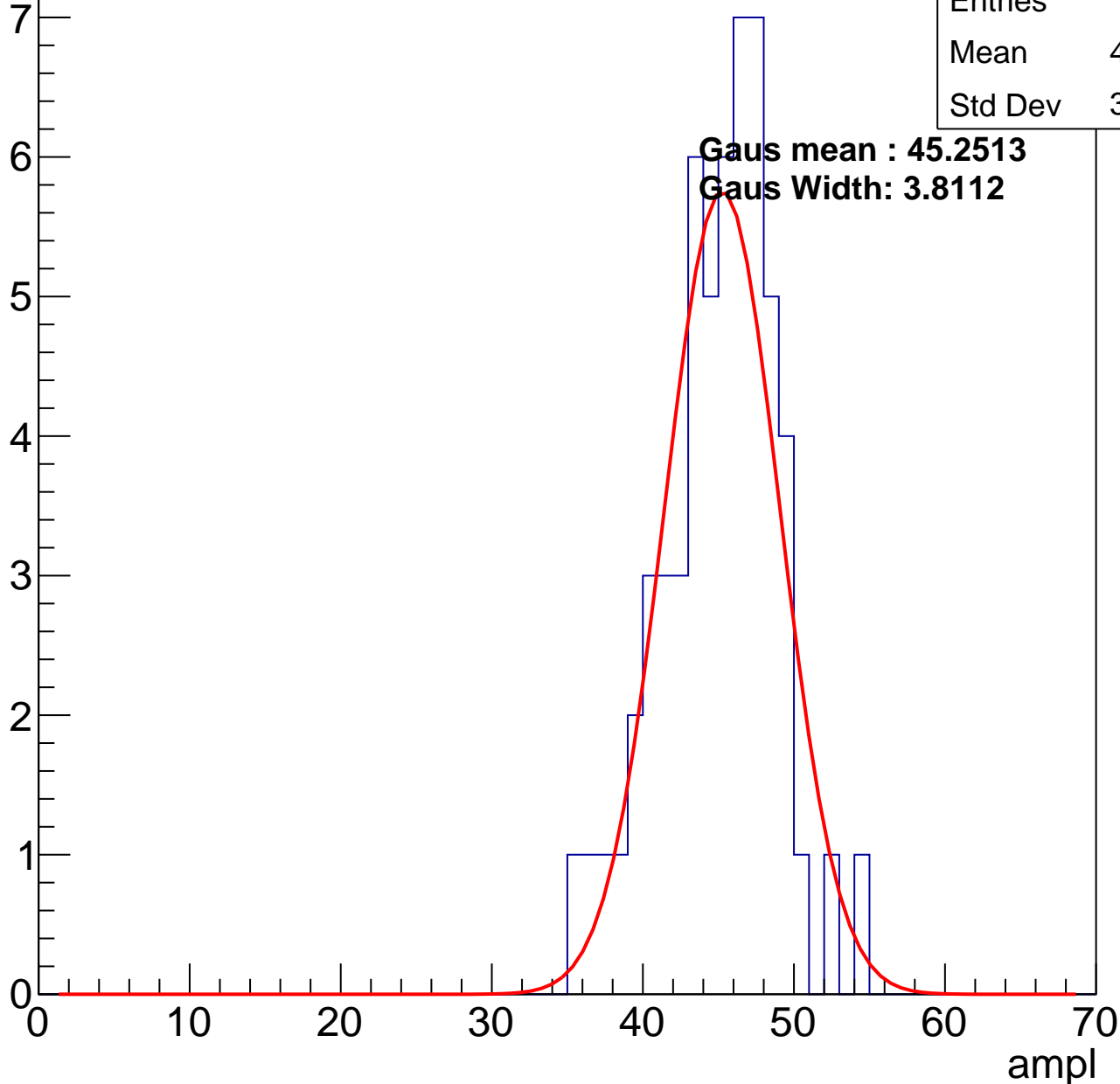
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.55
Std Dev	3.788

Gaus mean : 45.2513

Gaus Width: 3.8112

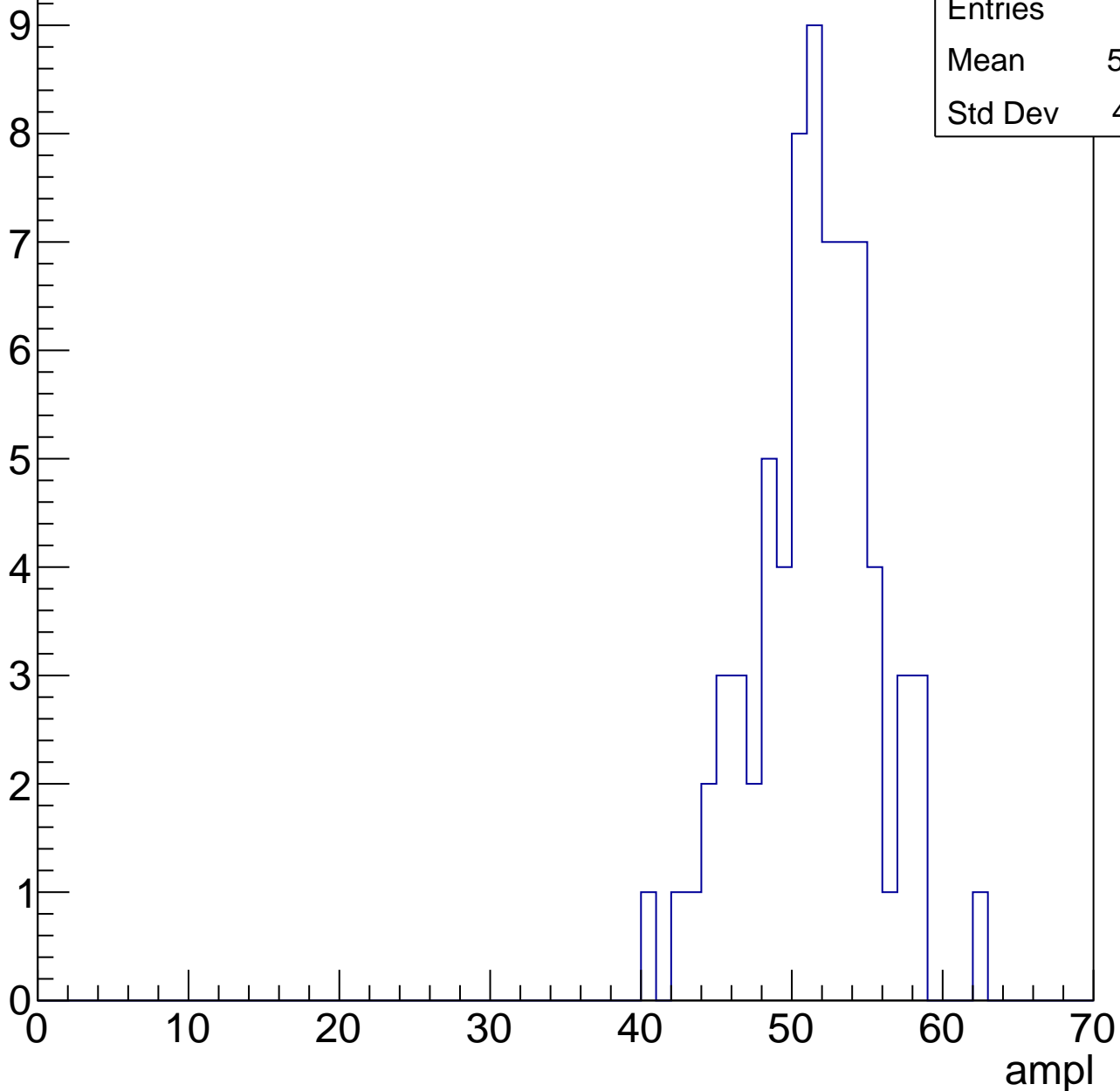


B1L103S, U20-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	50.99
Std Dev	4.101

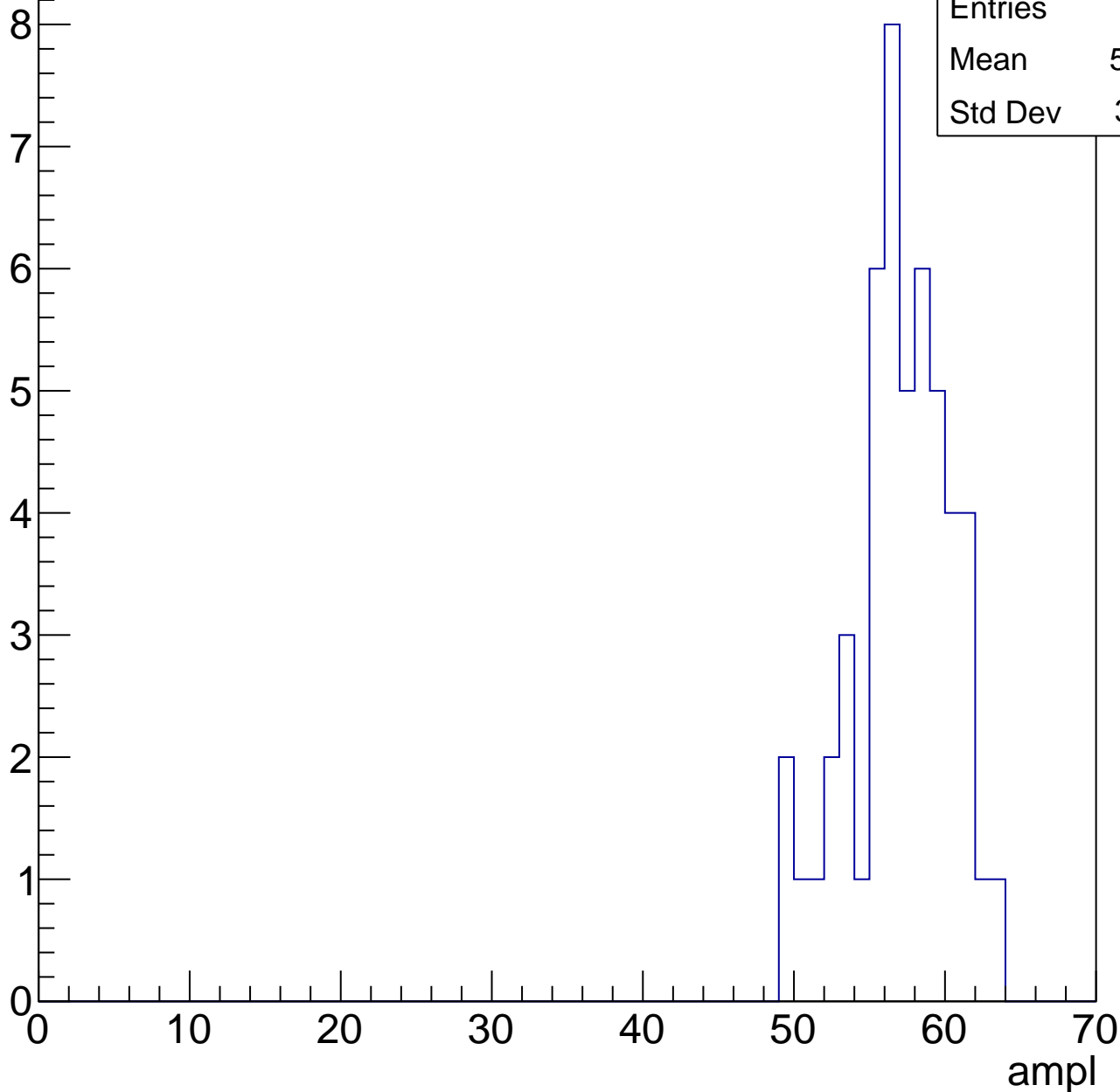


B1L103S, U20-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	56.62
Std Dev	3.261

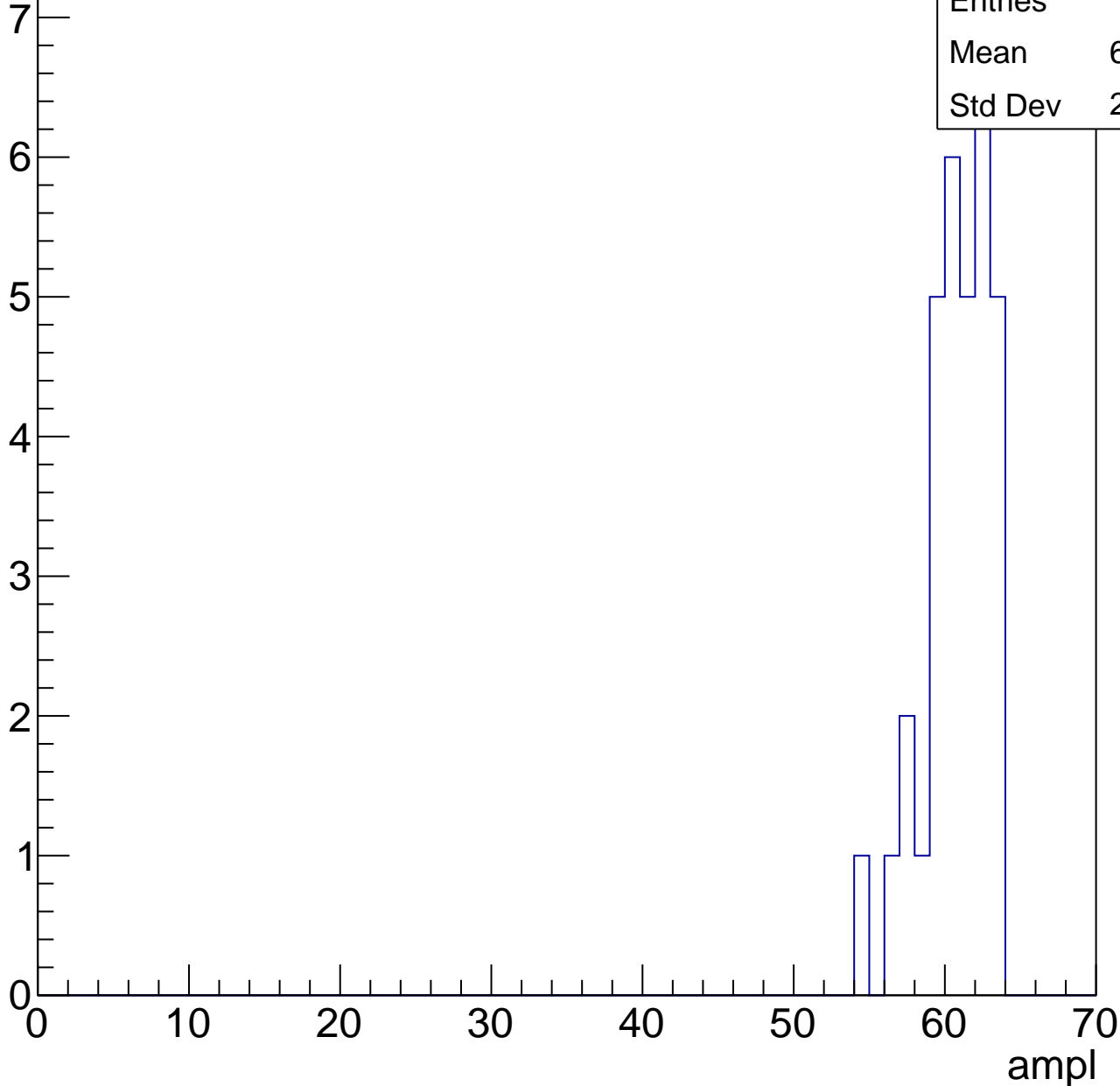


B1L103S, U20-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

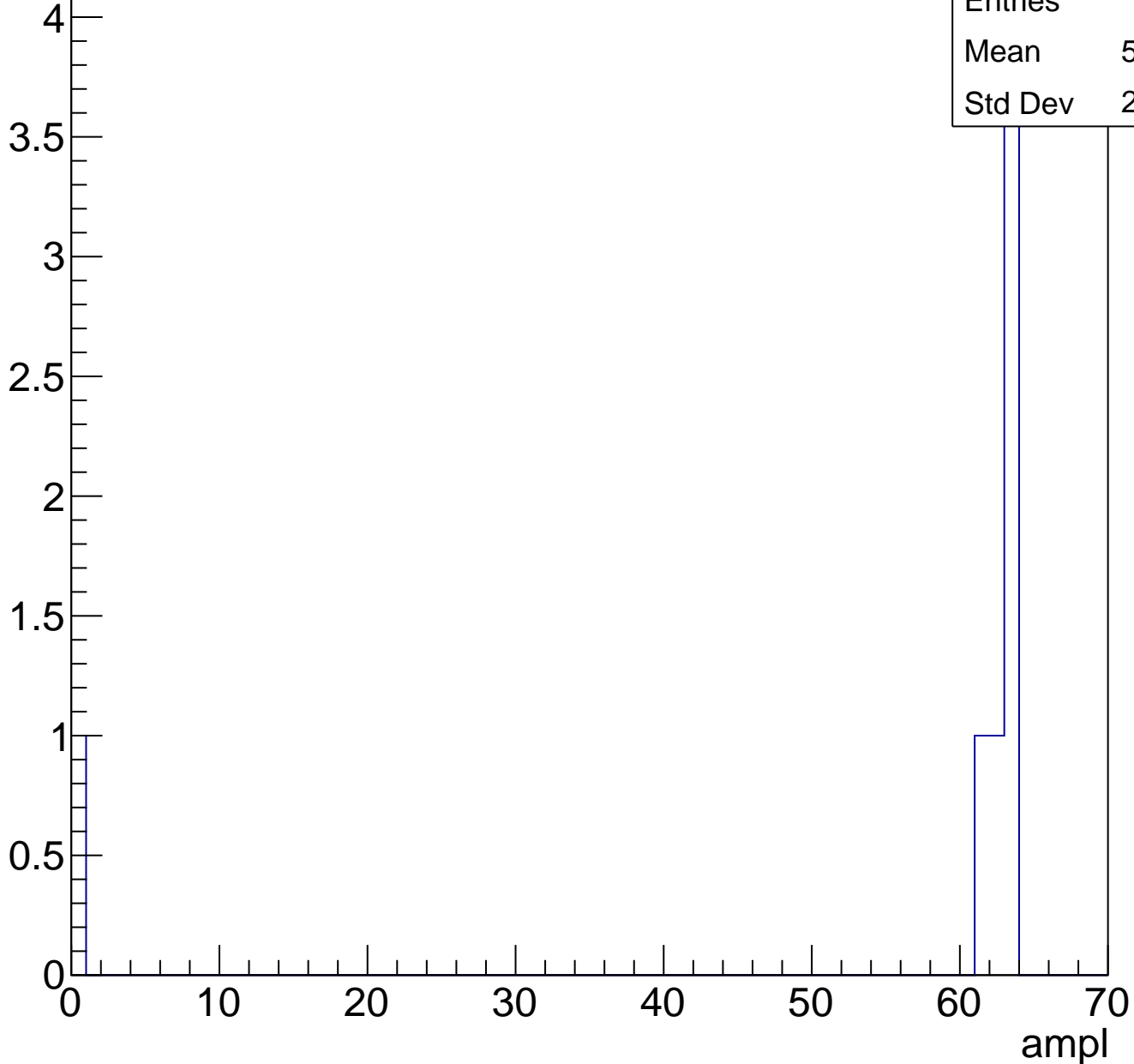
Entries	33
Mean	60.33
Std Dev	2.156



B1L103S, U20-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch51, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	29.23
Std Dev	5.291

Gaus mean : 30.0586

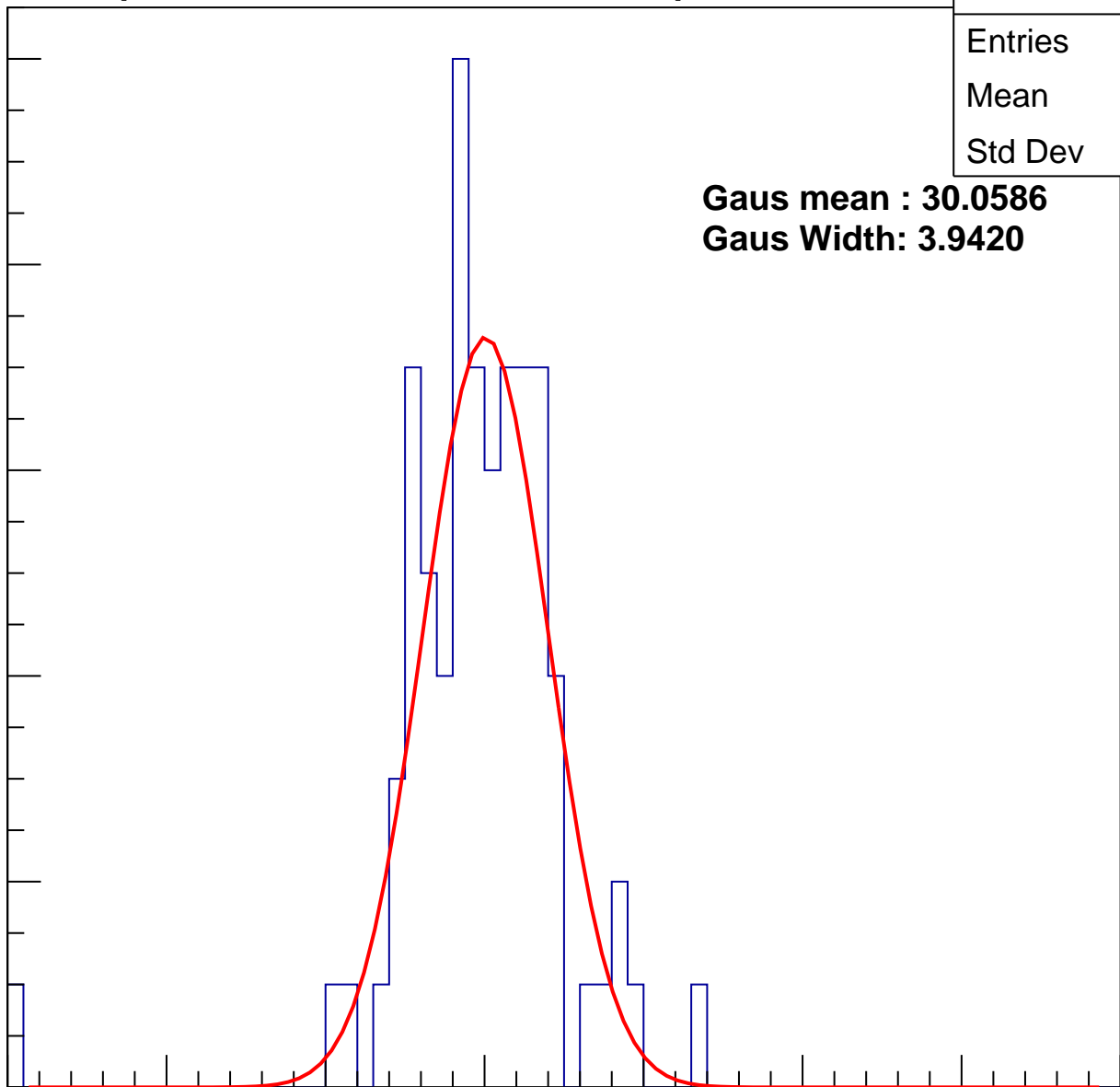
Gaus Width: 3.9420

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch51, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	36.28
Std Dev	4.014

Gaus mean : 36.6524

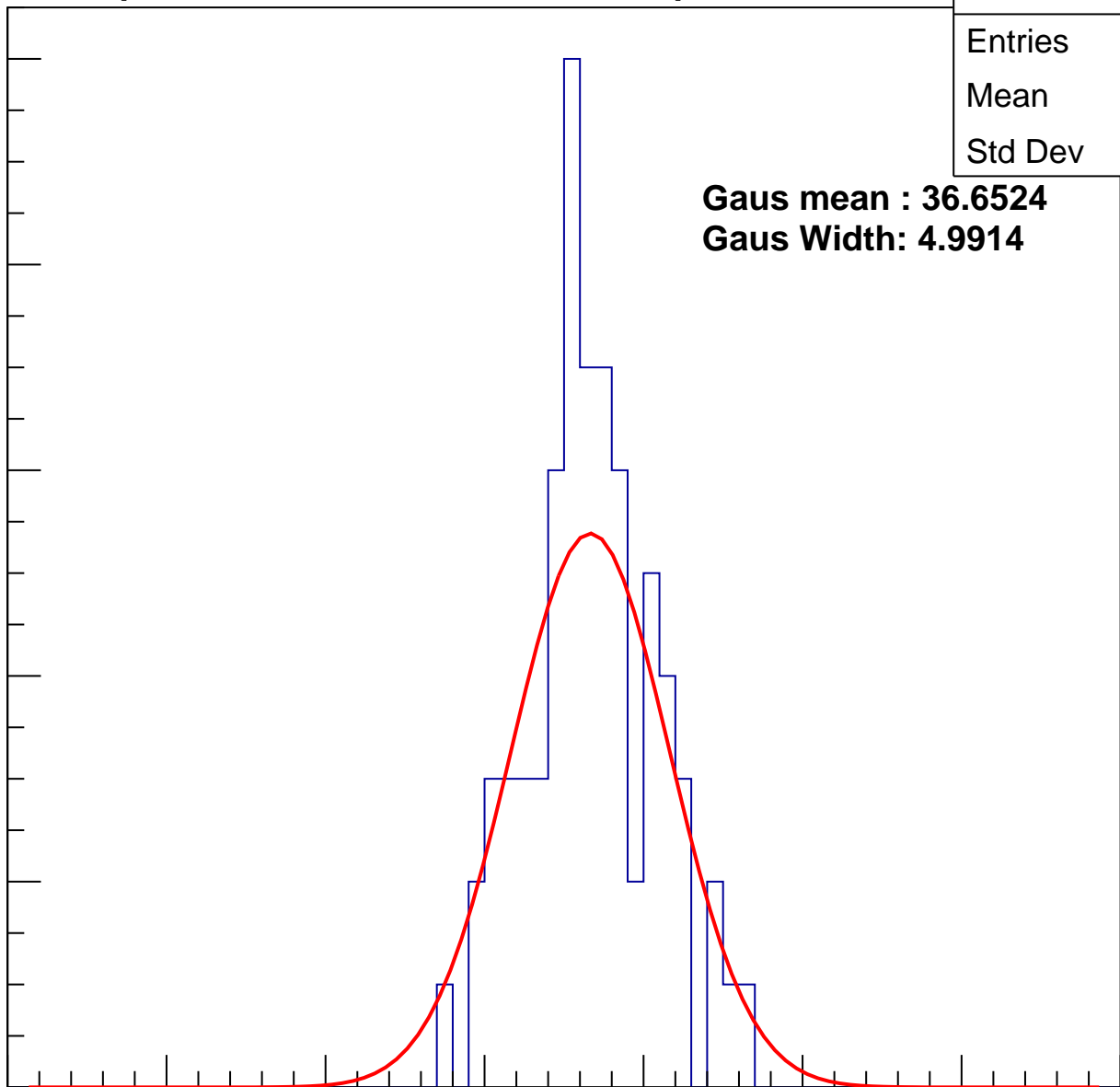
Gaus Width: 4.9914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch51, adc2

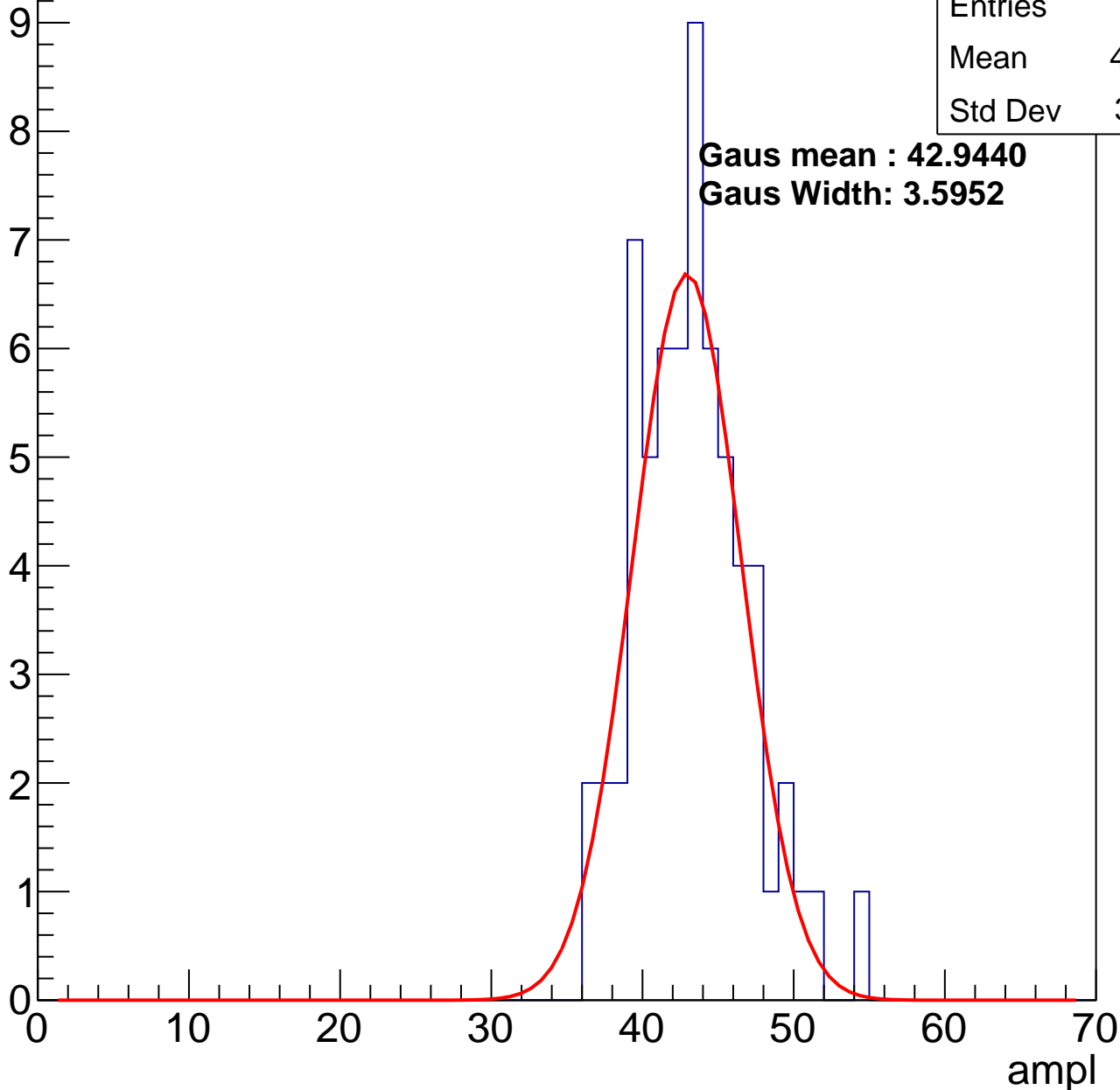
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.84
Std Dev	3.671

Gaus mean : 42.9440

Gaus Width: 3.5952

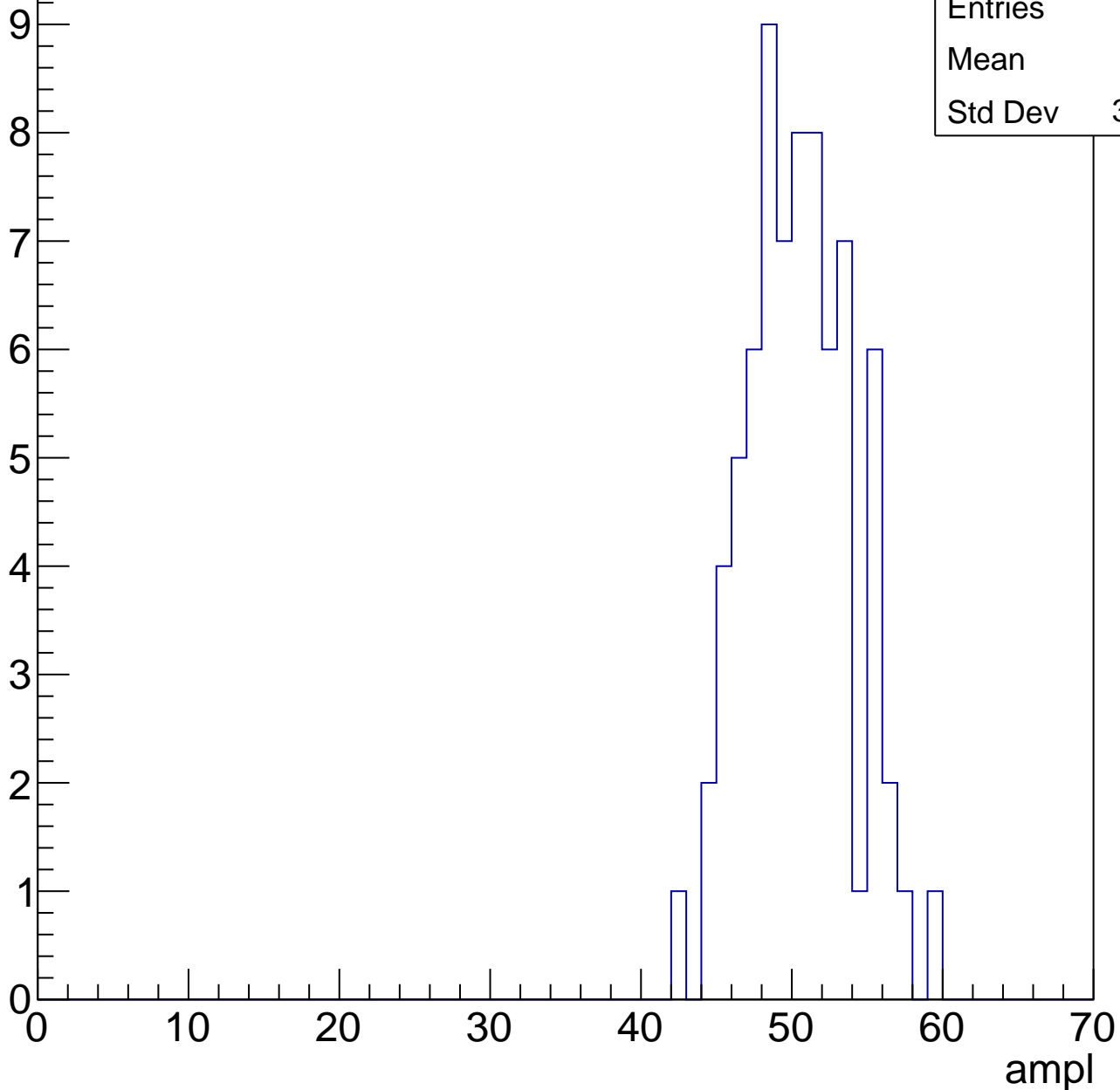


B1L103S, U20-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	50
Std Dev	3.441

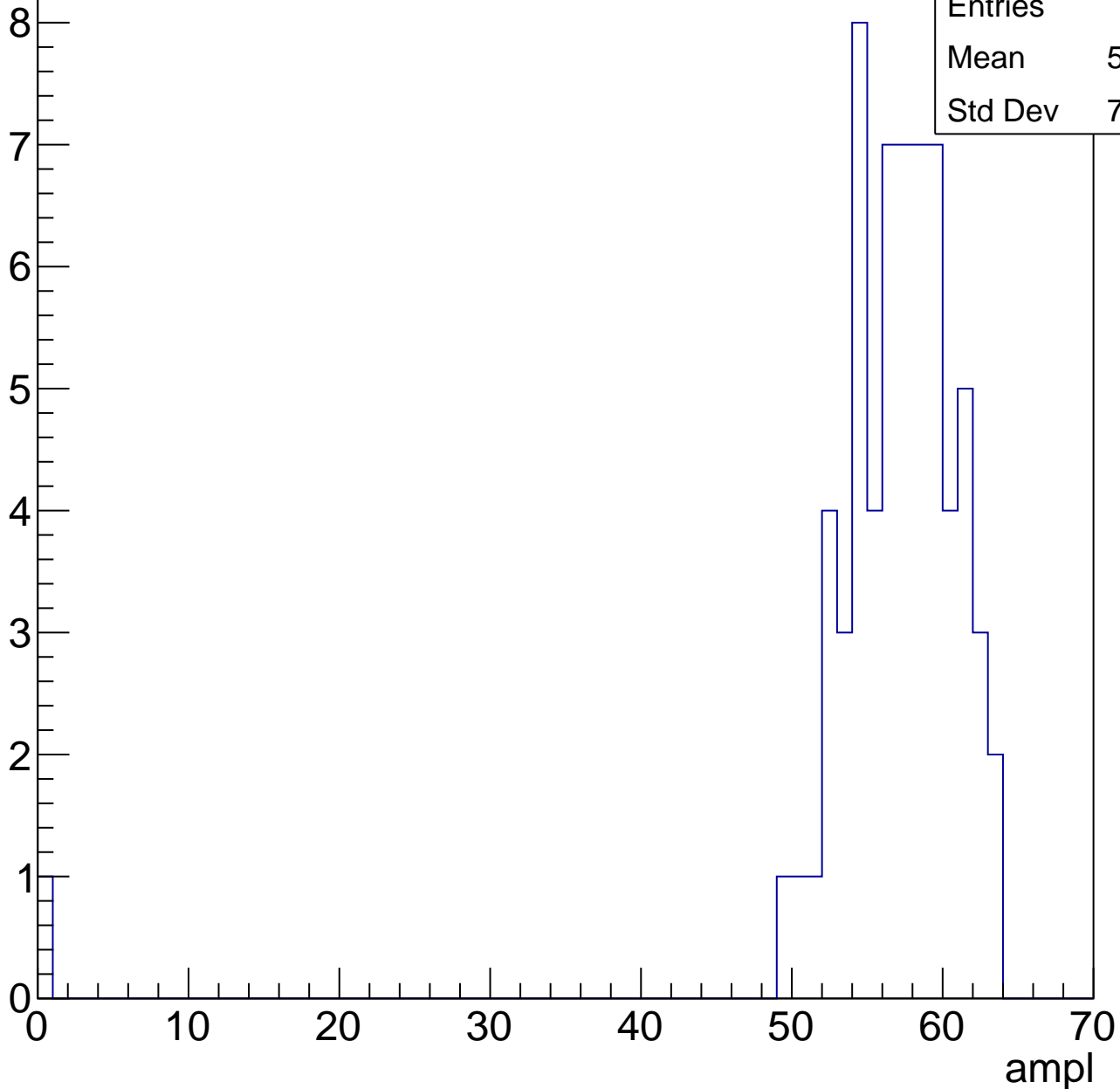


B1L103S, U20-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

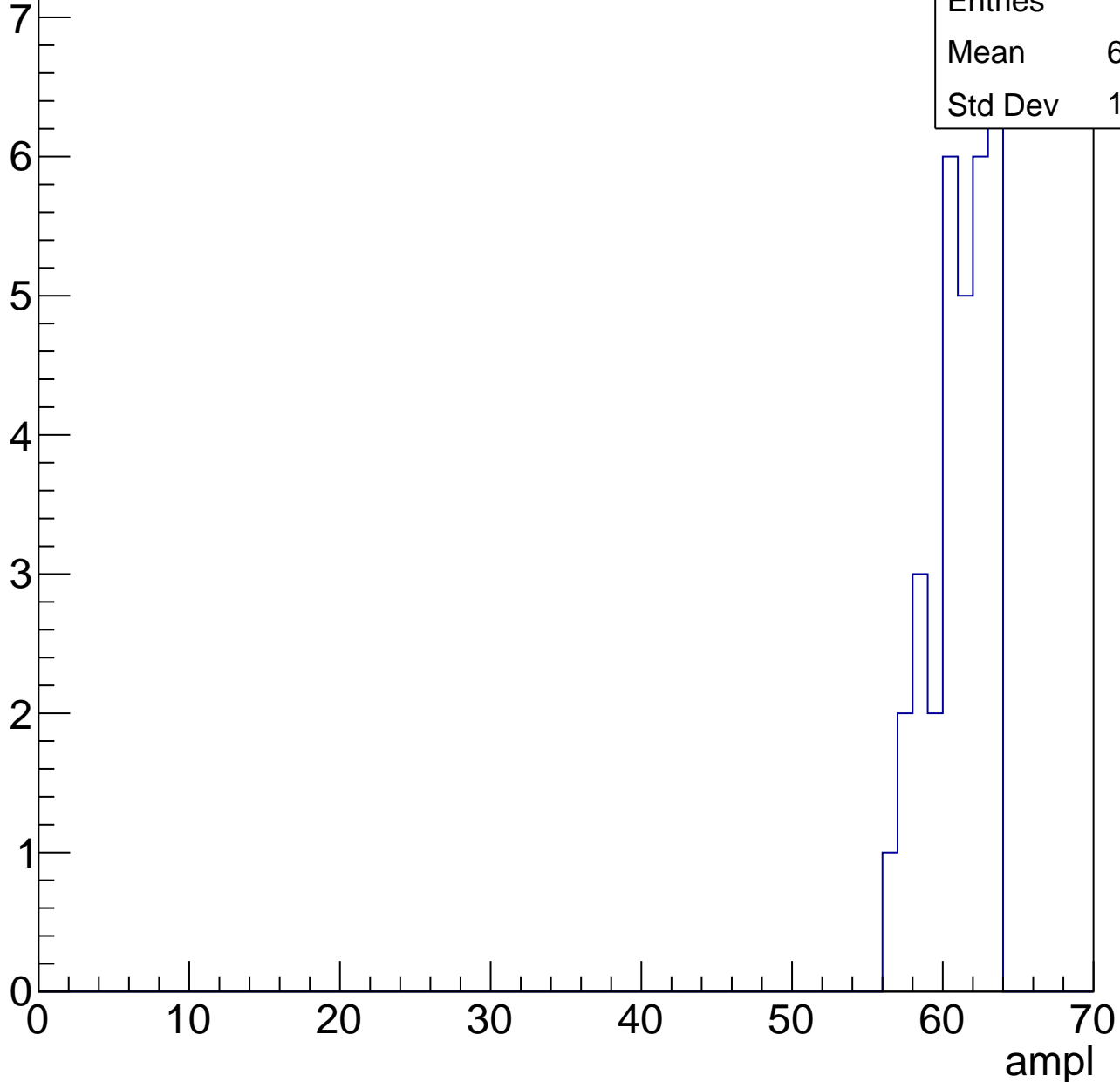
Entries	65
Mean	55.94
Std Dev	7.712



B1L103S, U20-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch52, adc0

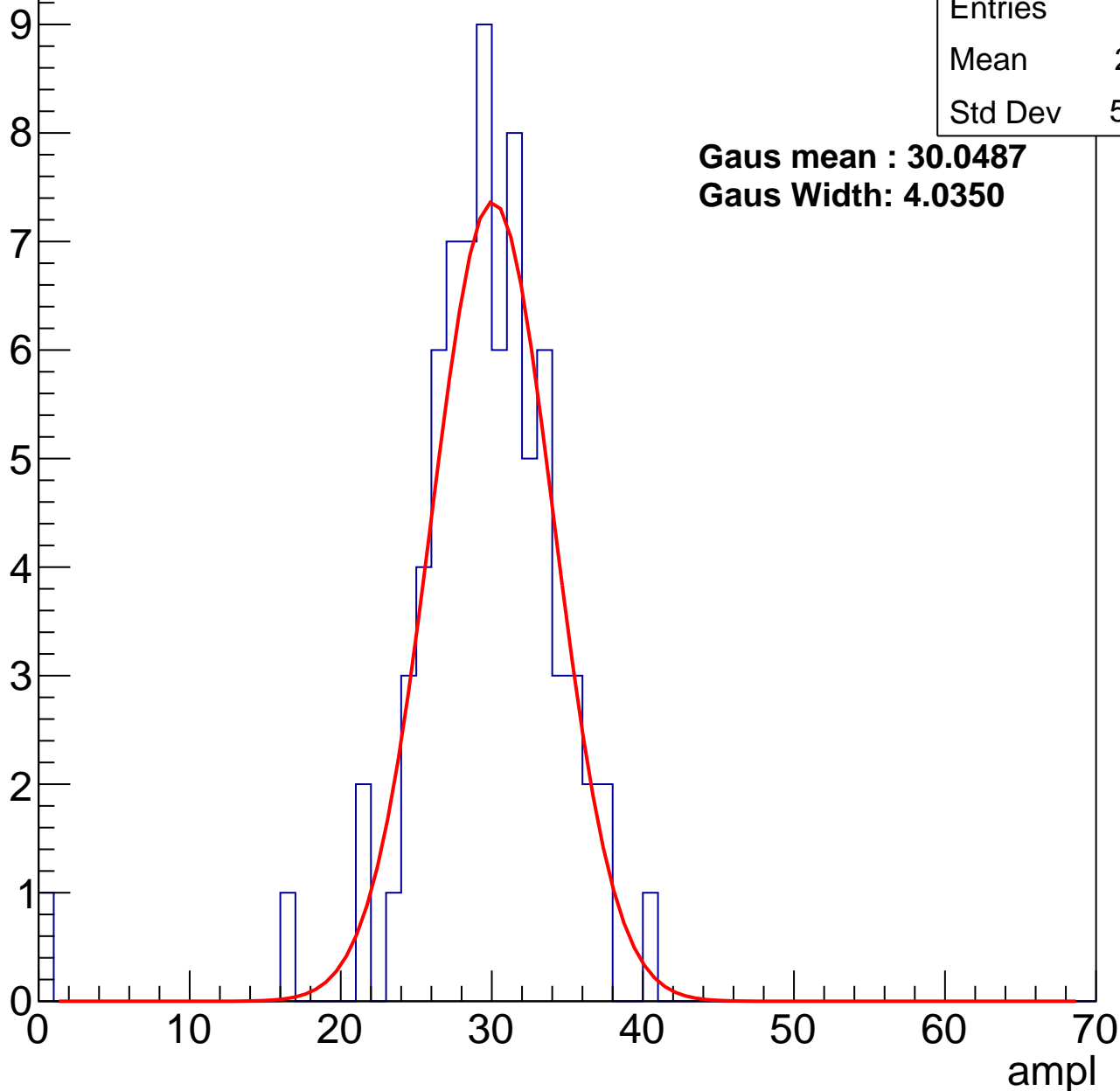
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	29.01
Std Dev	5.236

Gaus mean : 30.0487

Gaus Width: 4.0350



B1L103S, U20-ch52, adc1

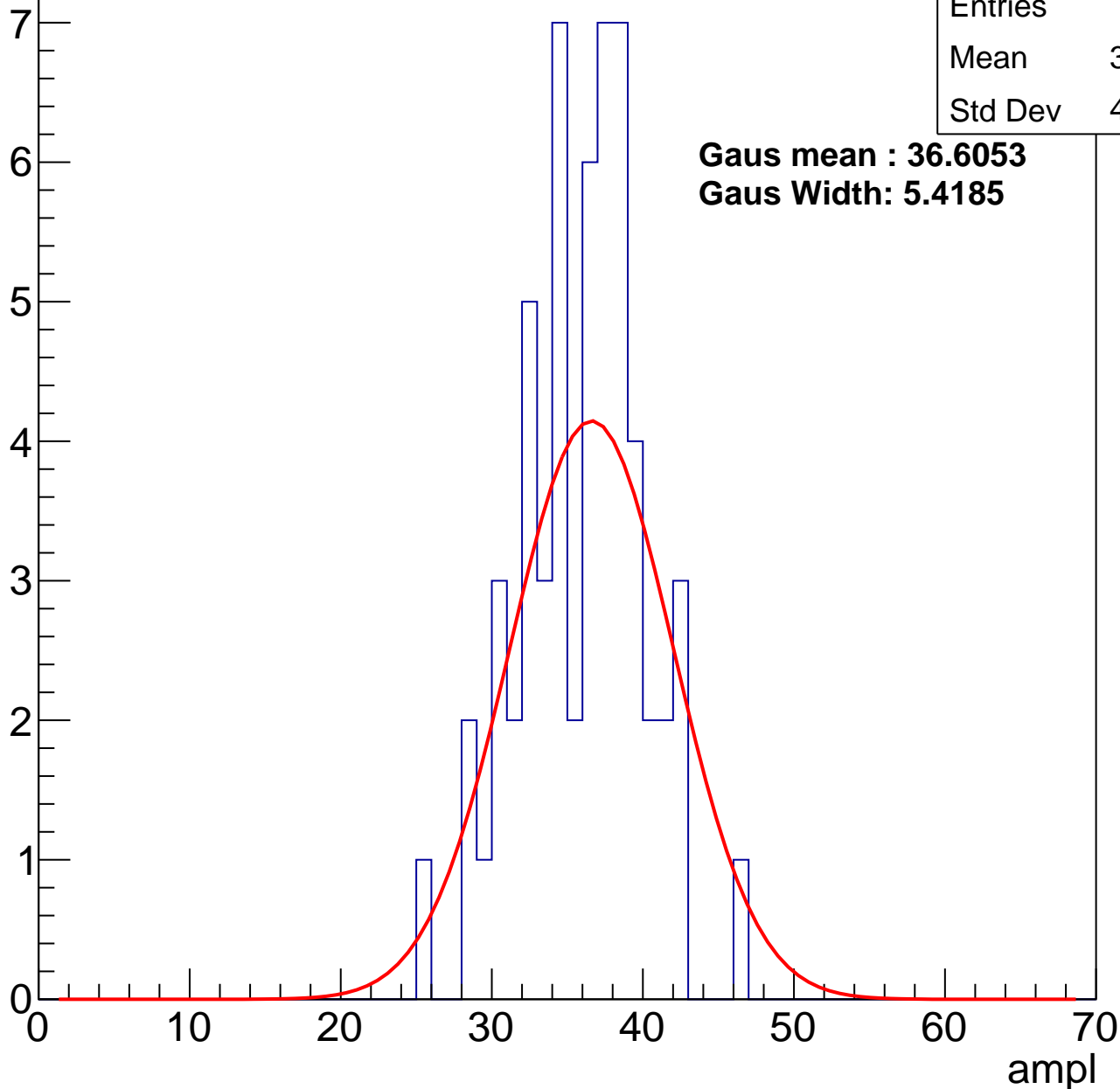
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.52
Std Dev	4.027

Gaus mean : 36.6053

Gaus Width: 5.4185



B1L103S, U20-ch52, adc2

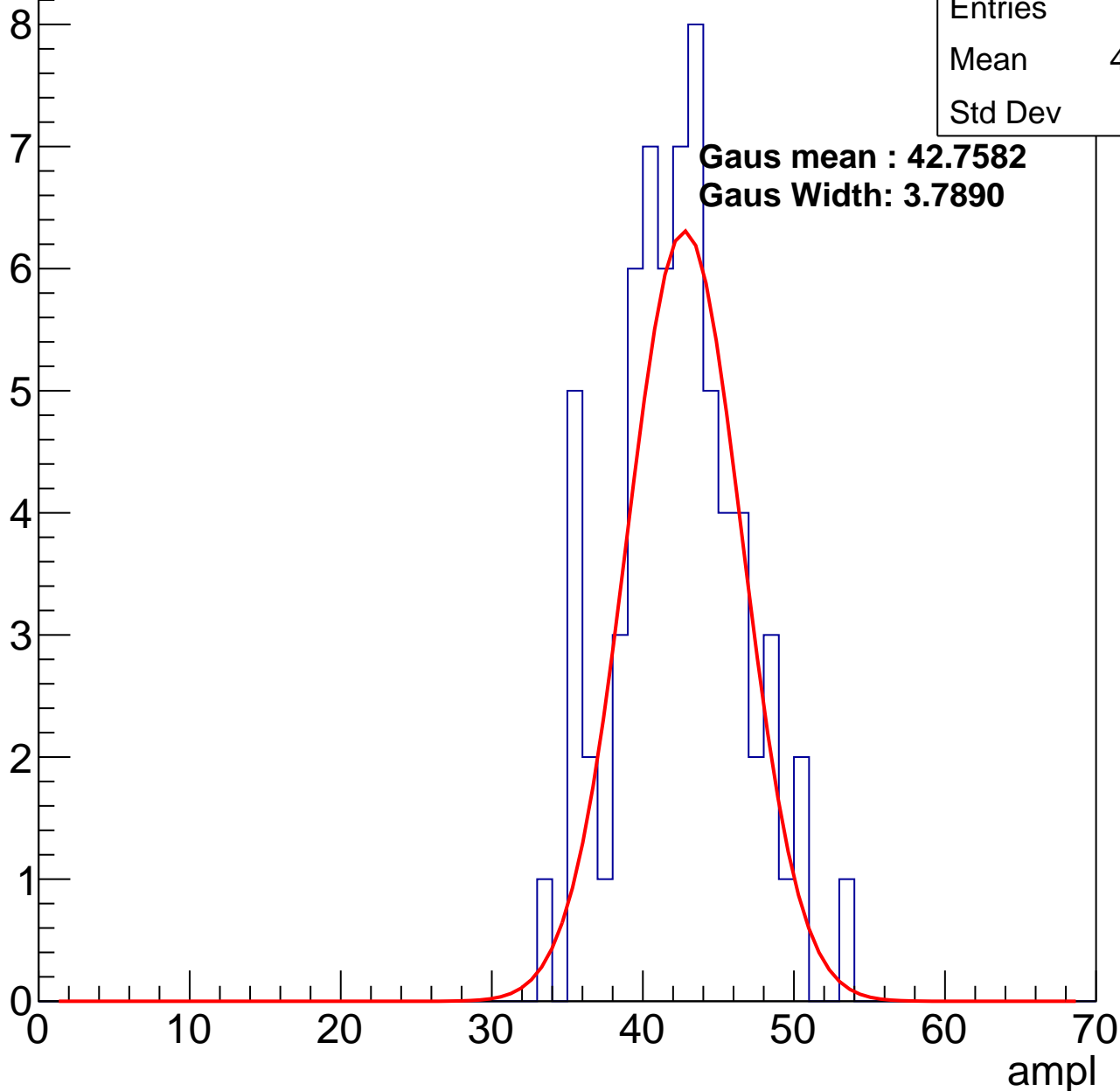
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	41.96
Std Dev	4.1

Gaus mean : 42.7582

Gaus Width: 3.7890

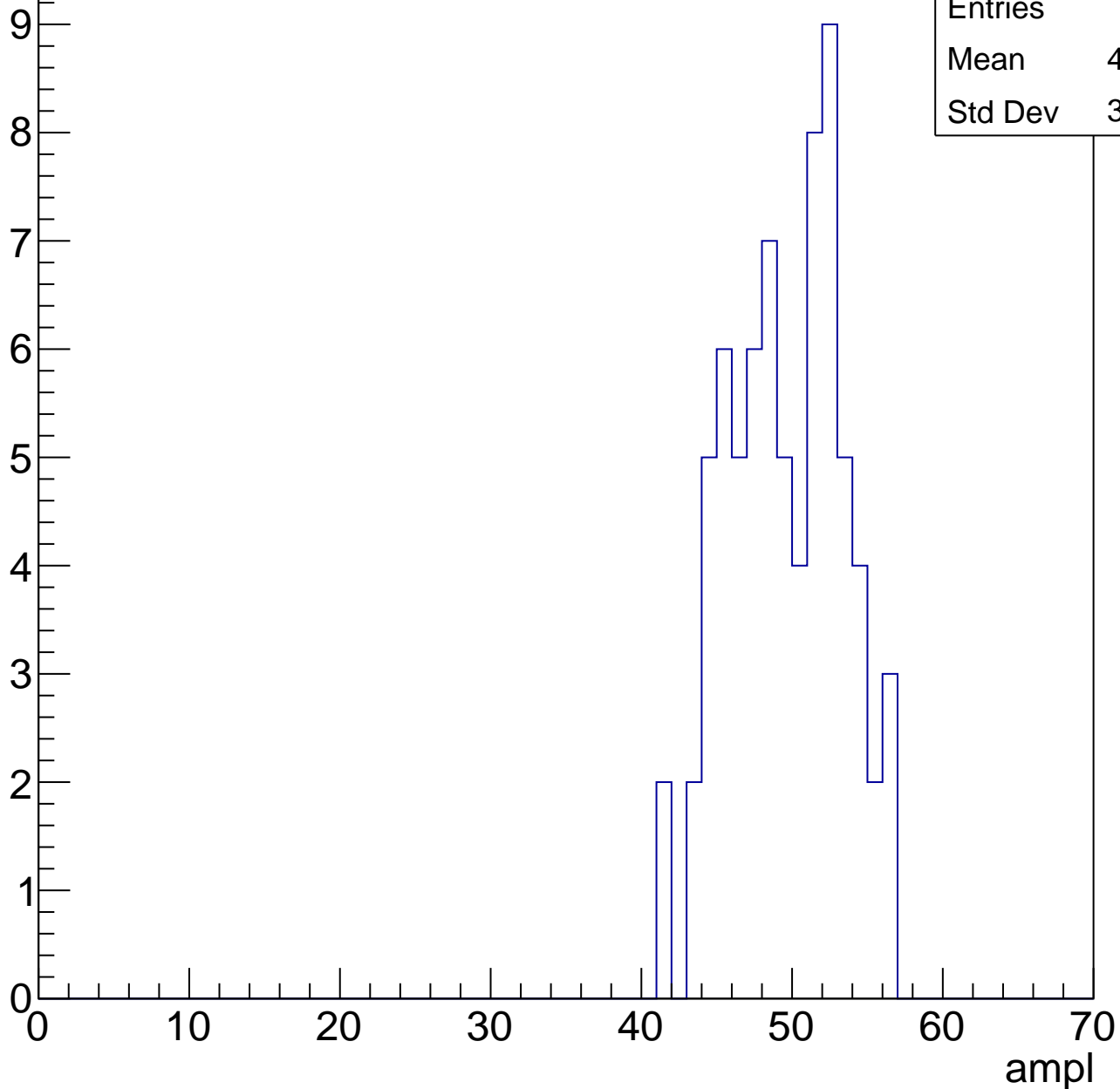


B1L103S, U20-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	49.12
Std Dev	3.723

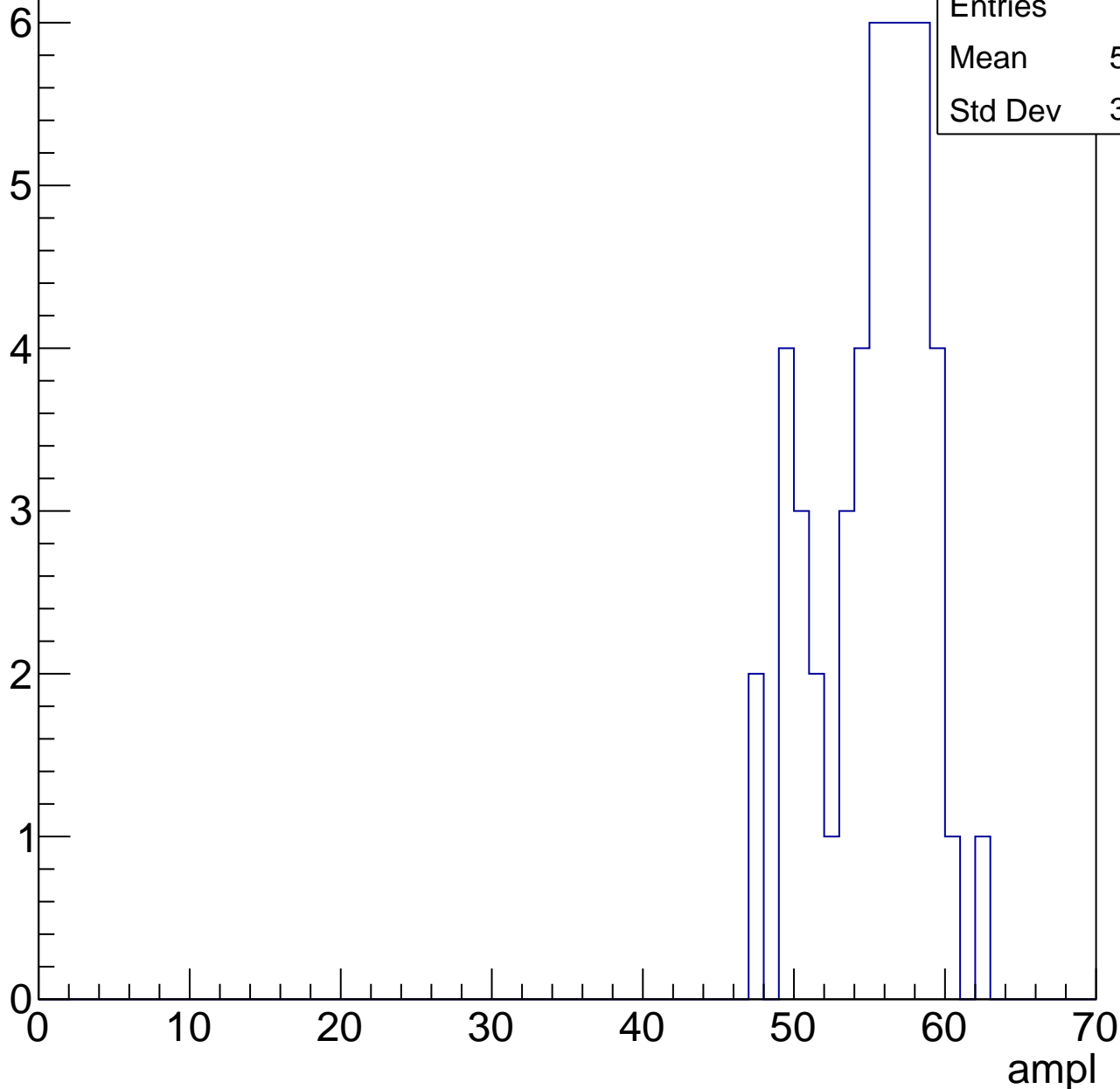


B1L103S, U20-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.76
Std Dev	3.566



B1L103S, U20-ch52, adc5

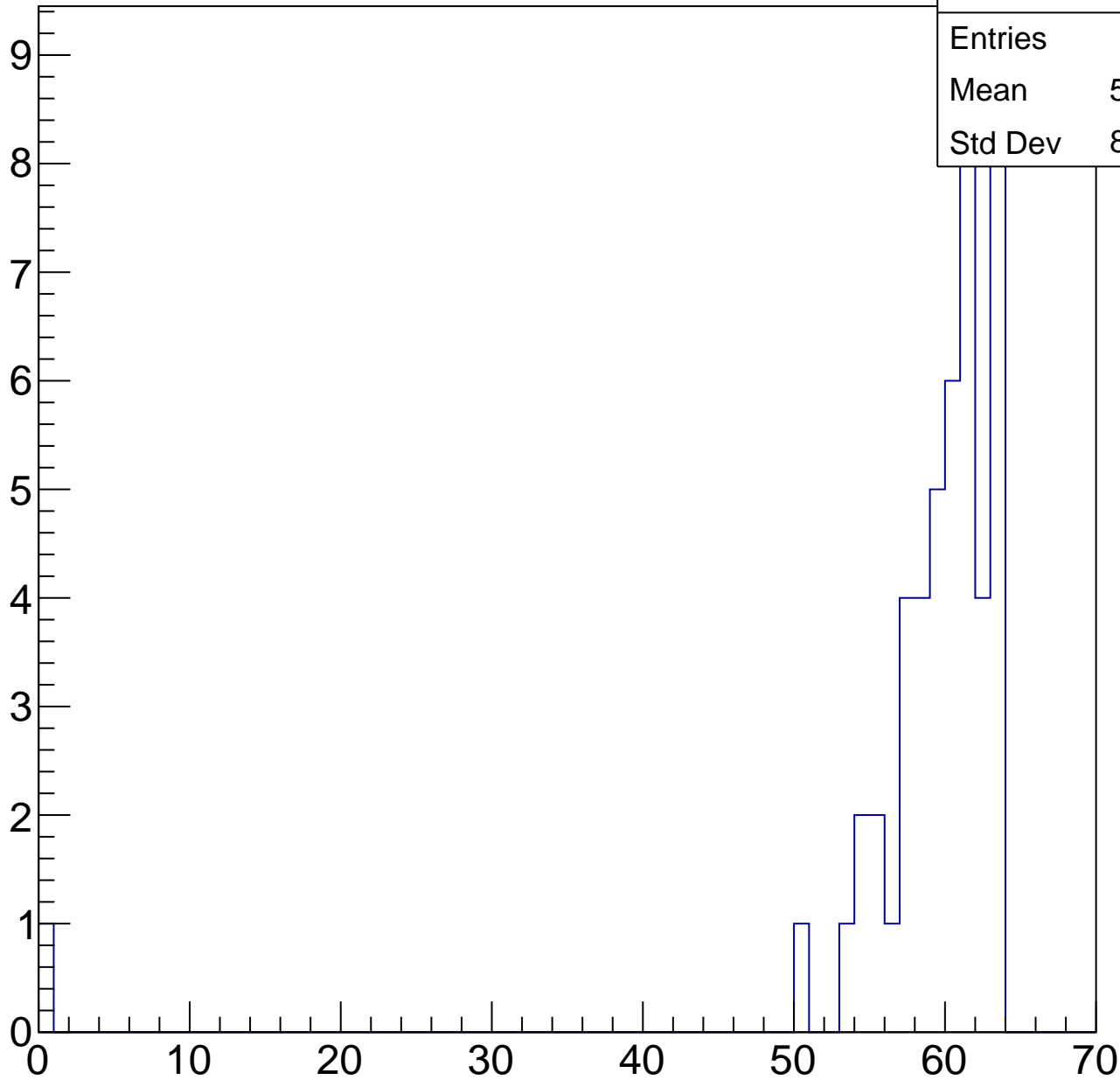
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.29
Std Dev	8.928

ampl

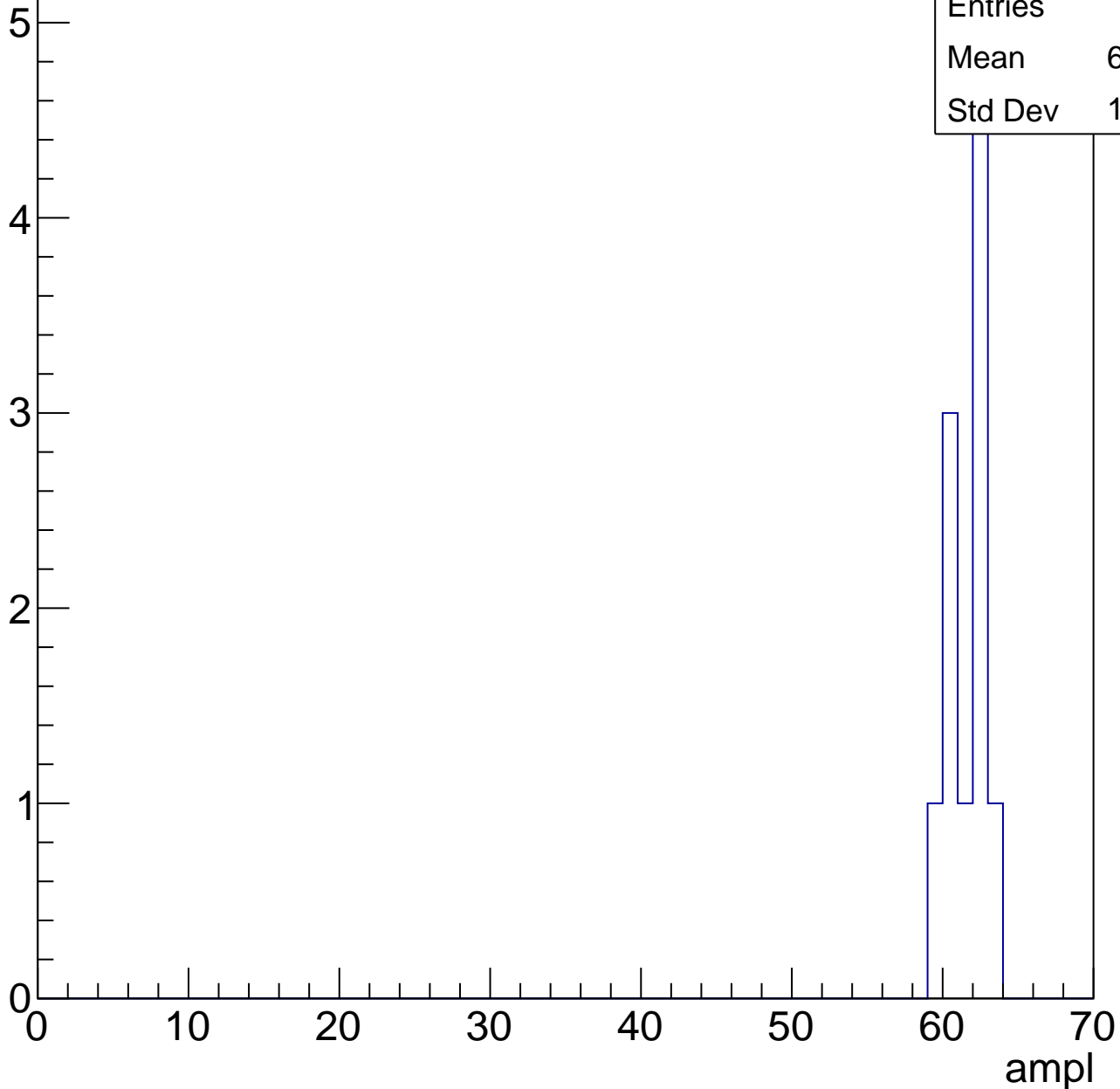


B1L103S, U20-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	61.18
Std Dev	1.192



B1L103S, U20-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U20-ch53, adc0

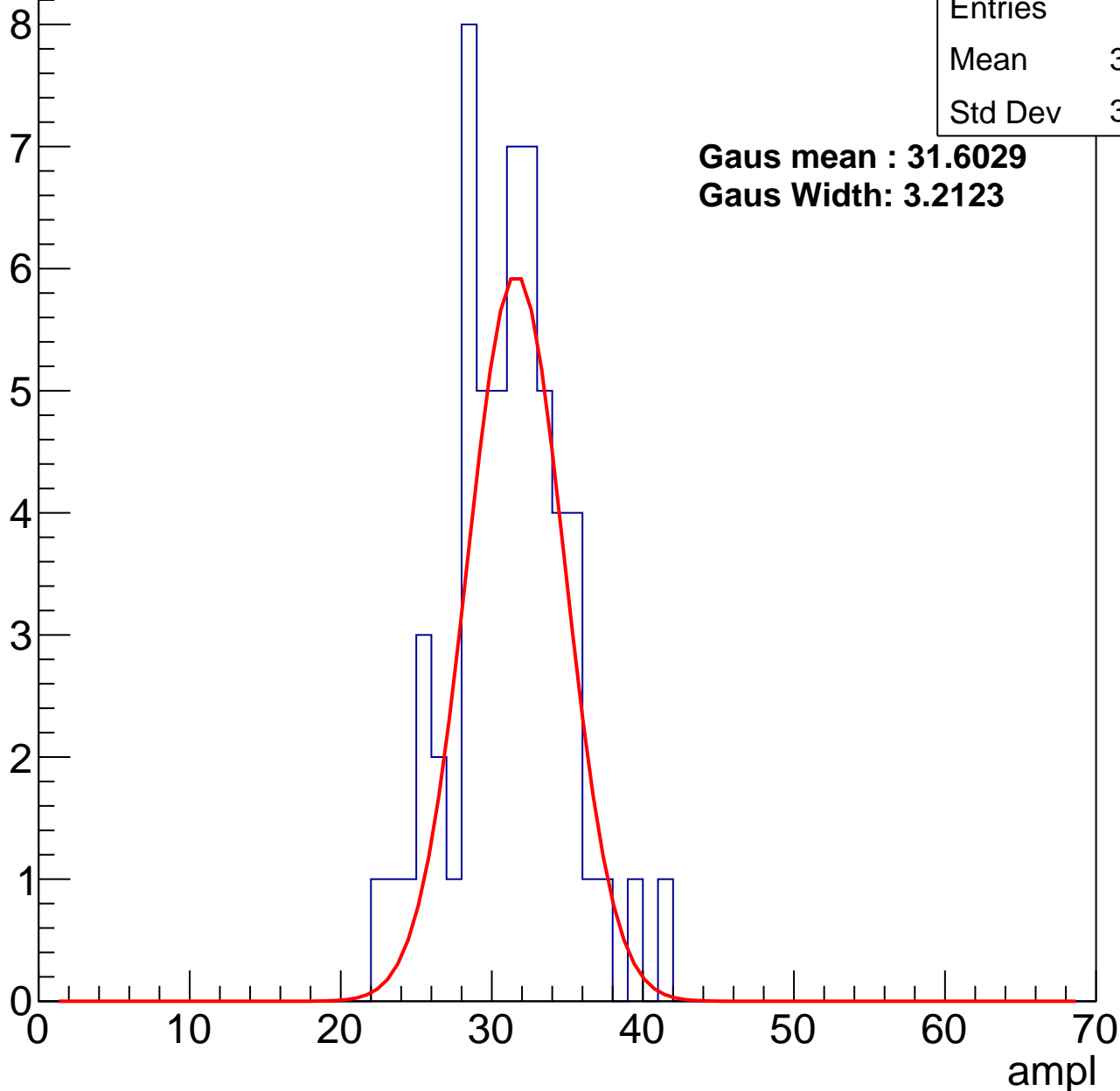
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	30.64
Std Dev	3.736

Gaus mean : 31.6029

Gaus Width: 3.2123



B1L103S, U20-ch53, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	37.52
Std Dev	3.612

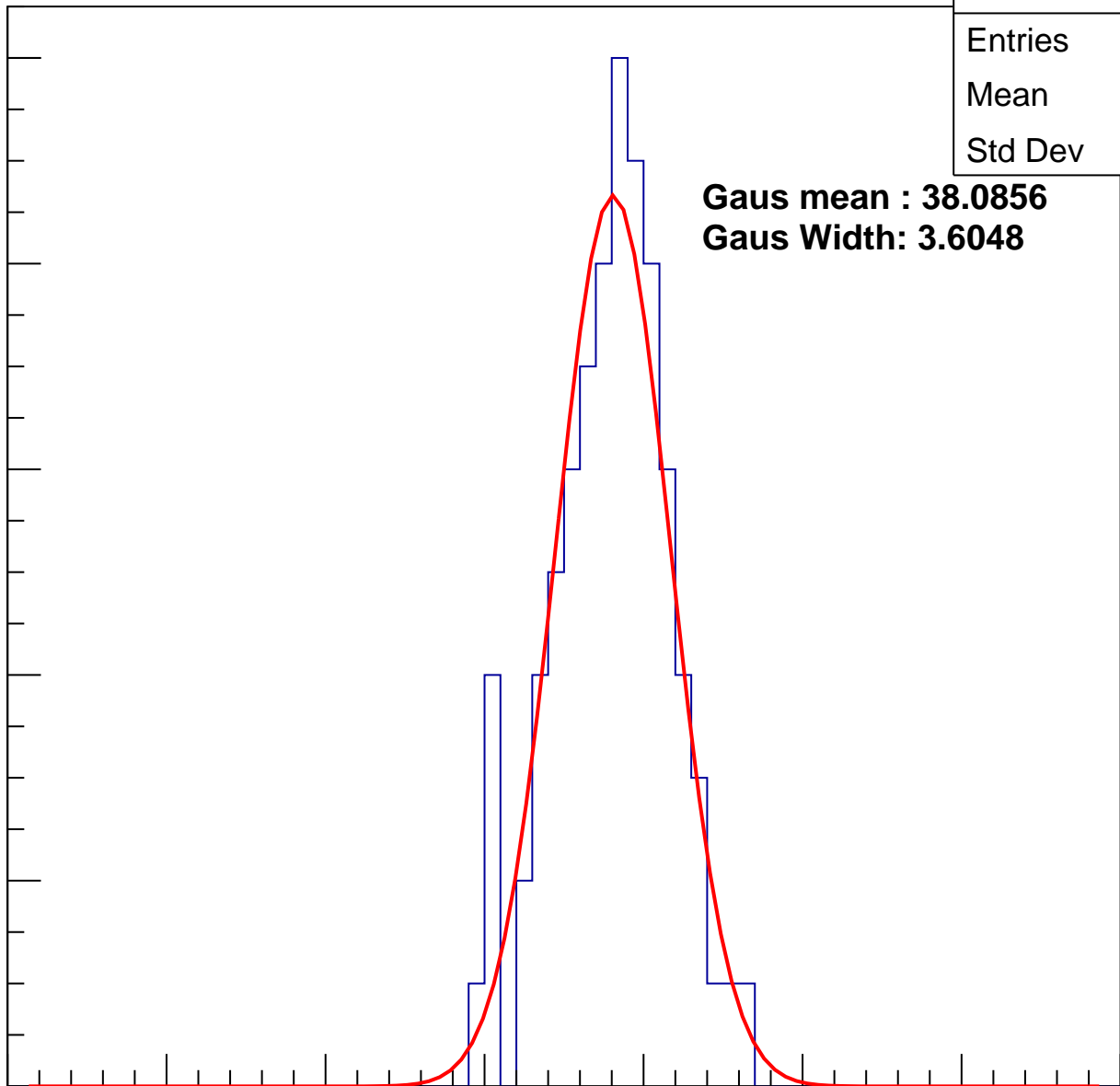
Gaus mean : 38.0856
Gaus Width: 3.6048

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch53, adc2

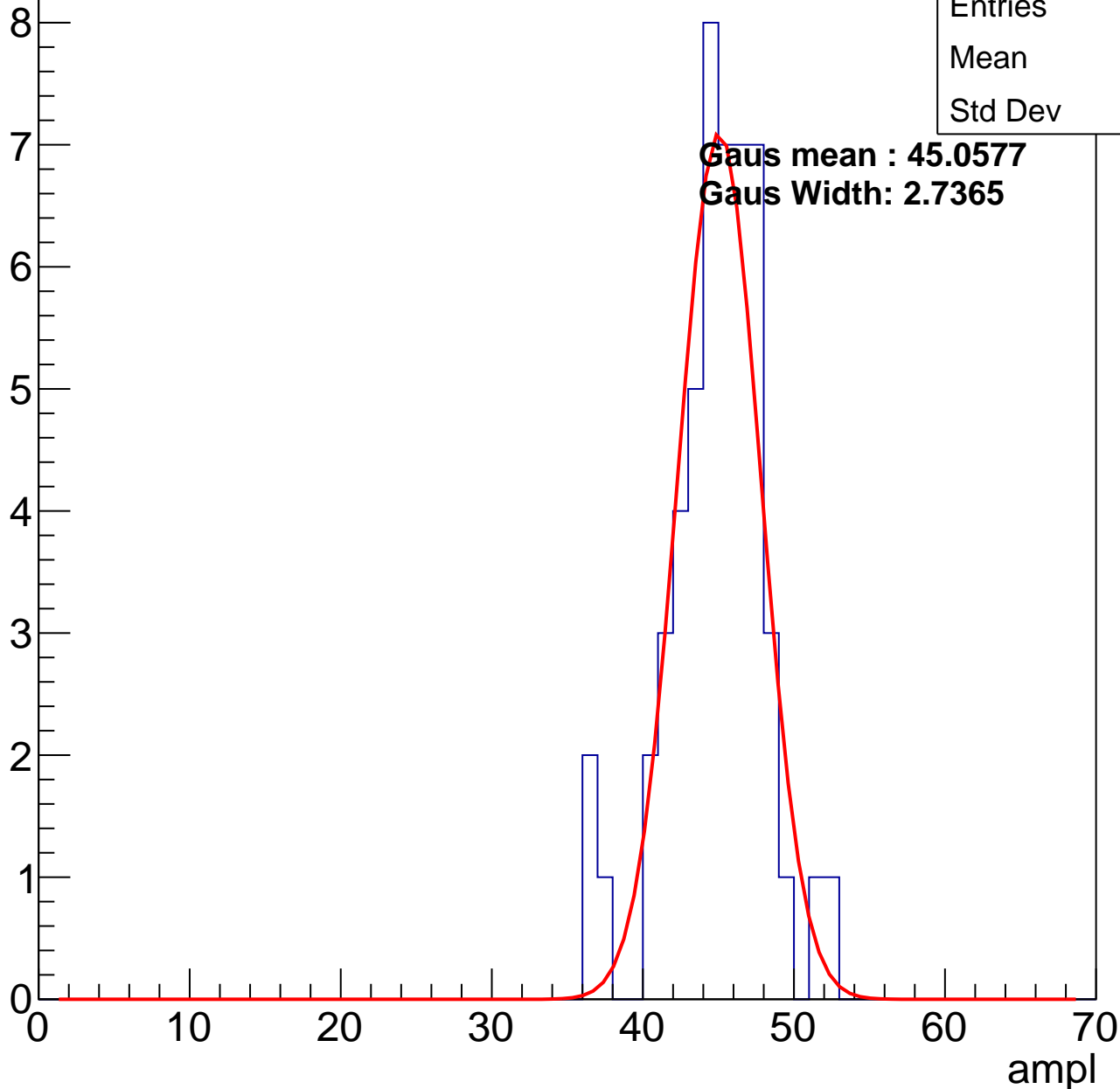
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	44.4
Std Dev	3.2

Gaus mean : 45.0577

Gaus Width: 2.7365

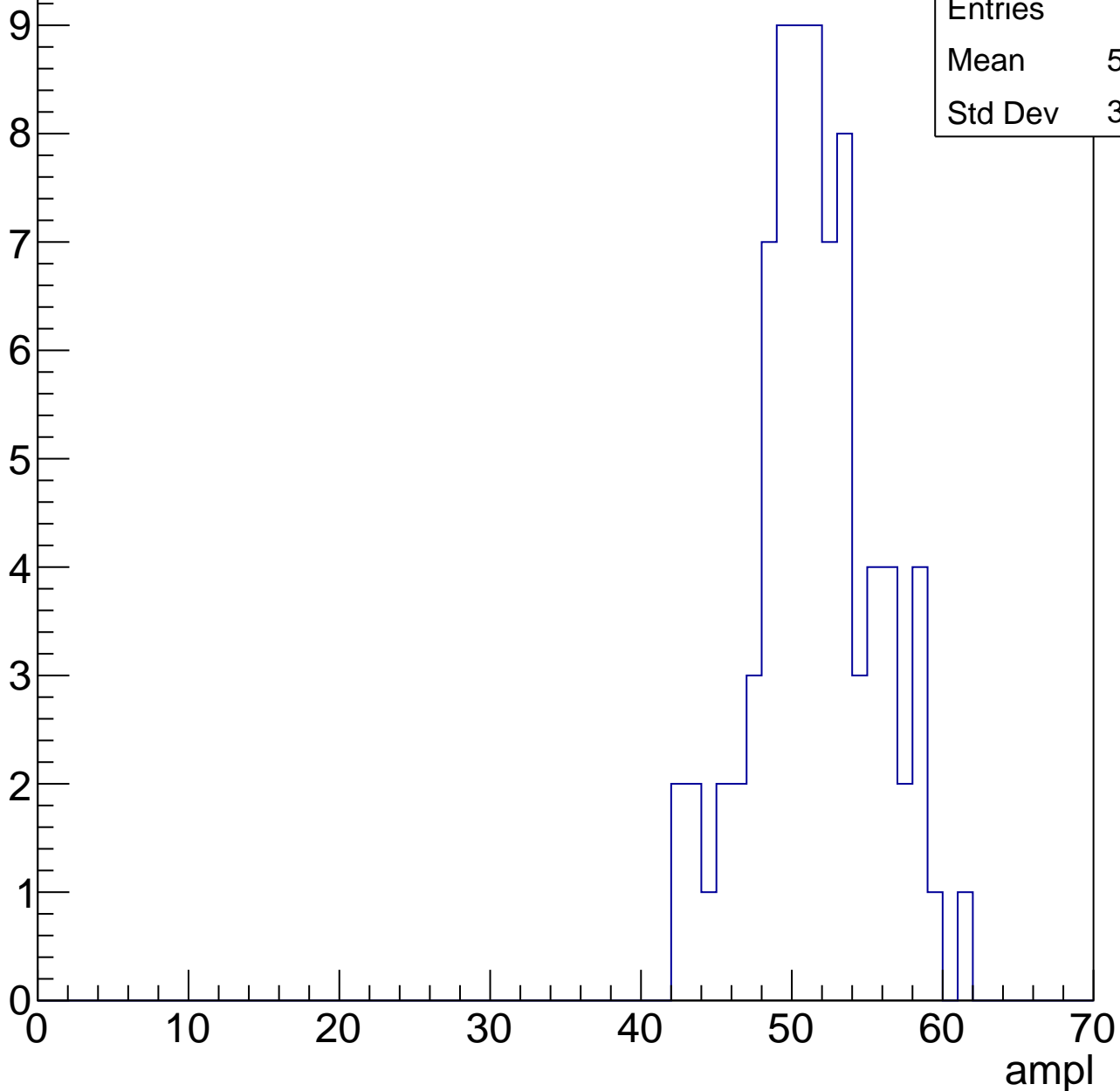


B1L103S, U20-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	51.04
Std Dev	3.998

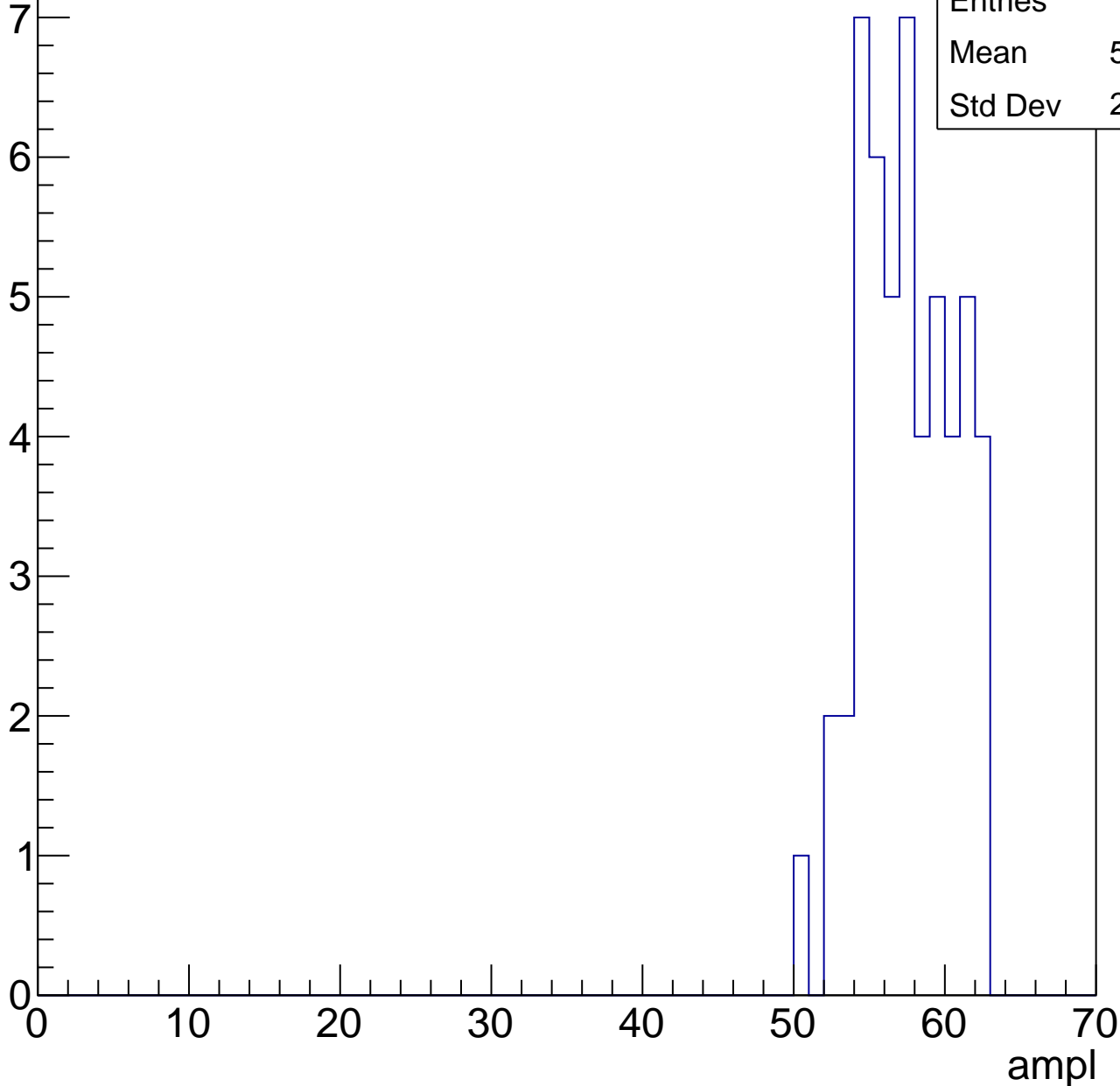


B1L103S, U20-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.06
Std Dev	2.983

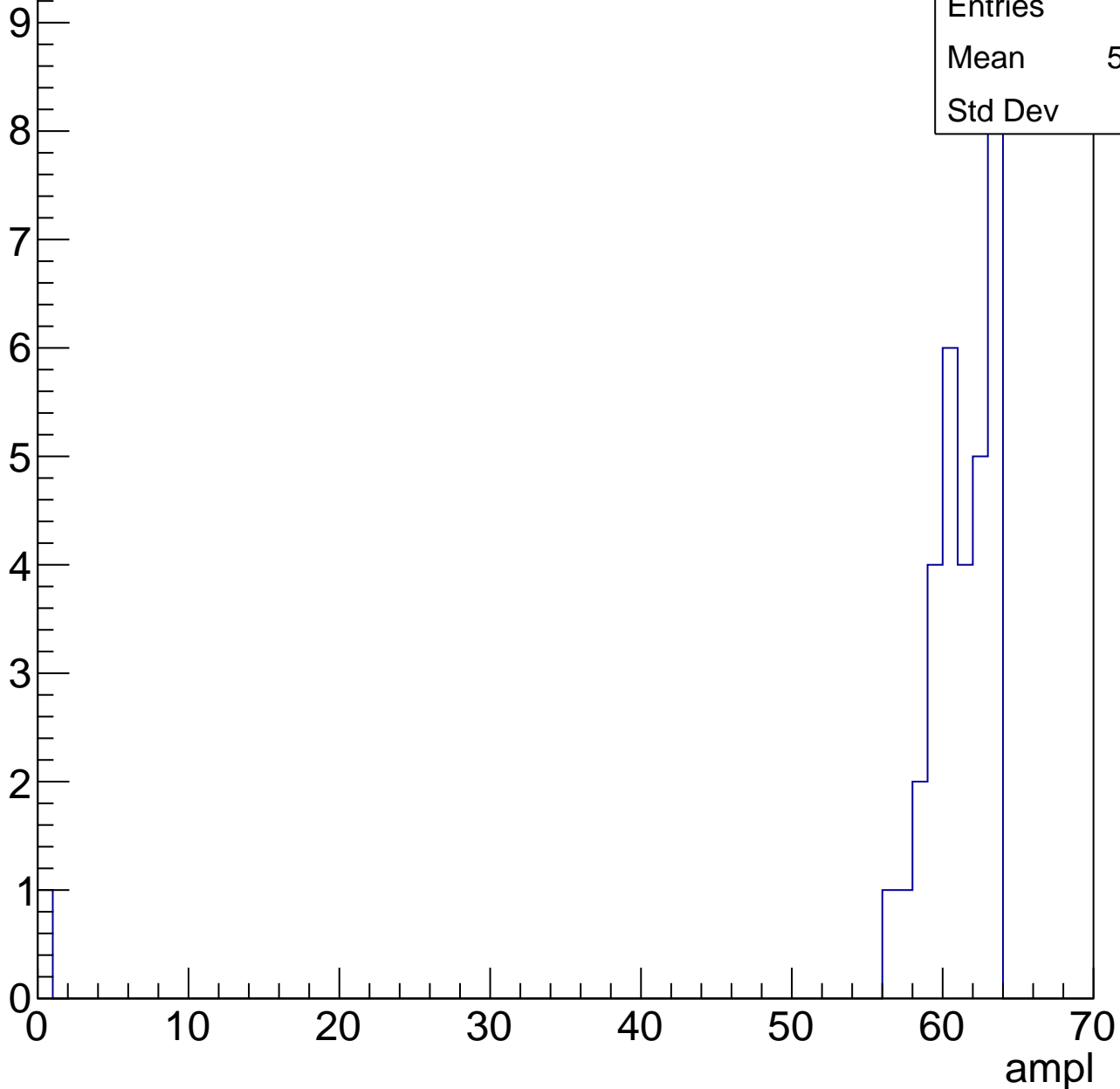


B1L103S, U20-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.97
Std Dev	10.6



B1L103S, U20-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch54, adc0

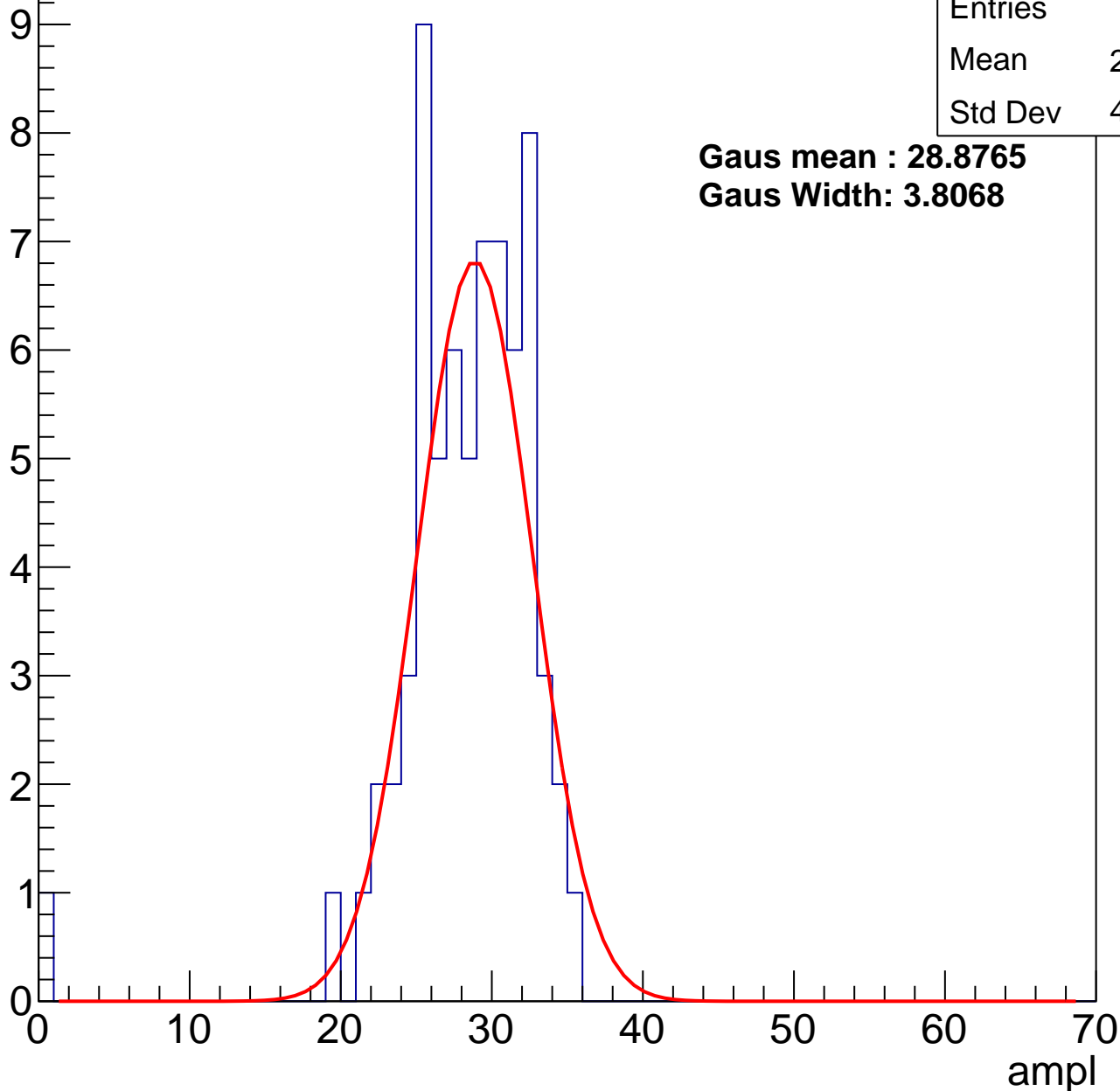
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.77
Std Dev	4.816

Gaus mean : 28.8765

Gaus Width: 3.8068



B1L103S, U20-ch54, adc1

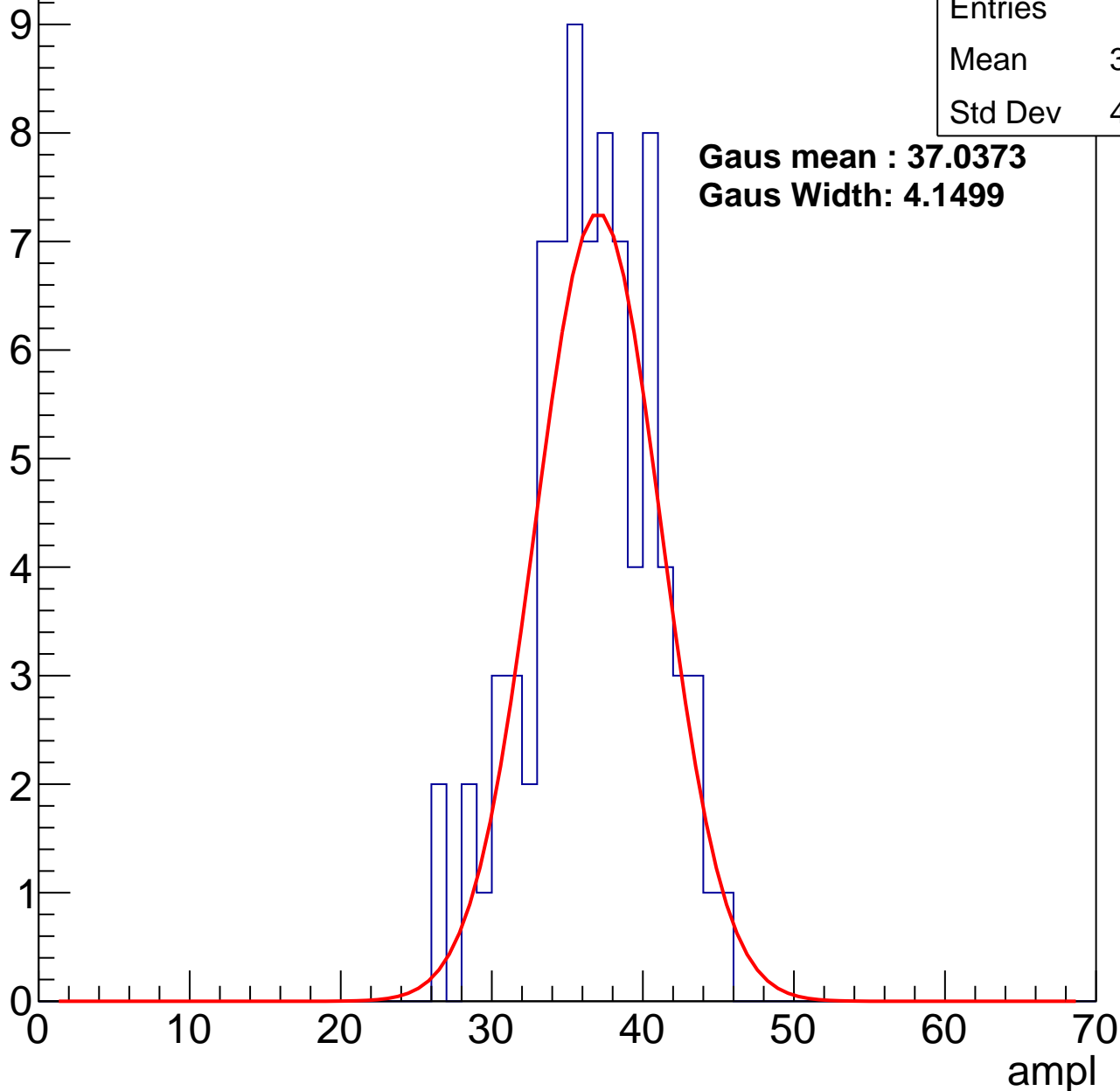
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	36.17
Std Dev	4.117

Gaus mean : 37.0373

Gaus Width: 4.1499



B1L103S, U20-ch54, adc2

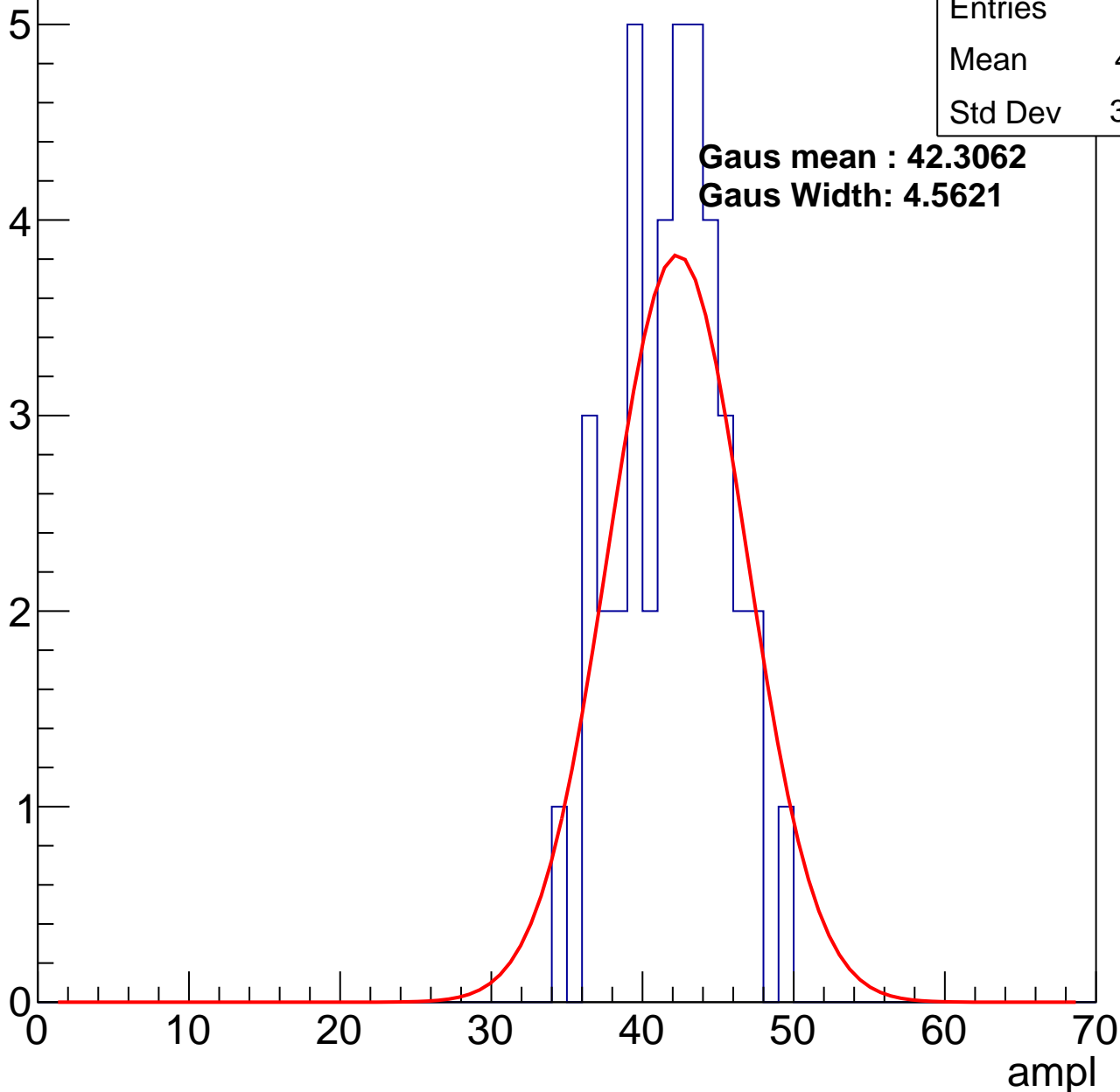
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	41.51
Std Dev	3.422

Gaus mean : 42.3062

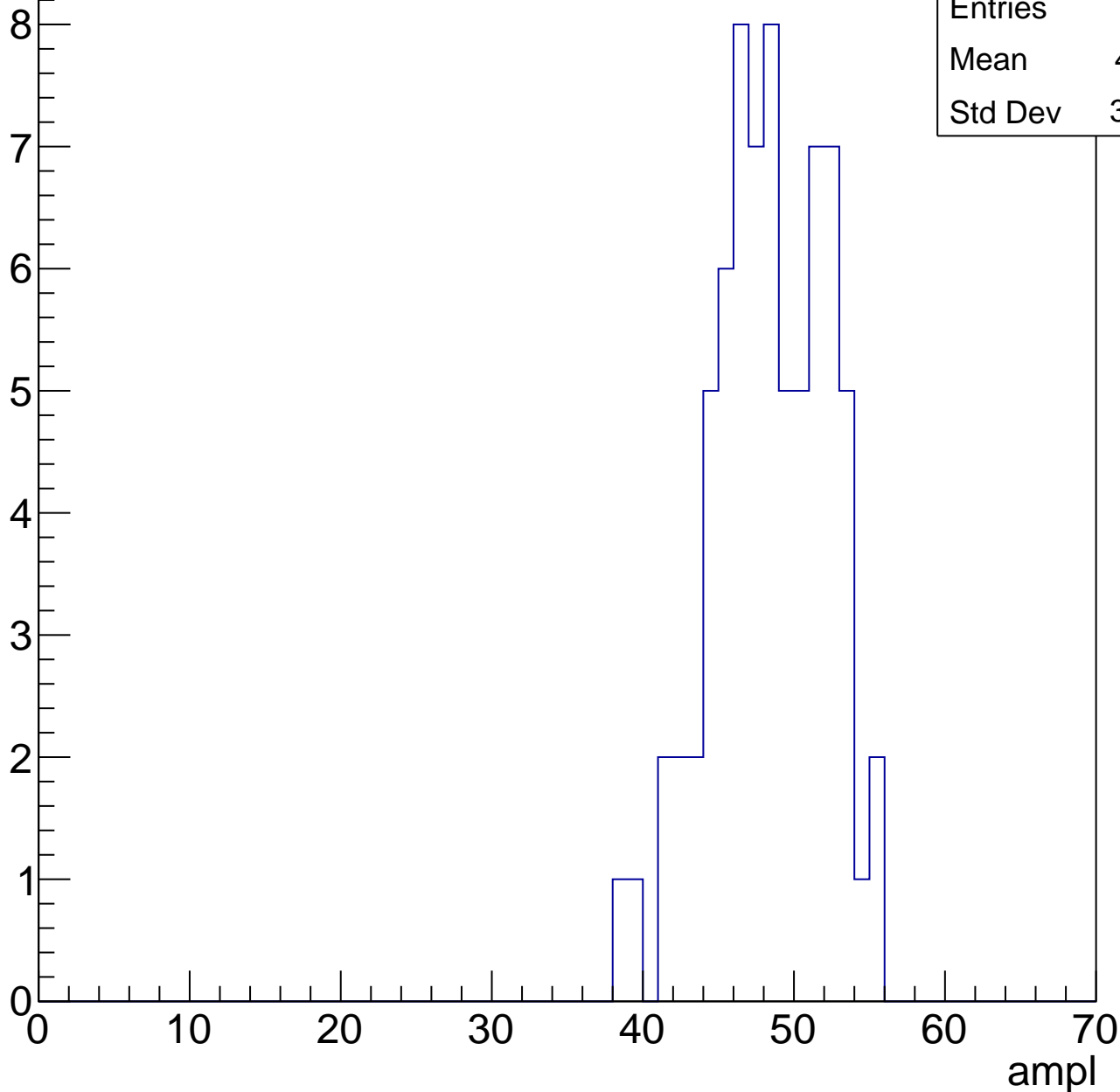
Gaus Width: 4.5621



B1L103S, U20-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

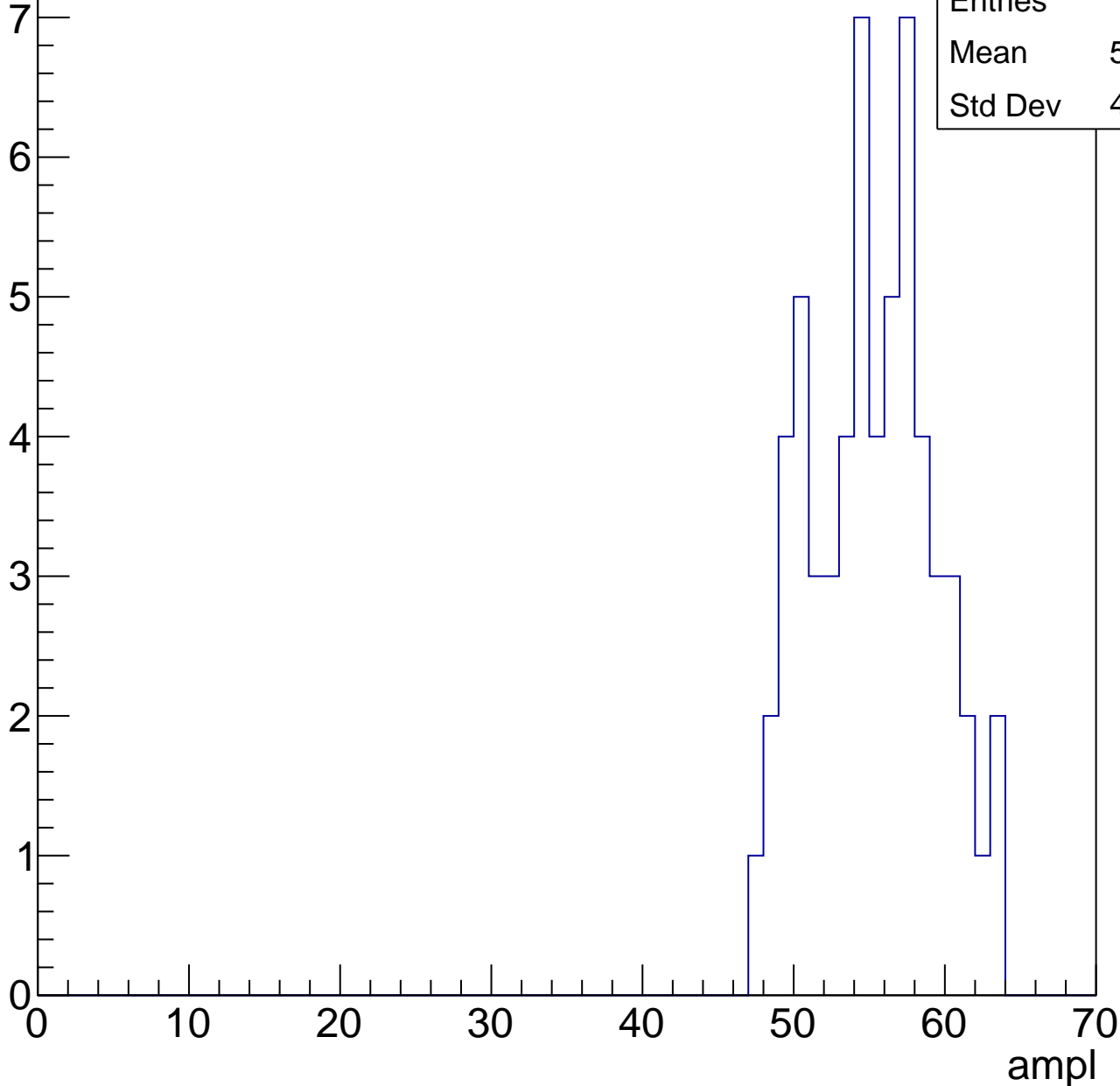


B1L103S, U20-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

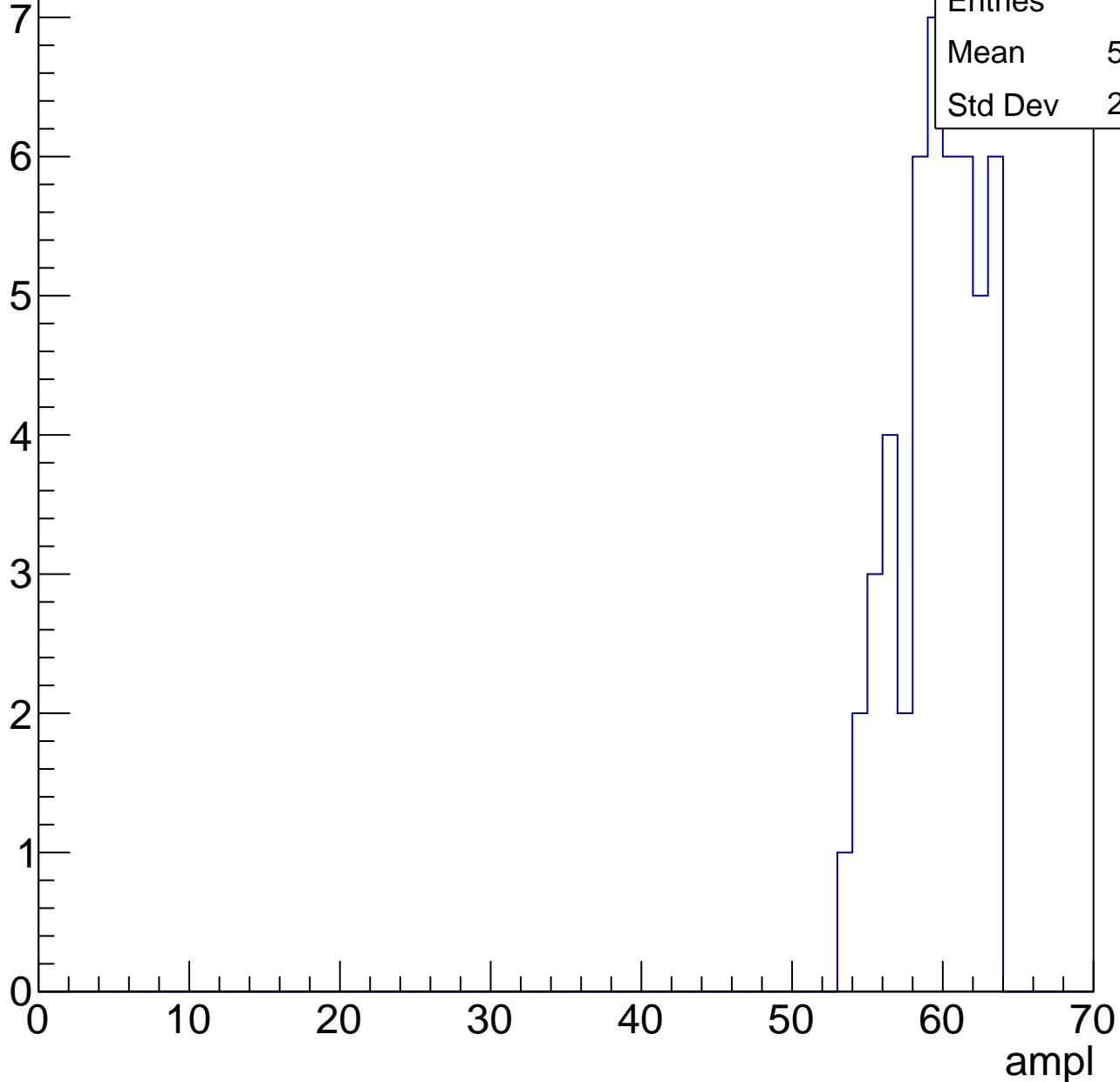
Entries	60
Mean	54.77
Std Dev	4.014



B1L103S, U20-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

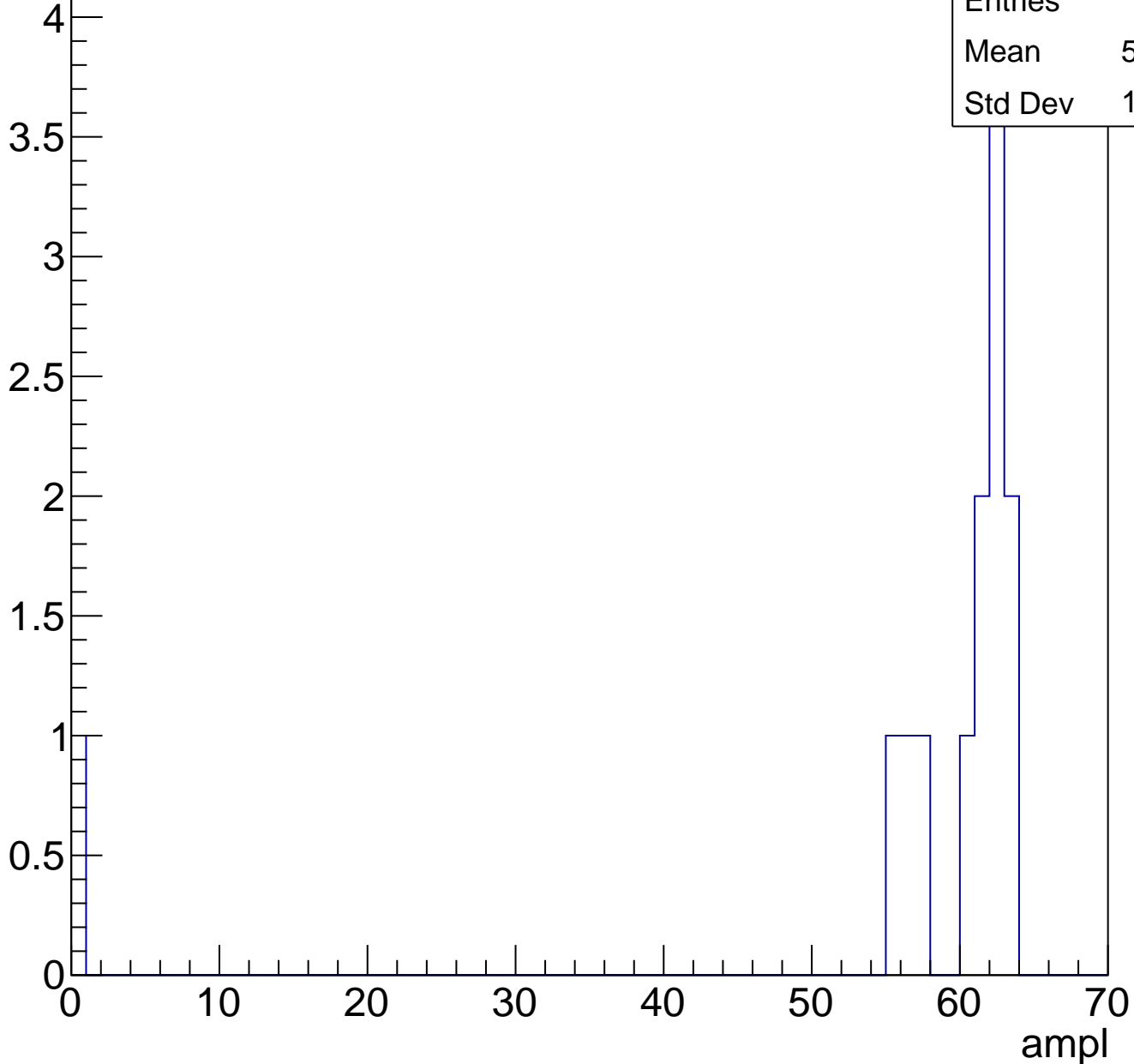


Entries	48
Mean	59.15
Std Dev	2.716

B1L103S, U20-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch55, adc0

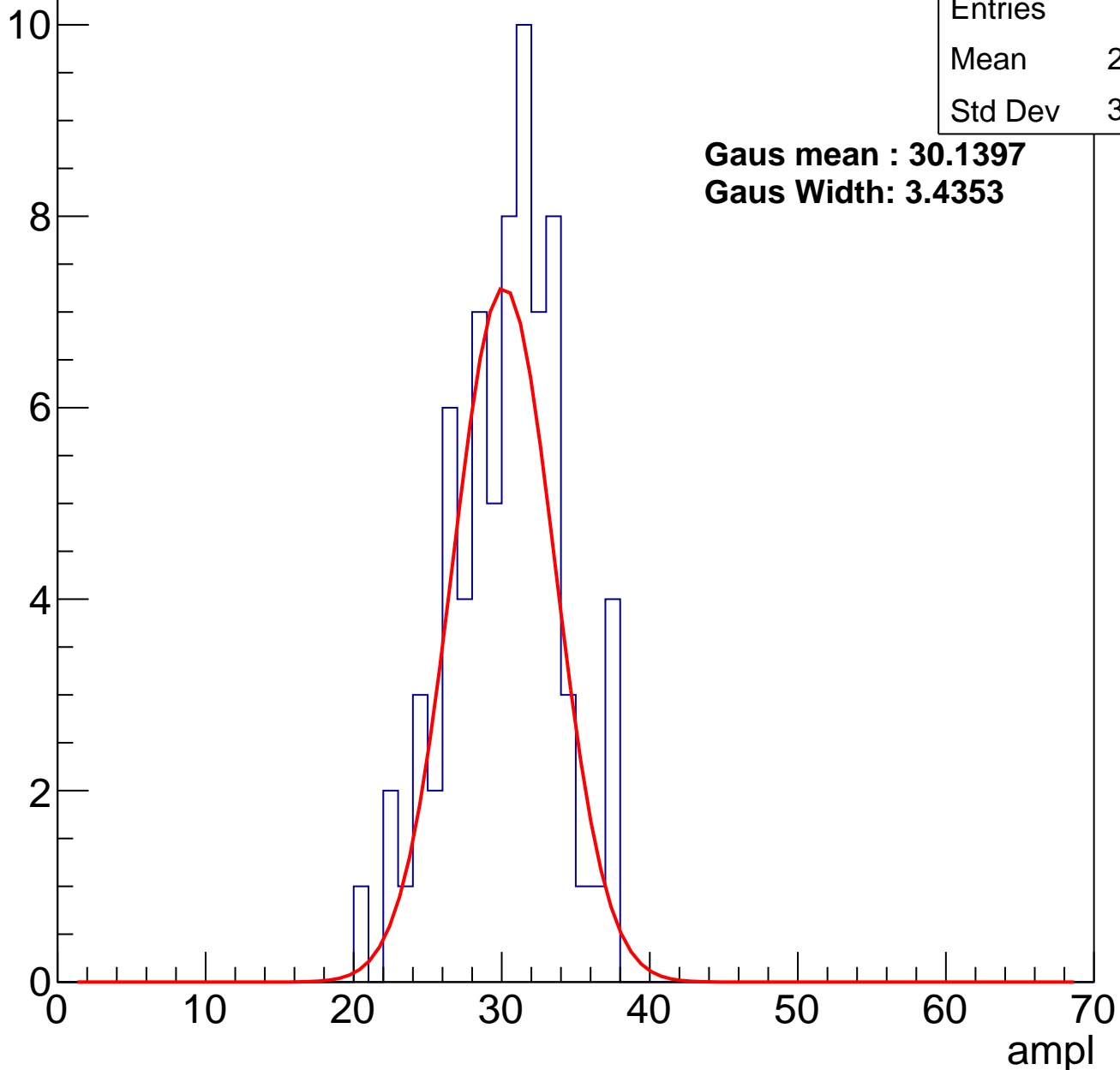
calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	29.77
Std Dev	3.722

Gaus mean : 30.1397

Gaus Width: 3.4353

Entry



B1L103S, U20-ch55, adc1

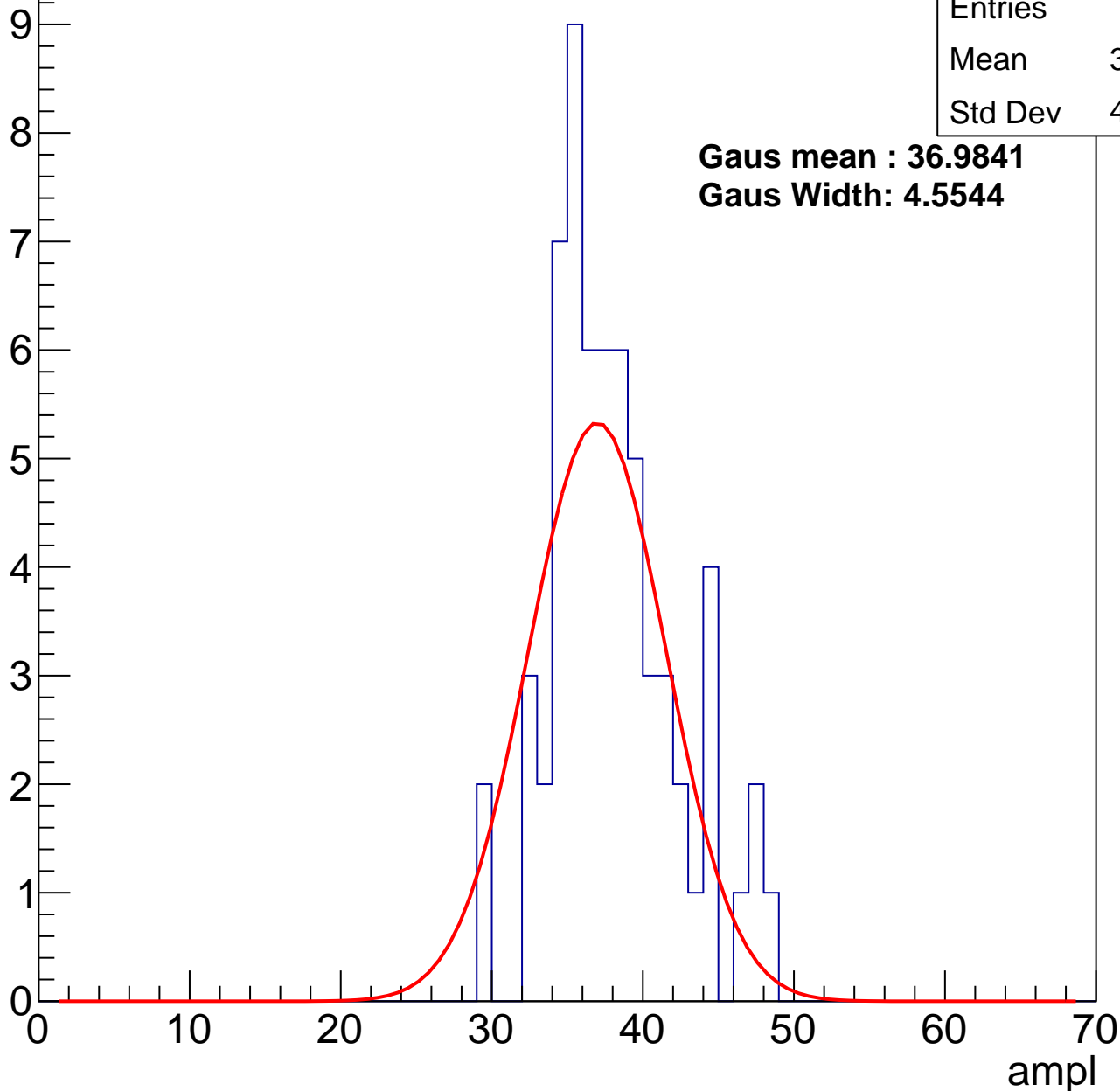
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.59
Std Dev	4.185

Gaus mean : 36.9841

Gaus Width: 4.5544



B1L103S, U20-ch55, adc2

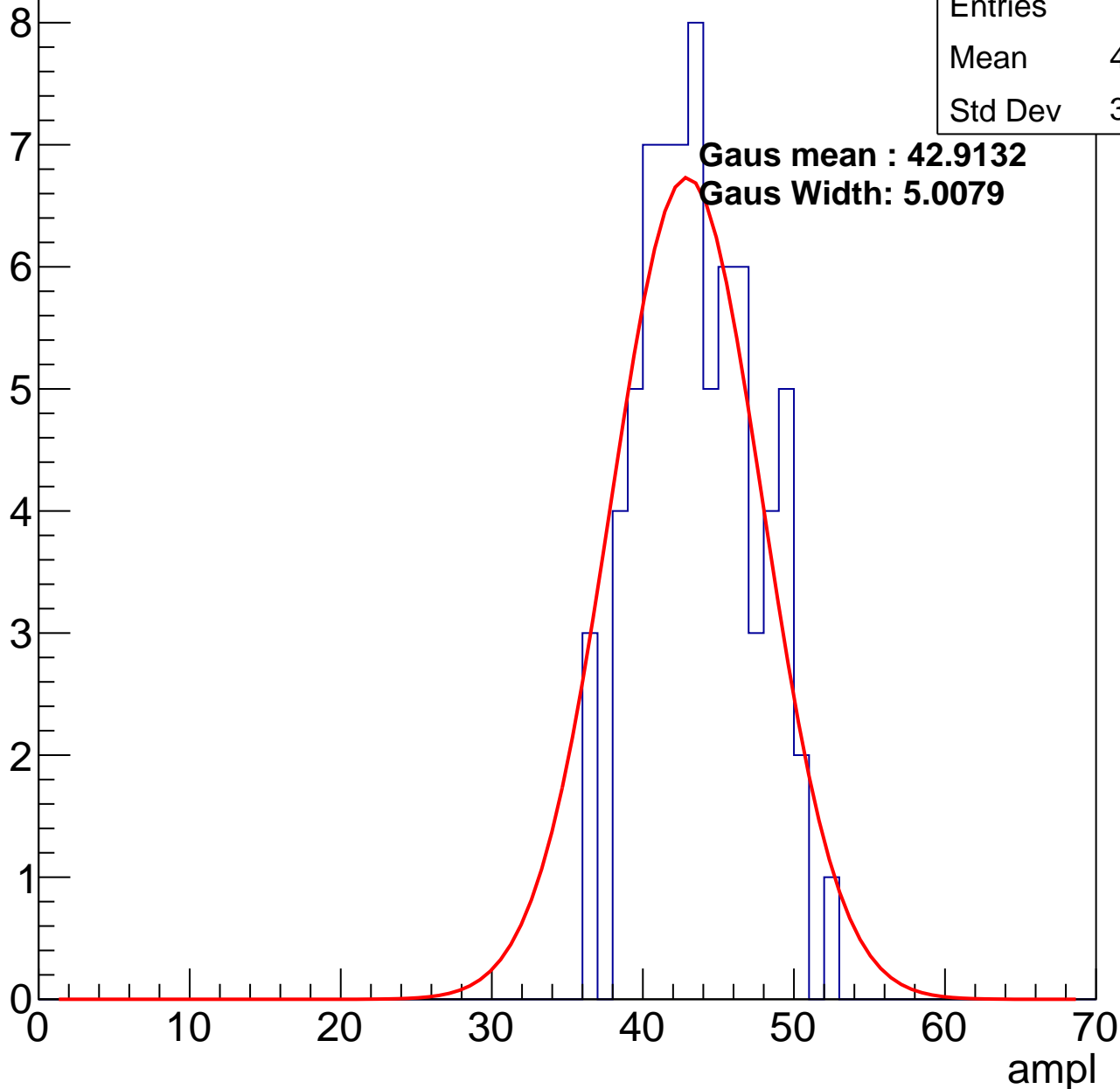
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.23
Std Dev	3.722

Gaus mean : 42.9132

Gaus Width: 5.0079

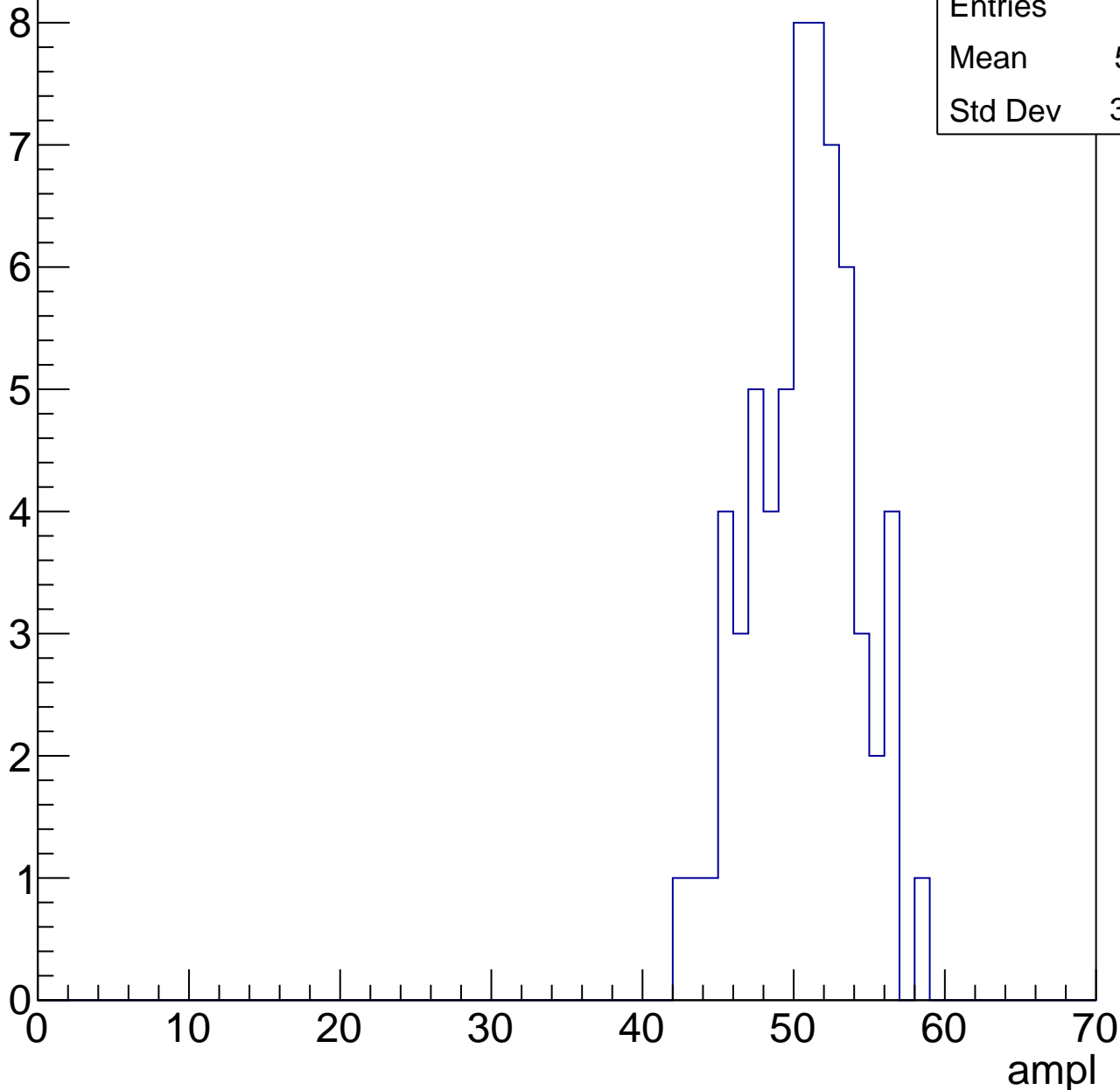


B1L103S, U20-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	50.21
Std Dev	3.465

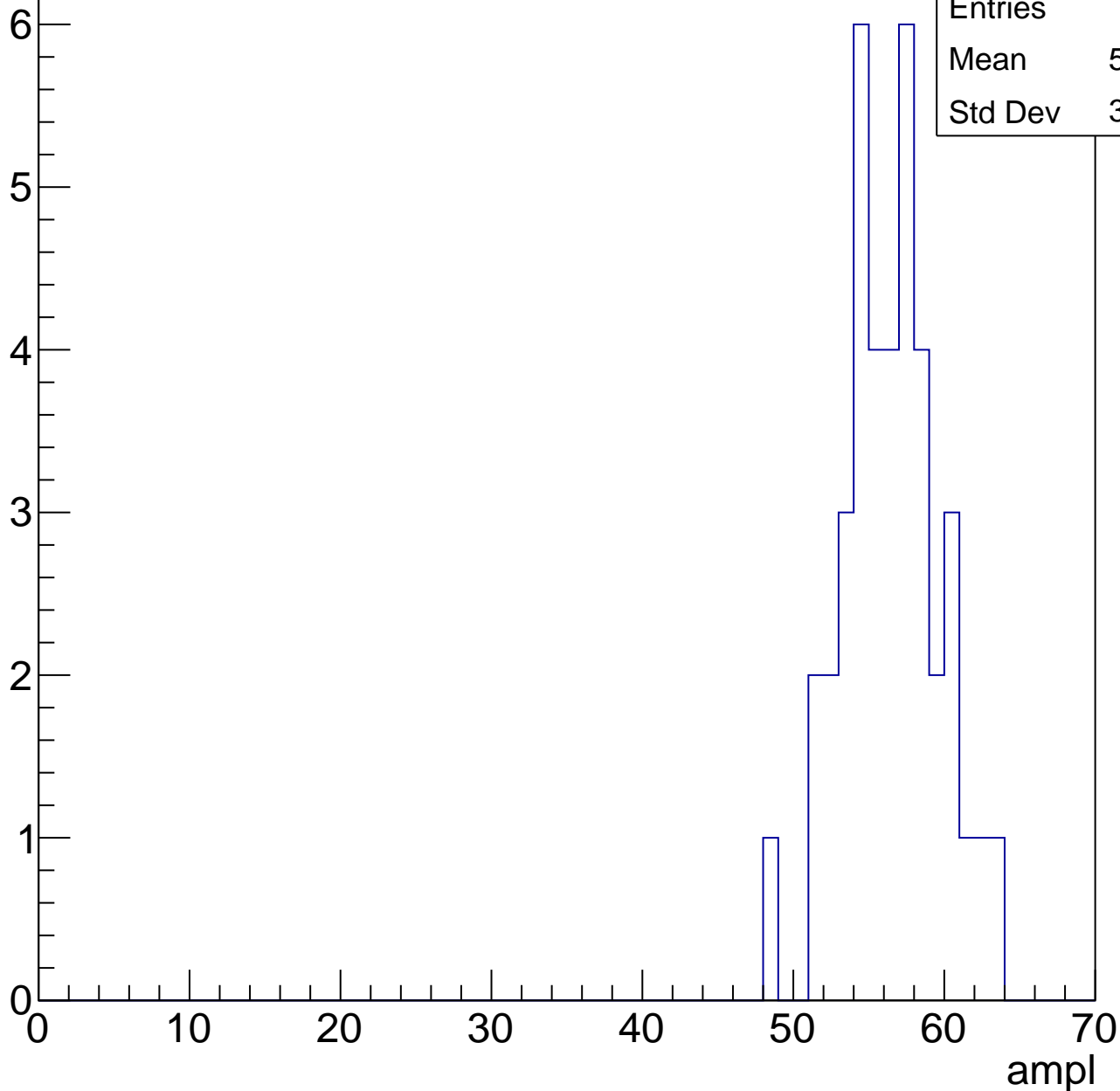


B1L103S, U20-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	55.98
Std Dev	3.158



B1L103S, U20-ch55, adc5

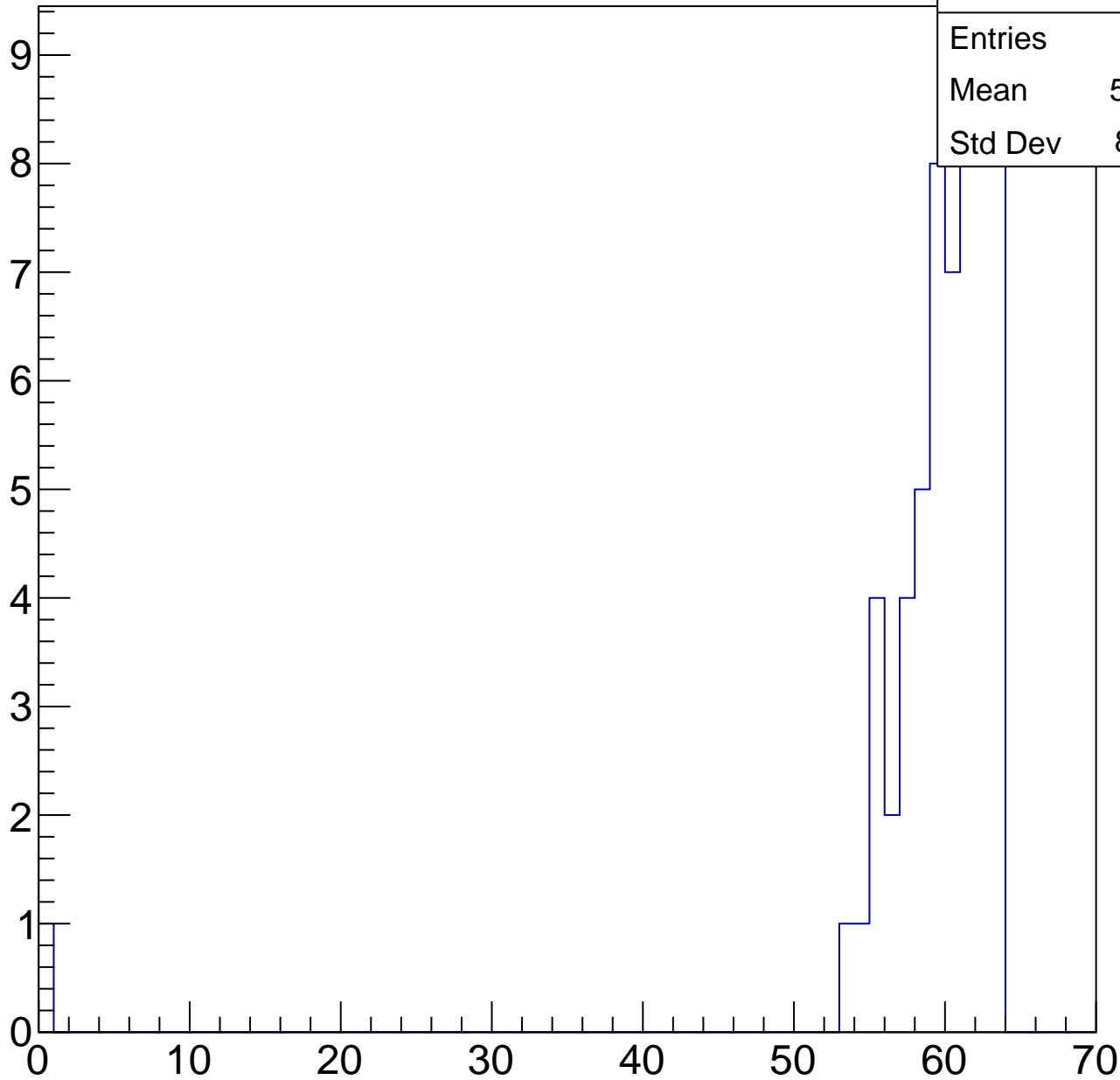
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.72
Std Dev	8.071

ampl



B1L103S, U20-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U20-ch56, adc0

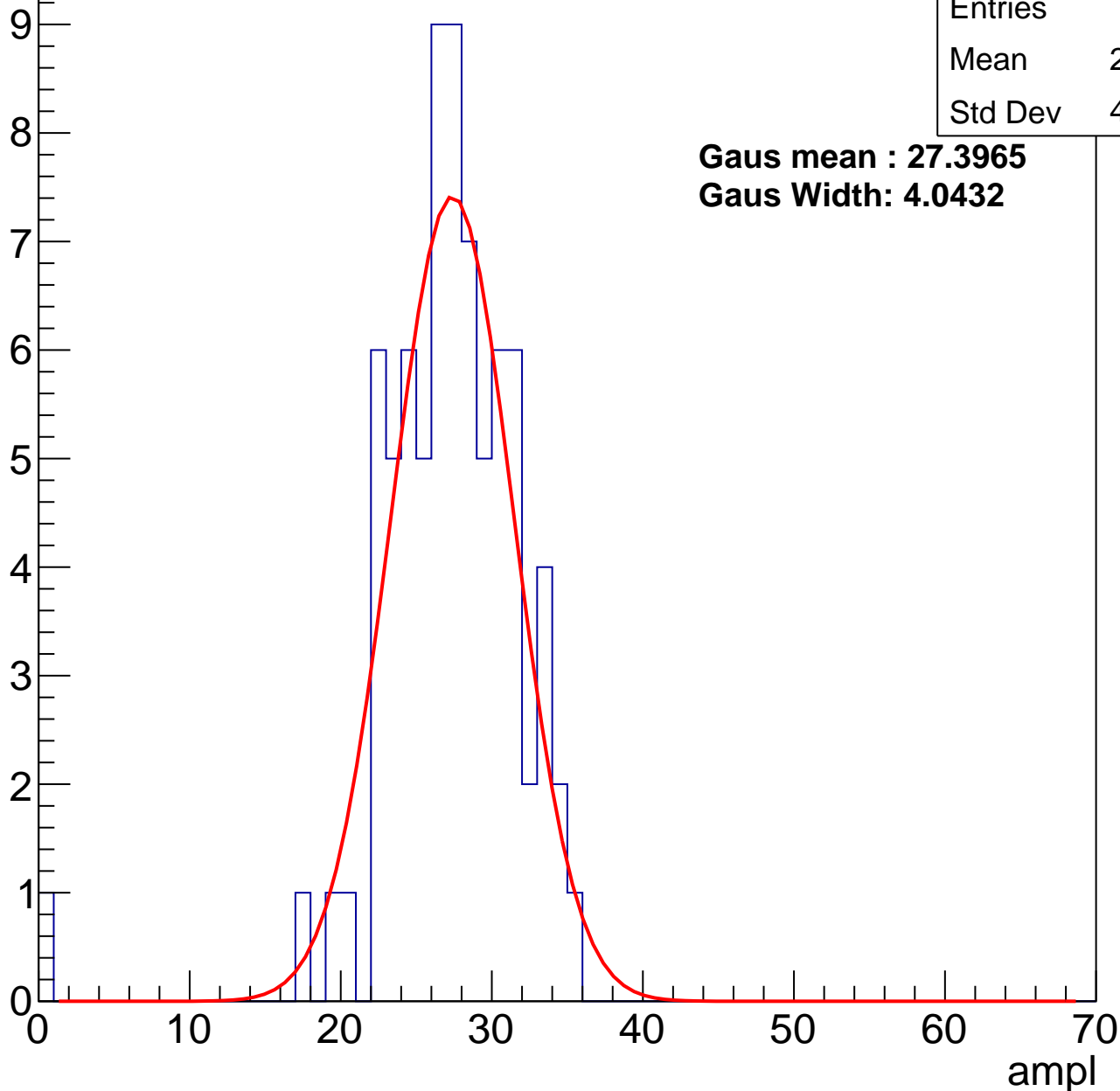
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	26.69
Std Dev	4.809

Gaus mean : 27.3965

Gaus Width: 4.0432



B1L103S, U20-ch56, adc1

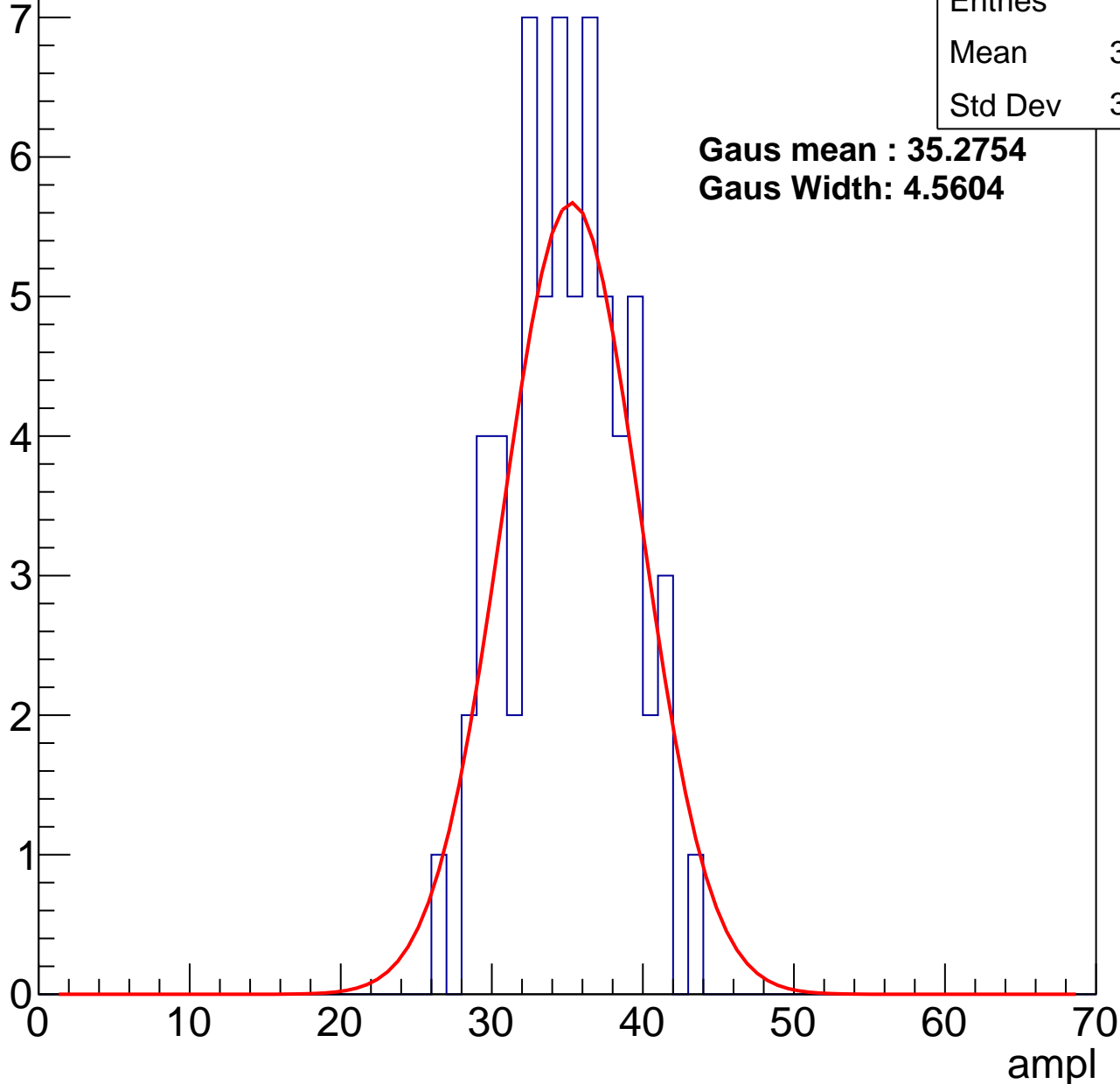
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	34.56
Std Dev	3.745

Gaus mean : 35.2754

Gaus Width: 4.5604



B1L103S, U20-ch56, adc2

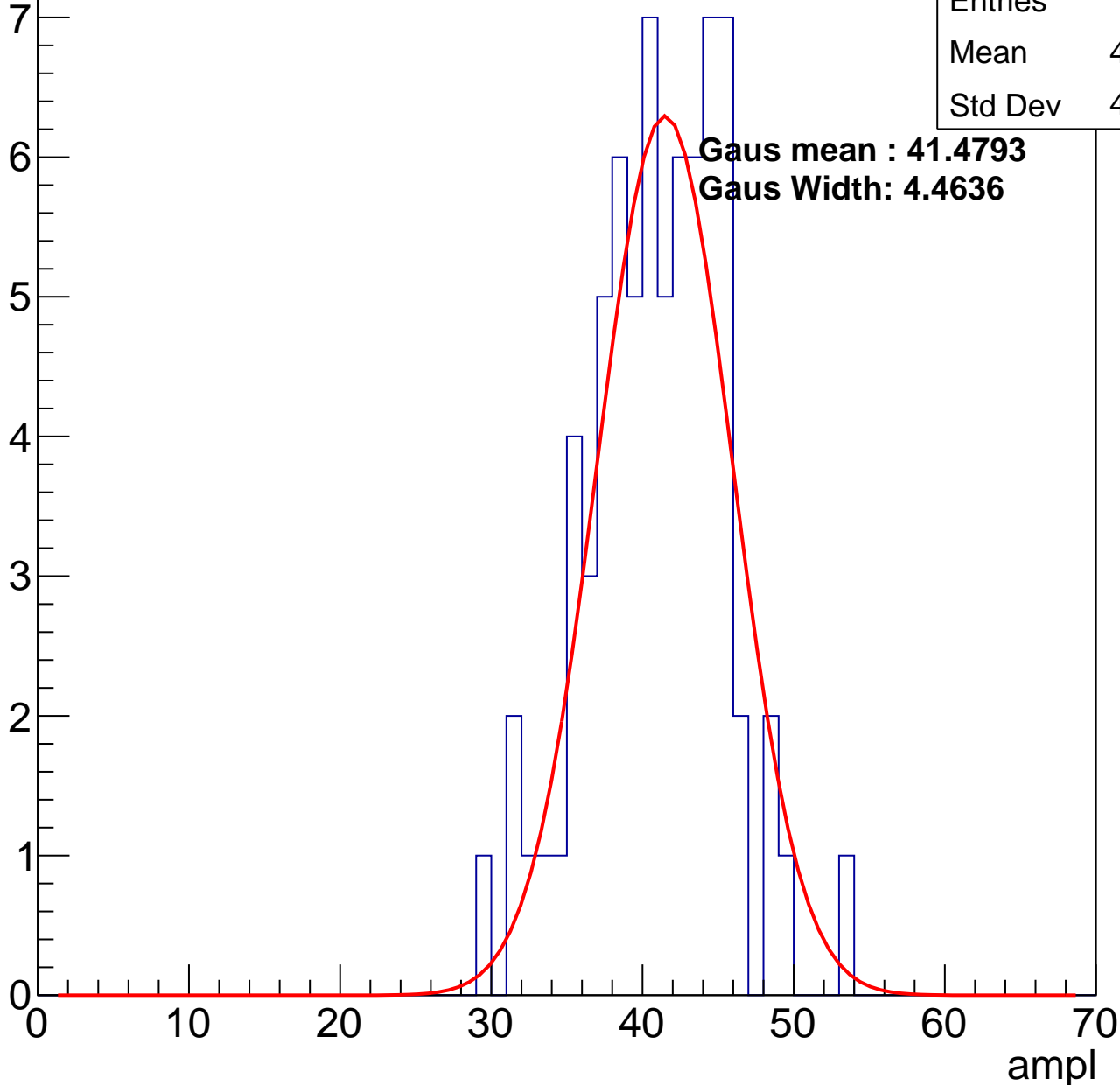
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	40.47
Std Dev	4.469

Gaus mean : 41.4793

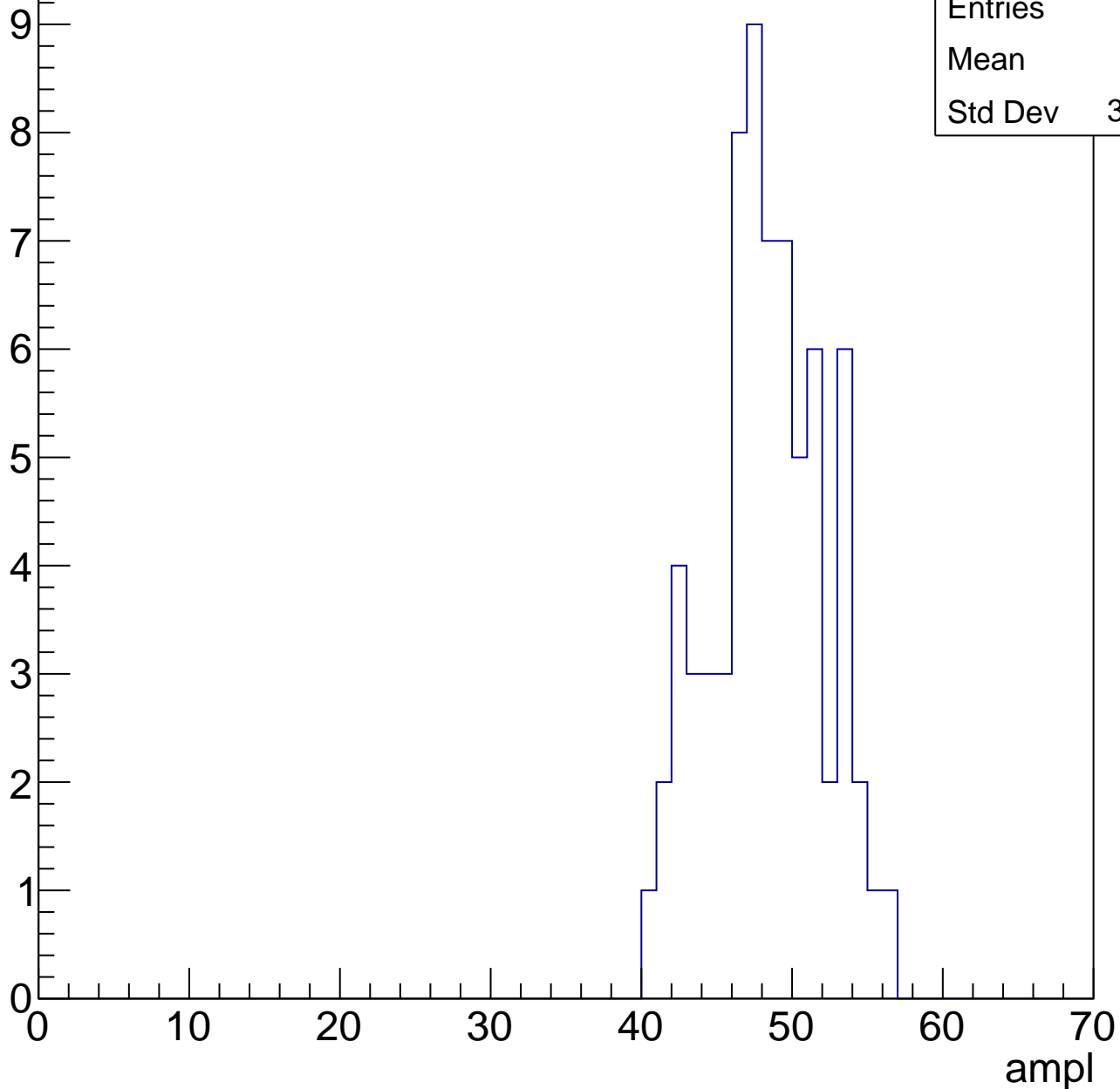
Gaus Width: 4.4636



B1L103S, U20-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

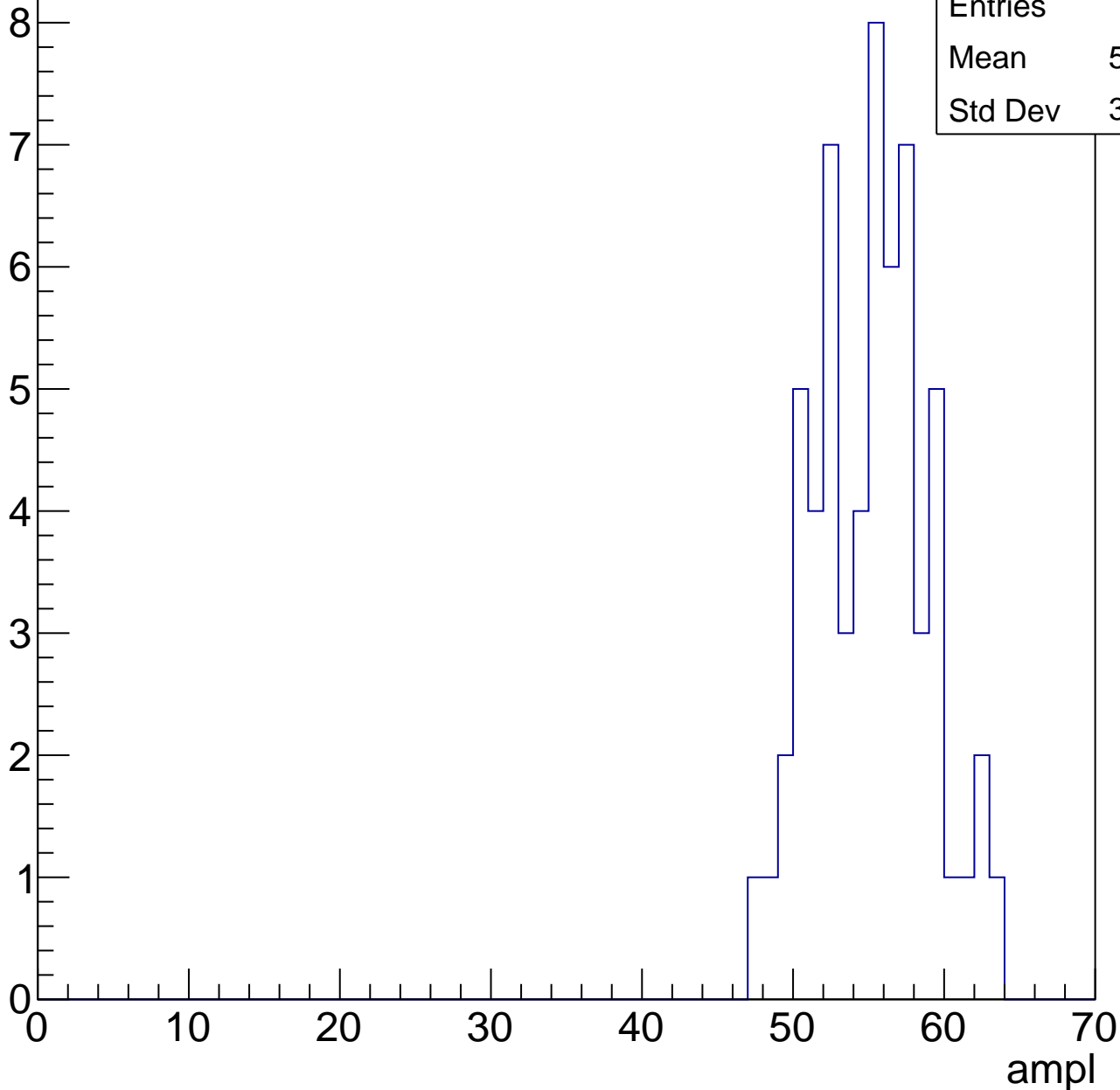


B1L103S, U20-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.72
Std Dev	3.649

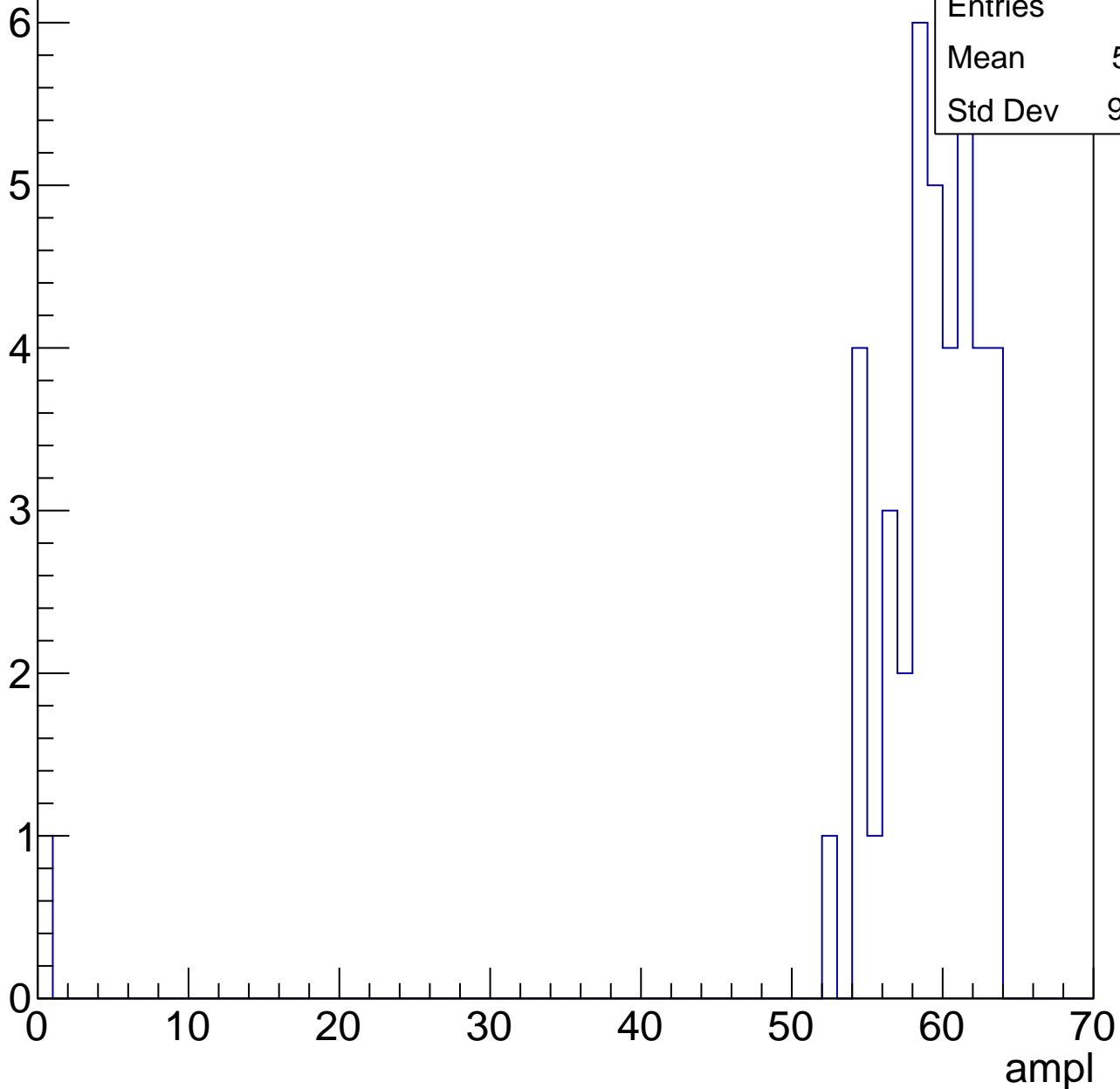


B1L103S, U20-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	57.41
Std Dev	9.515

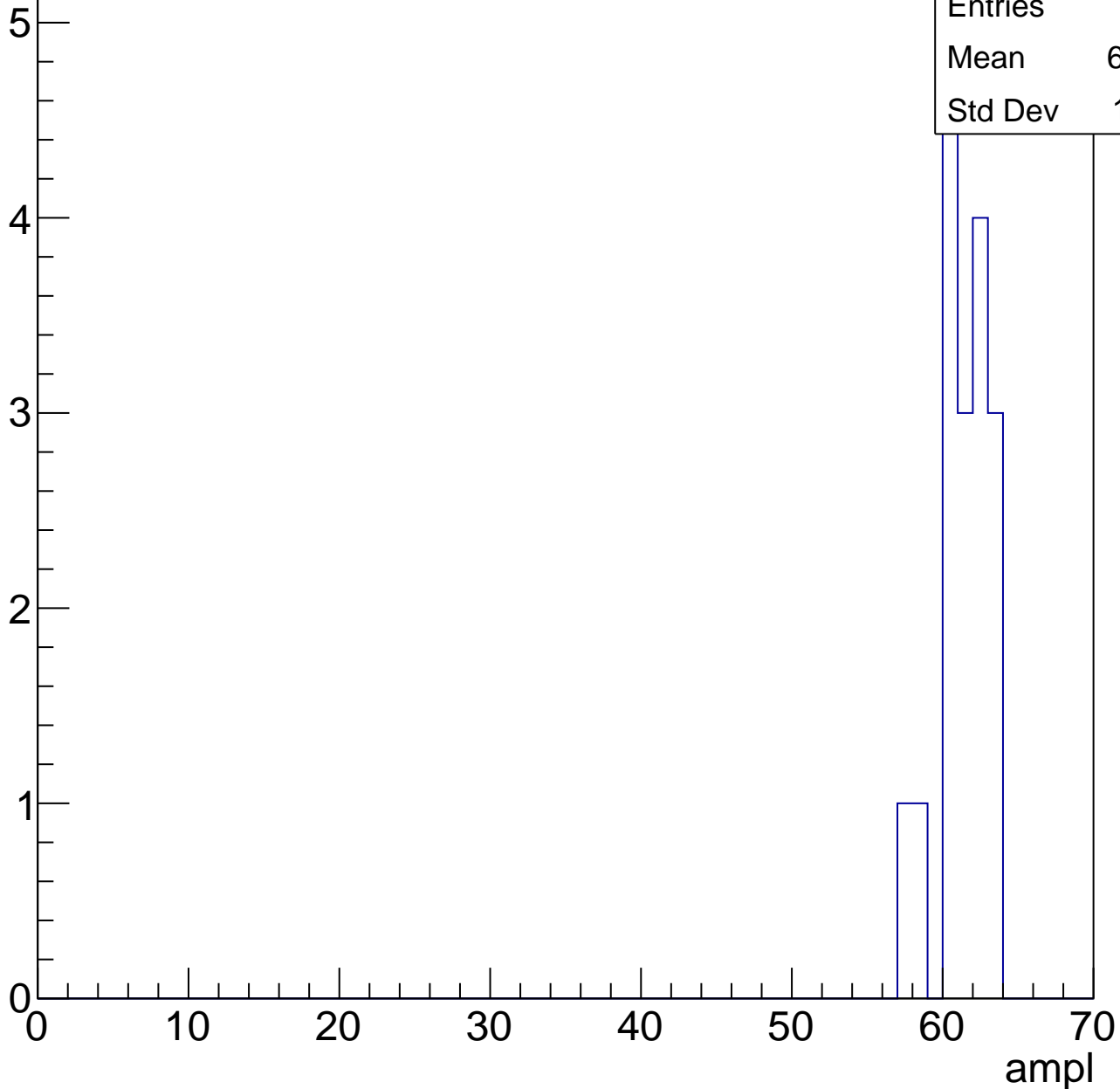


B1L103S, U20-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	60.88
Std Dev	1.641



B1L103S, U20-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch57, adc0

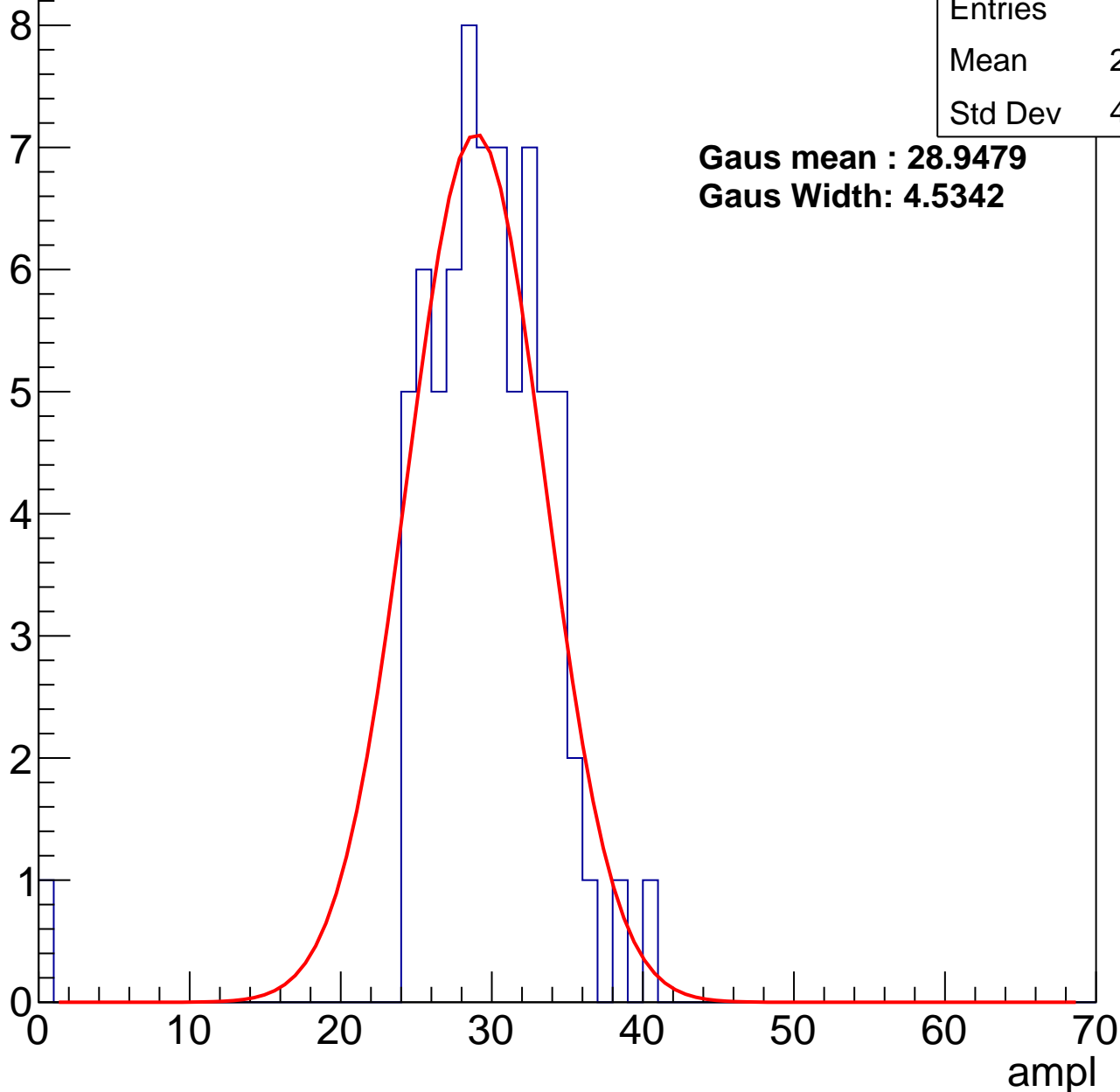
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.12
Std Dev	4.938

Gaus mean : 28.9479

Gaus Width: 4.5342



B1L103S, U20-ch57, adc1

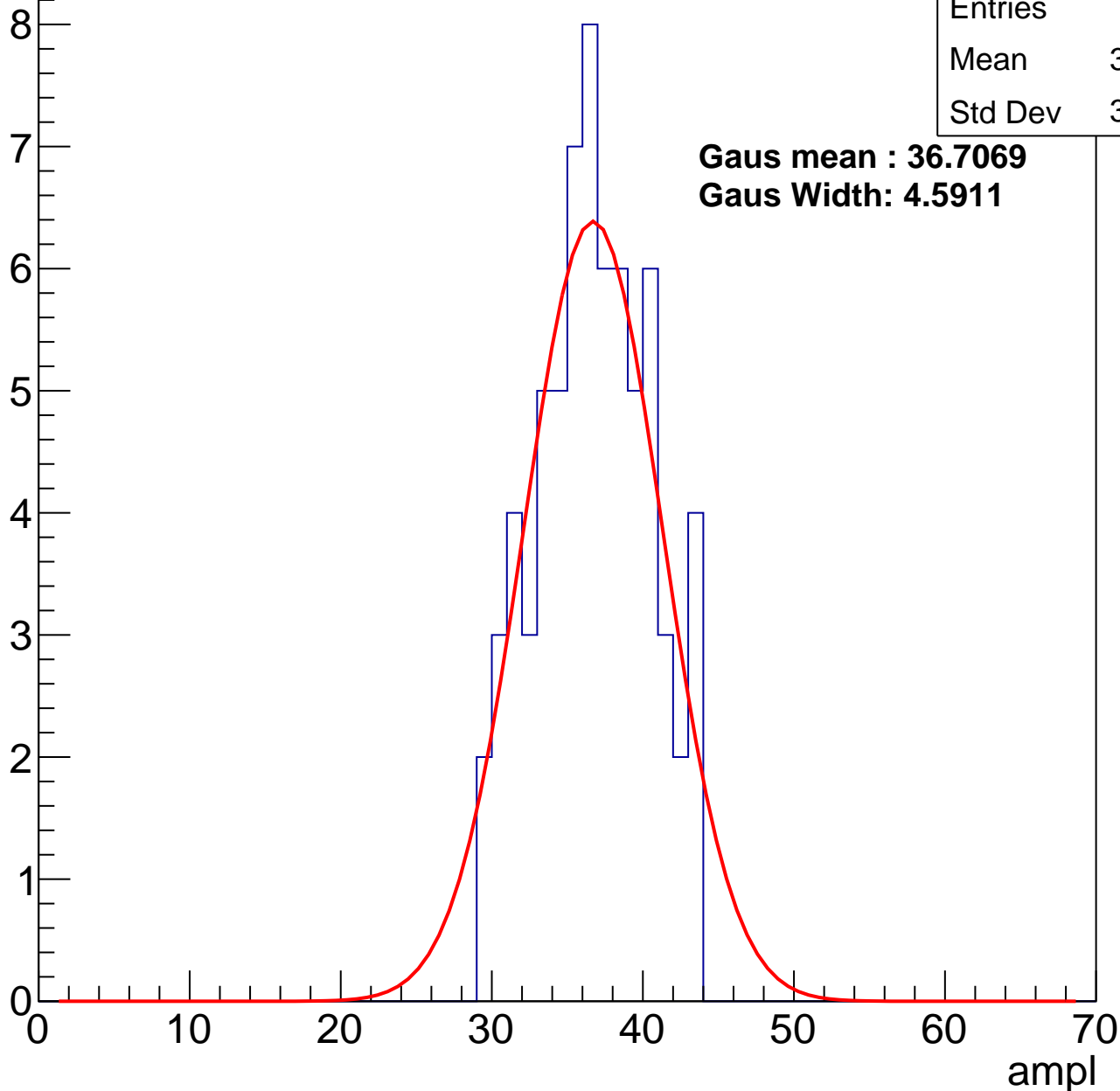
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.23
Std Dev	3.684

Gaus mean : 36.7069

Gaus Width: 4.5911



B1L103S, U20-ch57, adc2

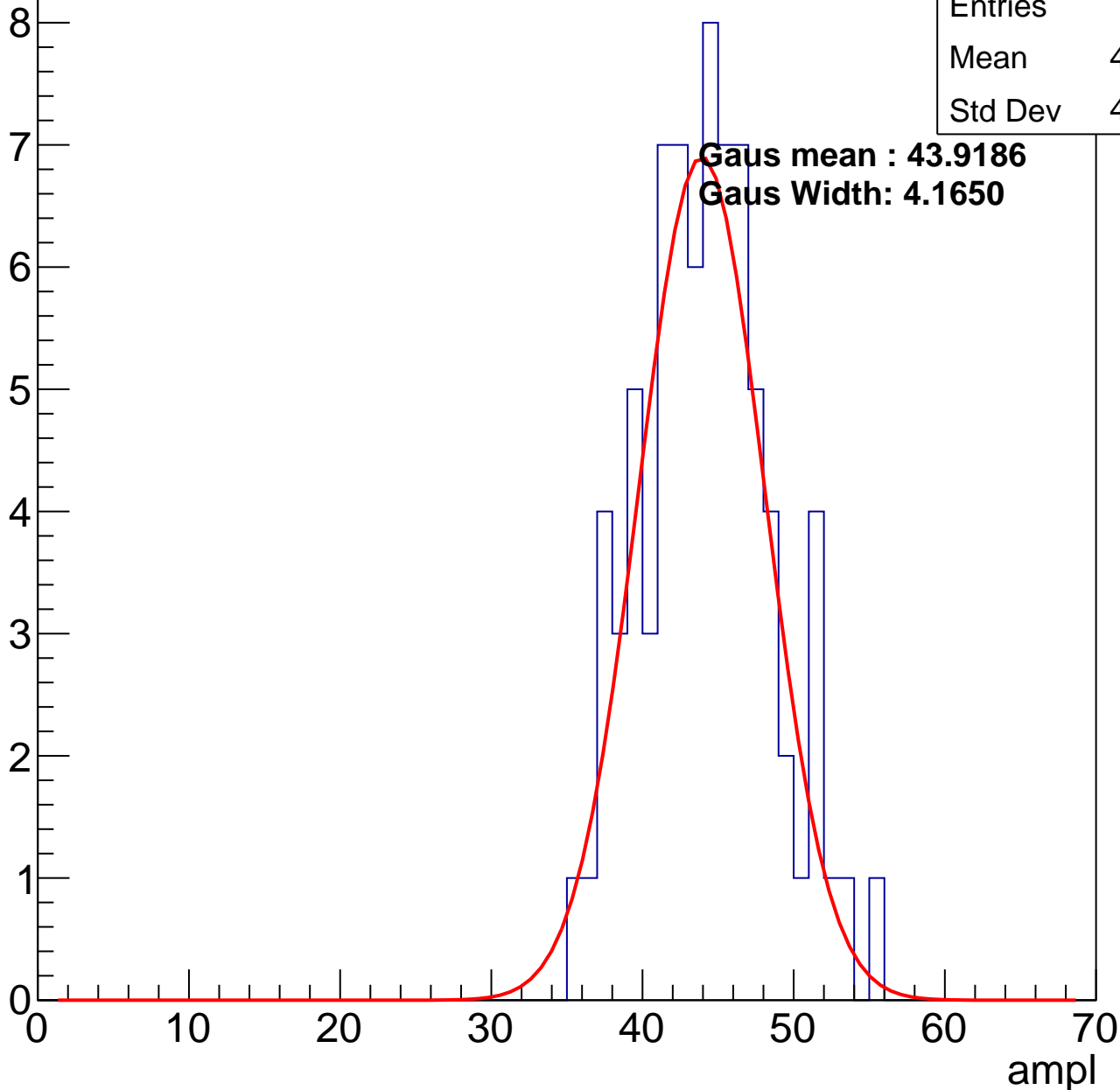
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	43.78
Std Dev	4.248

Gaus mean : 43.9186

Gaus Width: 4.1650

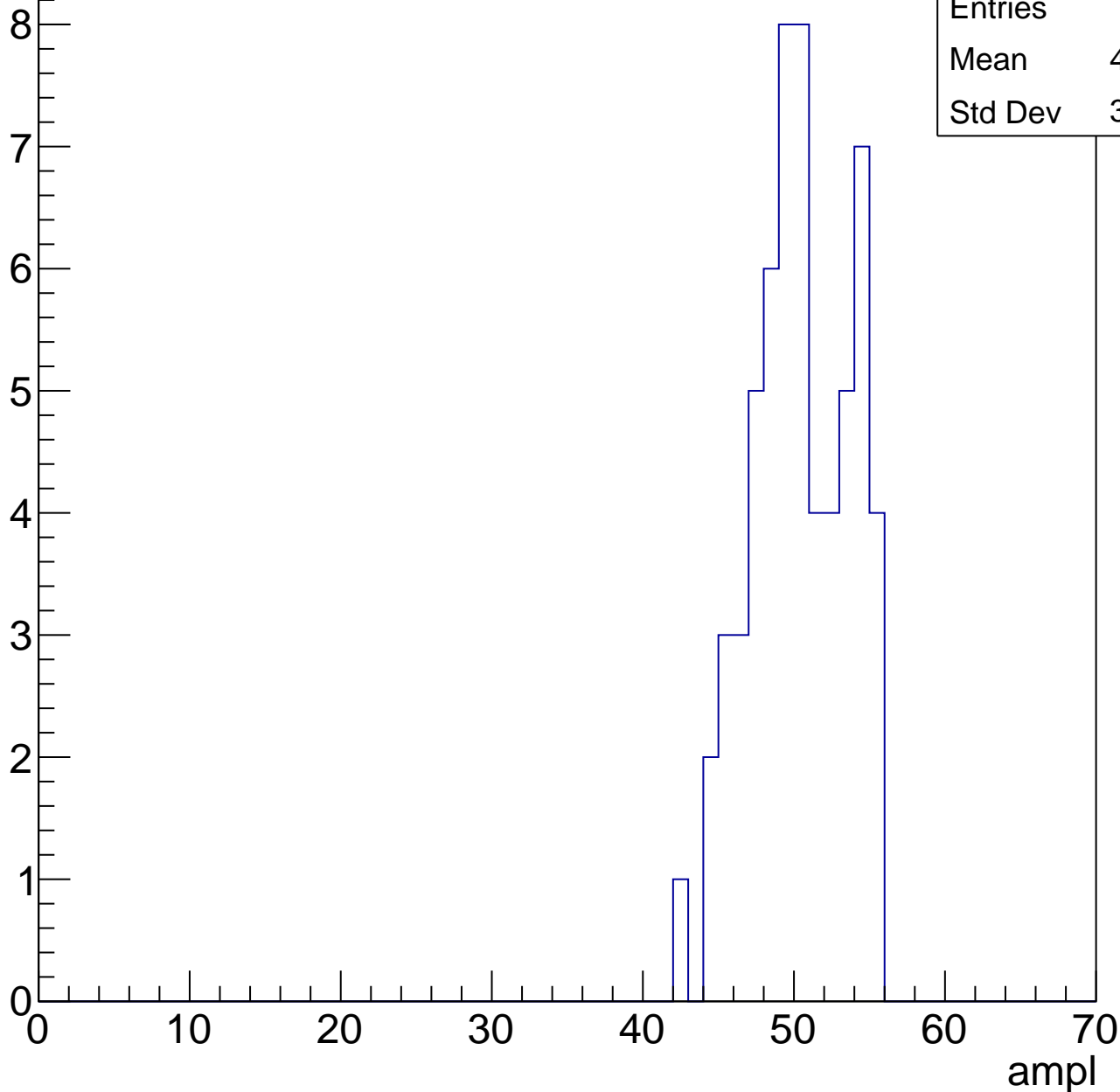


B1L103S, U20-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.88
Std Dev	3.194



B1L103S, U20-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	53
Mean	56.36
Std Dev	3.187

Entry

10

8

6

4

2

0

0

10

20

30

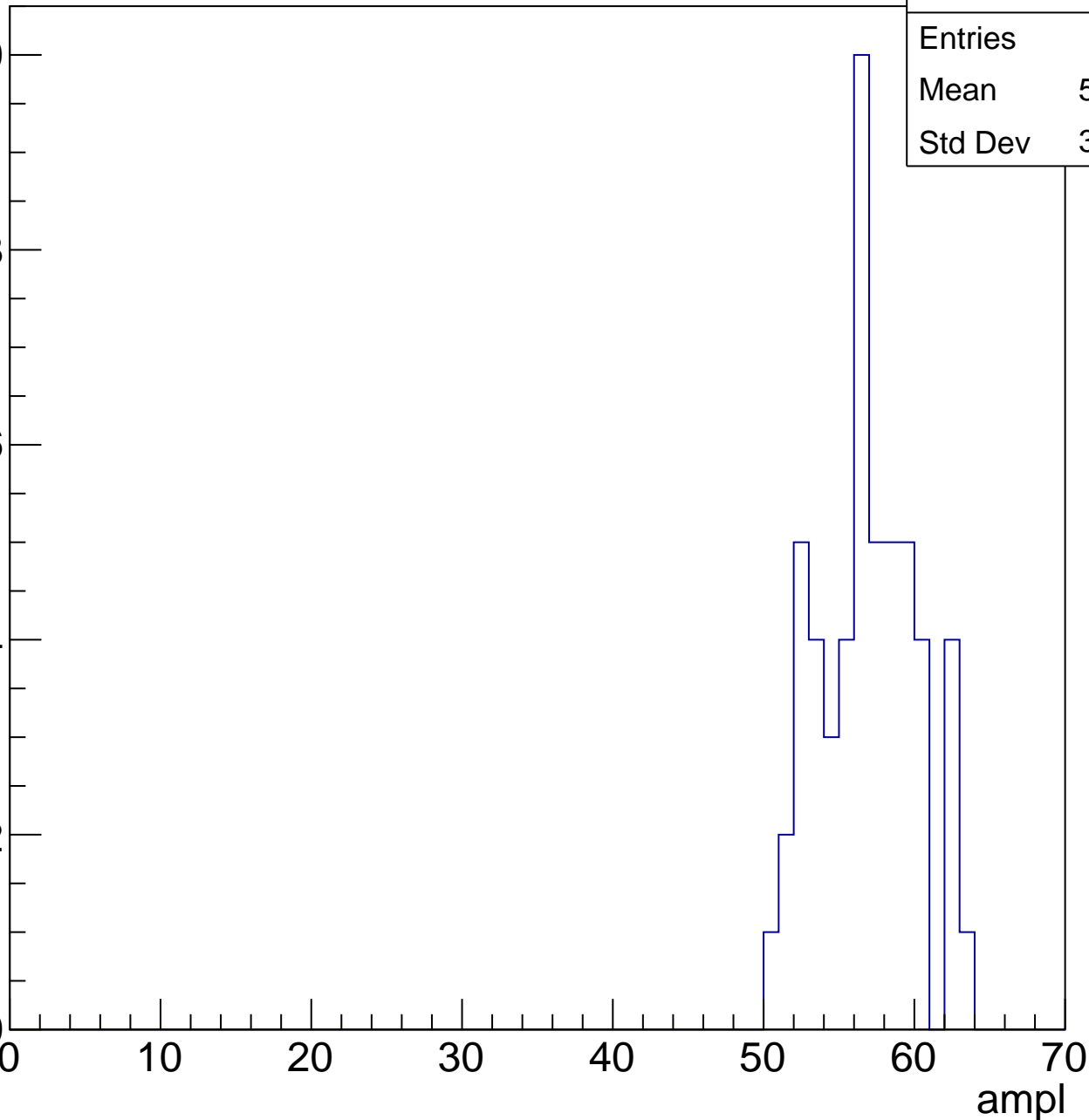
40

50

ampl

60

70

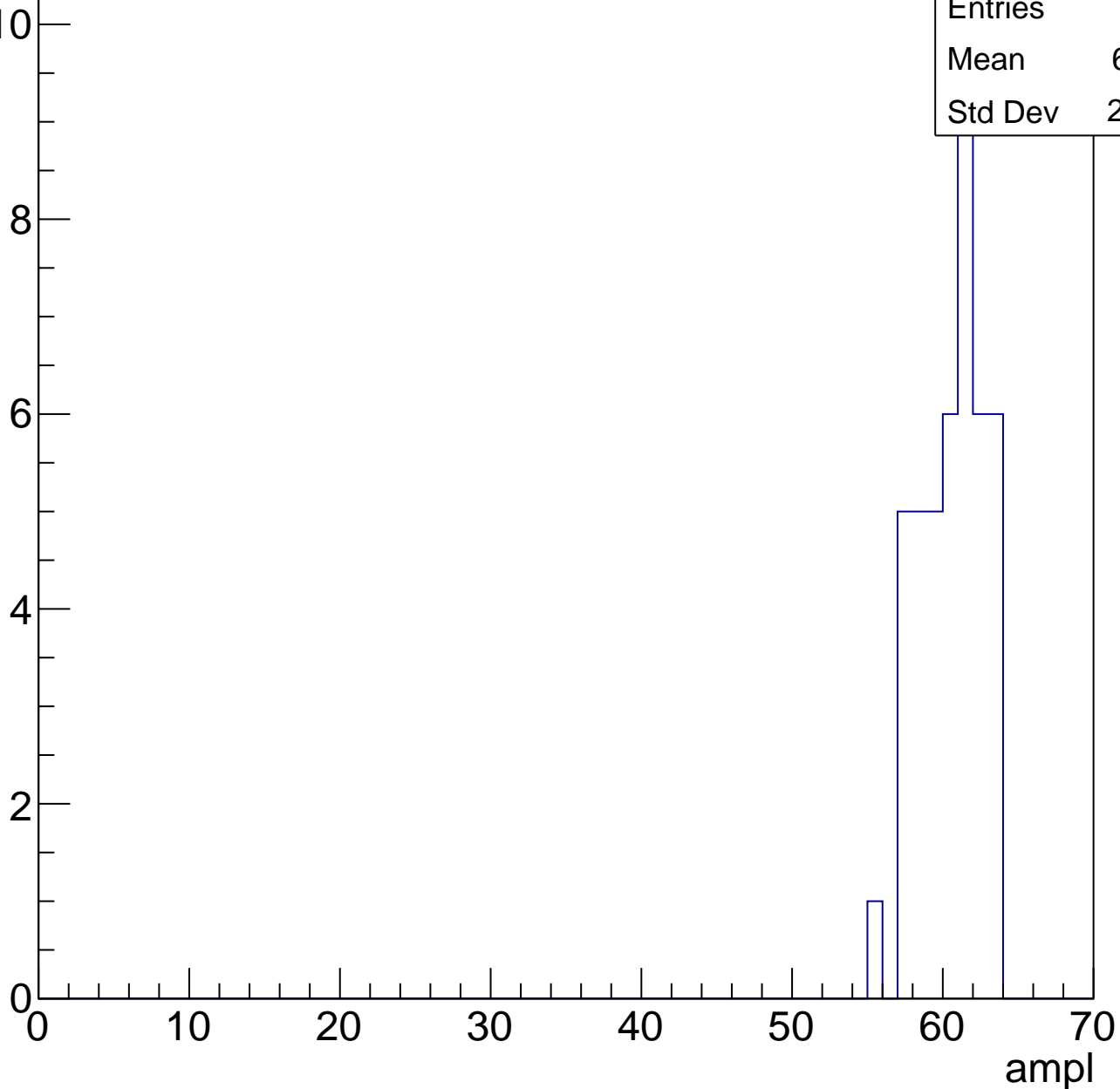


B1L103S, U20-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

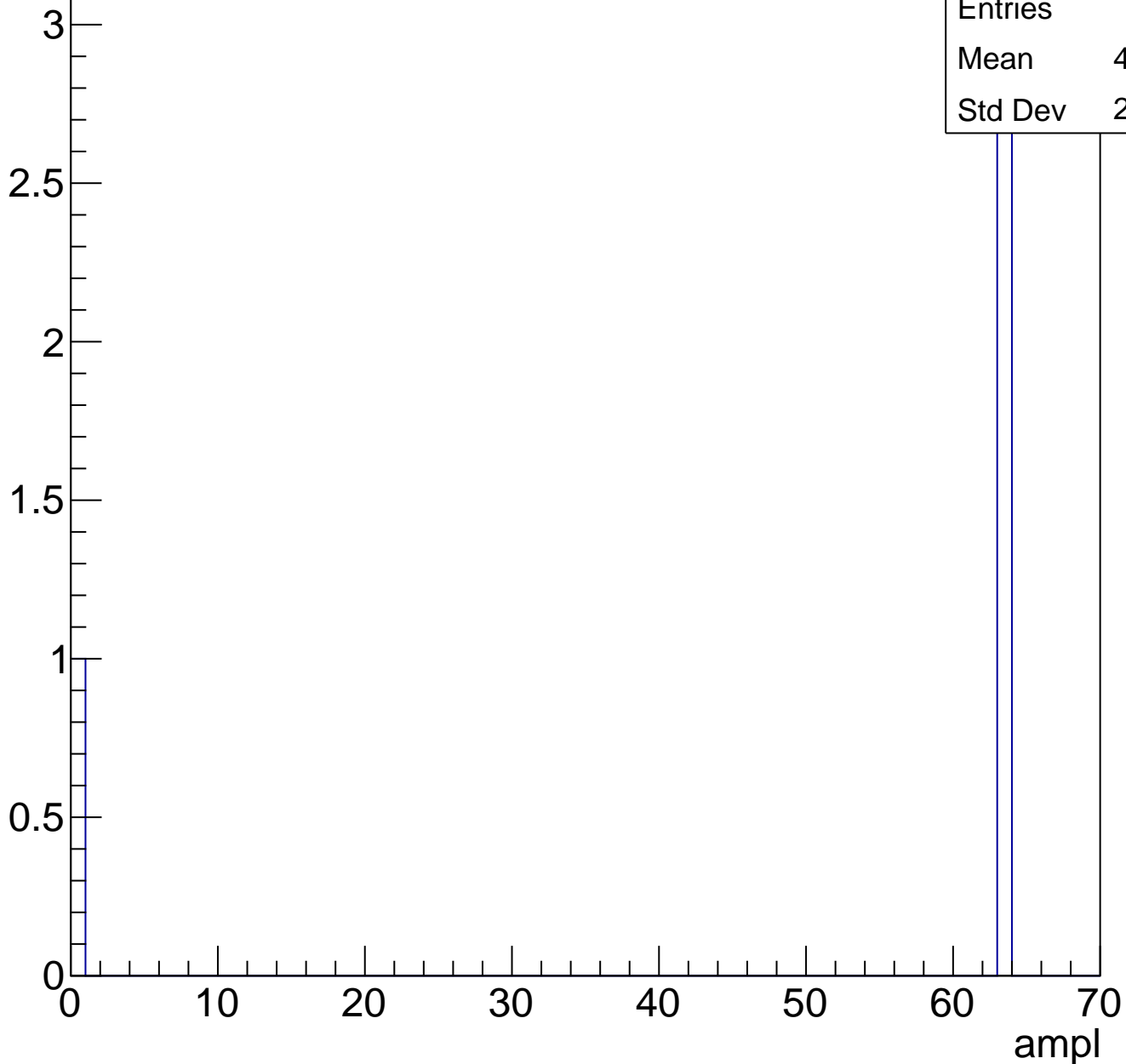
Entries	44
Mean	60.11
Std Dev	2.036



B1L103S, U20-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch58, adc0

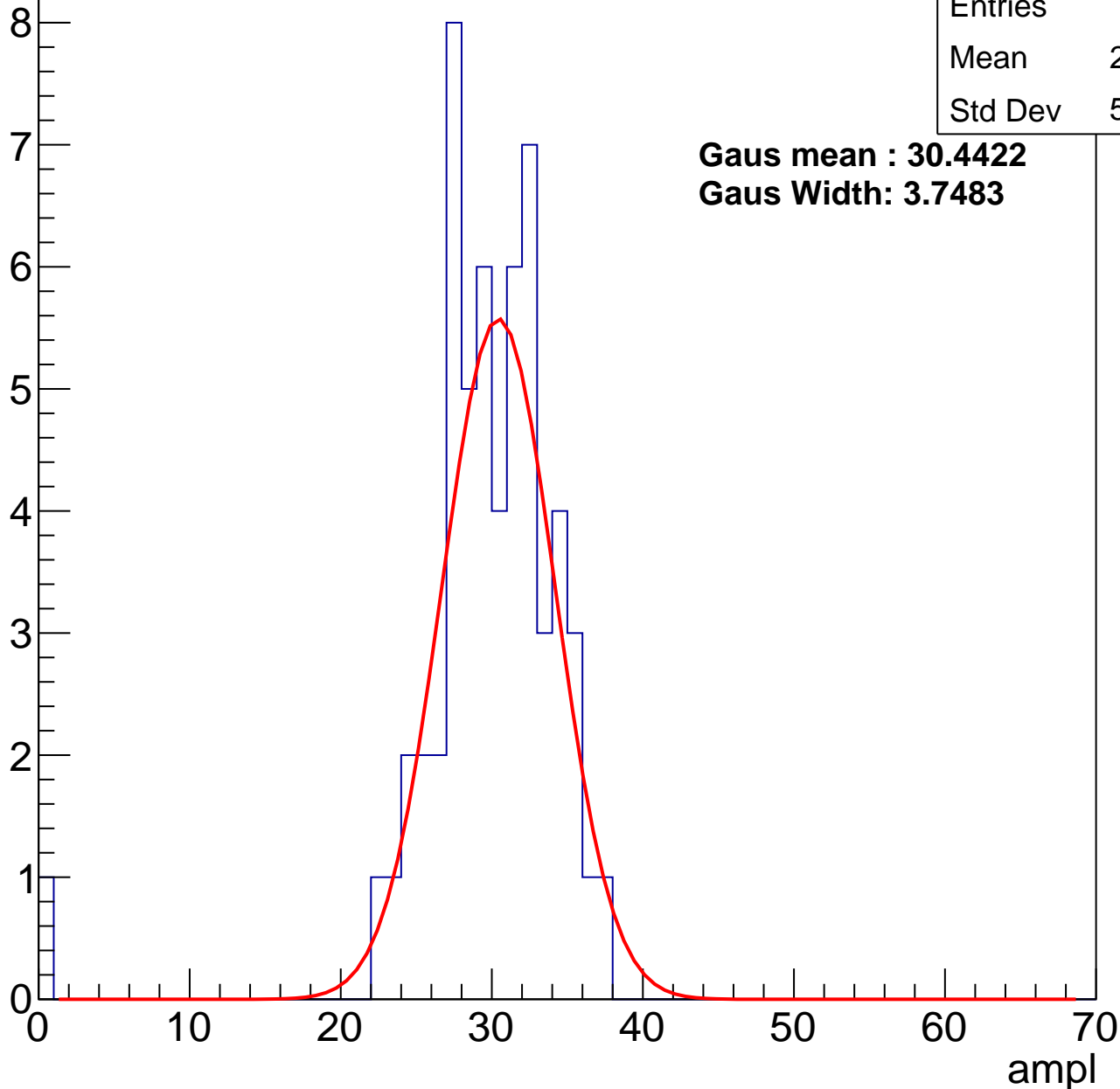
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	29.26
Std Dev	5.162

Gaus mean : 30.4422

Gaus Width: 3.7483



B1L103S, U20-ch58, adc1

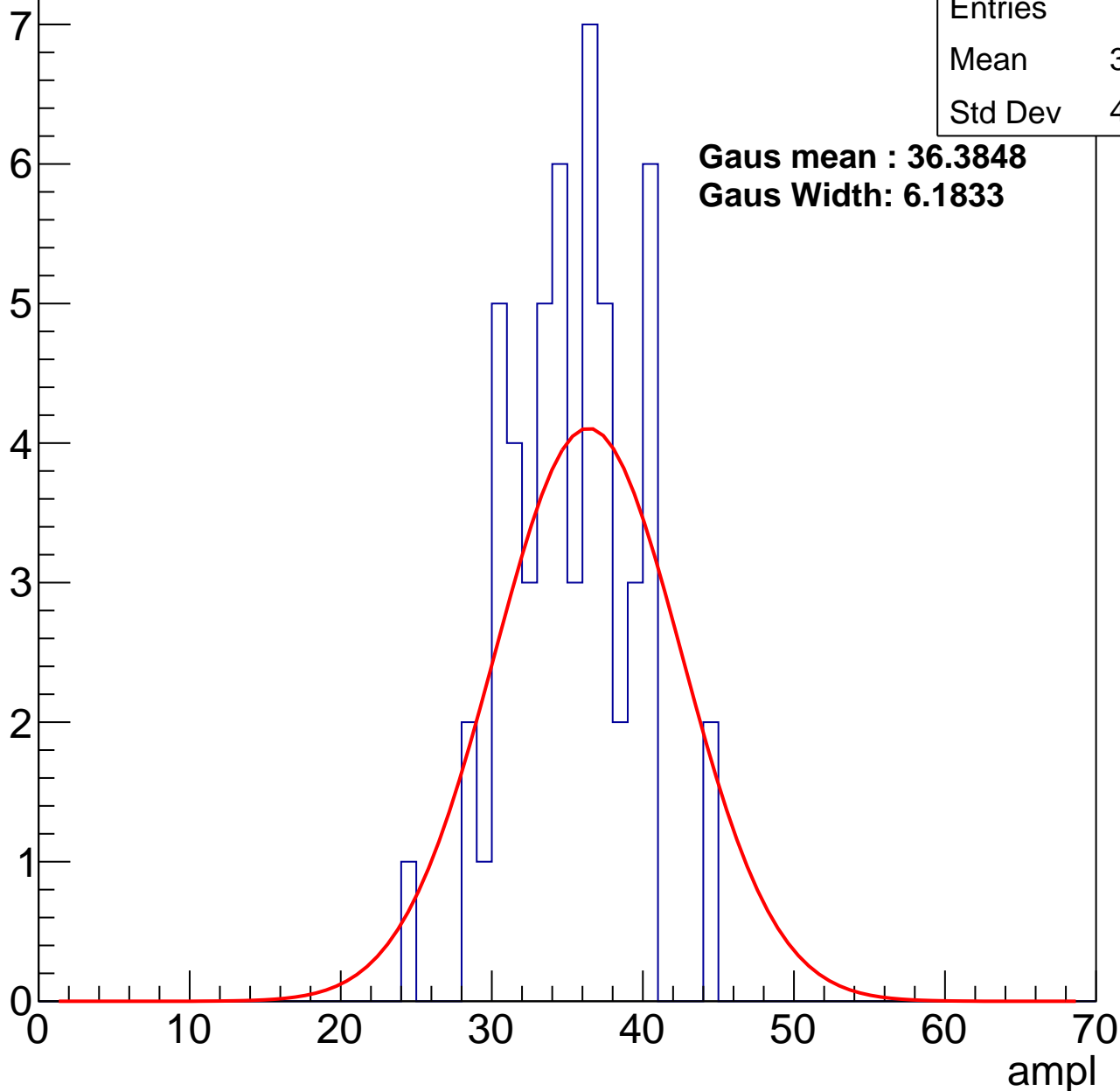
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	34.75
Std Dev	4.042

Gaus mean : 36.3848

Gaus Width: 6.1833



B1L103S, U20-ch58, adc2

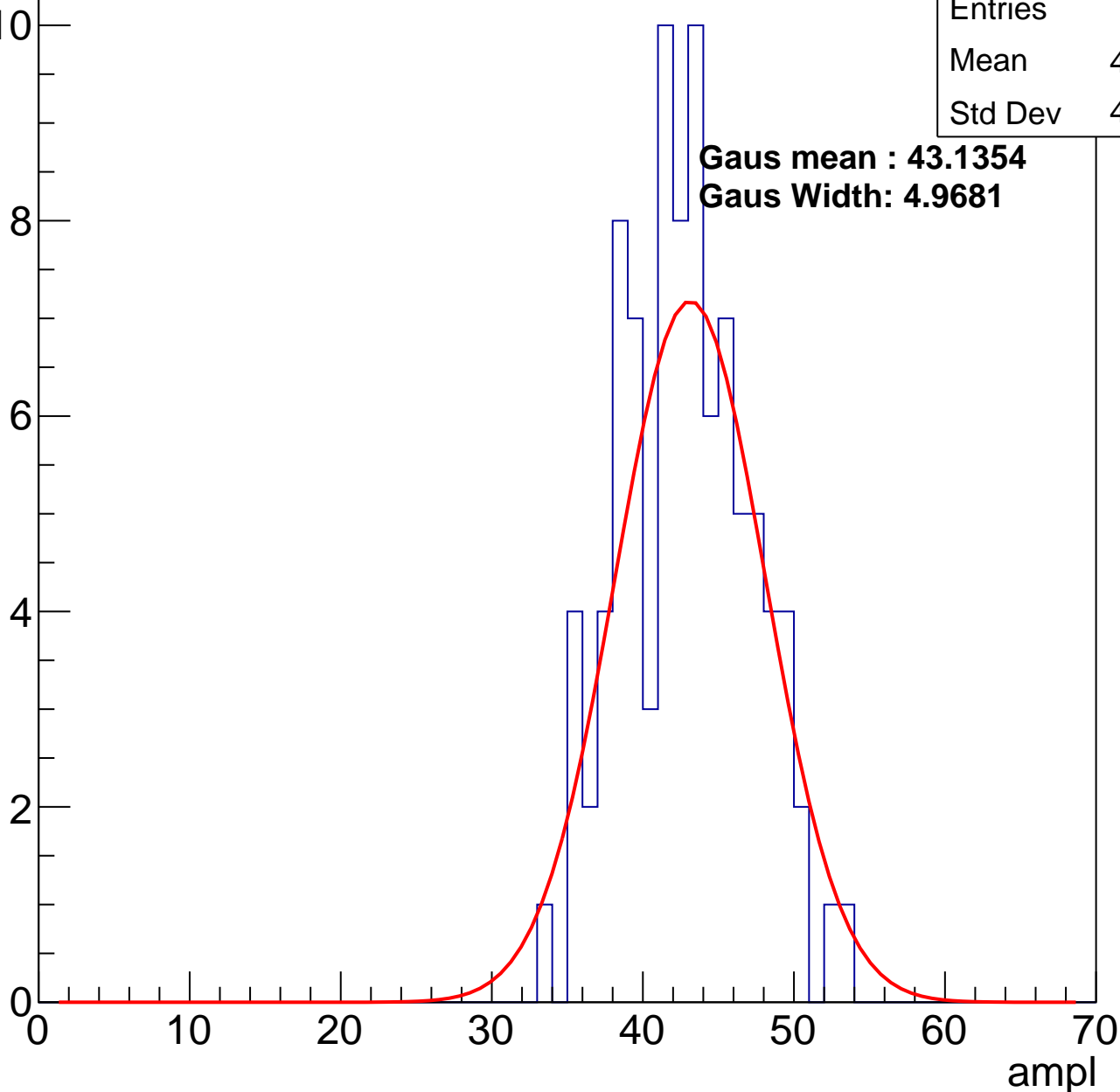
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	92
Mean	42.42
Std Dev	4.218

Gaus mean : 43.1354

Gaus Width: 4.9681

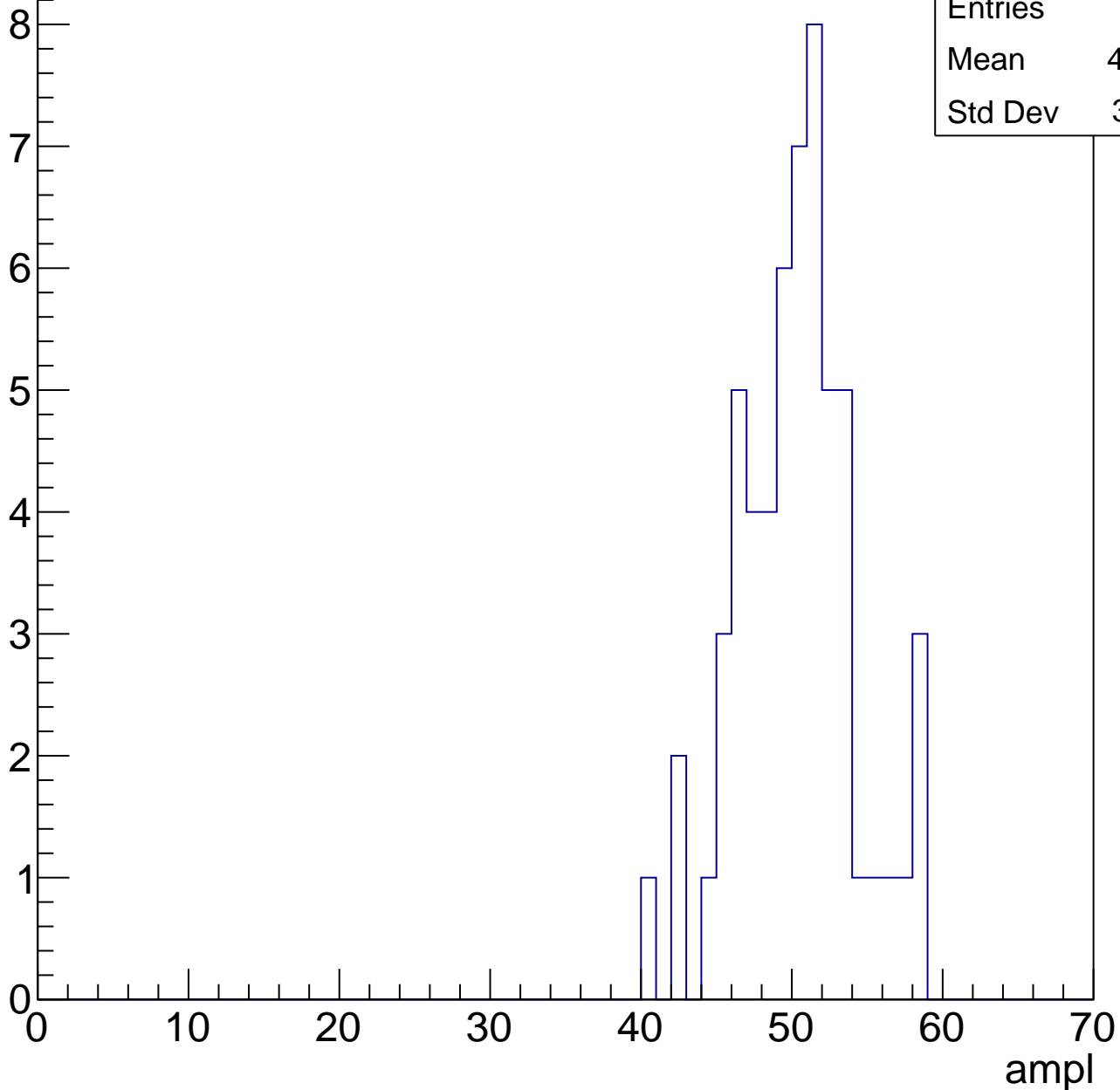


B1L103S, U20-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.76
Std Dev	3.861

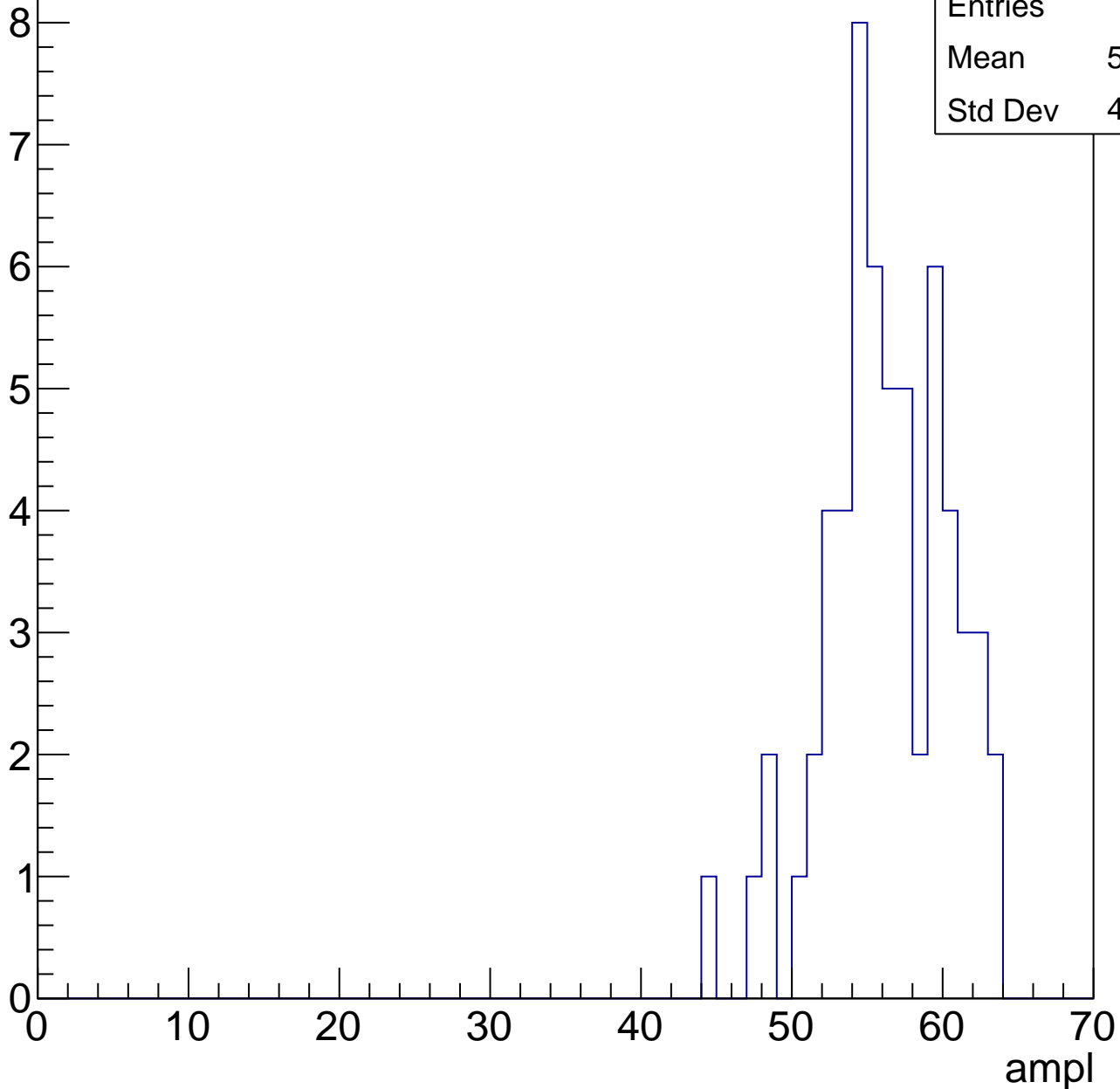


B1L103S, U20-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.78
Std Dev	4.088

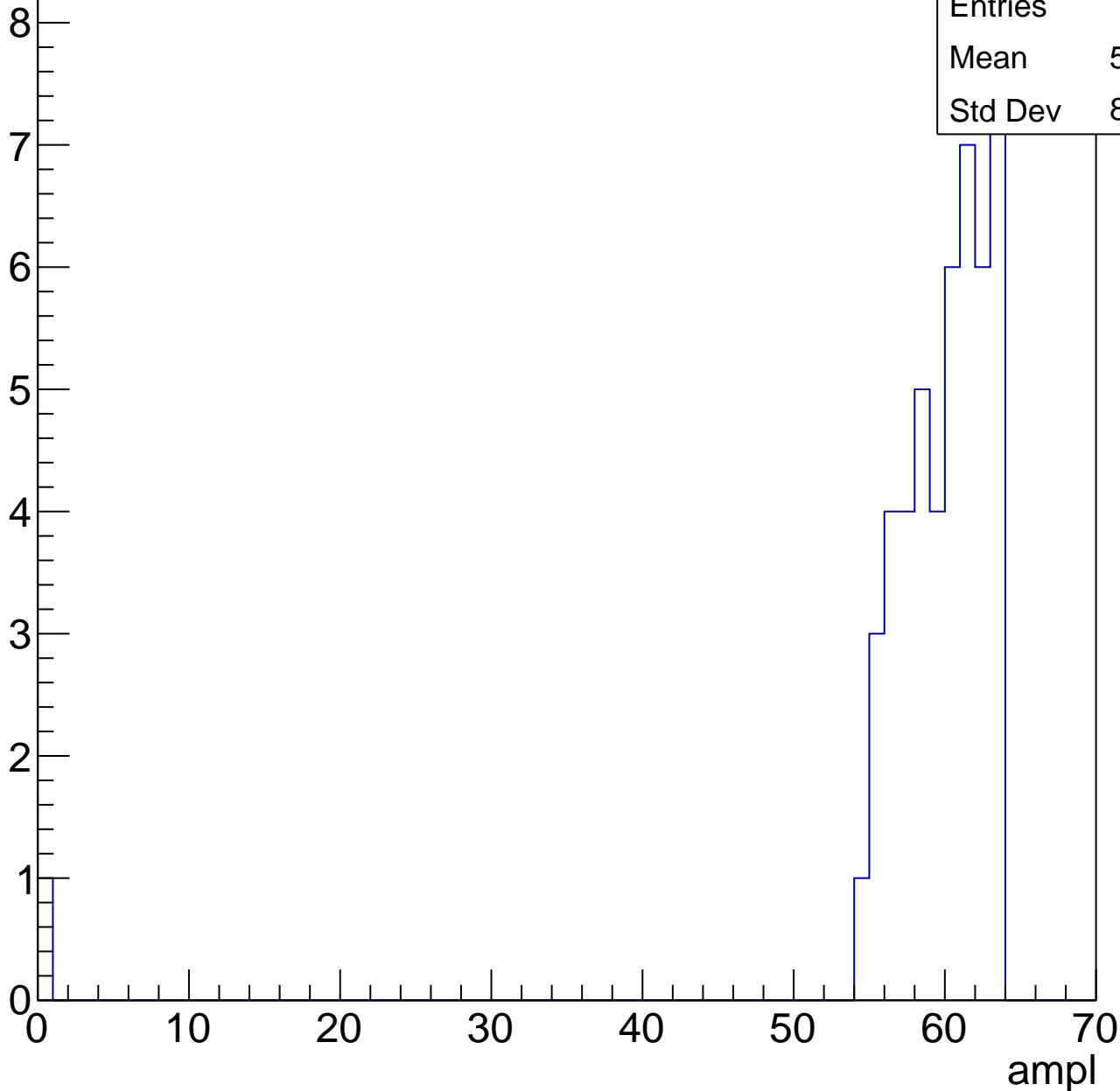


B1L103S, U20-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.37
Std Dev	8.815



B1L103S, U20-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch59, adc0

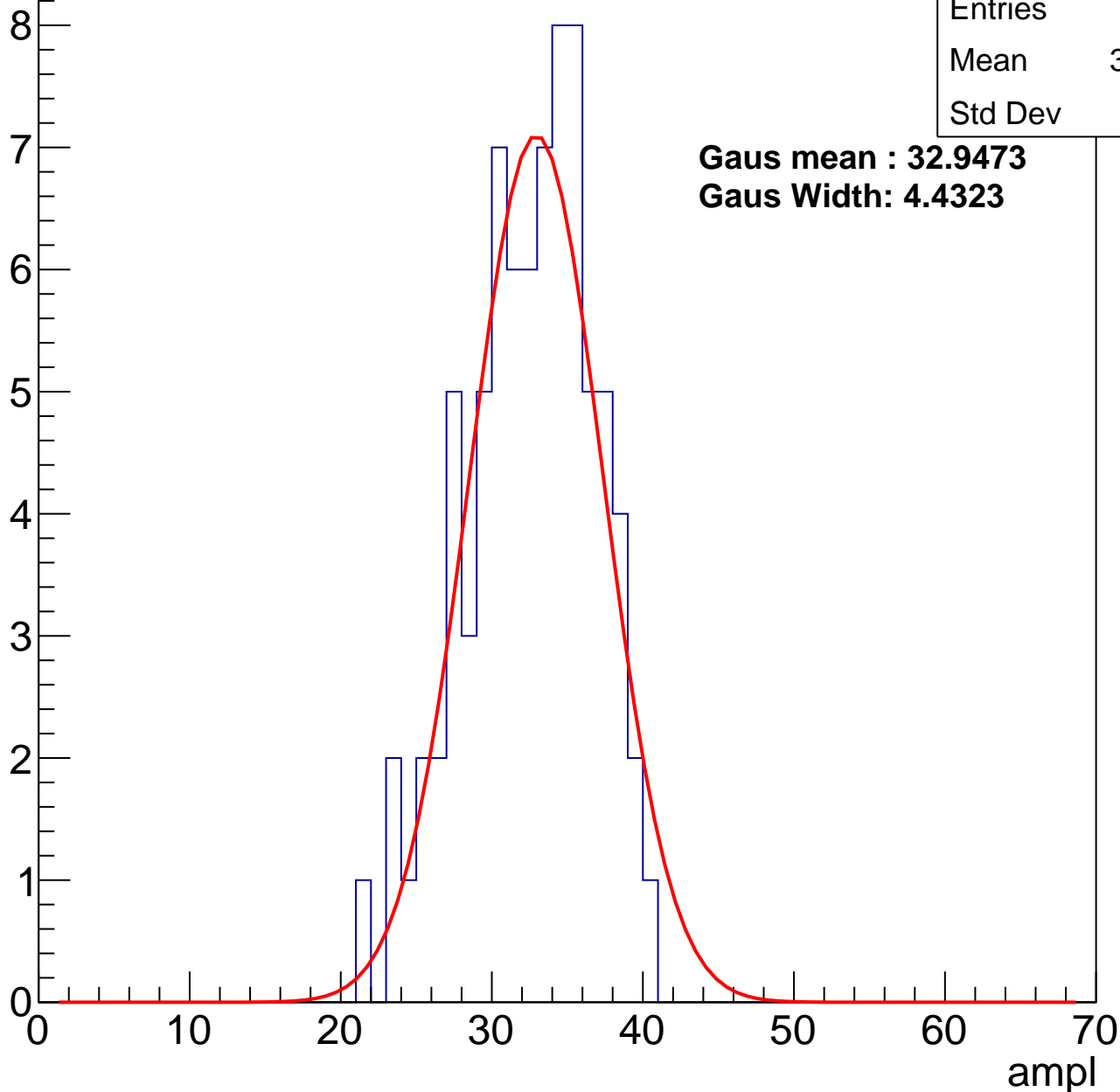
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	32.04
Std Dev	4.17

Gaus mean : 32.9473

Gaus Width: 4.4323



B1L103S, U20-ch59, adc1

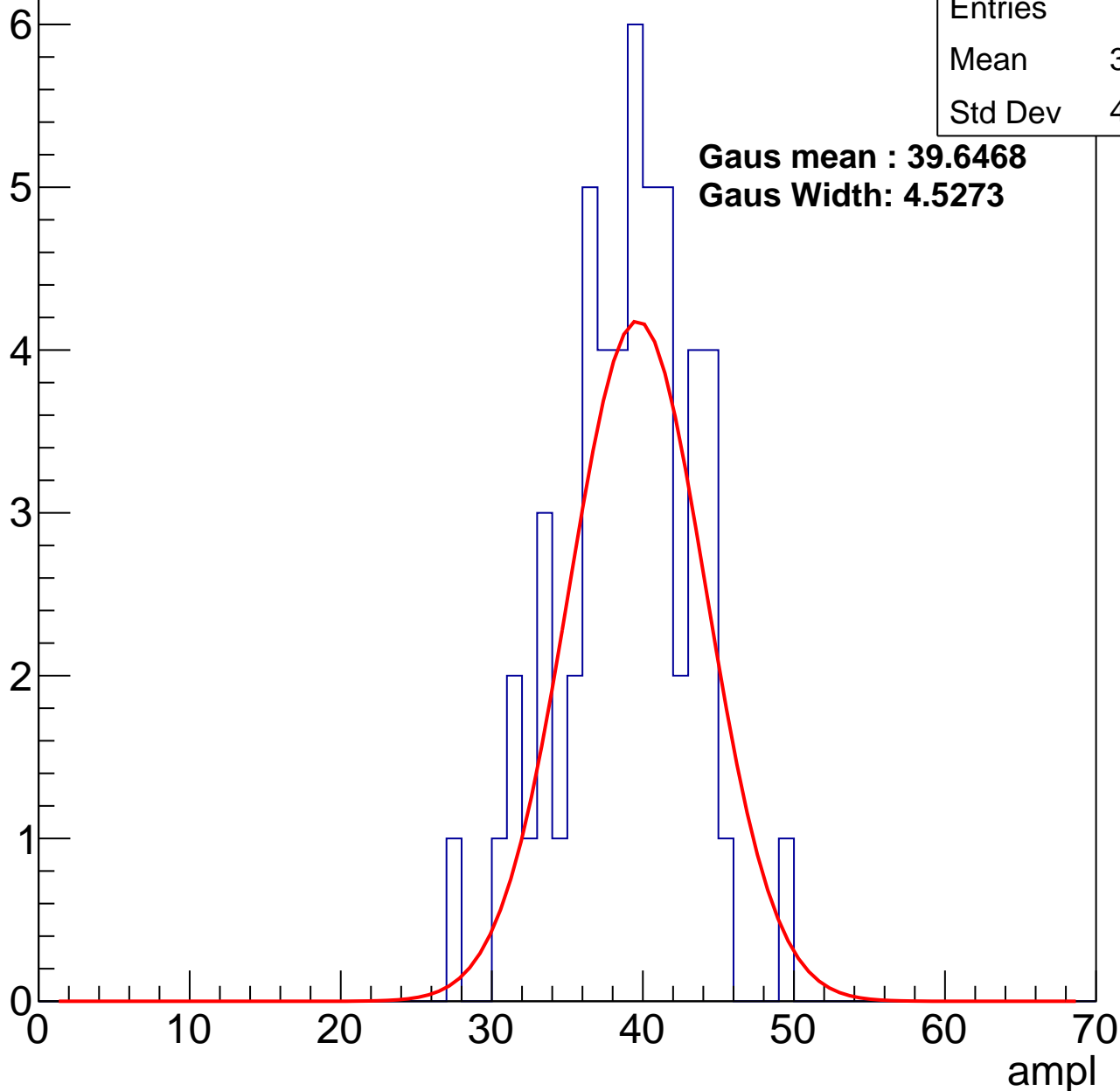
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	38.44
Std Dev	4.294

Gaus mean : 39.6468

Gaus Width: 4.5273



B1L103S, U20-ch59, adc2

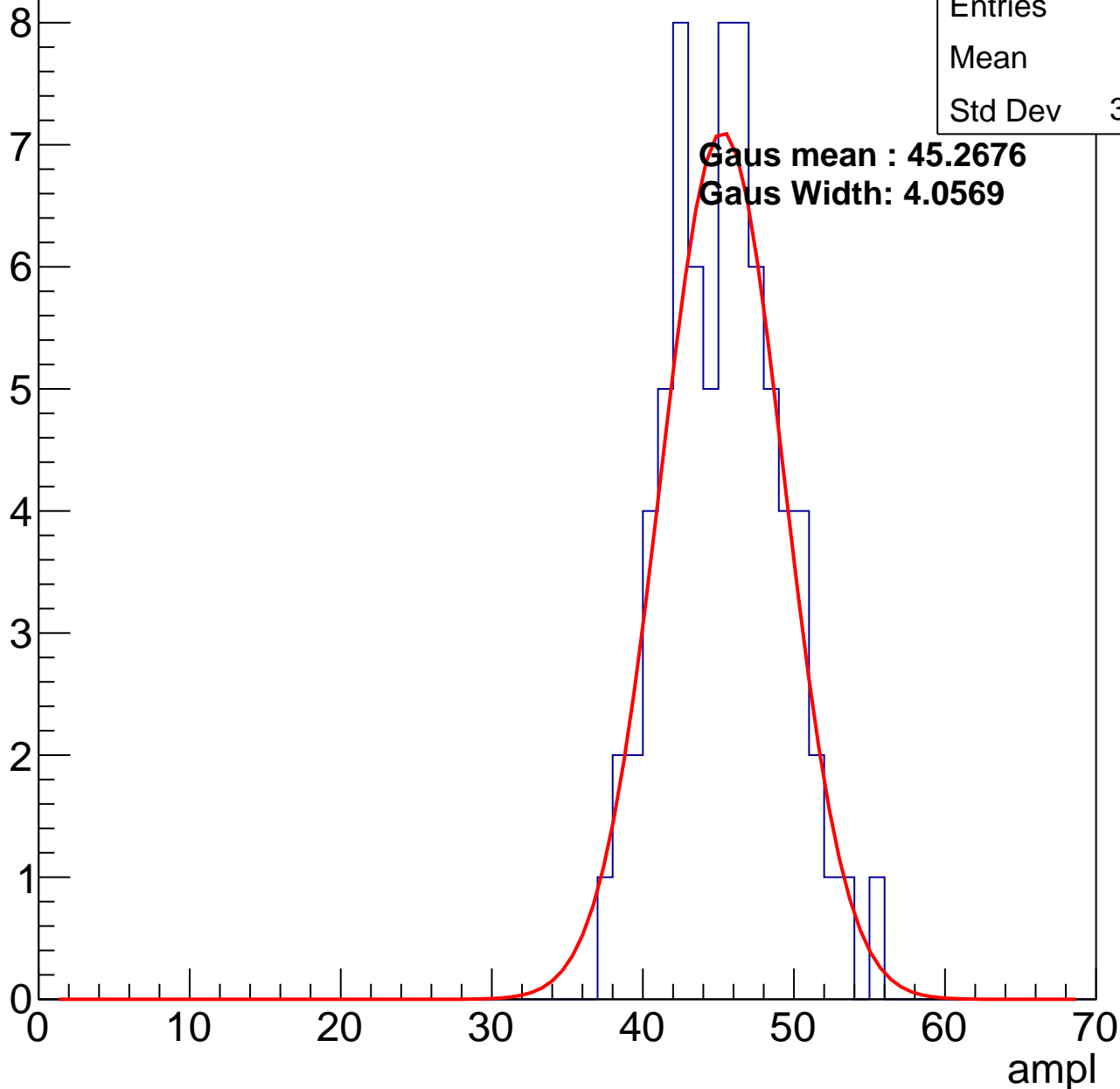
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	44.9
Std Dev	3.775

Gaus mean : 45.2676

Gaus Width: 4.0569

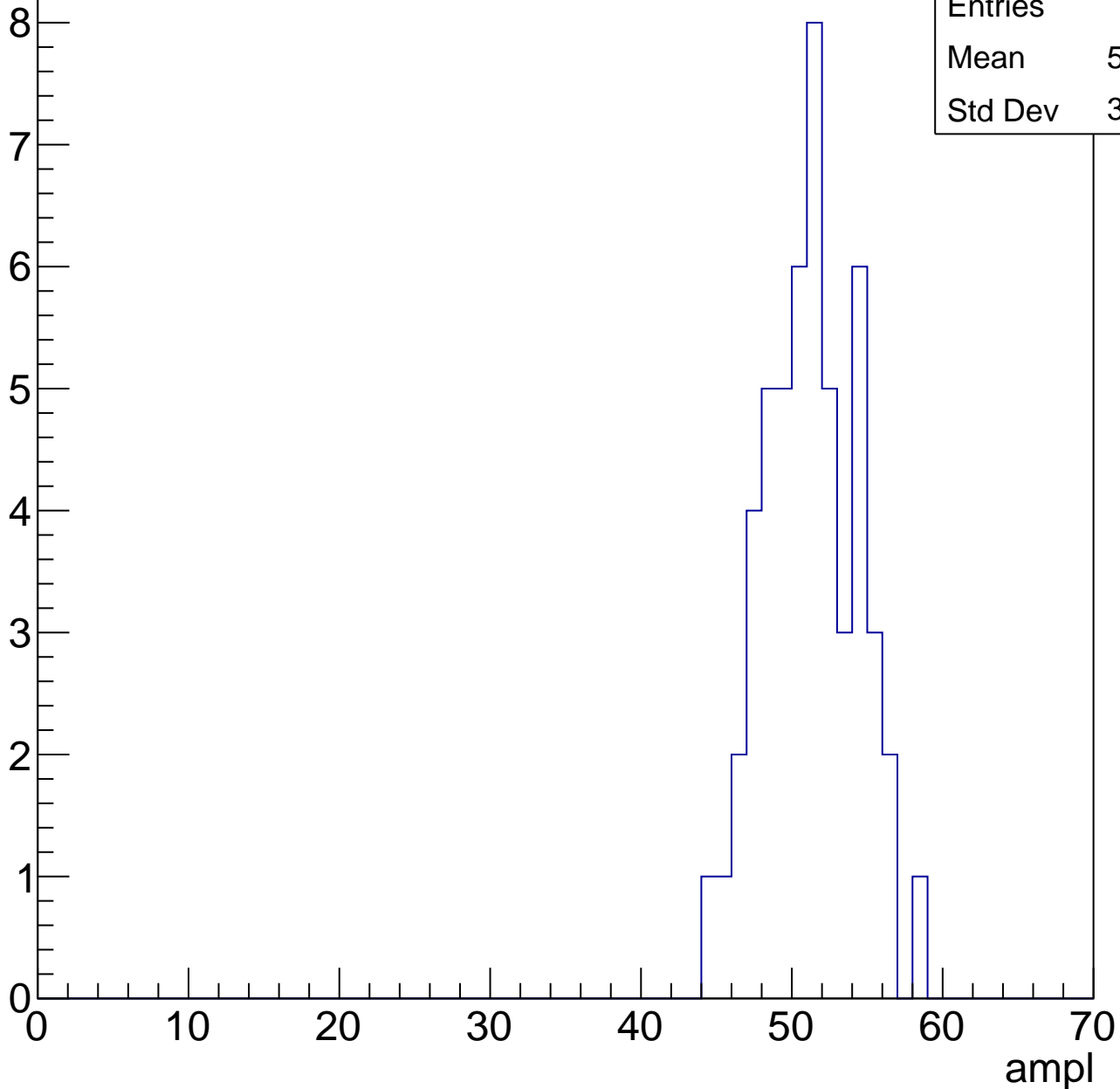


B1L103S, U20-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	50.77
Std Dev	3.055



B1L103S, U20-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	56.5
Std Dev	7.322

Entry

10

8

6

4

2

0

0

10

20

30

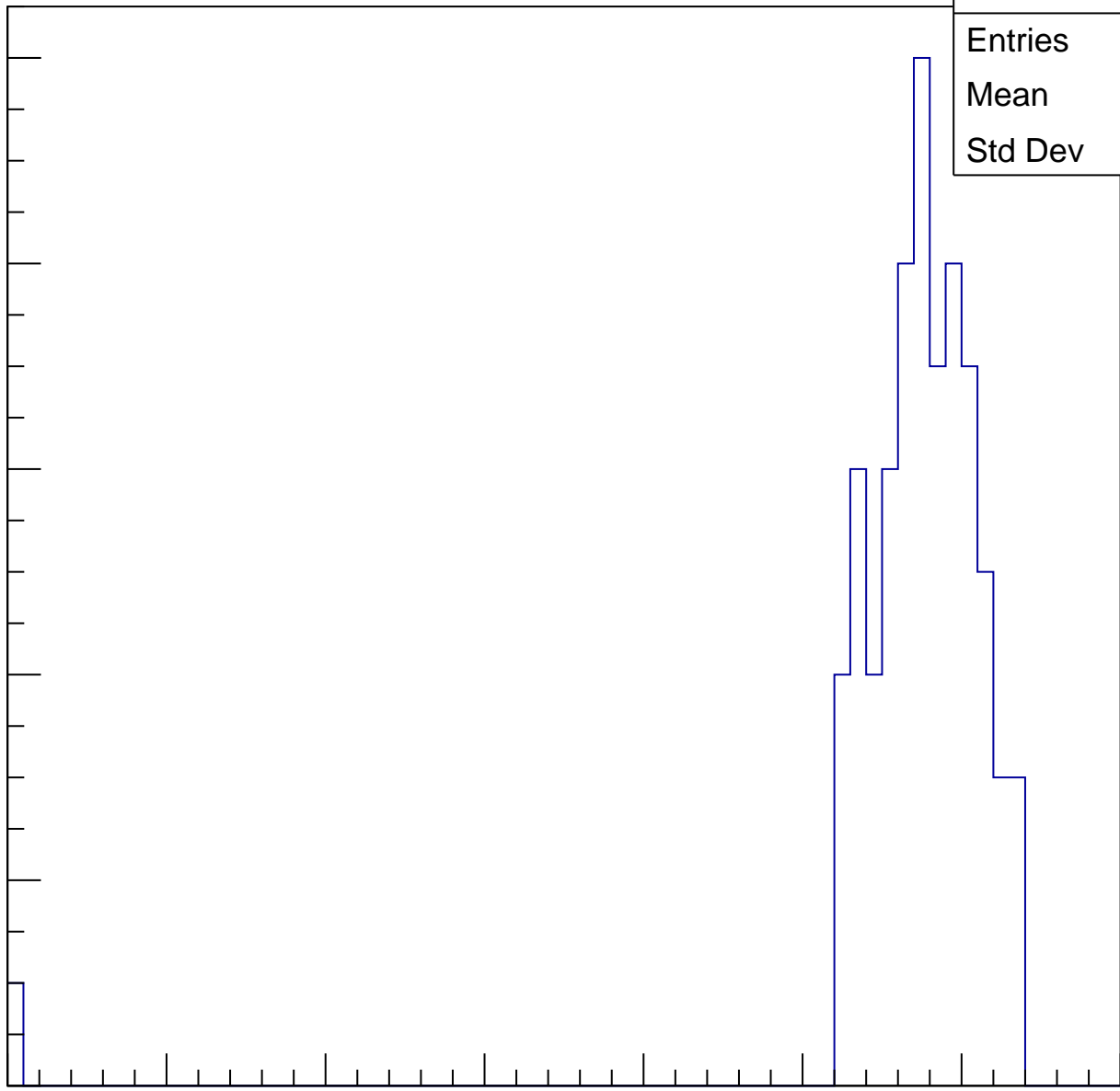
40

50

60

70

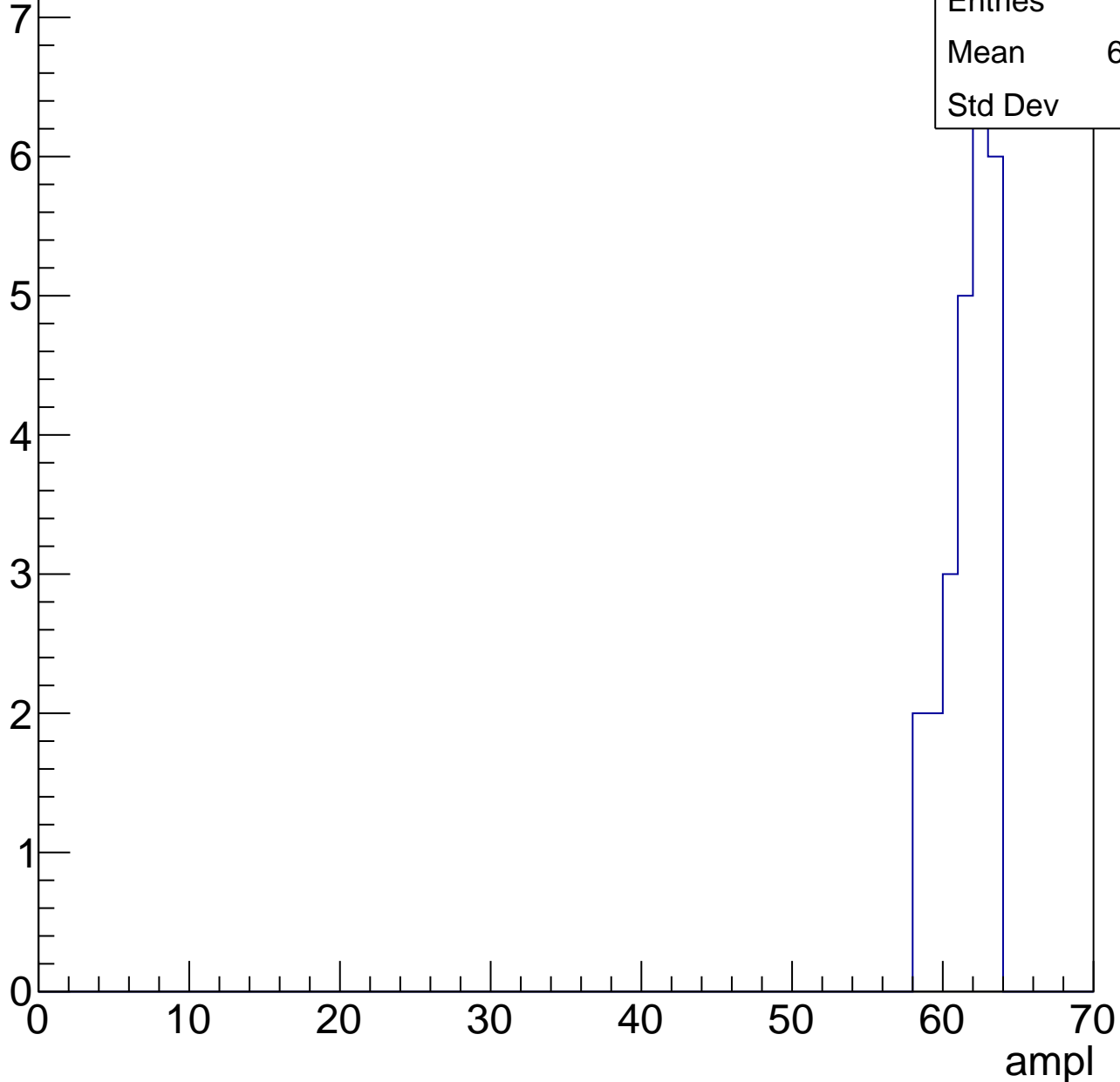
ampl



B1L103S, U20-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch60, adc0

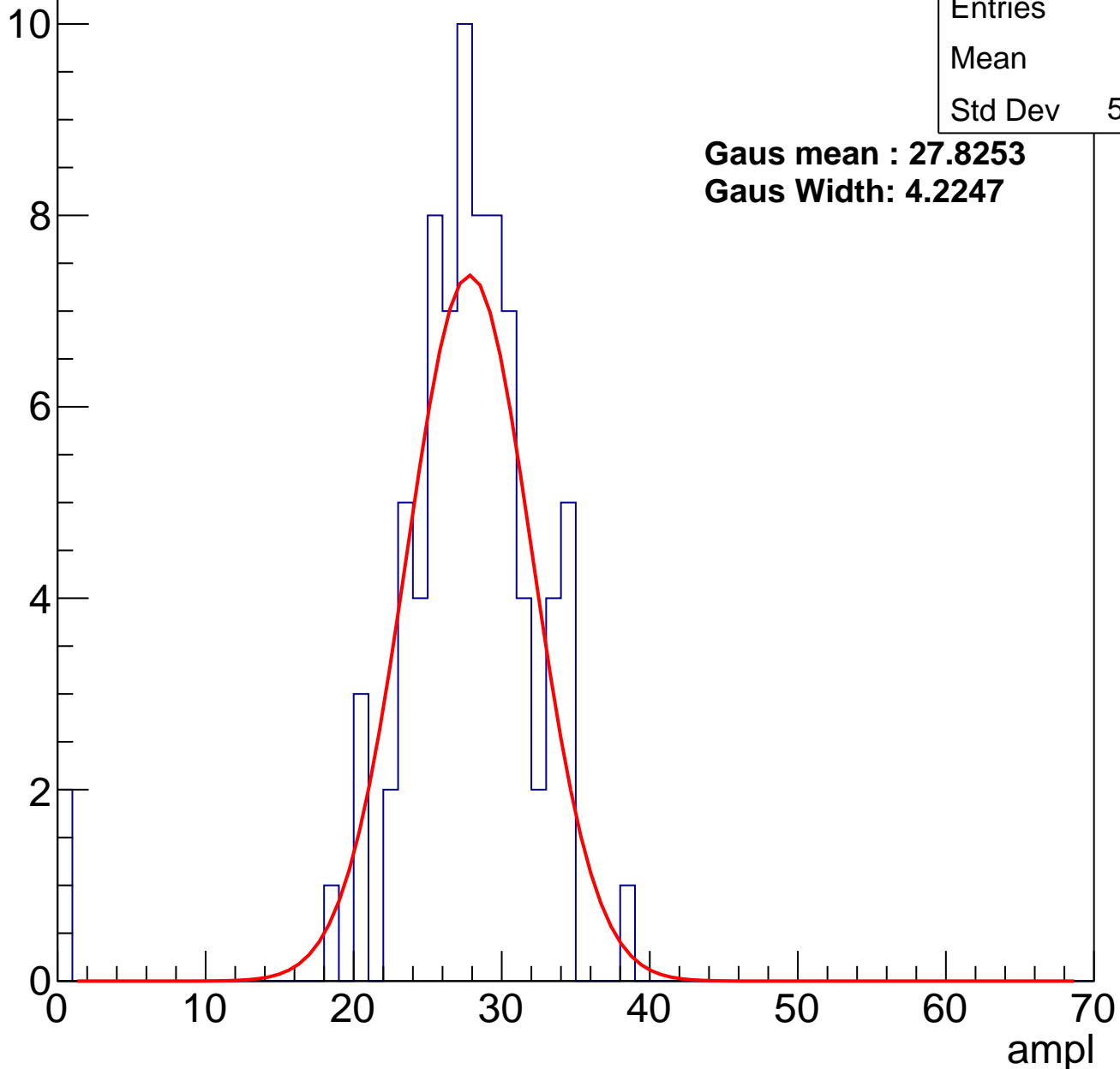
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	26.9
Std Dev	5.684

Gaus mean : 27.8253

Gaus Width: 4.2247

Entry



B1L103S, U20-ch60, adc1

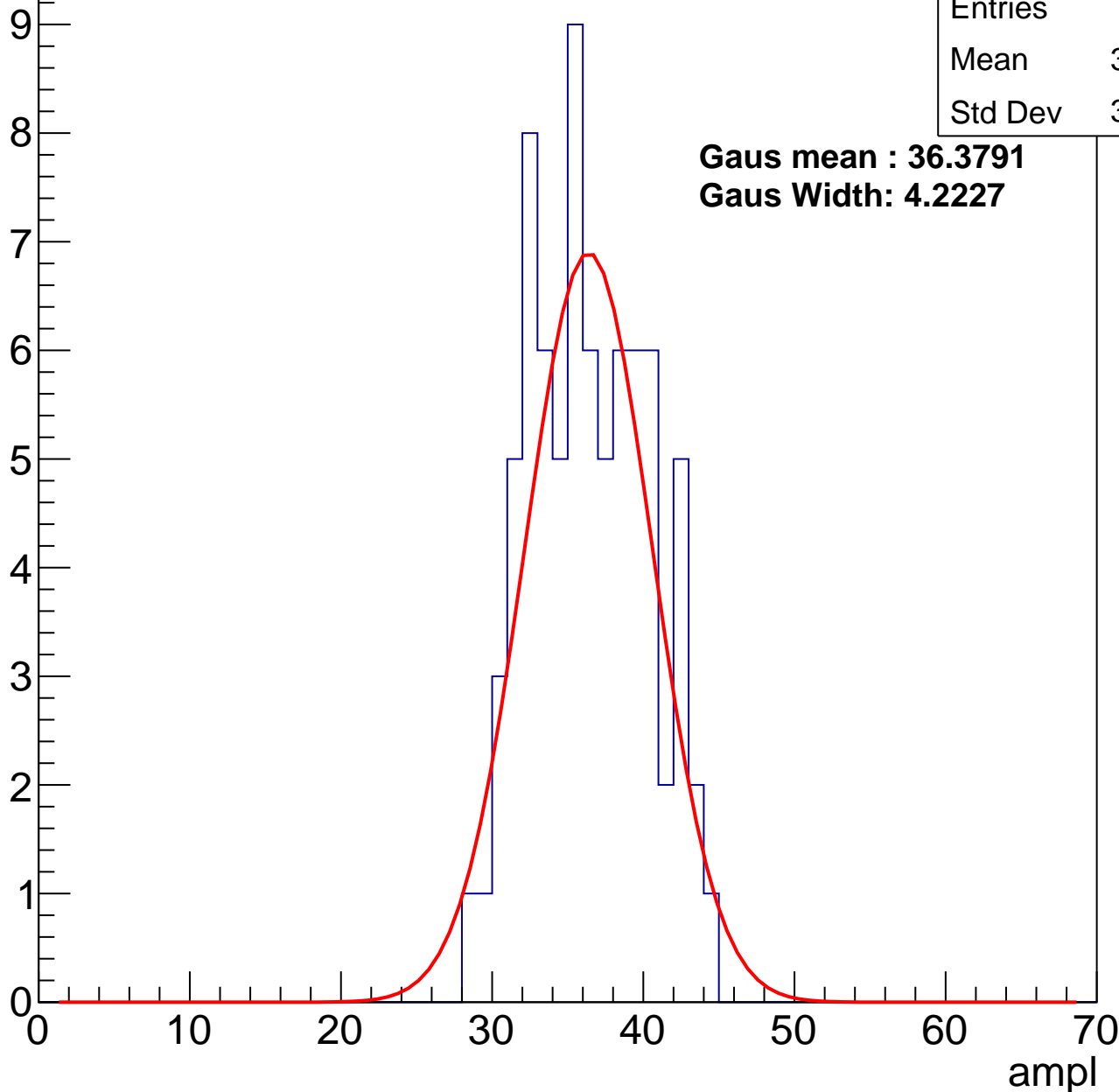
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	35.92
Std Dev	3.827

Gaus mean : 36.3791

Gaus Width: 4.2227



B1L103S, U20-ch60, adc2

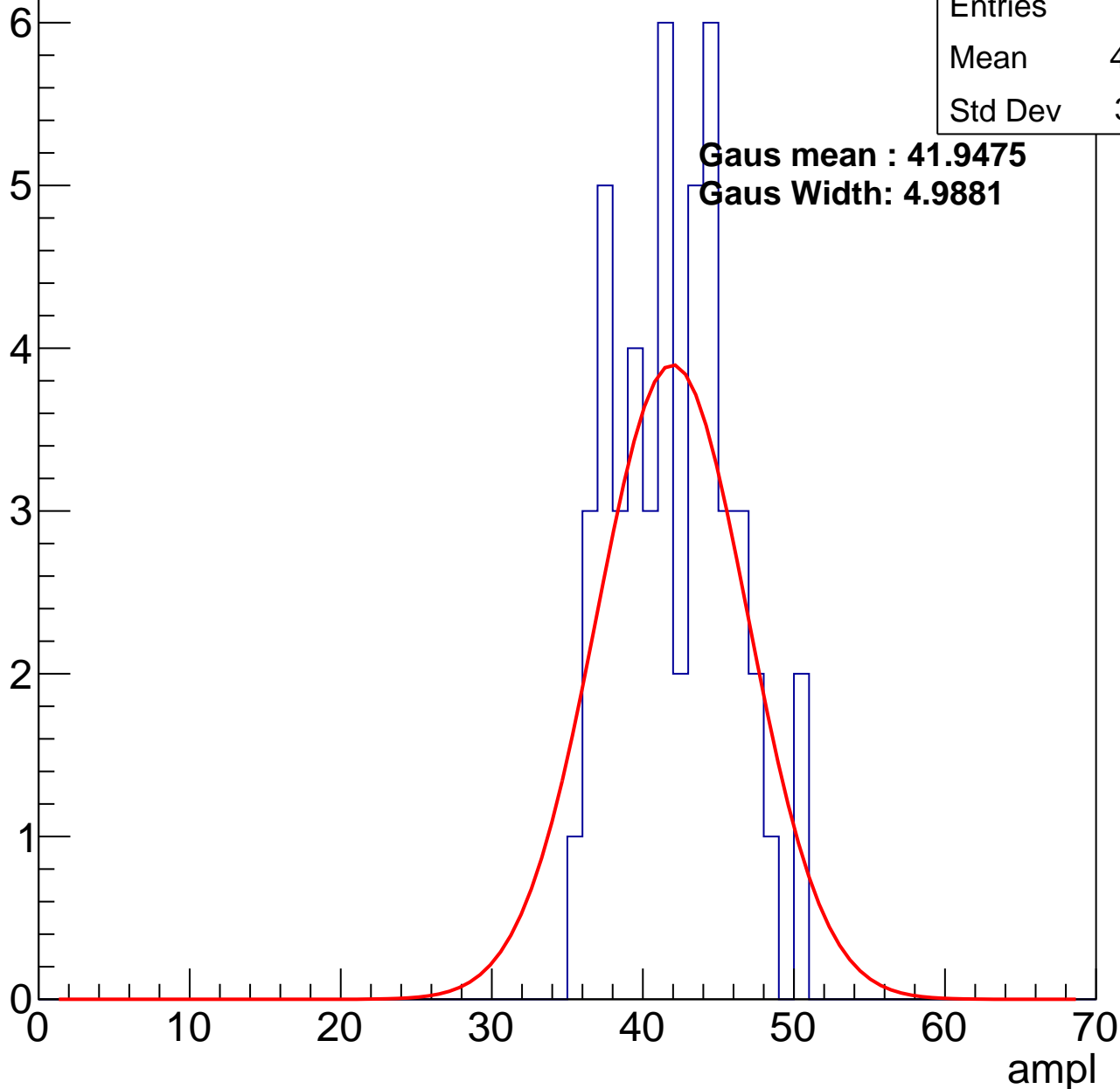
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	41.67
Std Dev	3.771

Gaus mean : 41.9475

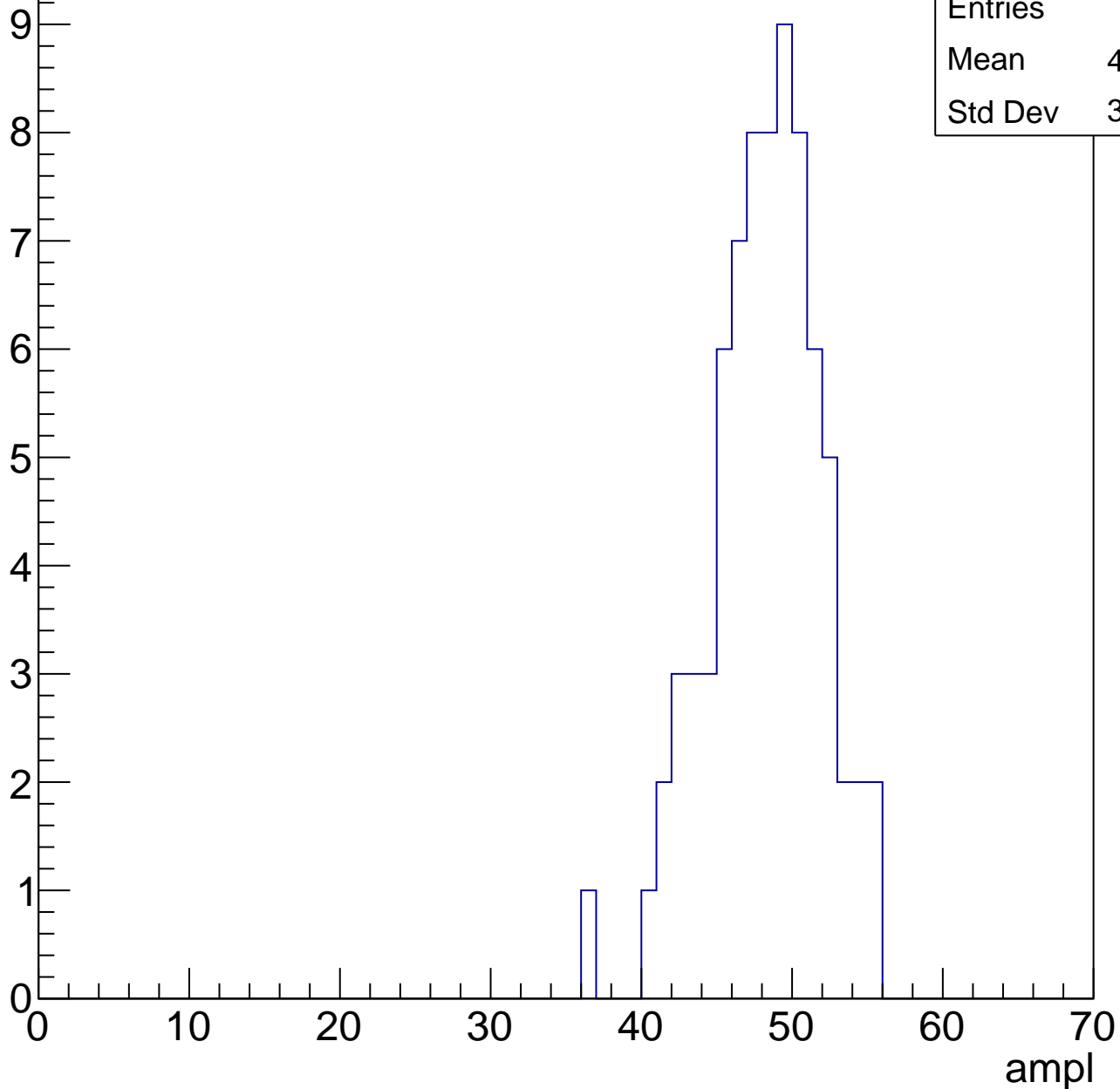
Gaus Width: 4.9881



B1L103S, U20-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

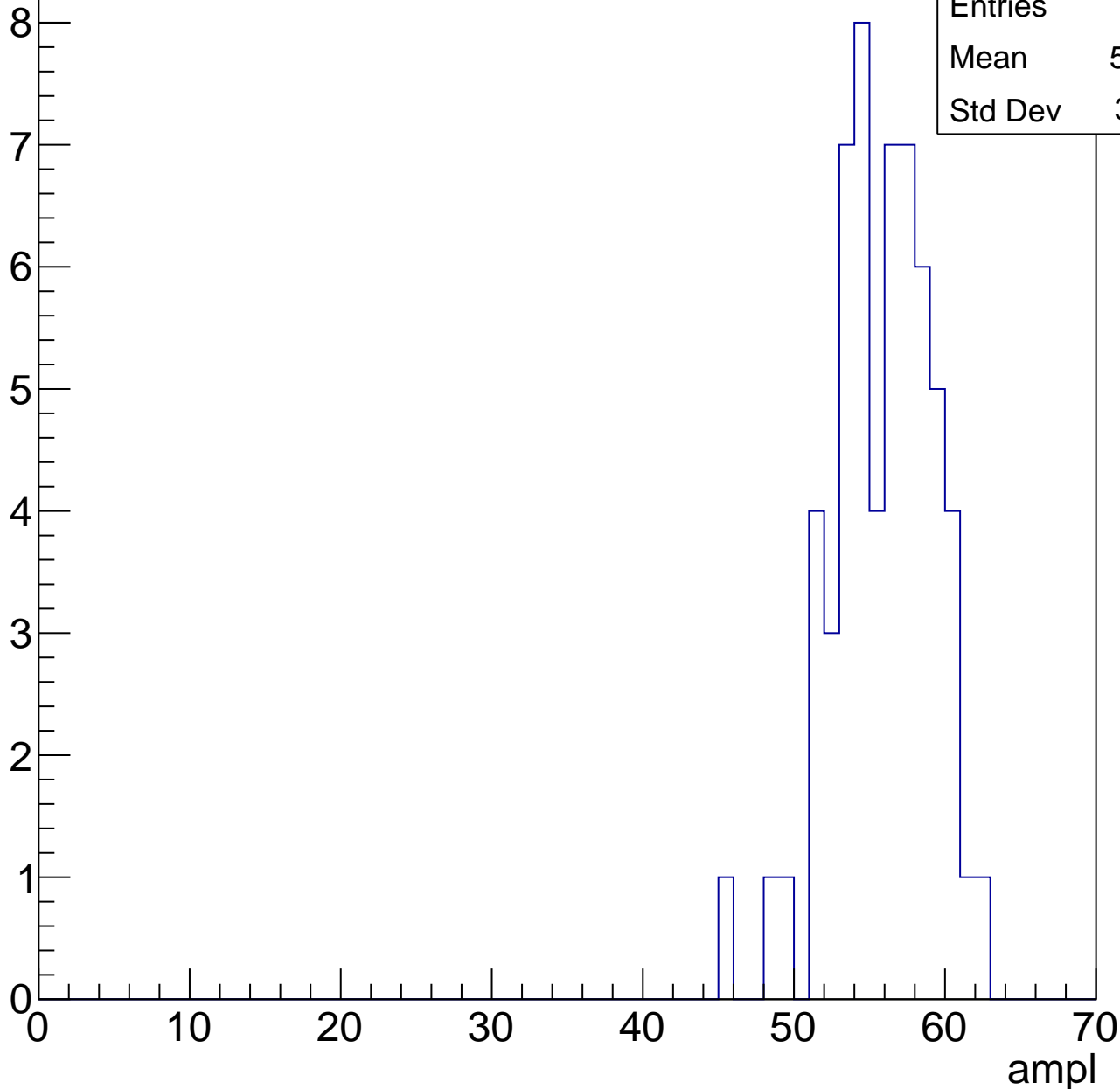


B1L103S, U20-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

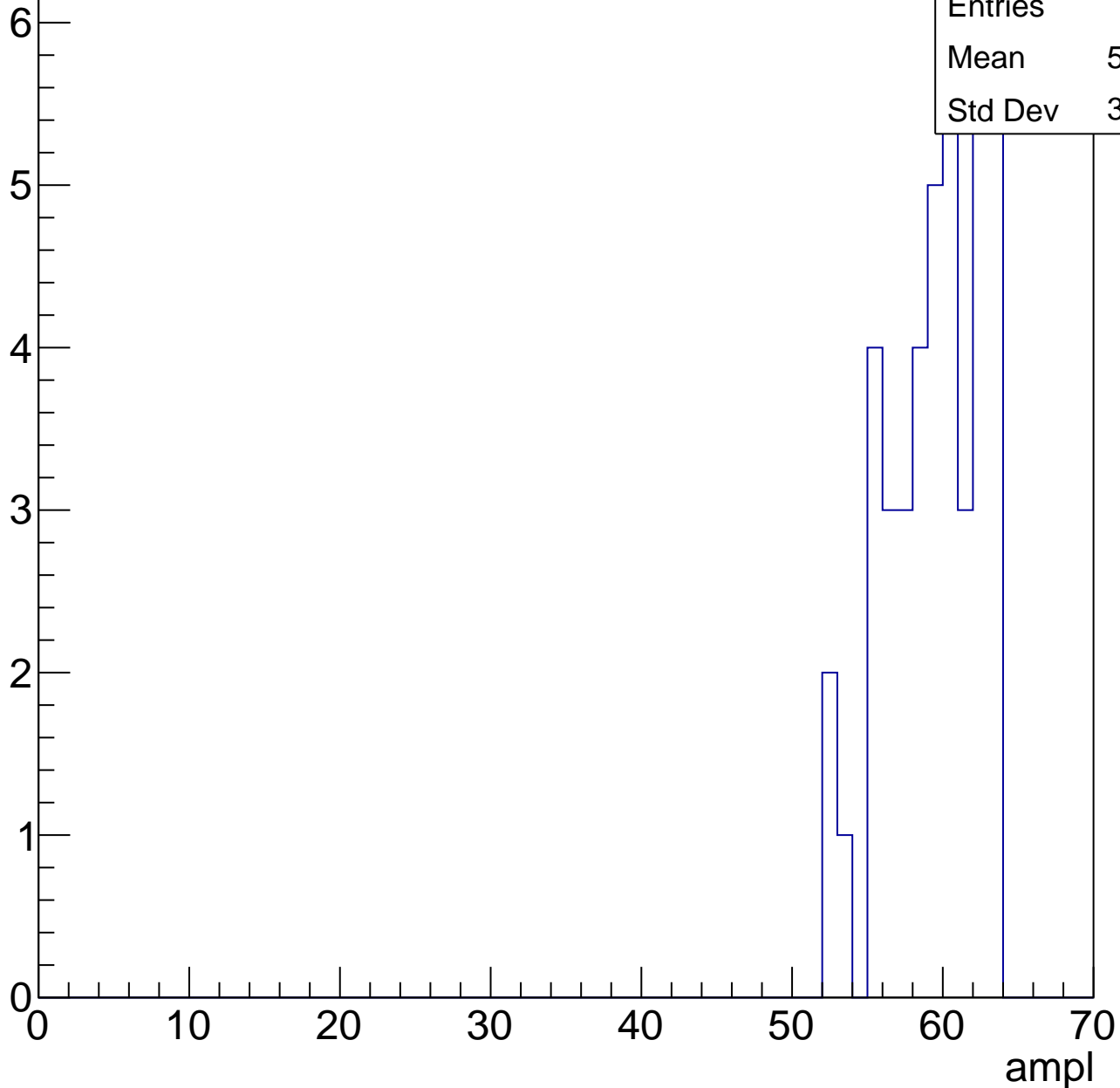
Entries	60
Mean	55.37
Std Dev	3.311



B1L103S, U20-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

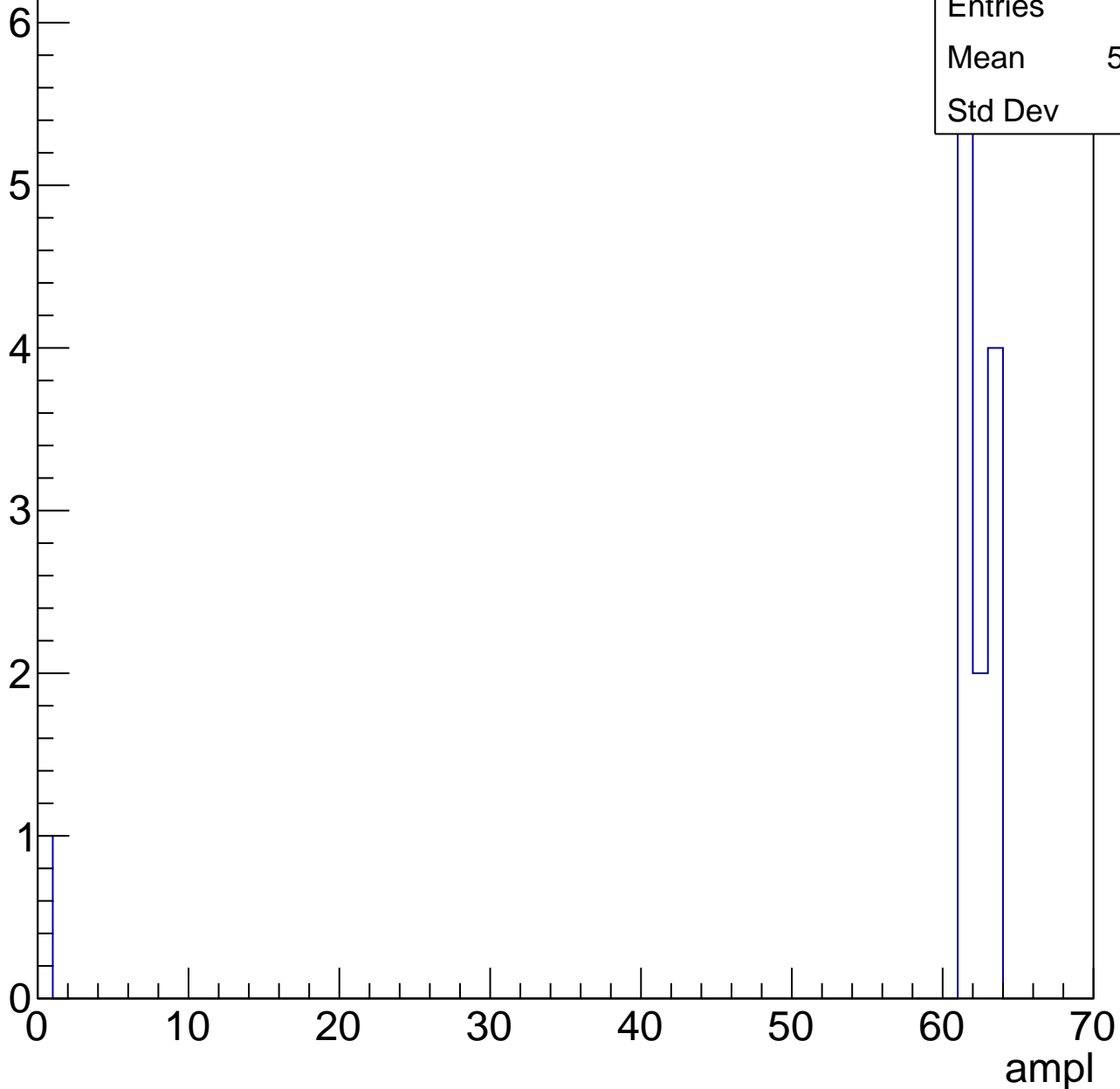


B1L103S, U20-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.5



B1L103S, U20-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch61, adc0

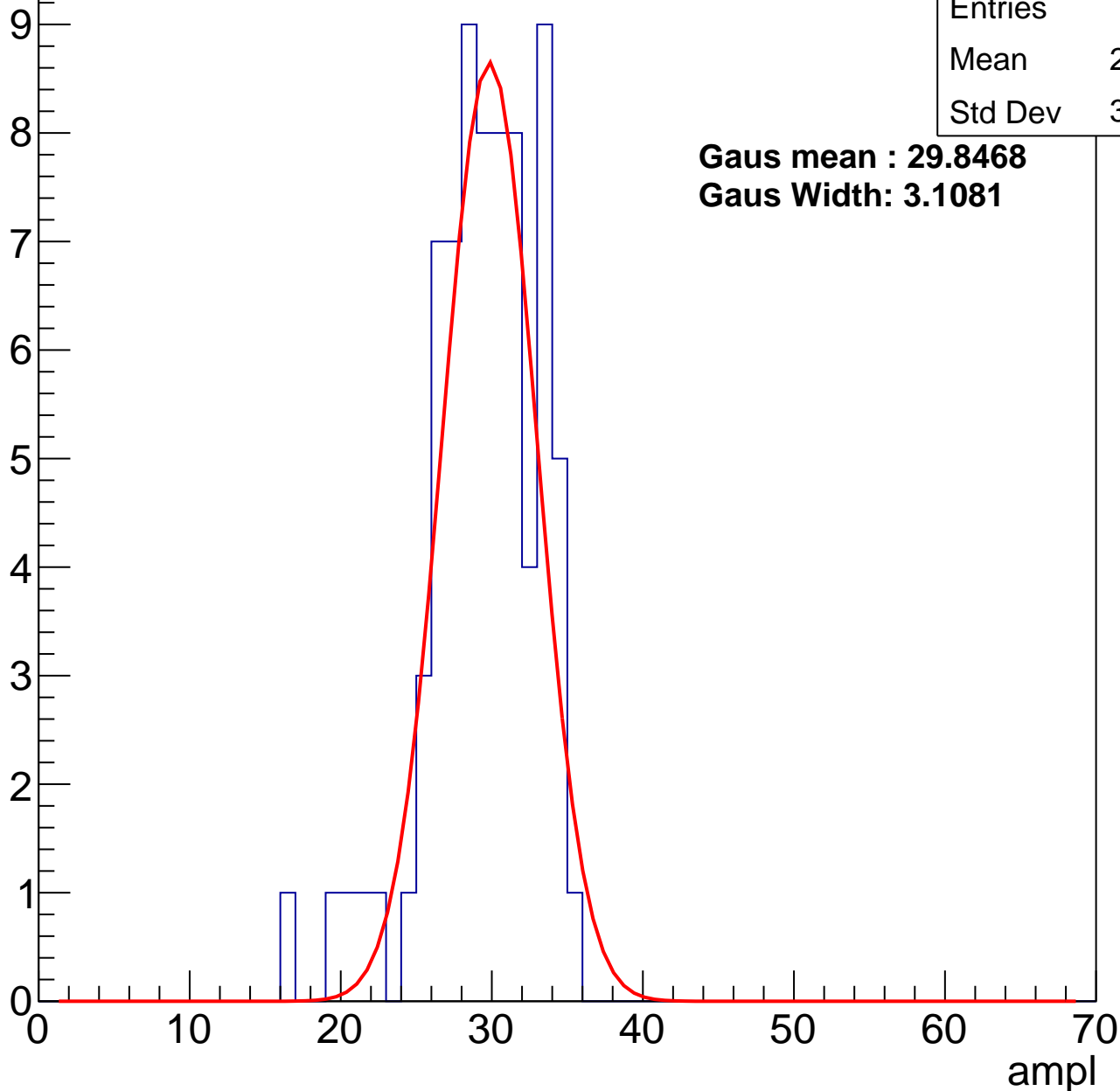
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.93
Std Dev	3.682

Gaus mean : 29.8468

Gaus Width: 3.1081



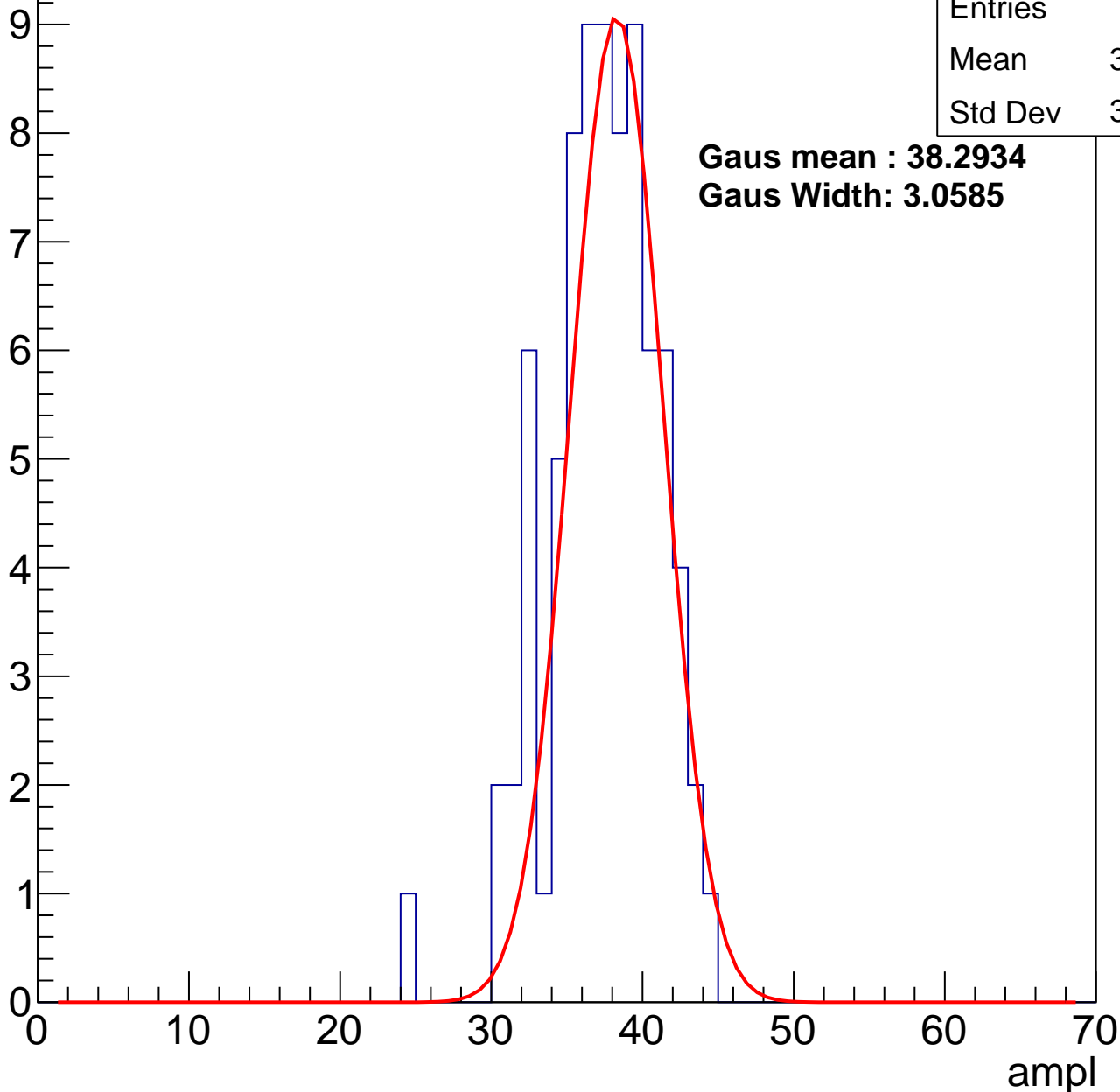
B1L103S, U20-ch61, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.92
Std Dev	3.568

Gaus mean : 38.2934
Gaus Width: 3.0585



B1L103S, U20-ch61, adc2

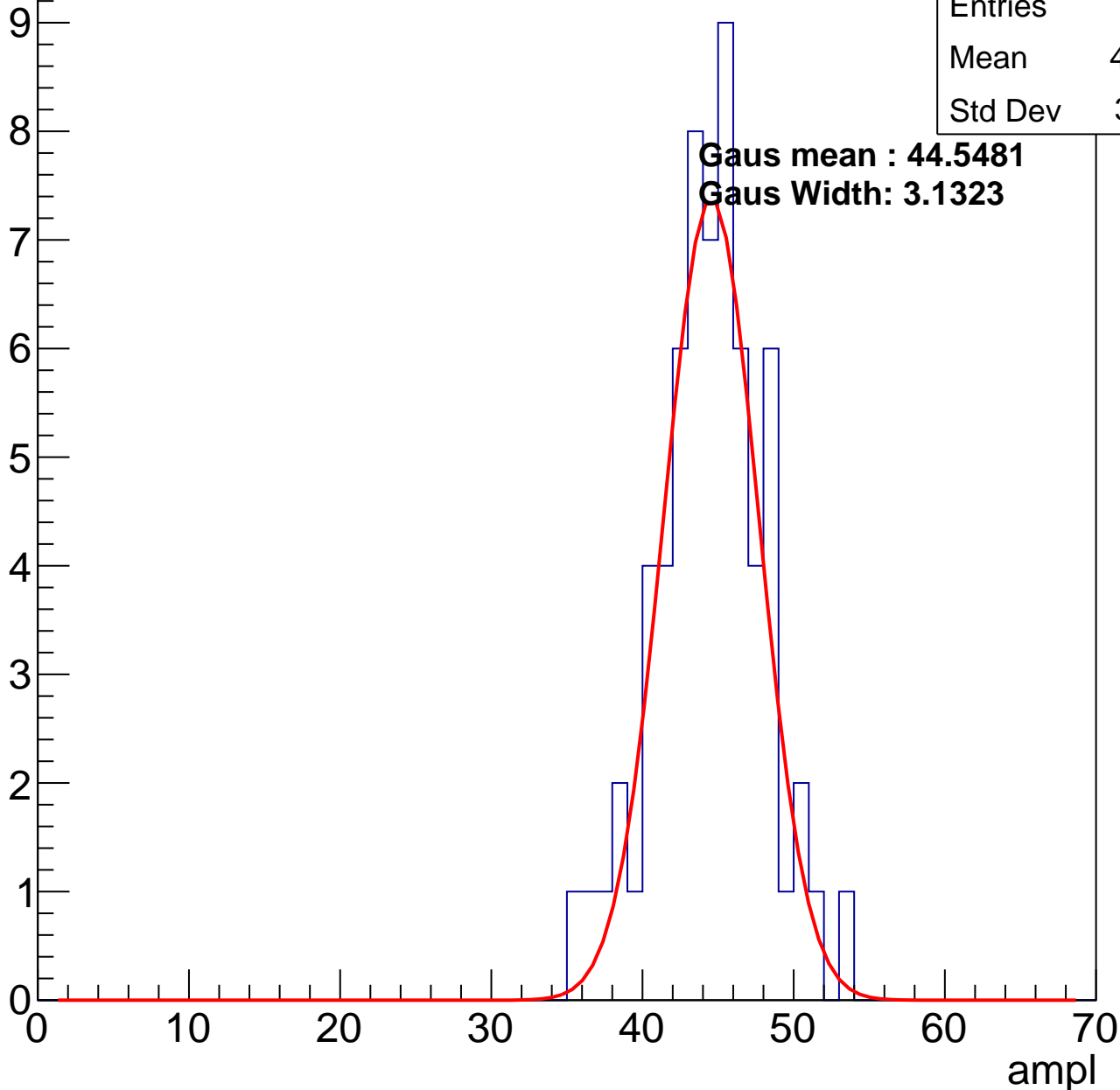
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.02
Std Dev	3.541

Gaus mean : 44.5481

Gaus Width: 3.1323

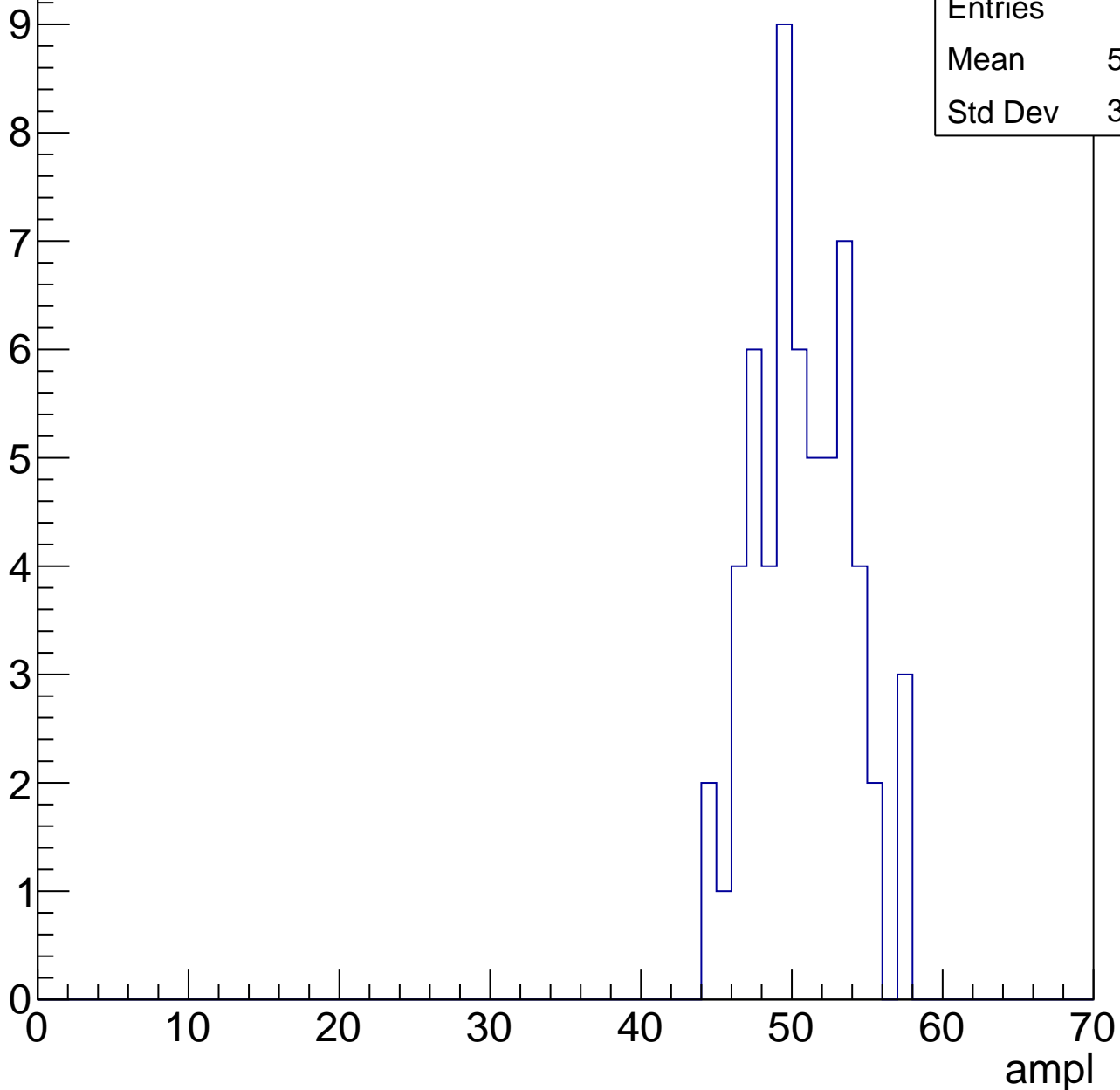


B1L103S, U20-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	50.26
Std Dev	3.176

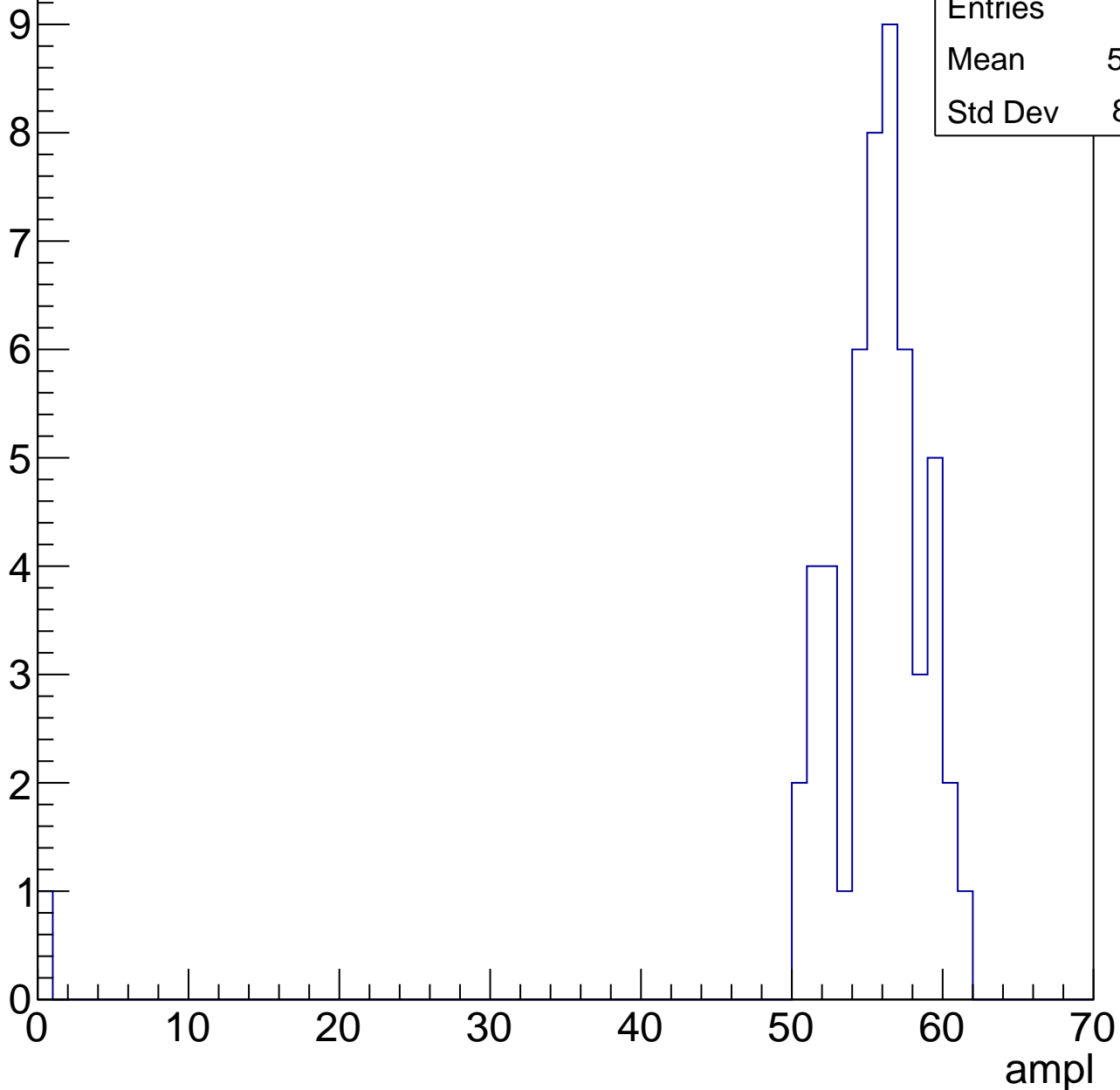


B1L103S, U20-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

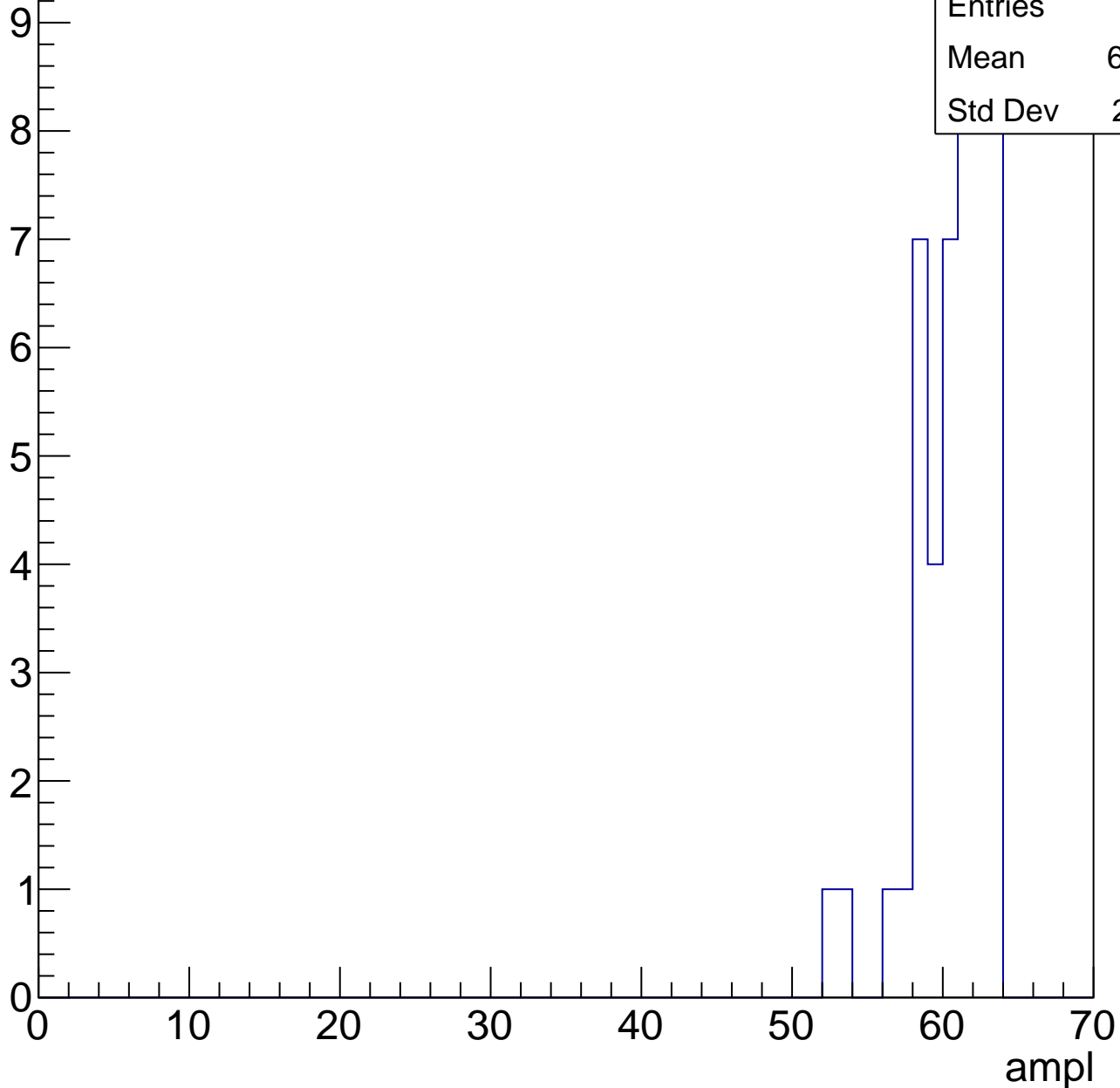
Entries	52
Mean	54.33
Std Dev	8.071



B1L103S, U20-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

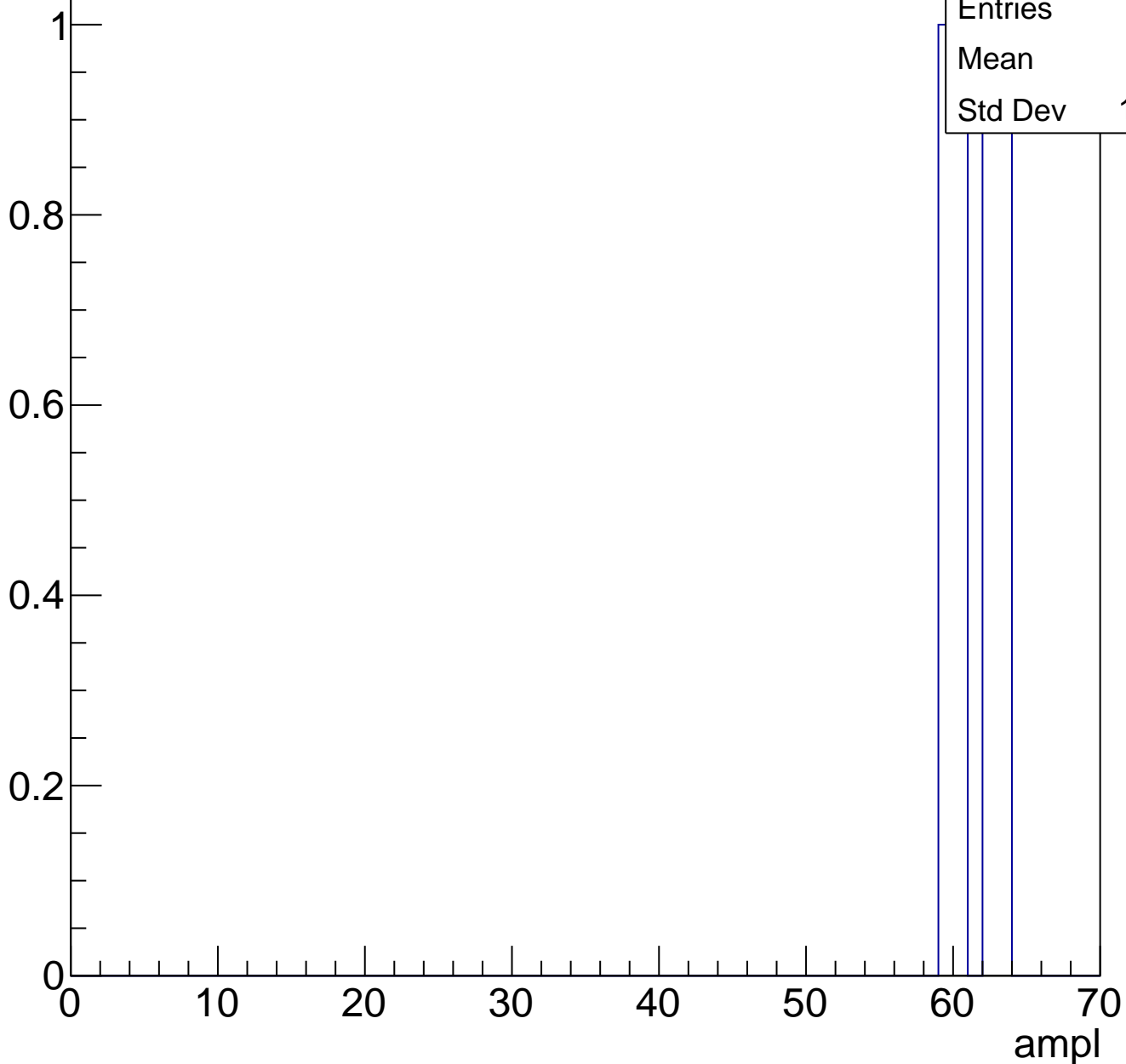
Entry



B1L103S, U20-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch62, adc0

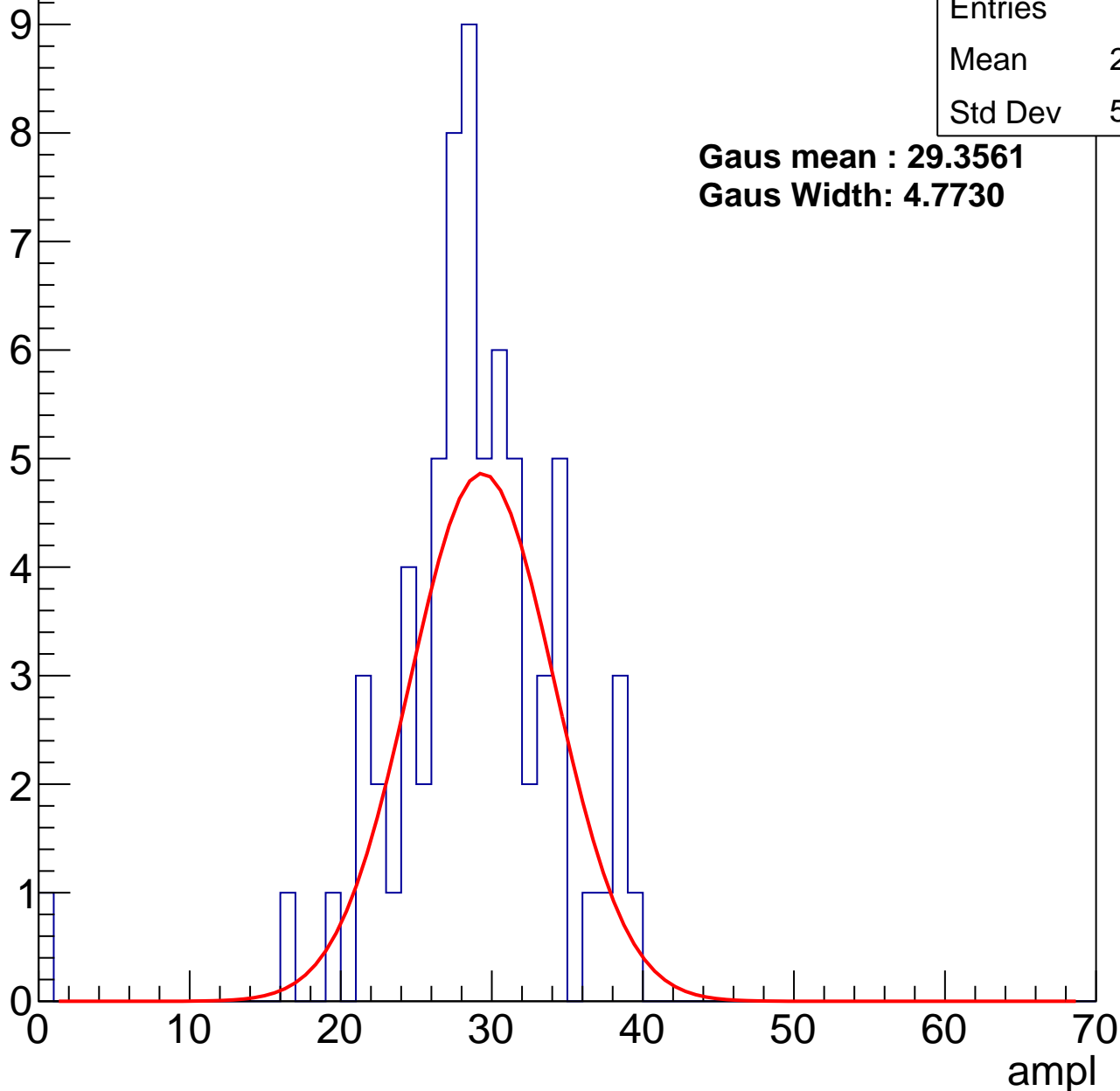
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.23
Std Dev	5.756

Gaus mean : 29.3561

Gaus Width: 4.7730



B1L103S, U20-ch62, adc1

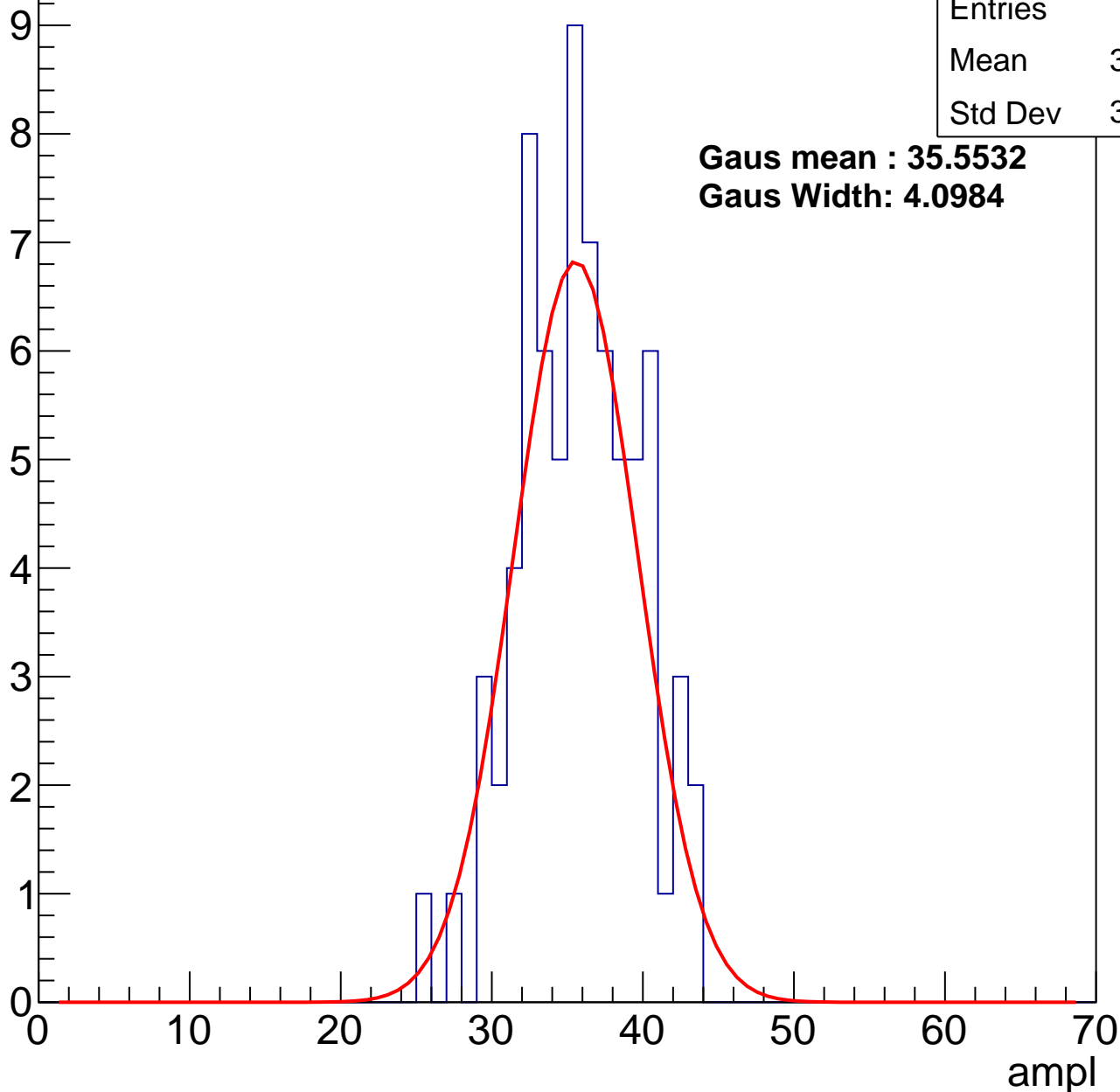
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.32
Std Dev	3.849

Gaus mean : 35.5532

Gaus Width: 4.0984



B1L103S, U20-ch62, adc2

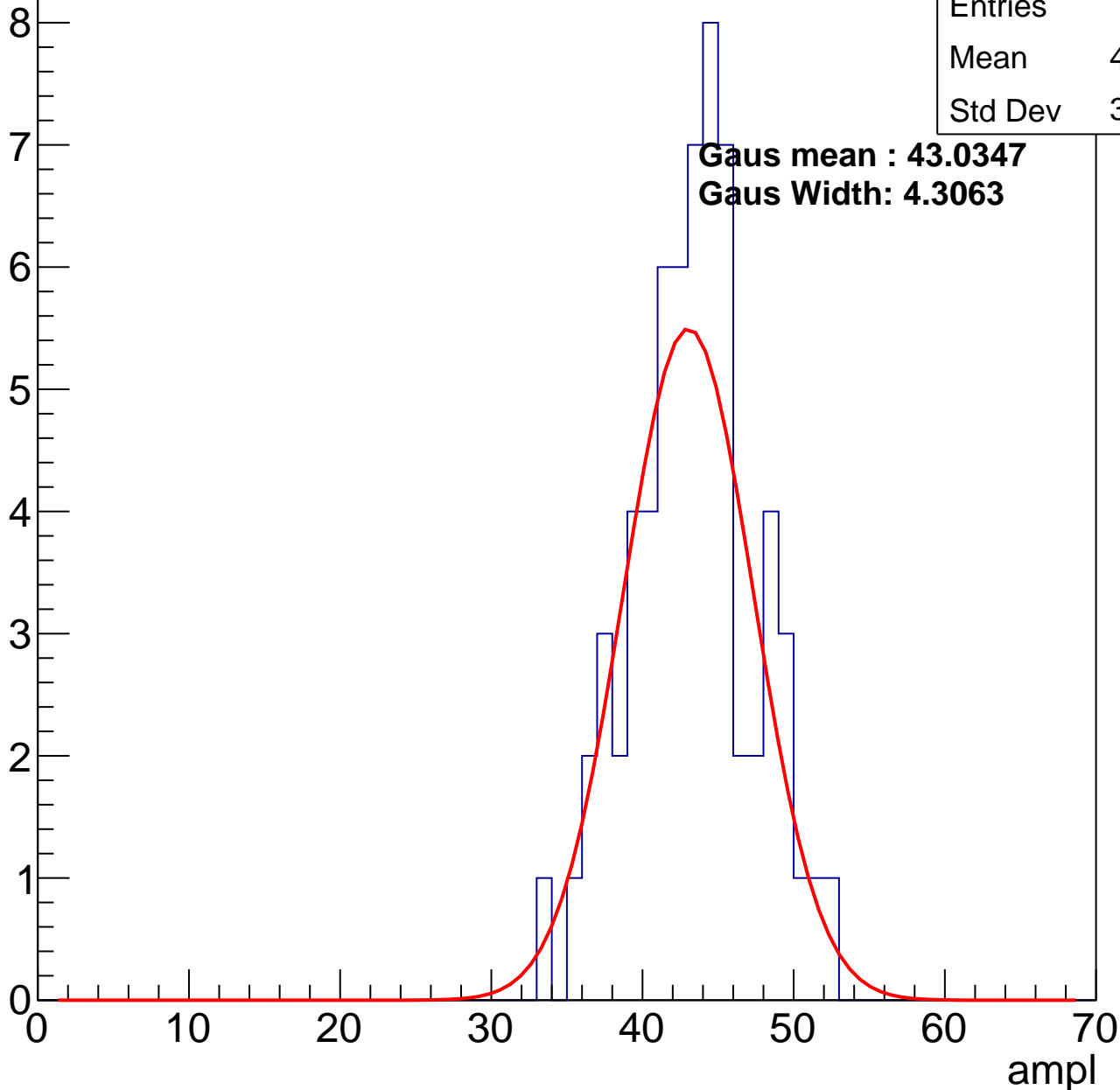
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.88
Std Dev	3.994

Gaus mean : 43.0347

Gaus Width: 4.3063

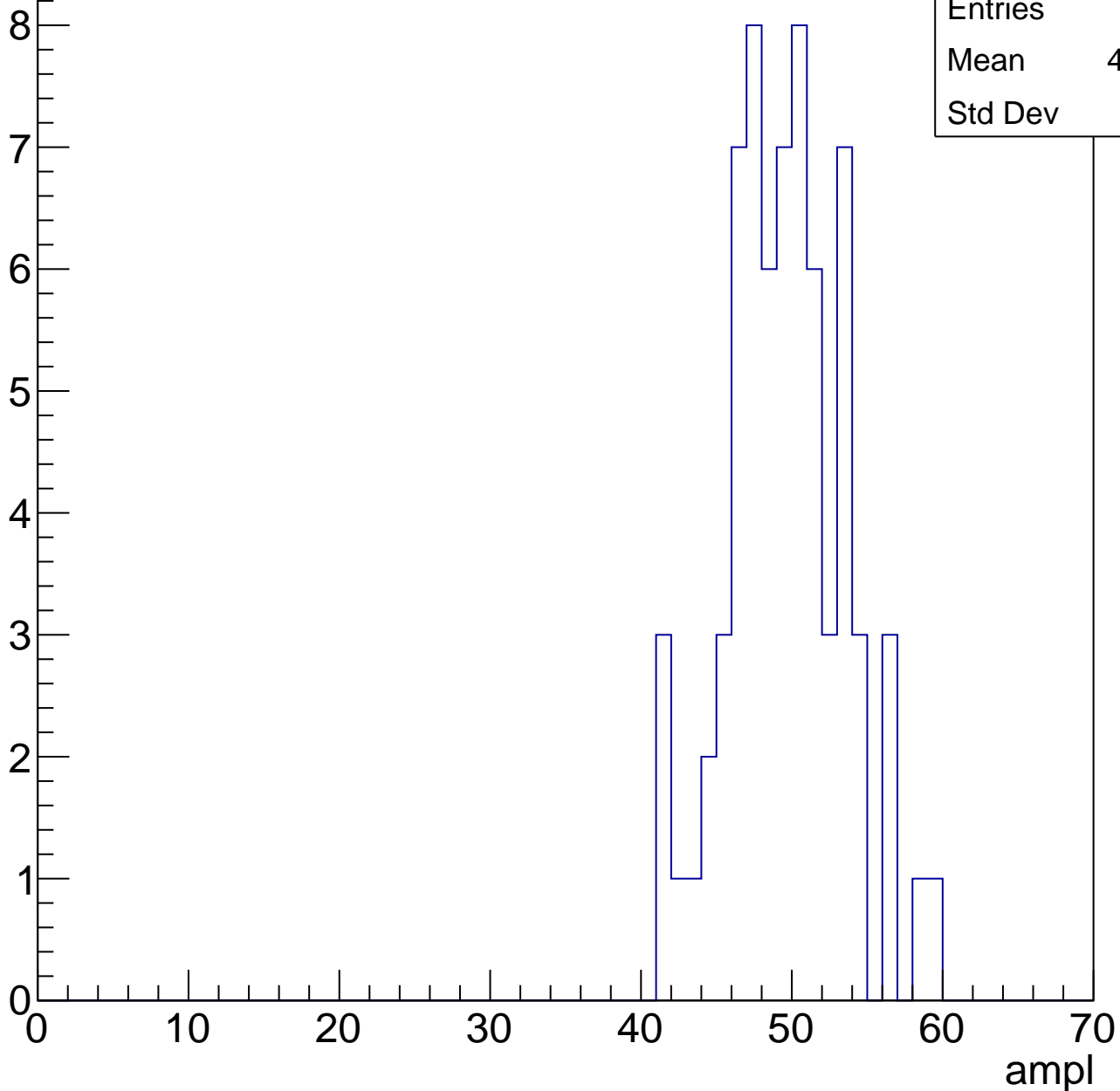


B1L103S, U20-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

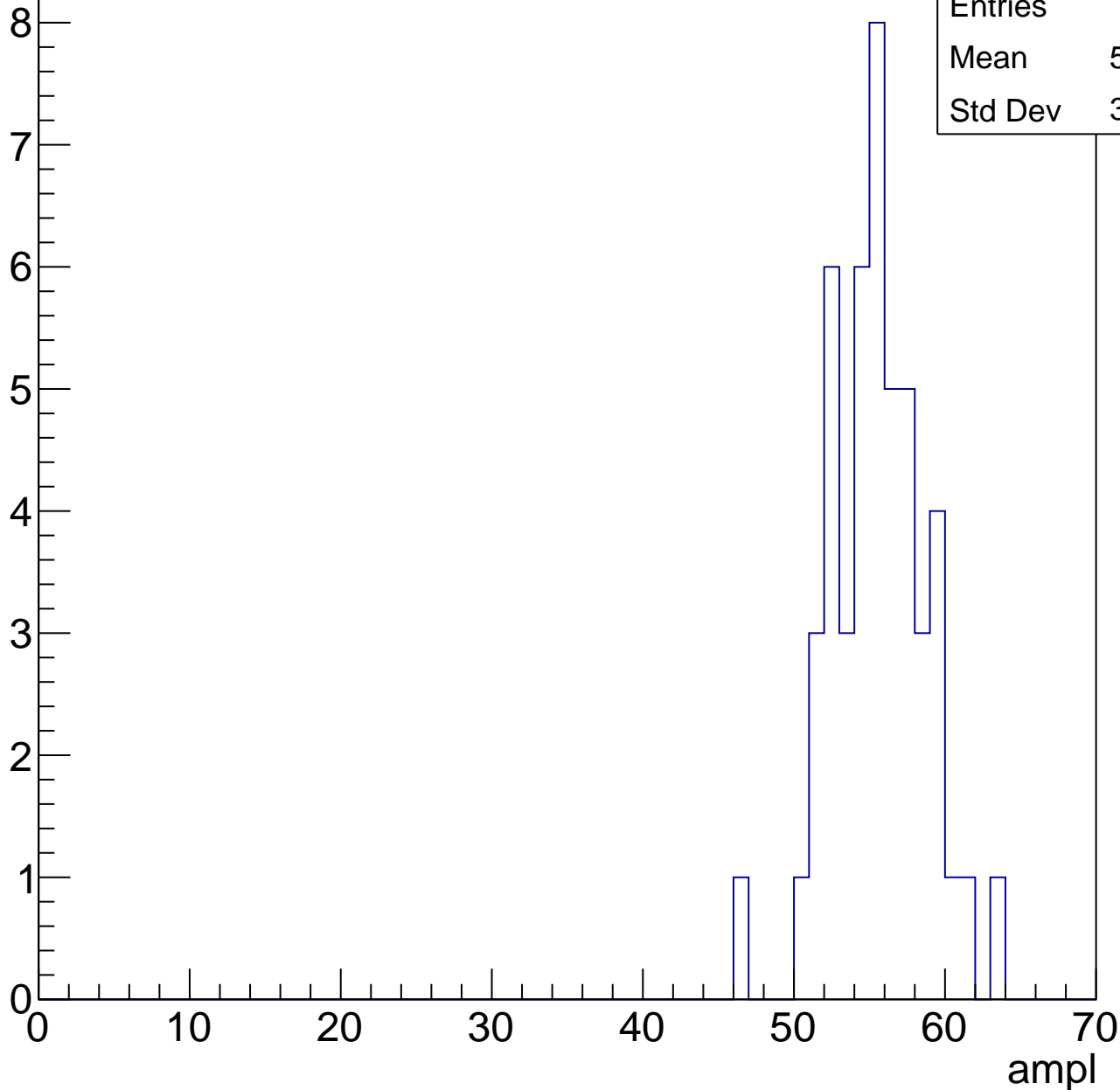
Entries	70
Mean	49.14
Std Dev	3.87



B1L103S, U20-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.79
Std Dev	8.581

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

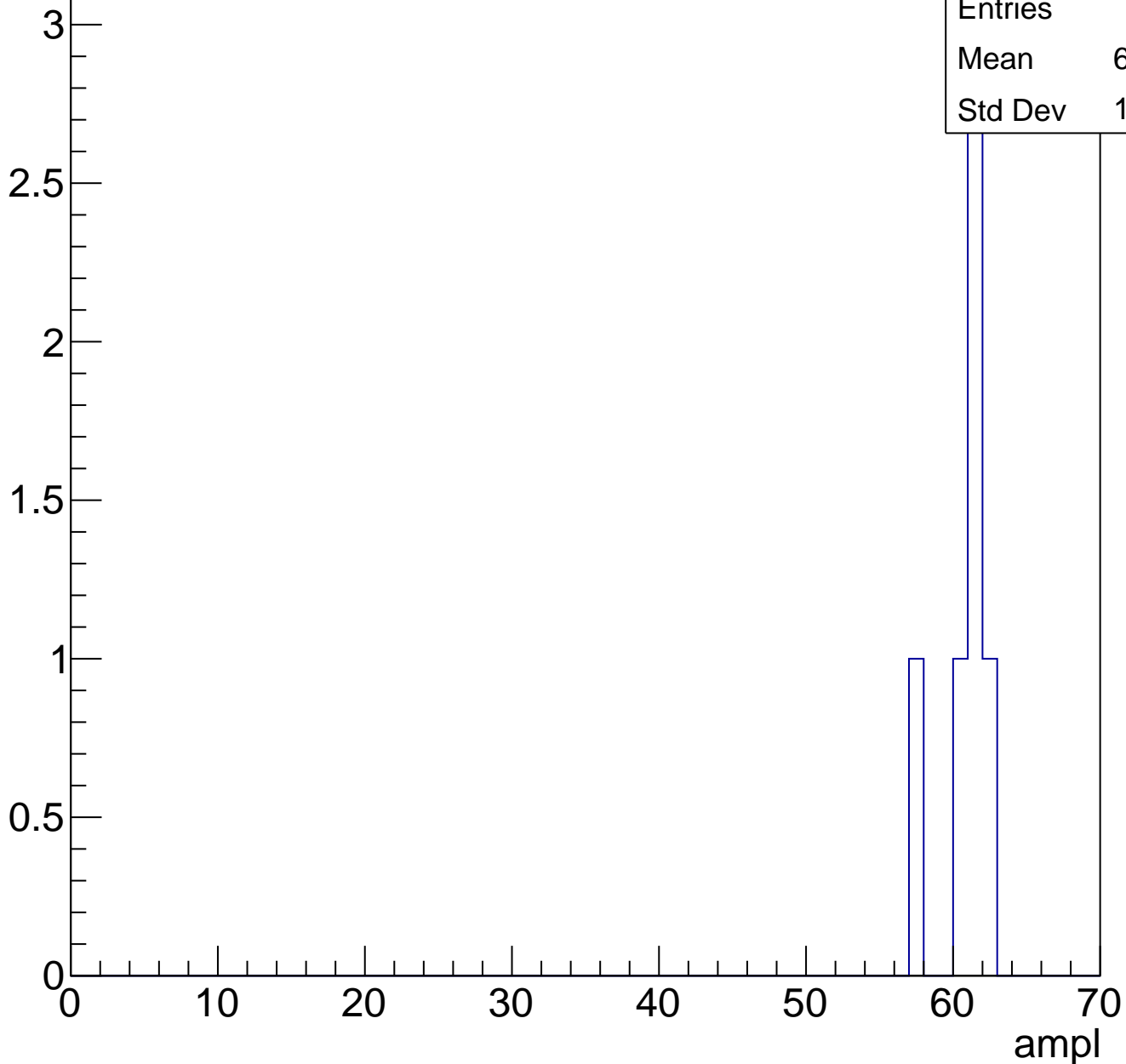
8

9

B1L103S, U20-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch63, adc0

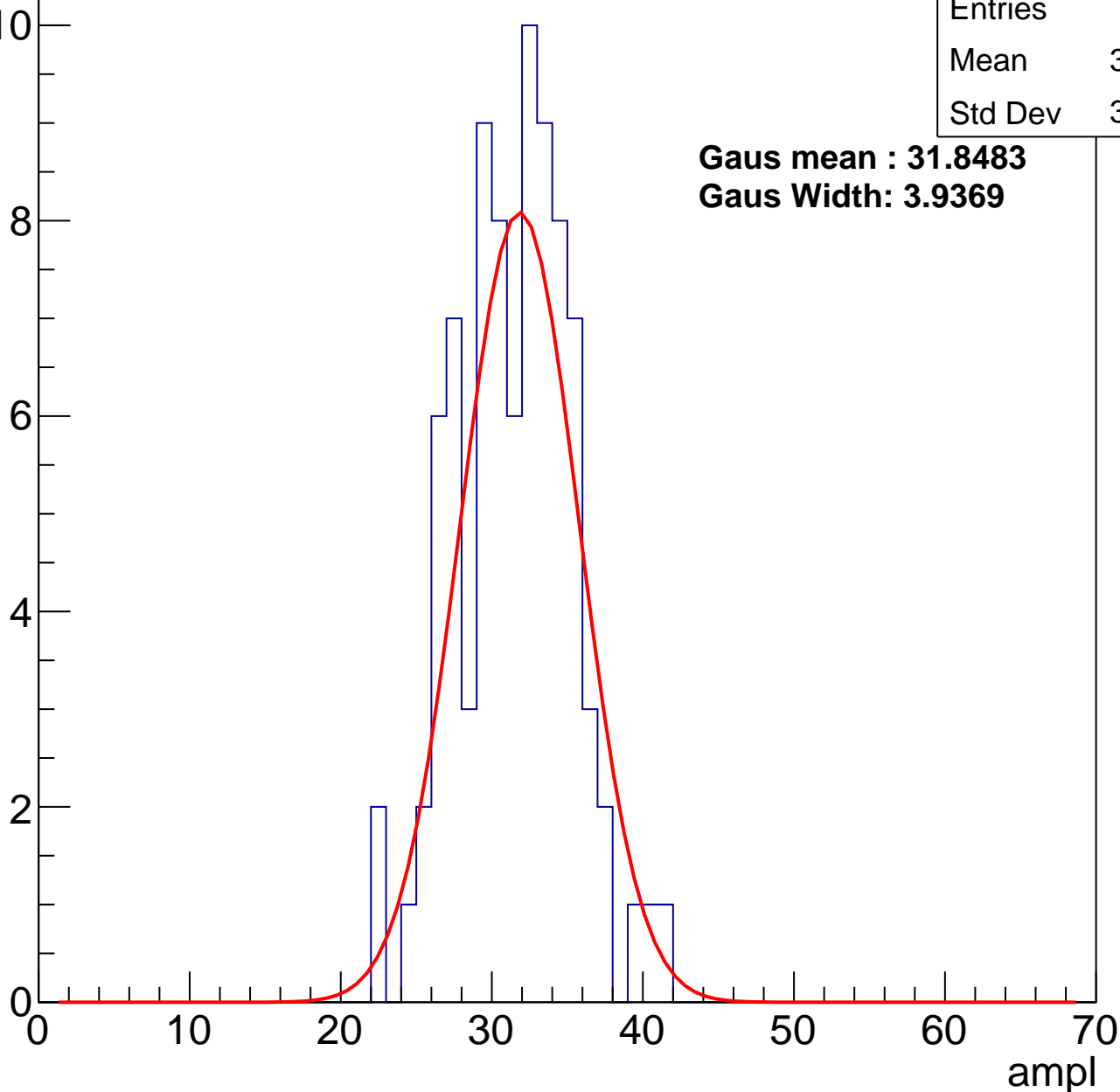
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	31.05
Std Dev	3.794

Gaus mean : 31.8483

Gaus Width: 3.9369



B1L103S, U20-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	38.91
Std Dev	3.311

Gaus mean : 39.3921

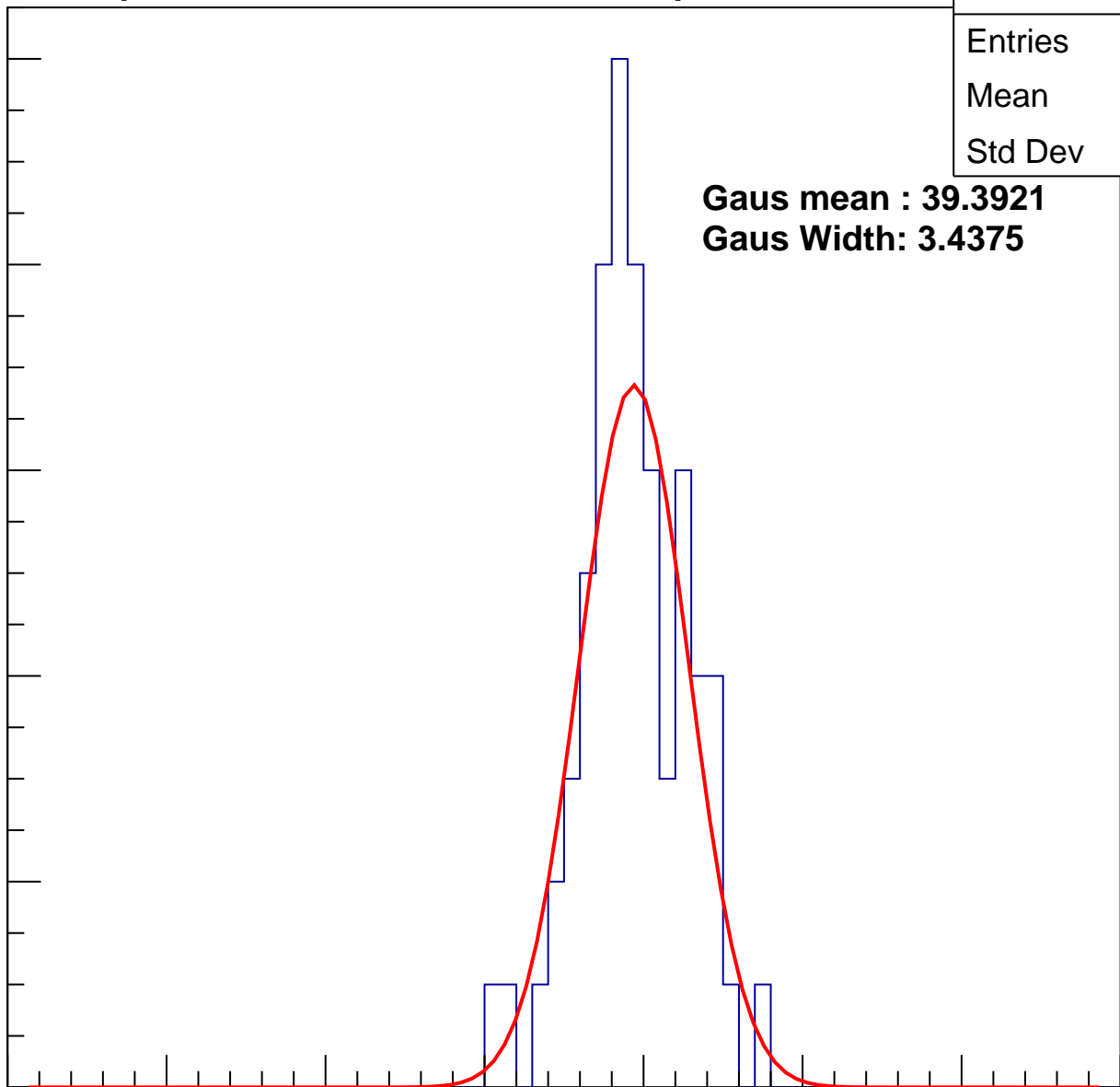
Gaus Width: 3.4375

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch63, adc2

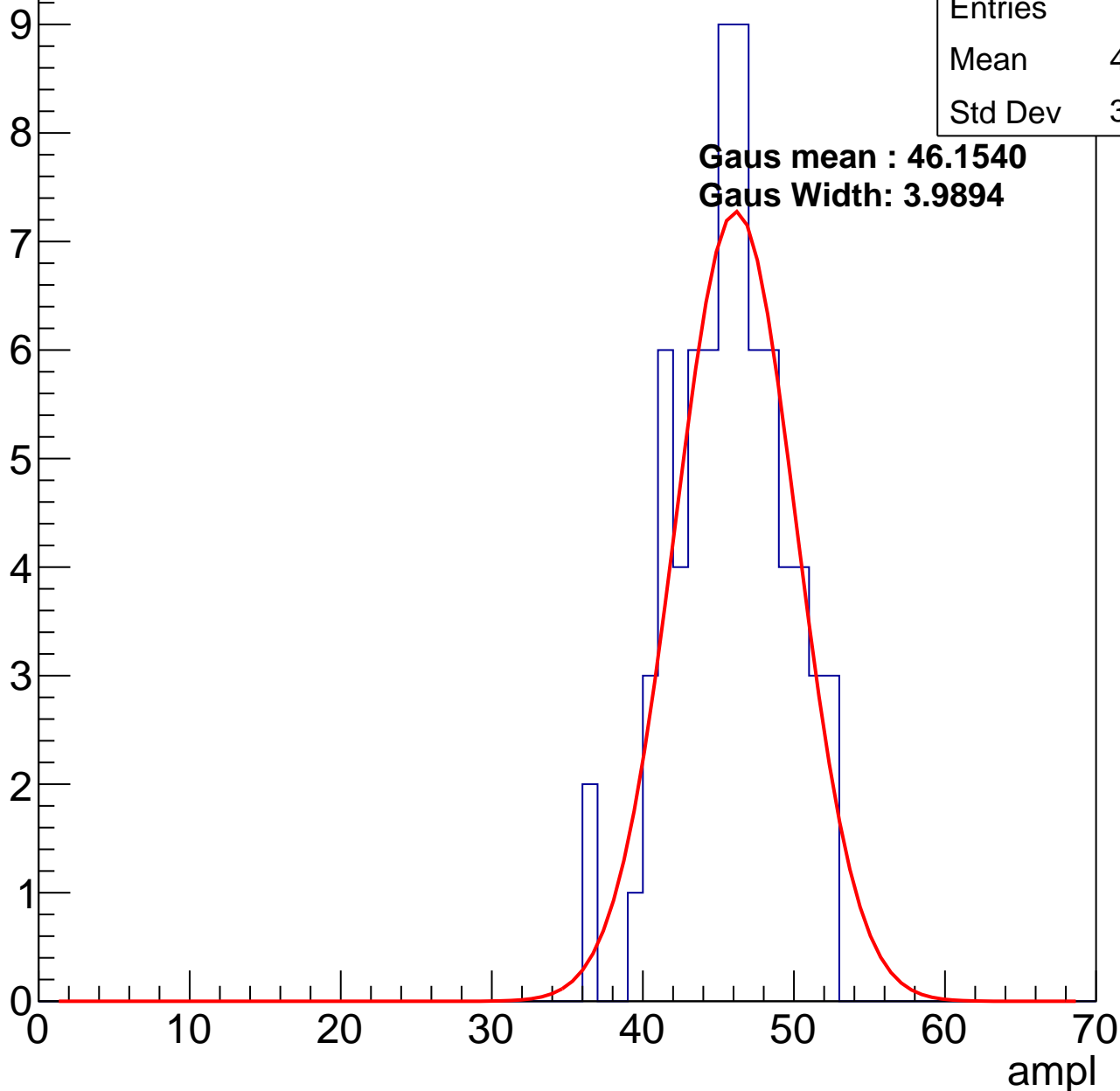
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	45.29
Std Dev	3.603

Gaus mean : 46.1540

Gaus Width: 3.9894

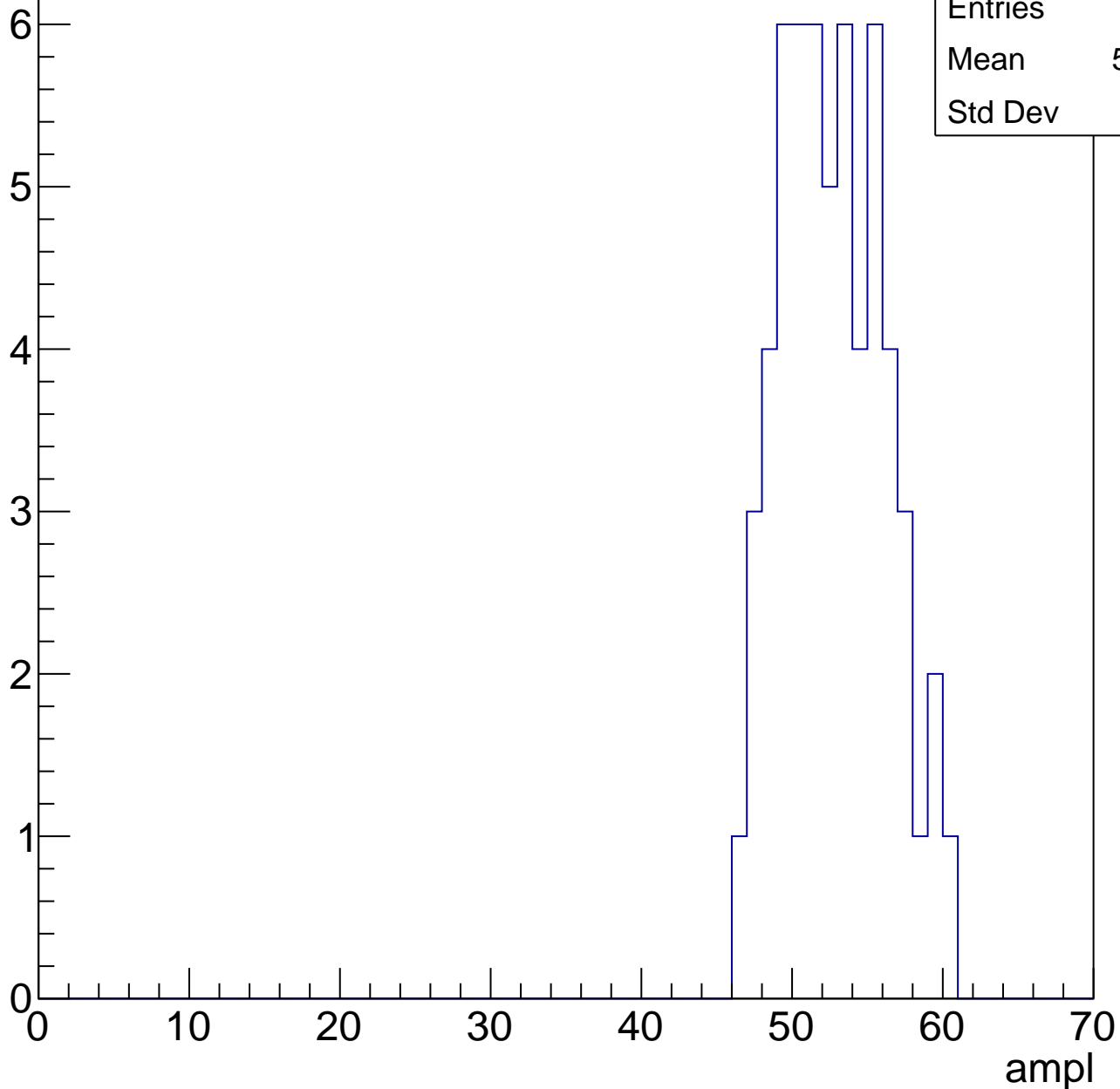


B1L103S, U20-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	52.31
Std Dev	3.39

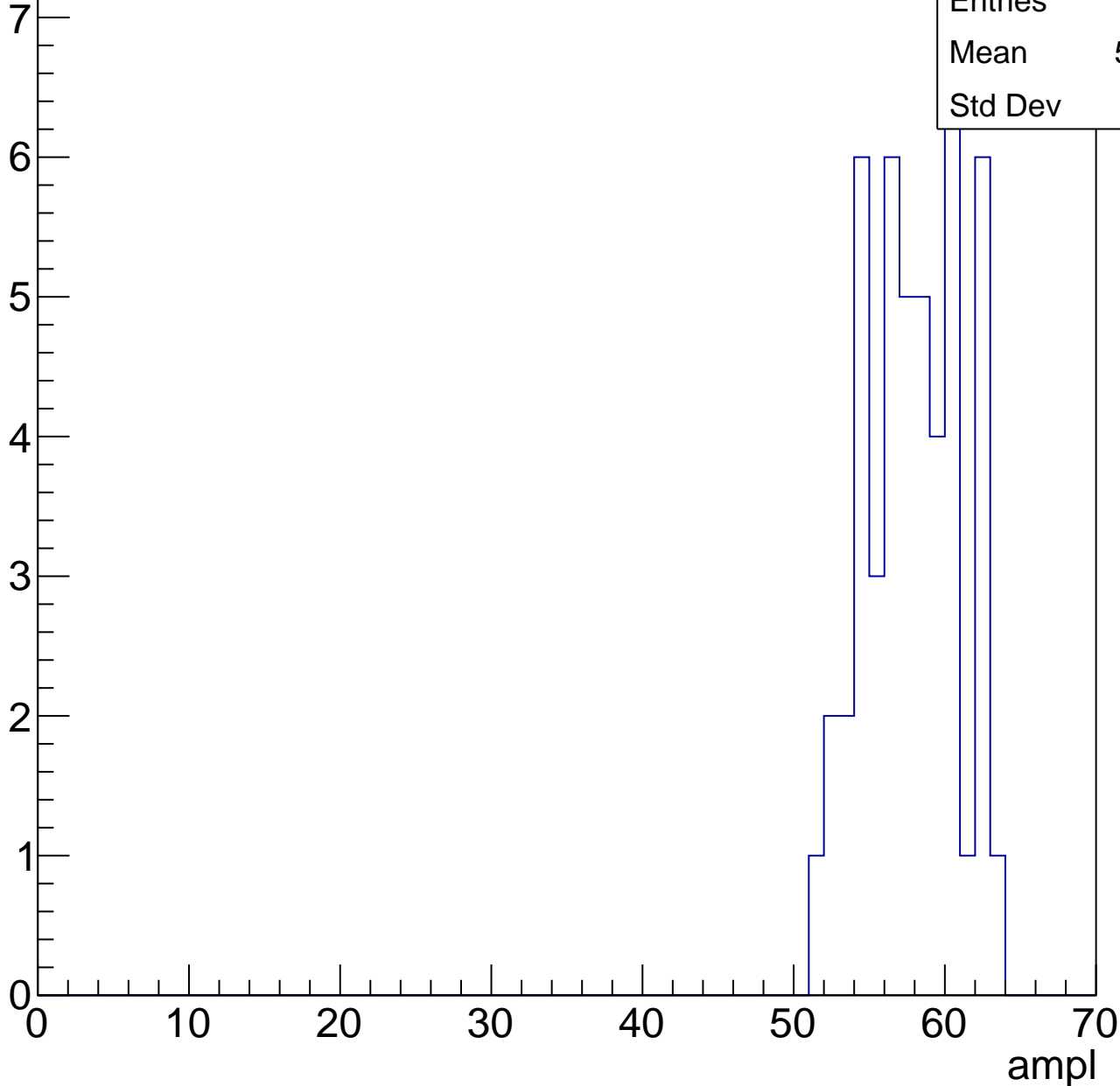


B1L103S, U20-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.41
Std Dev	3.09

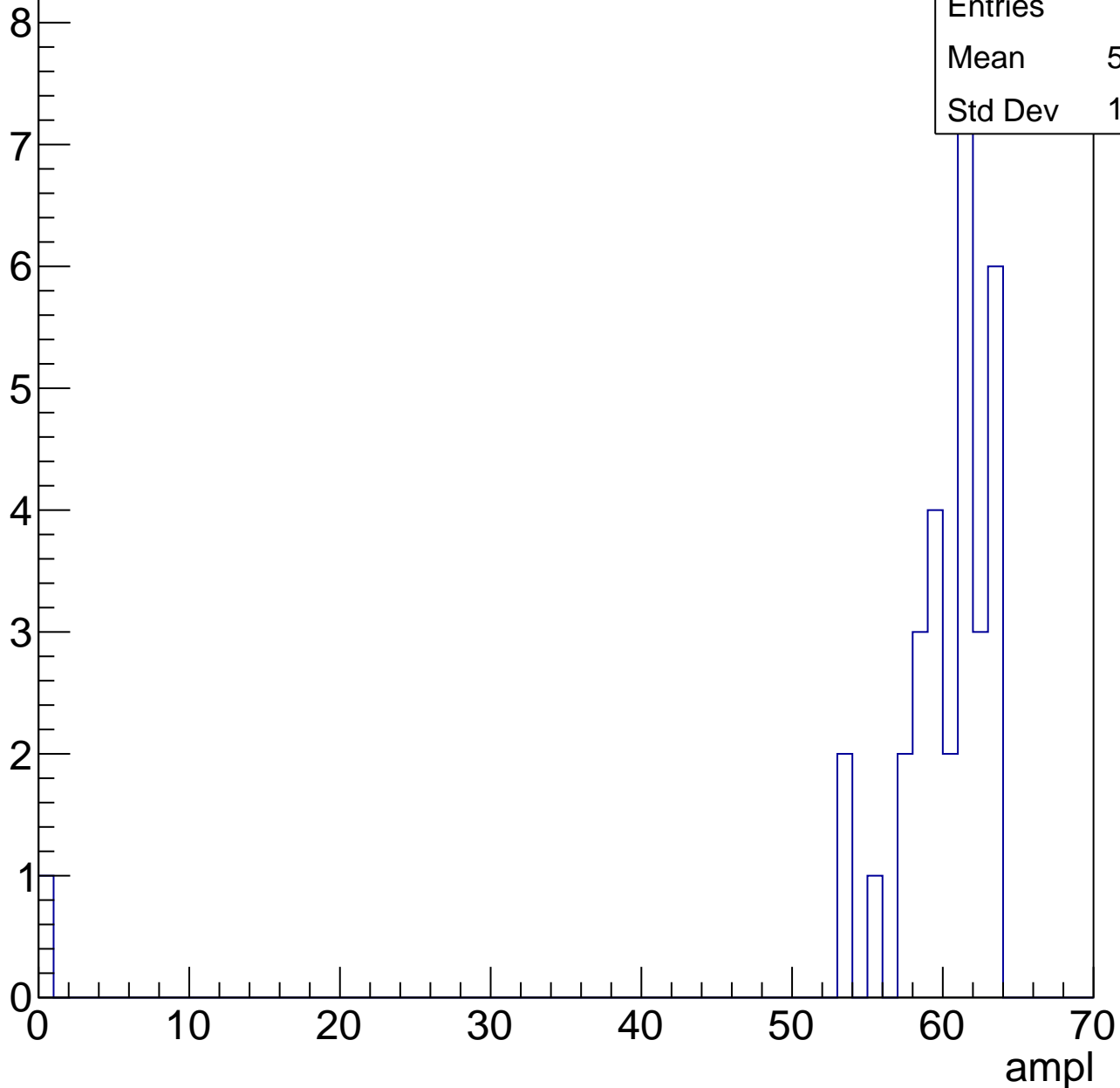


B1L103S, U20-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

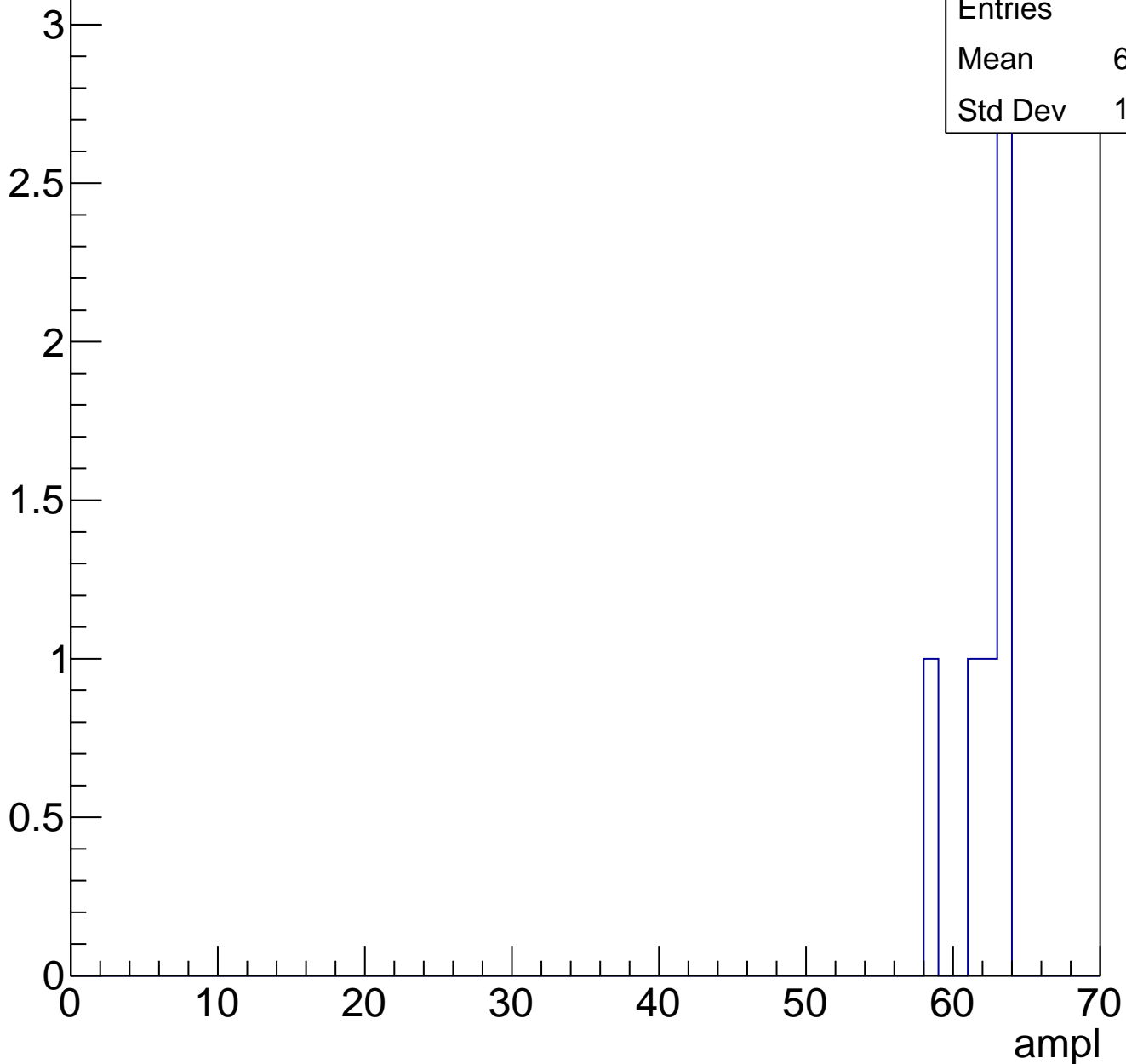
Entries	32
Mean	58.03
Std Dev	10.76



B1L103S, U20-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch64, adc0

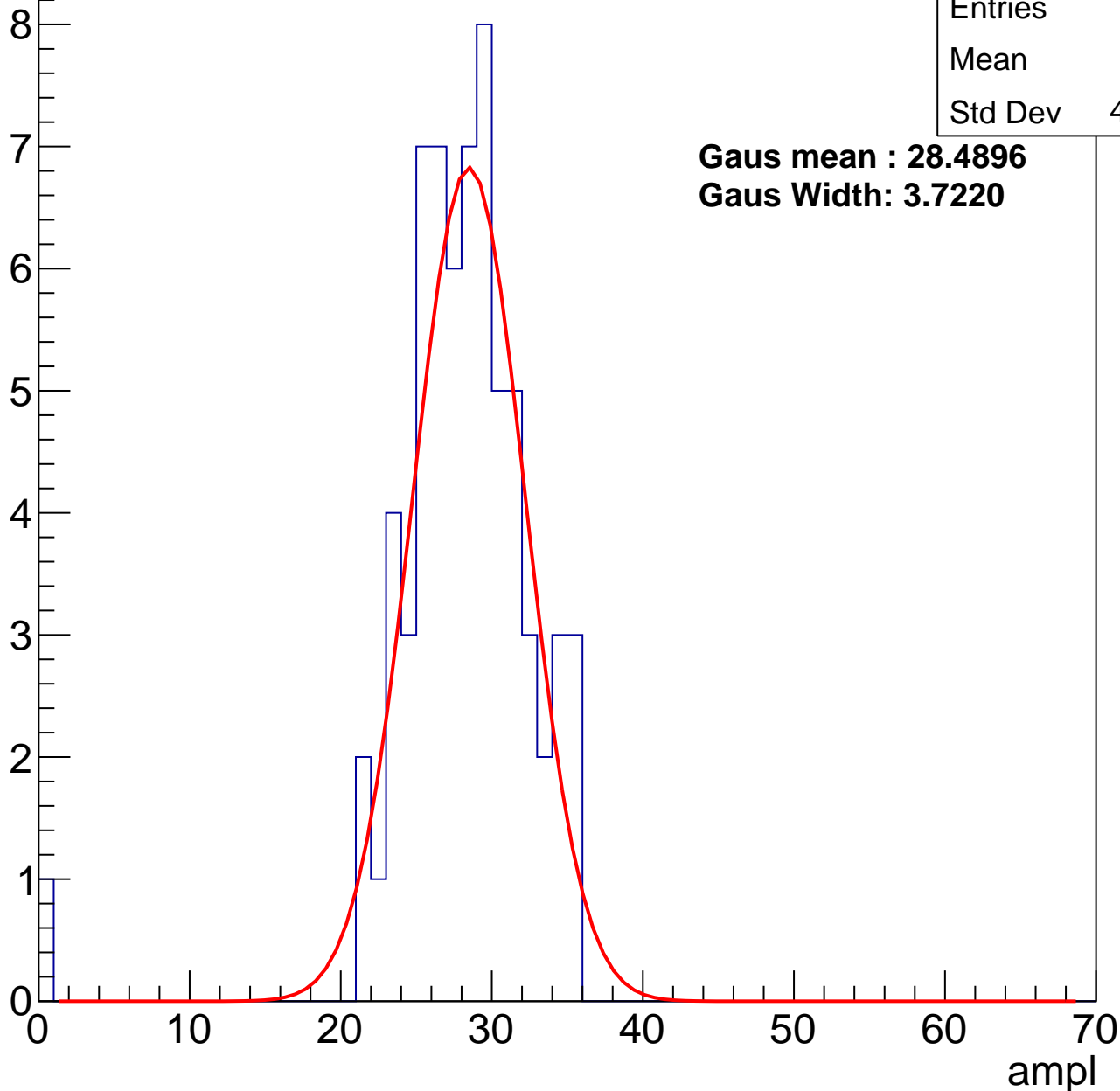
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.6
Std Dev	4.853

Gaus mean : 28.4896

Gaus Width: 3.7220



B1L103S, U20-ch64, adc1

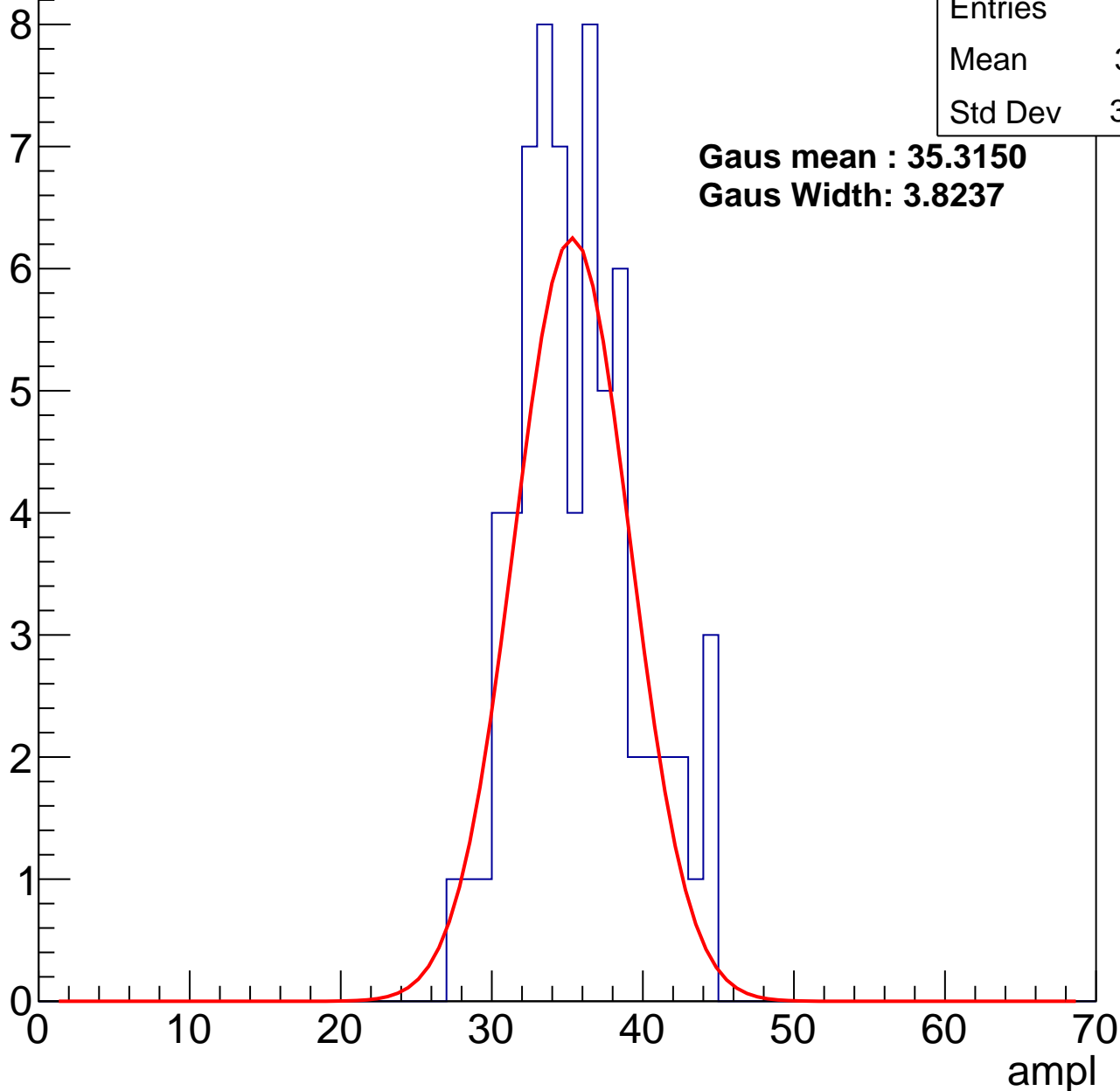
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.21
Std Dev	3.935

Gaus mean : 35.3150

Gaus Width: 3.8237



B1L103S, U20-ch64, adc2

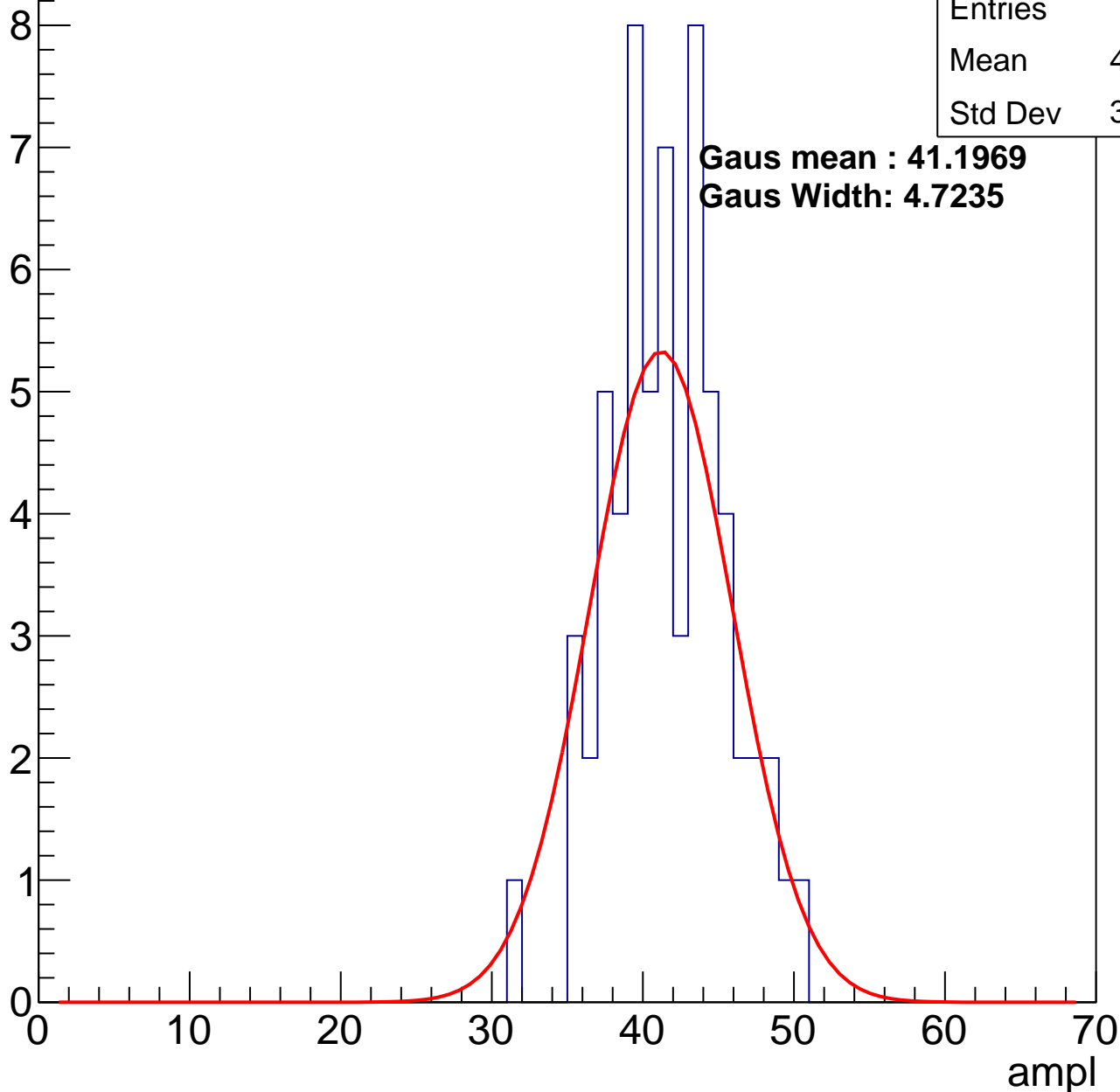
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.19
Std Dev	3.817

Gaus mean : 41.1969

Gaus Width: 4.7235

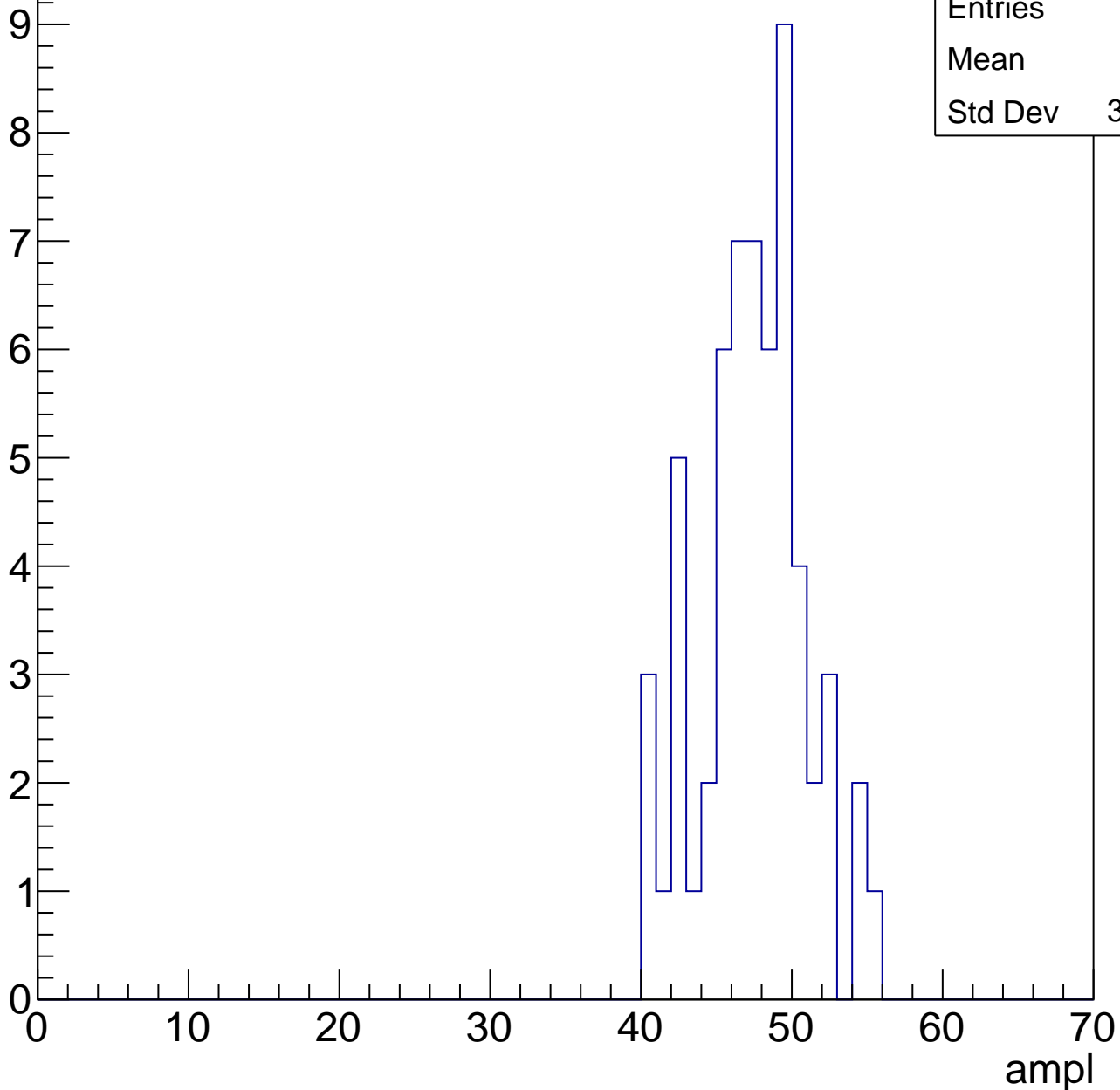


B1L103S, U20-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

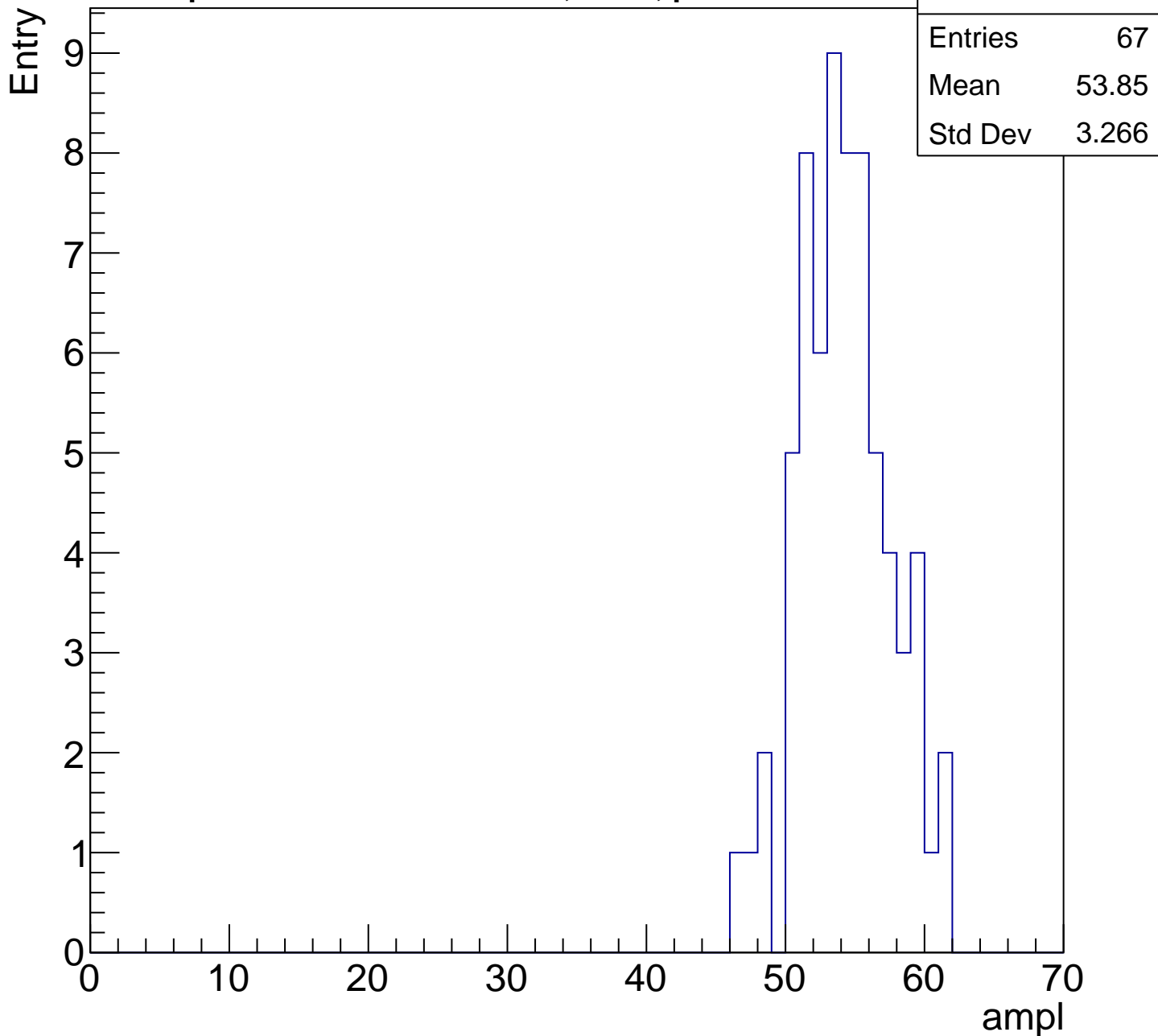
Entry

Entries	59
Mean	47
Std Dev	3.493



B1L103S, U20-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

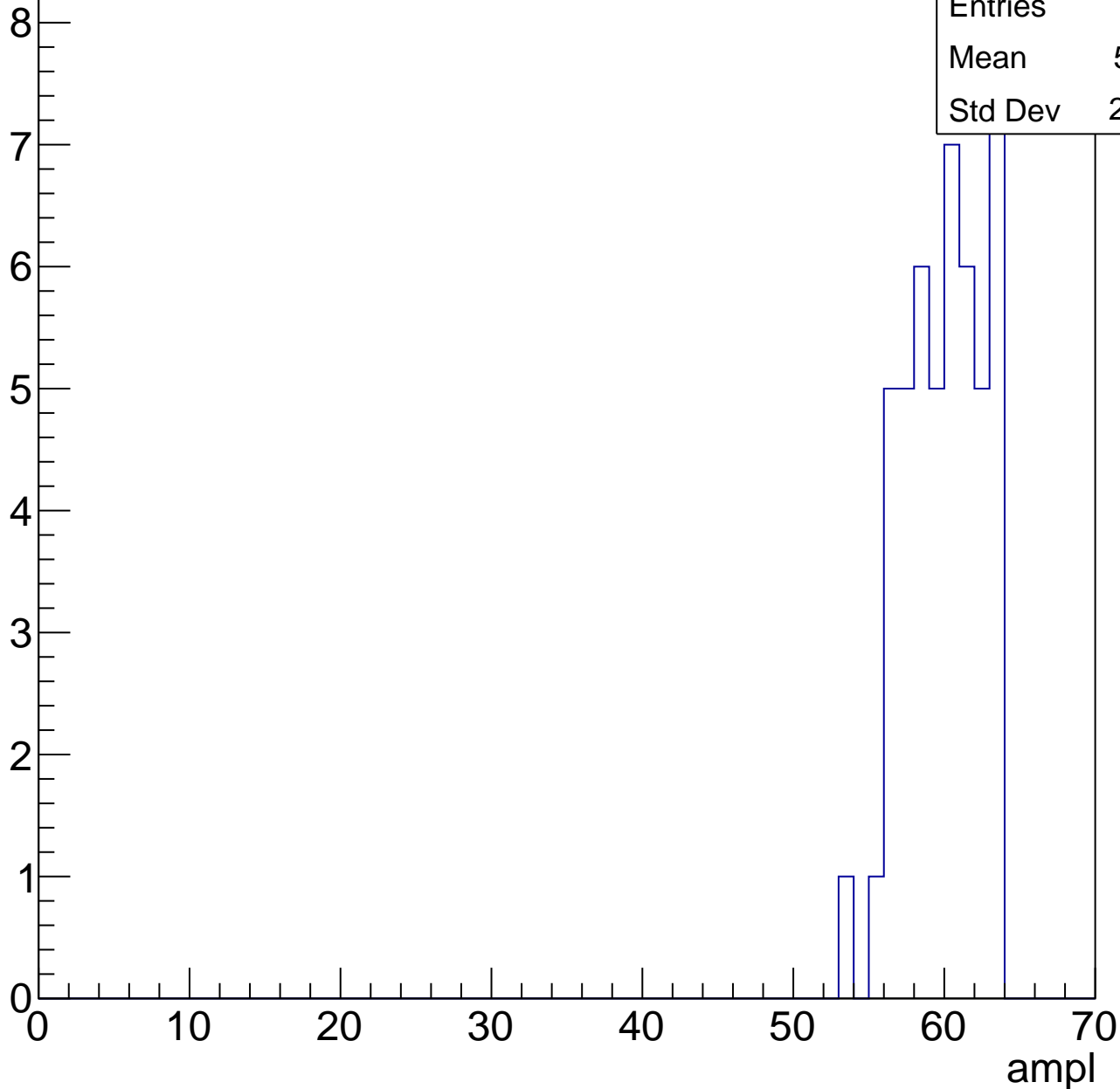


B1L103S, U20-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.51
Std Dev	2.532

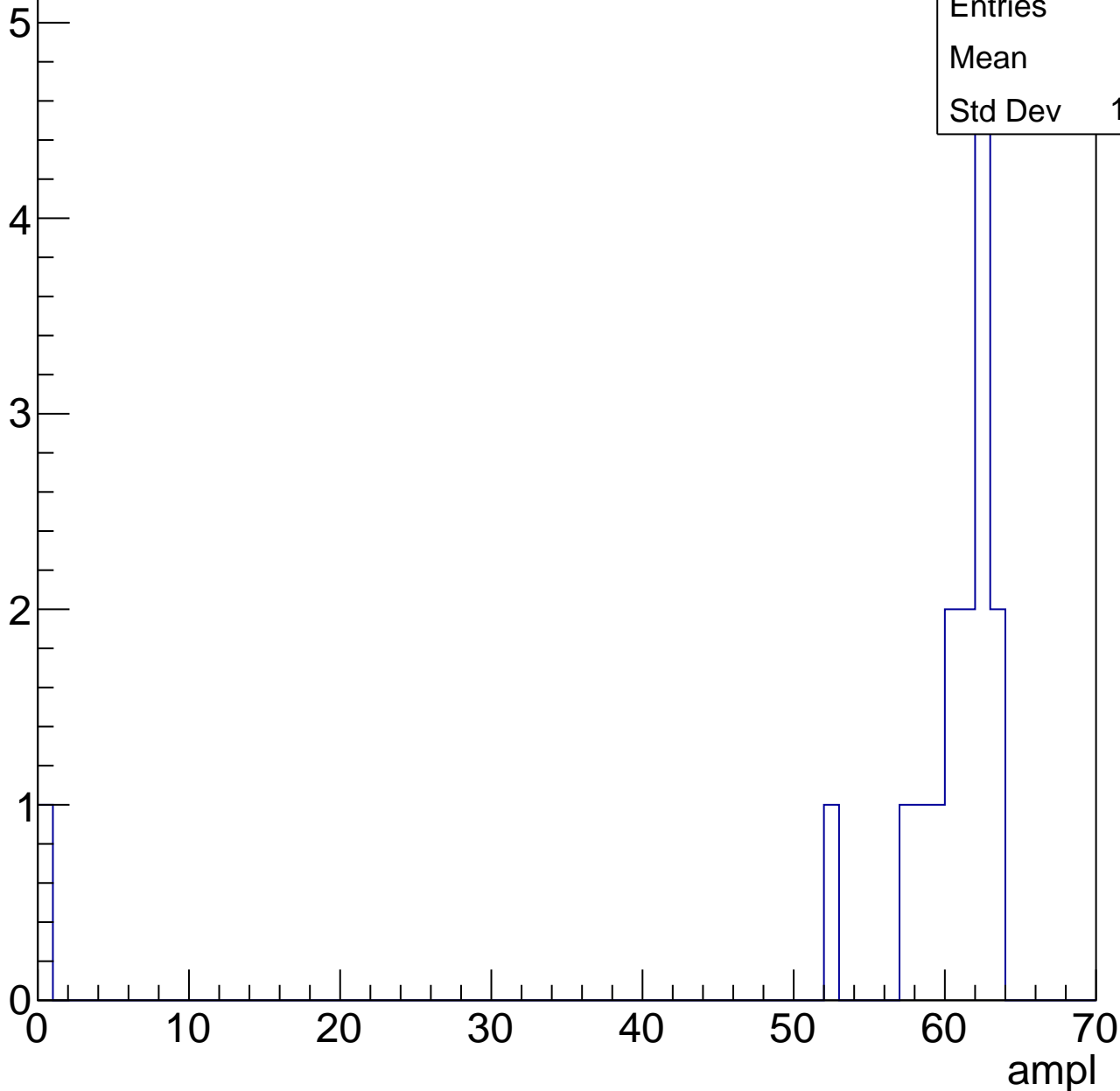


B1L103S, U20-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

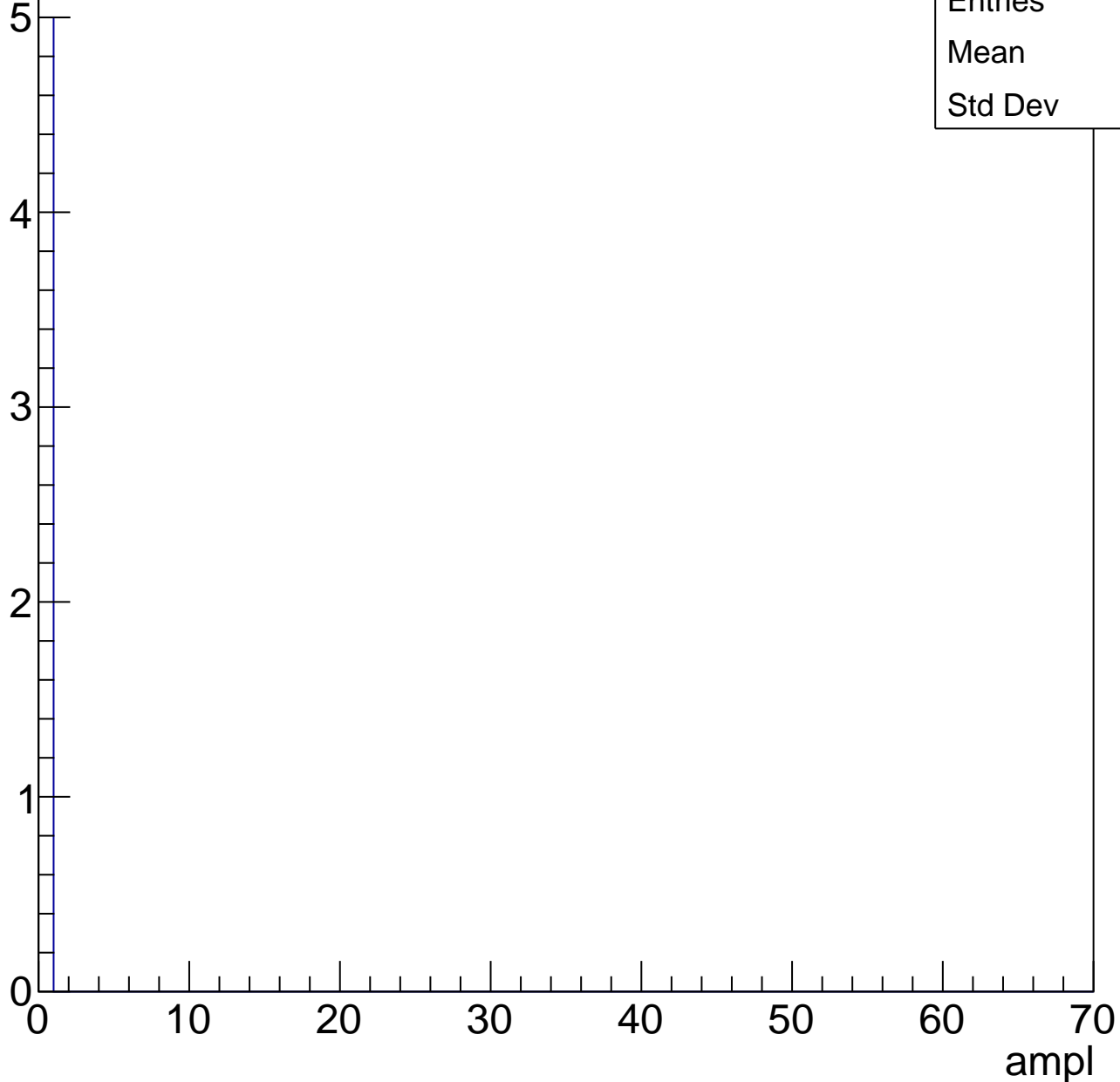
Entries	16
Mean	56.5
Std Dev	14.84



B1L103S, U20-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	0
Std Dev	0

B1L103S, U20-ch65, adc0

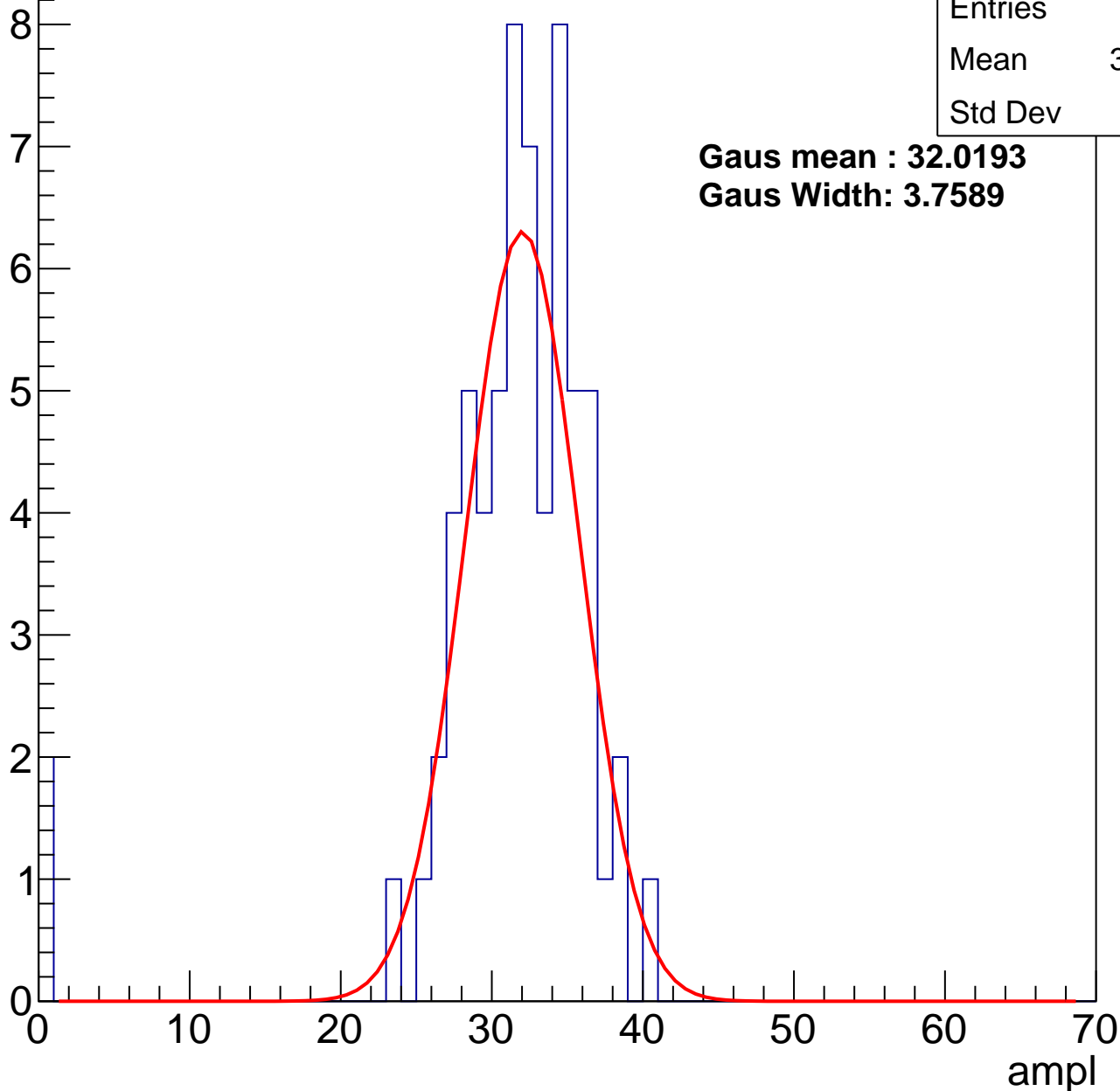
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.74
Std Dev	6.46

Gaus mean : 32.0193

Gaus Width: 3.7589



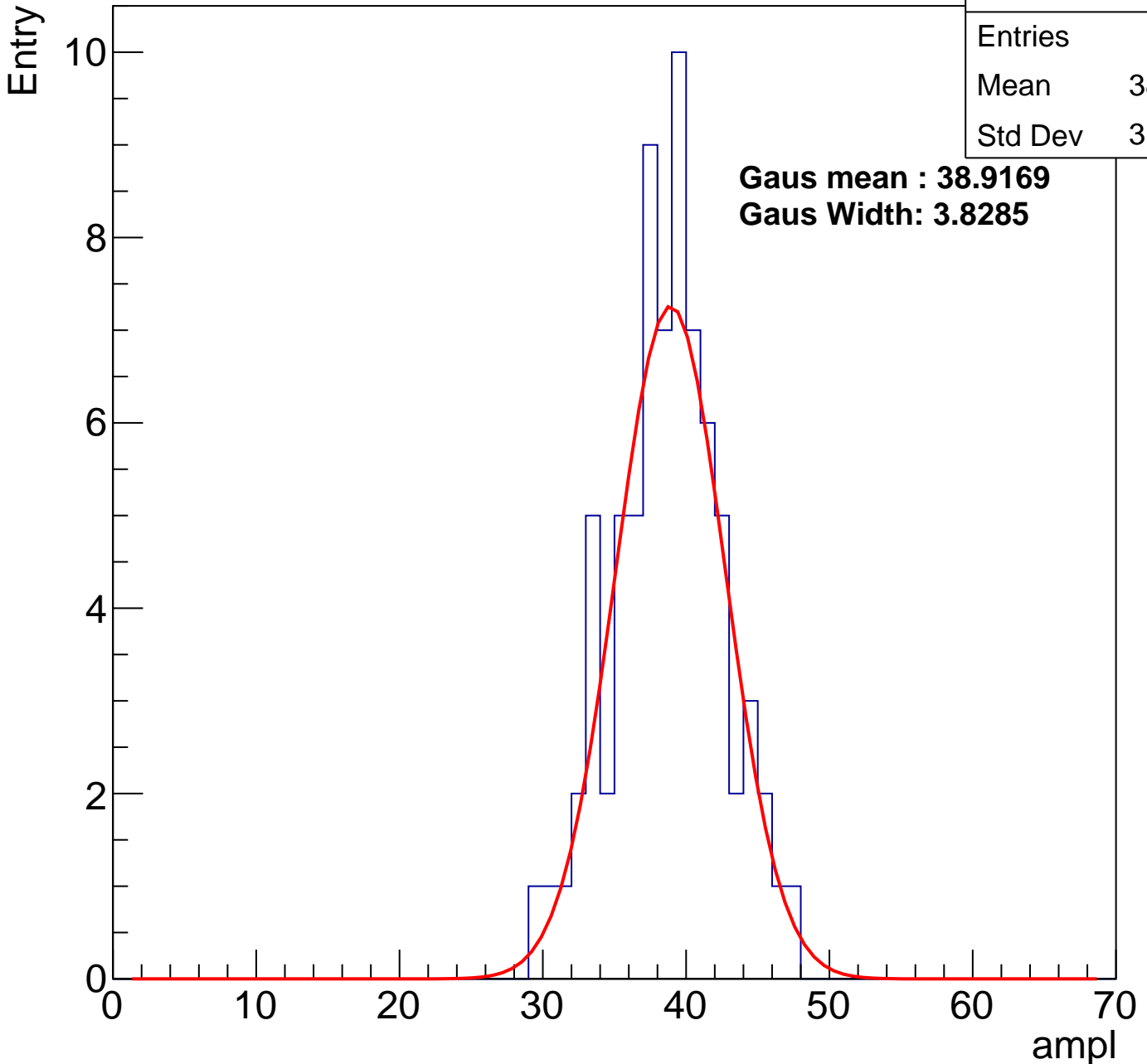
B1L103S, U20-ch65, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	38.24
Std Dev	3.769

Gaus mean : 38.9169

Gaus Width: 3.8285



B1L103S, U20-ch65, adc2

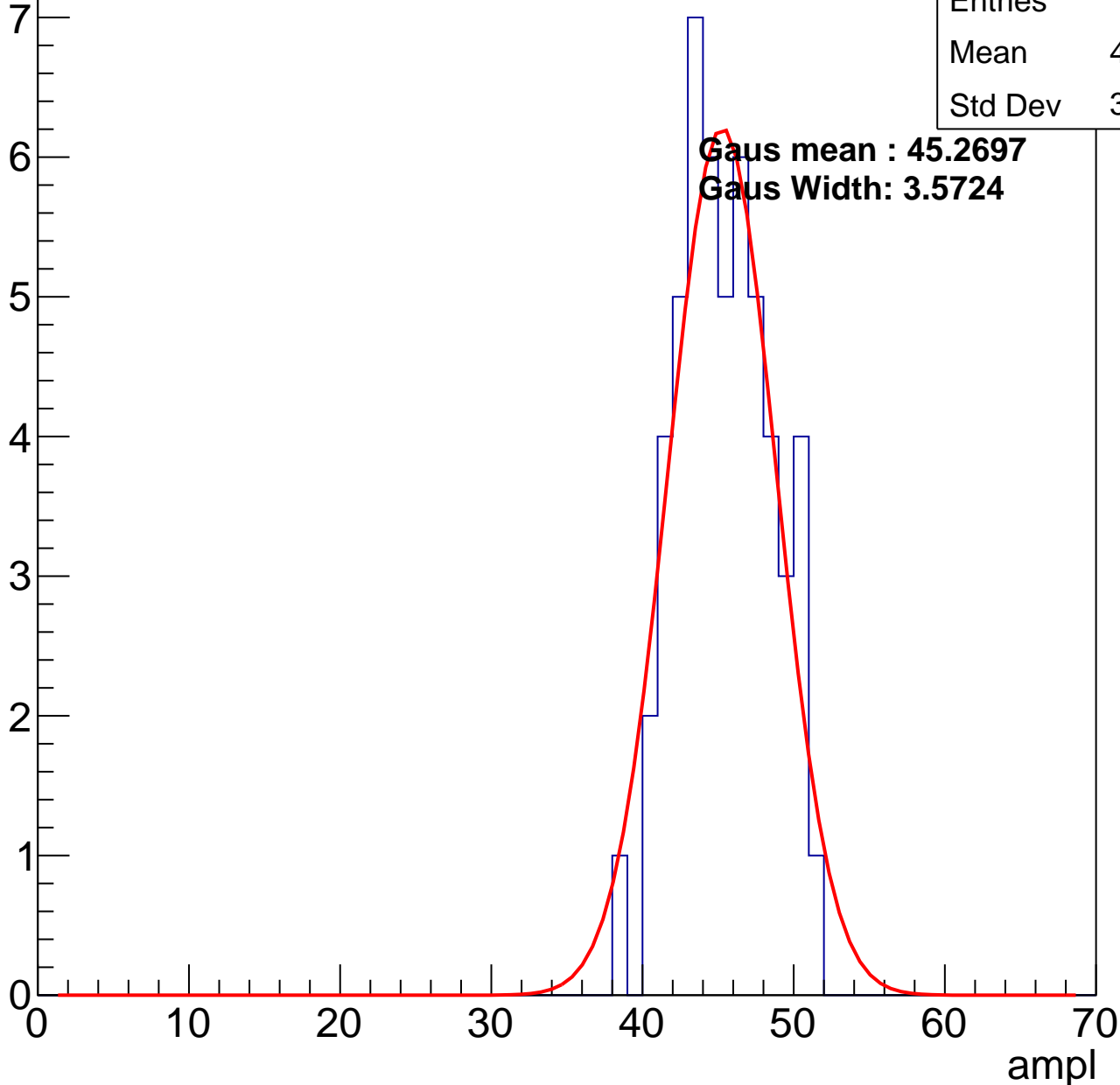
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.96
Std Dev	3.034

Gaus mean : 45.2697

Gaus Width: 3.5724

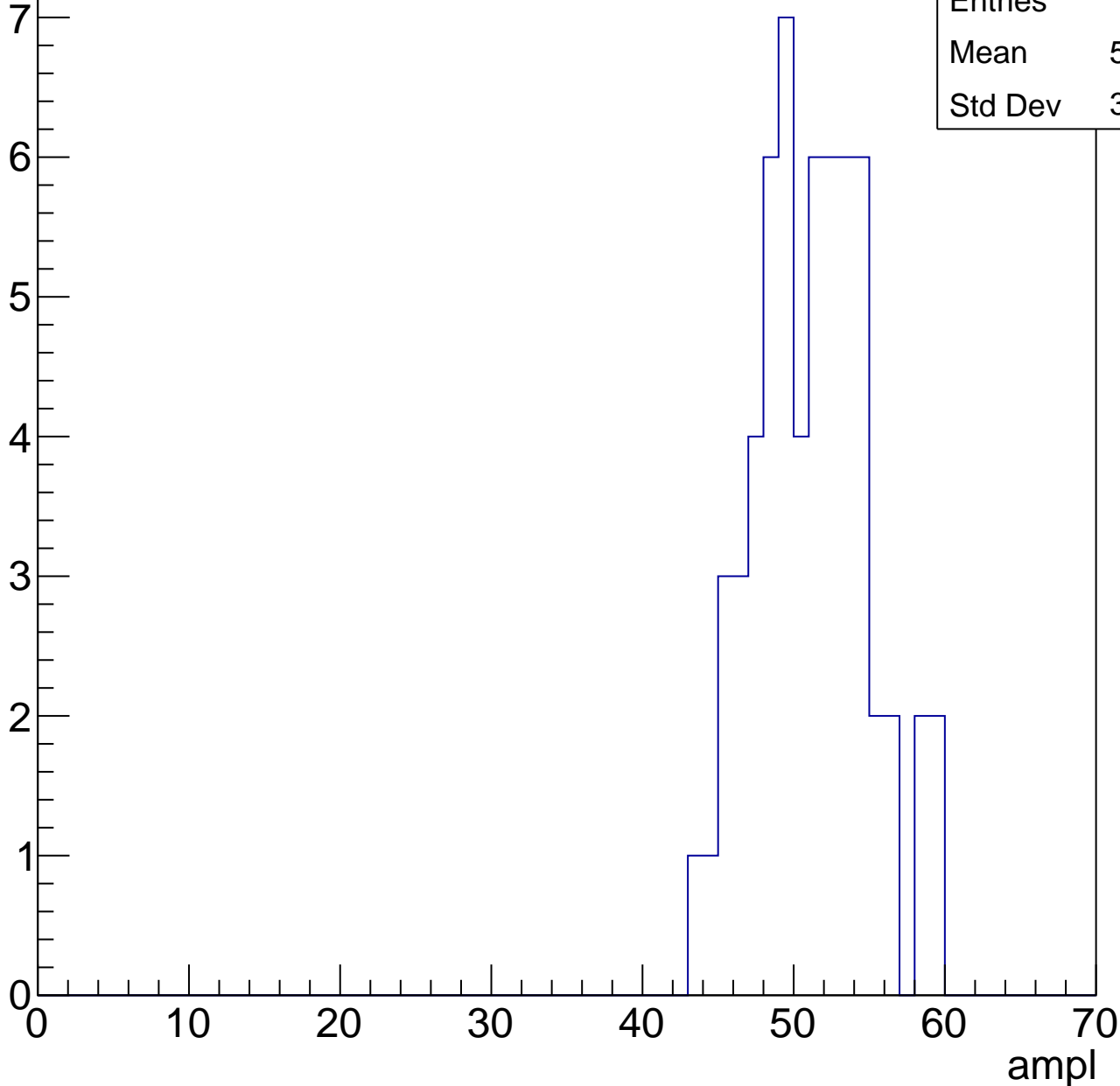


B1L103S, U20-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.74
Std Dev	3.697

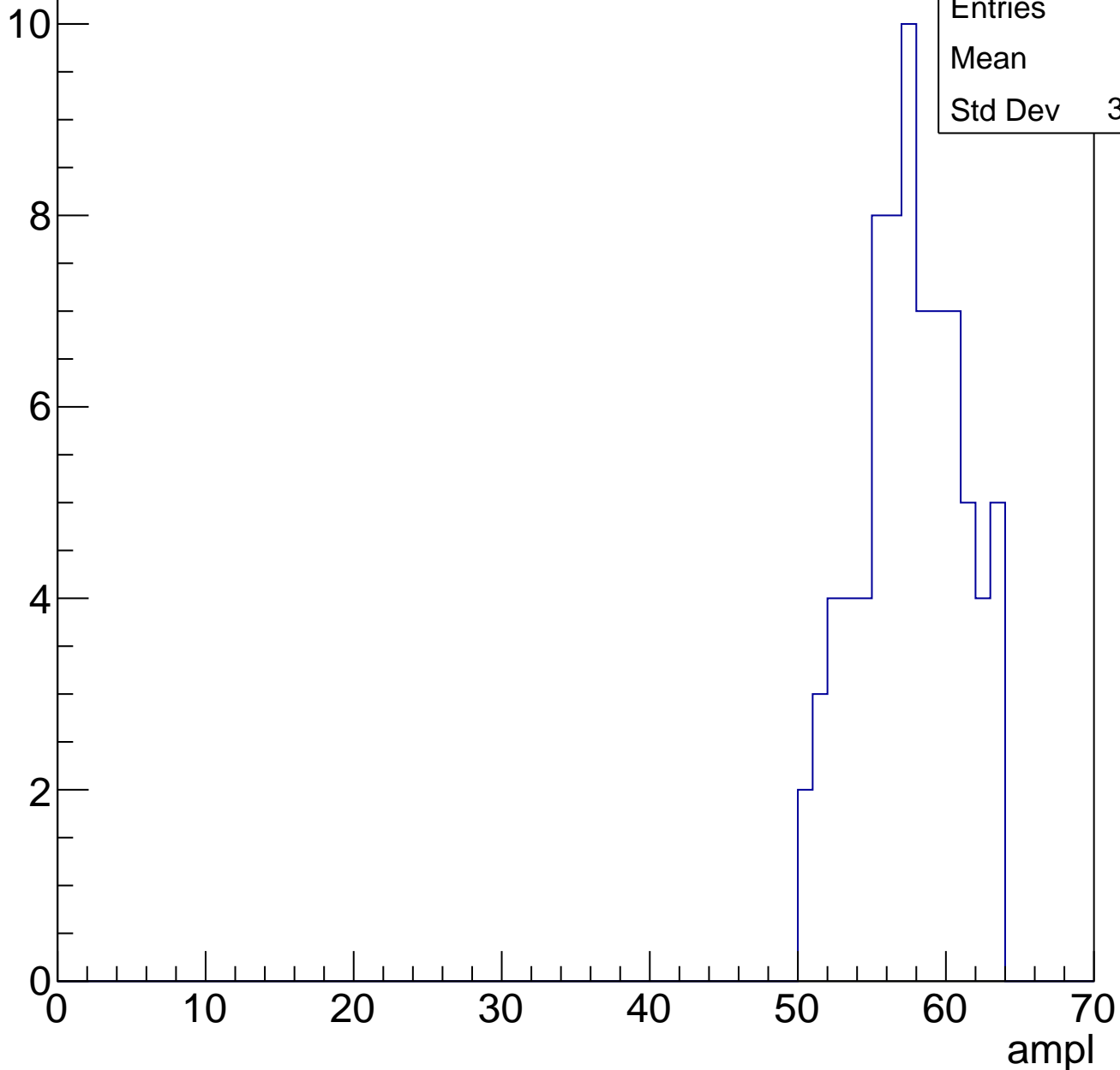


B1L103S, U20-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	57.1
Std Dev	3.403

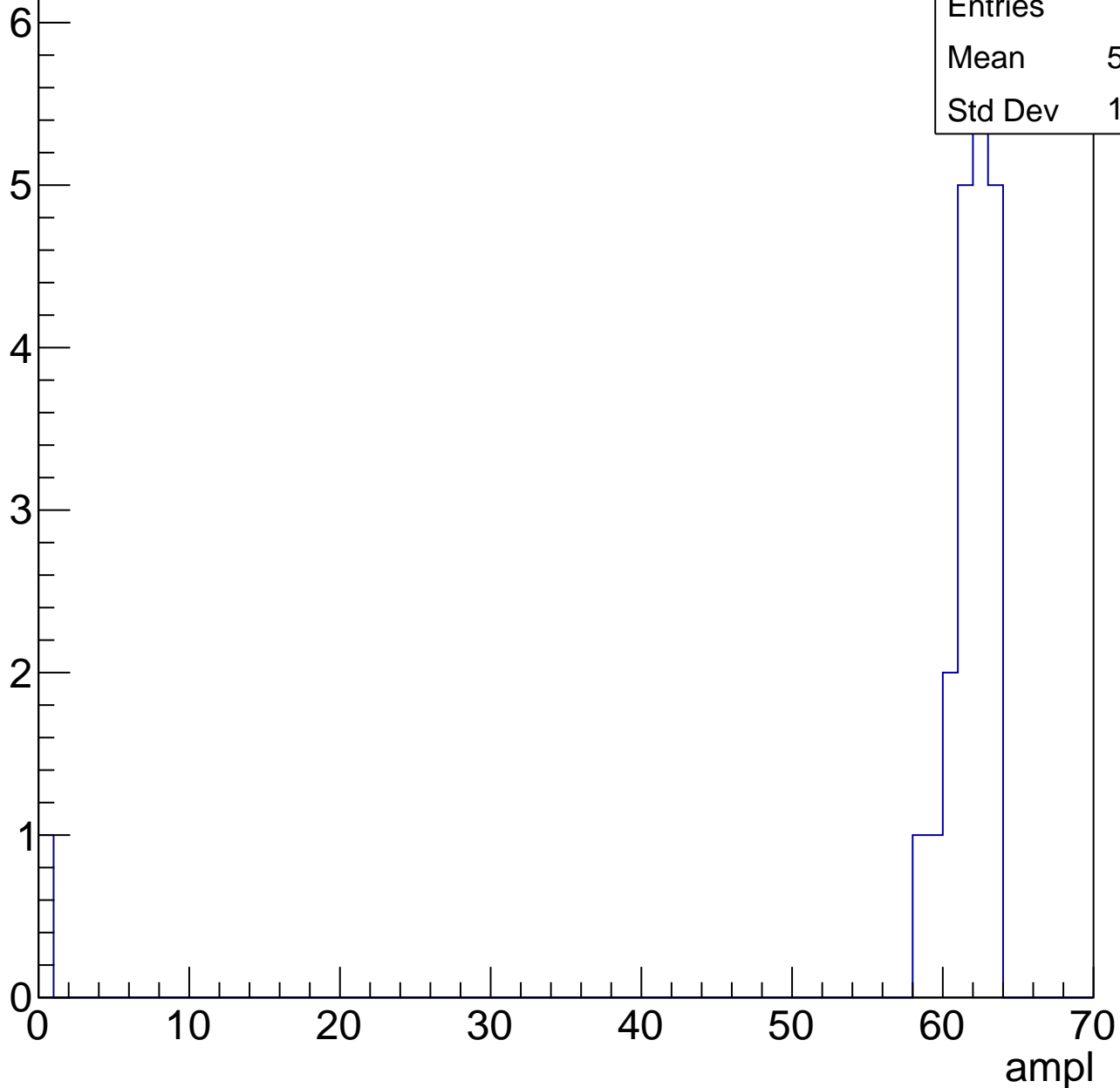
Entry



B1L103S, U20-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	21
Mean	58.52
Std Dev	13.15

B1L103S, U20-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch66, adc0

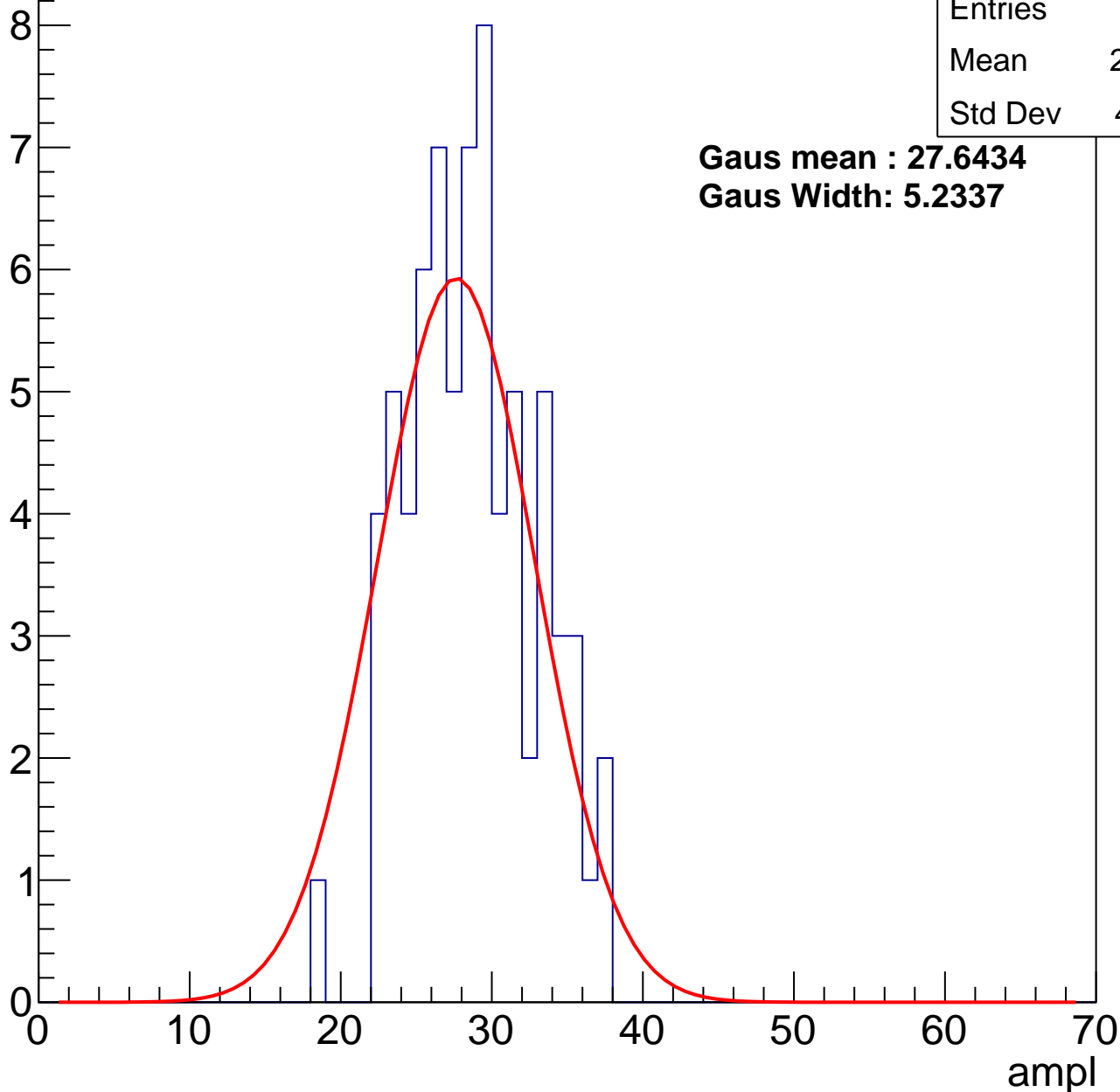
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.24
Std Dev	4.121

Gaus mean : 27.6434

Gaus Width: 5.2337



B1L103S, U20-ch66, adc1

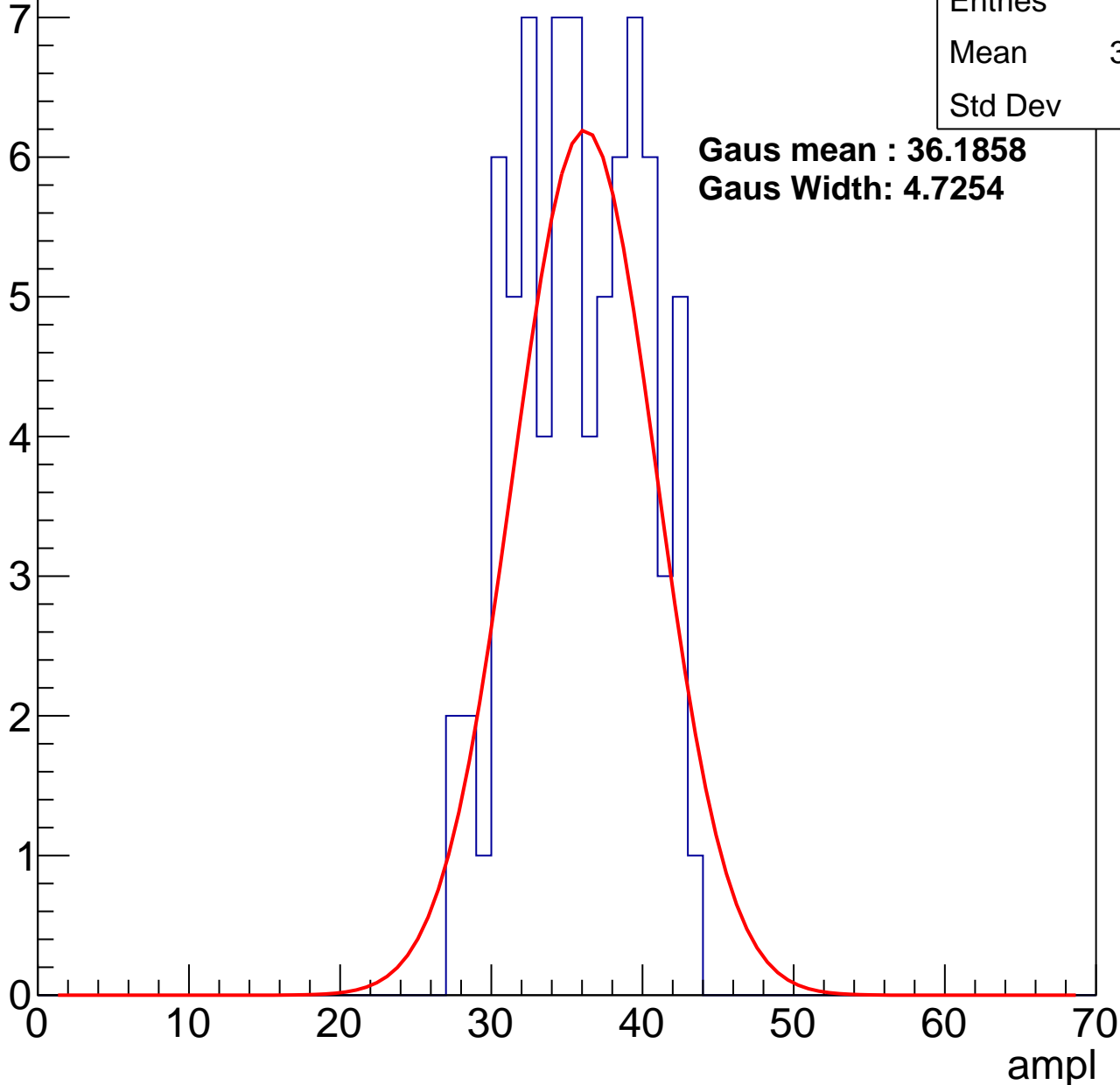
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.37
Std Dev	4.12

Gaus mean : 36.1858

Gaus Width: 4.7254



B1L103S, U20-ch66, adc2

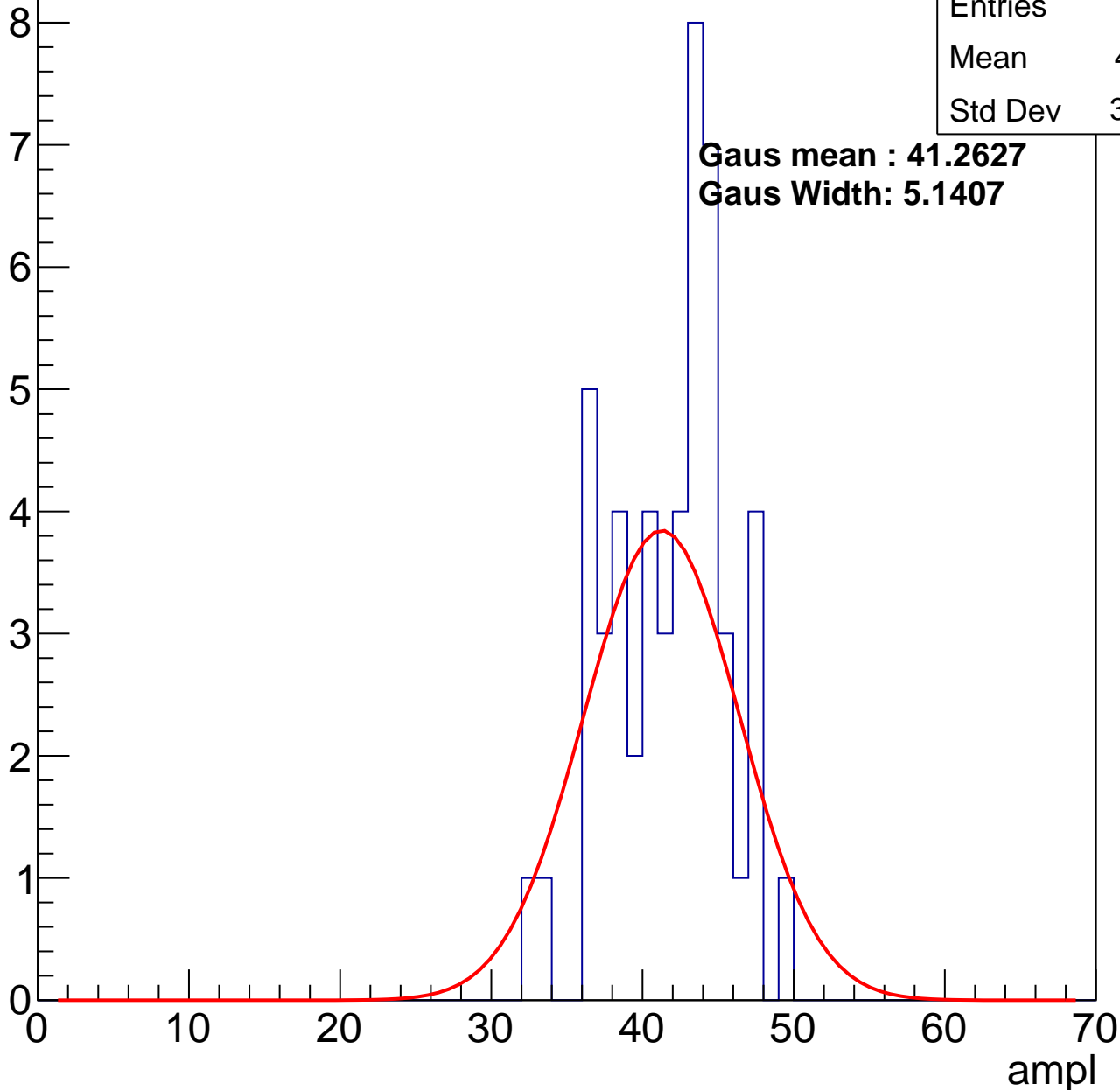
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.31
Std Dev	3.812

Gaus mean : 41.2627

Gaus Width: 5.1407

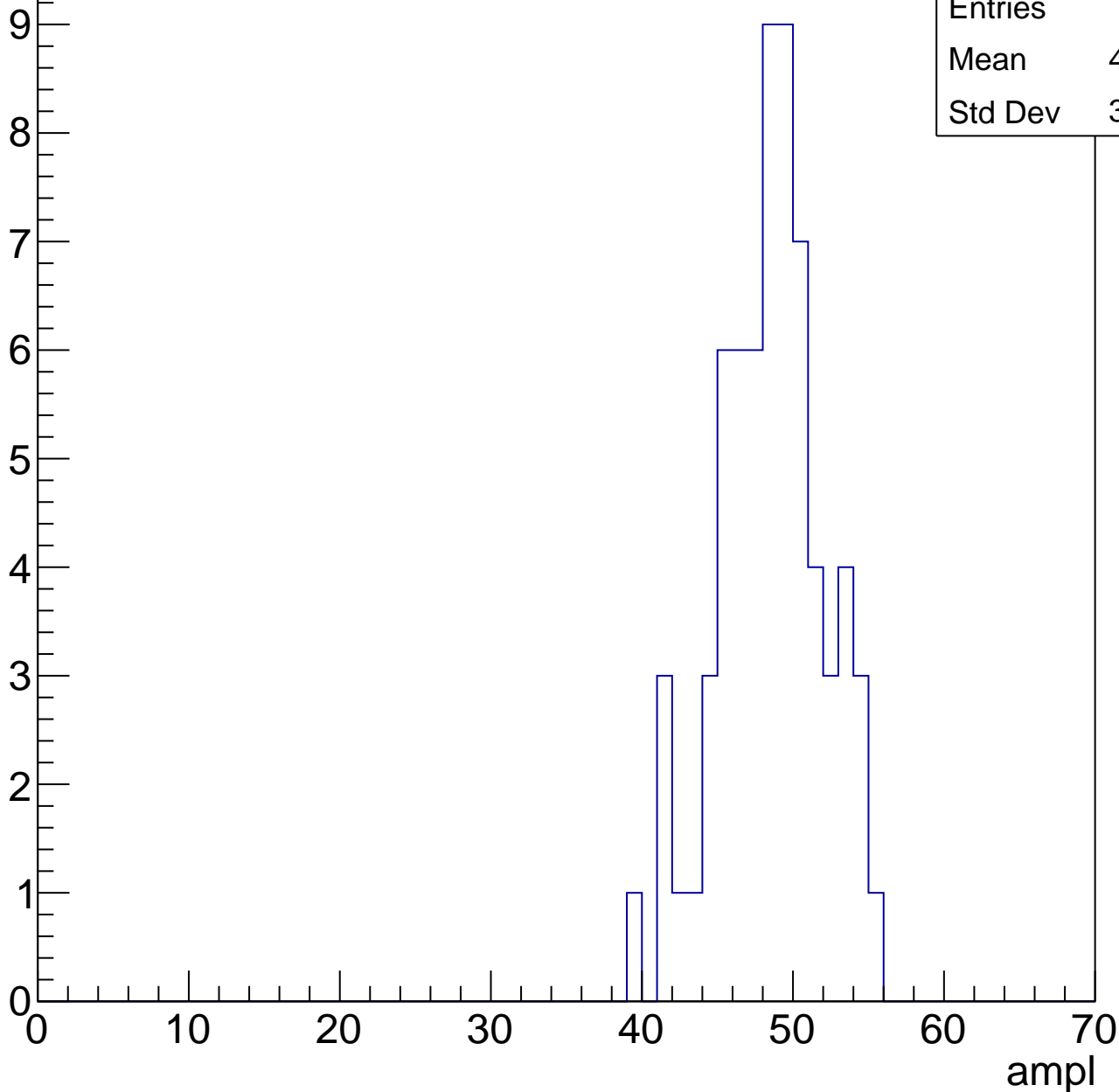


B1L103S, U20-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.04
Std Dev	3.453

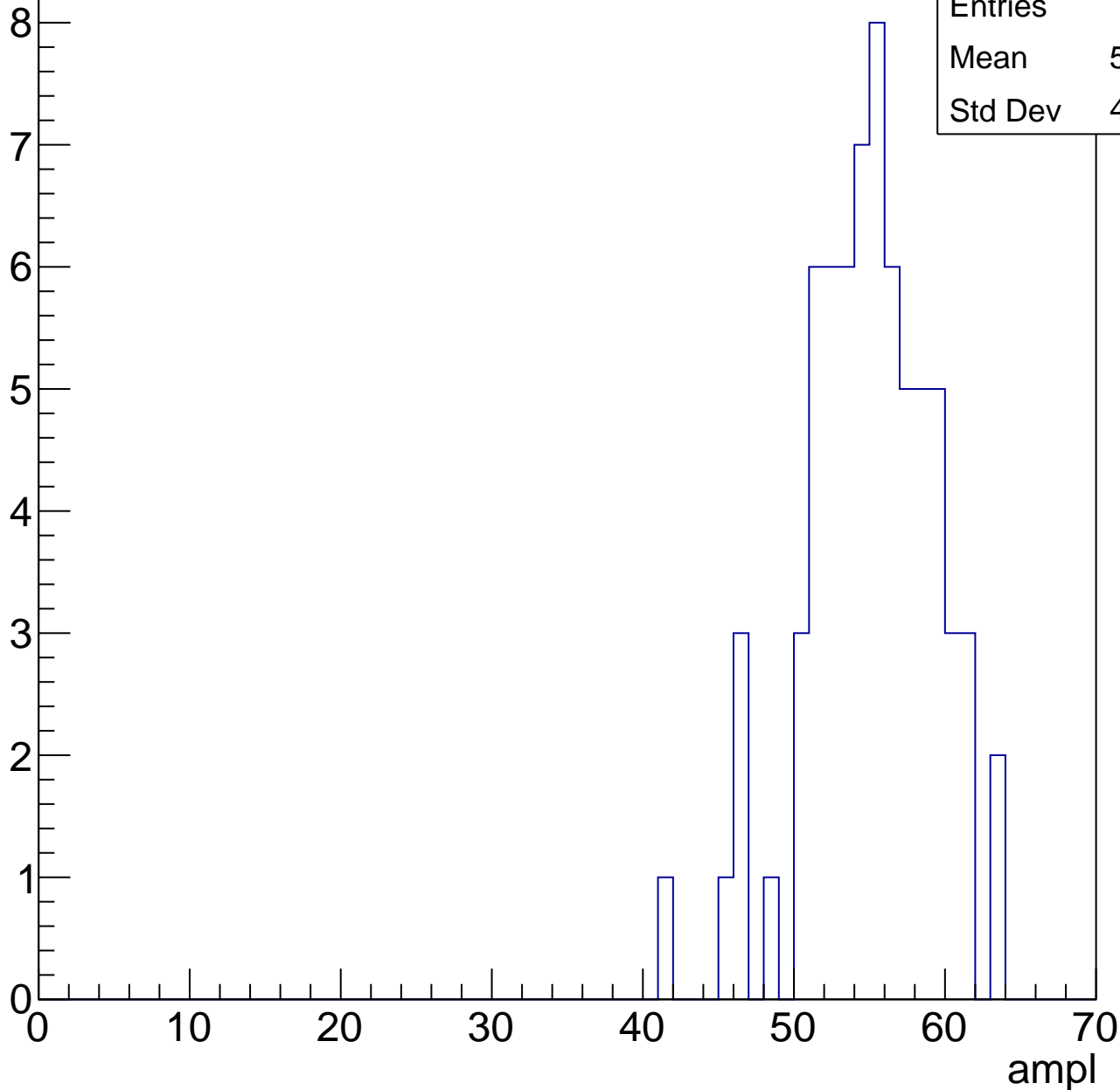


B1L103S, U20-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	54.52
Std Dev	4.262

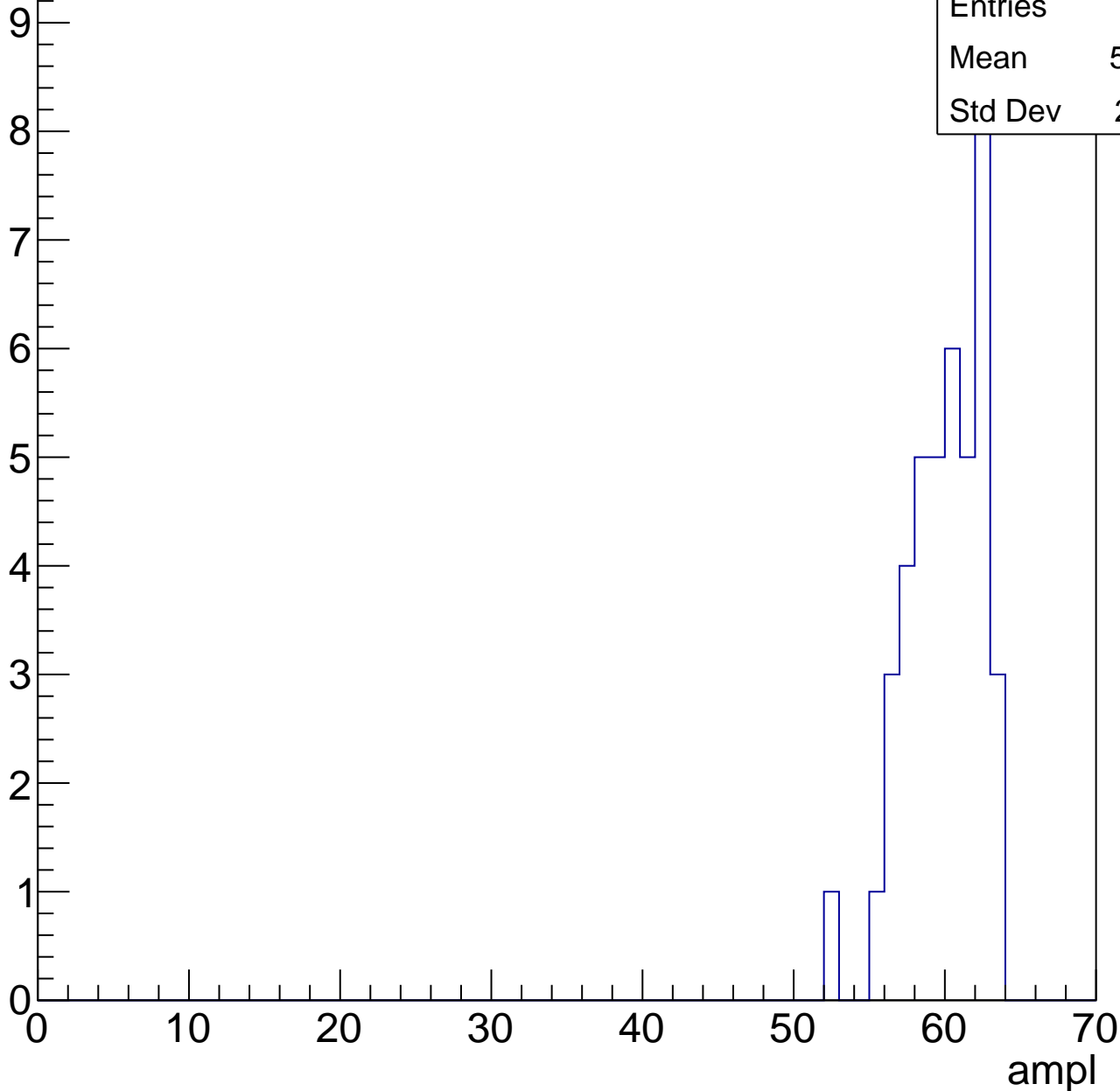


B1L103S, U20-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

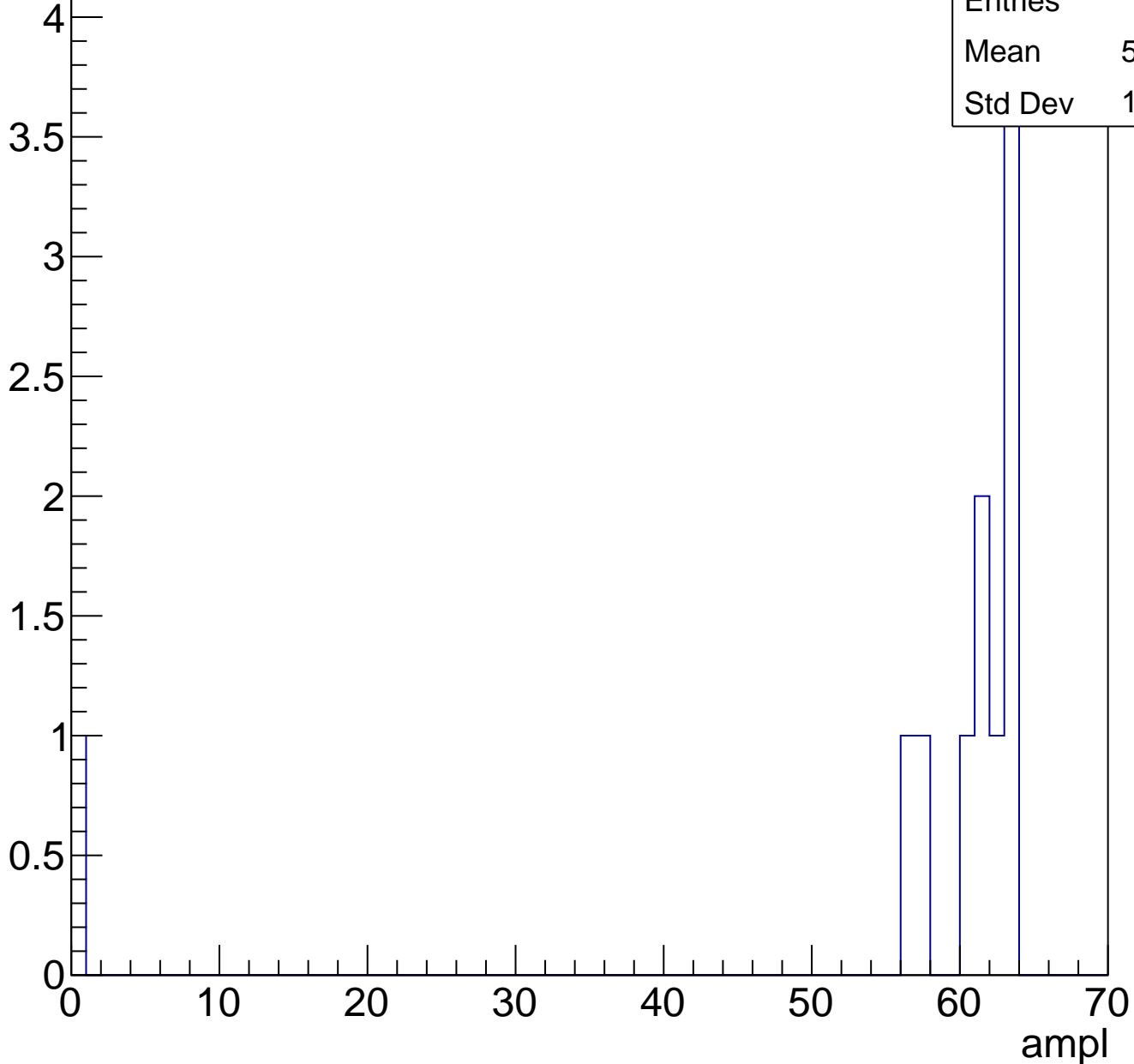
Entries	42
Mean	59.52
Std Dev	2.471



B1L103S, U20-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch67, adc0

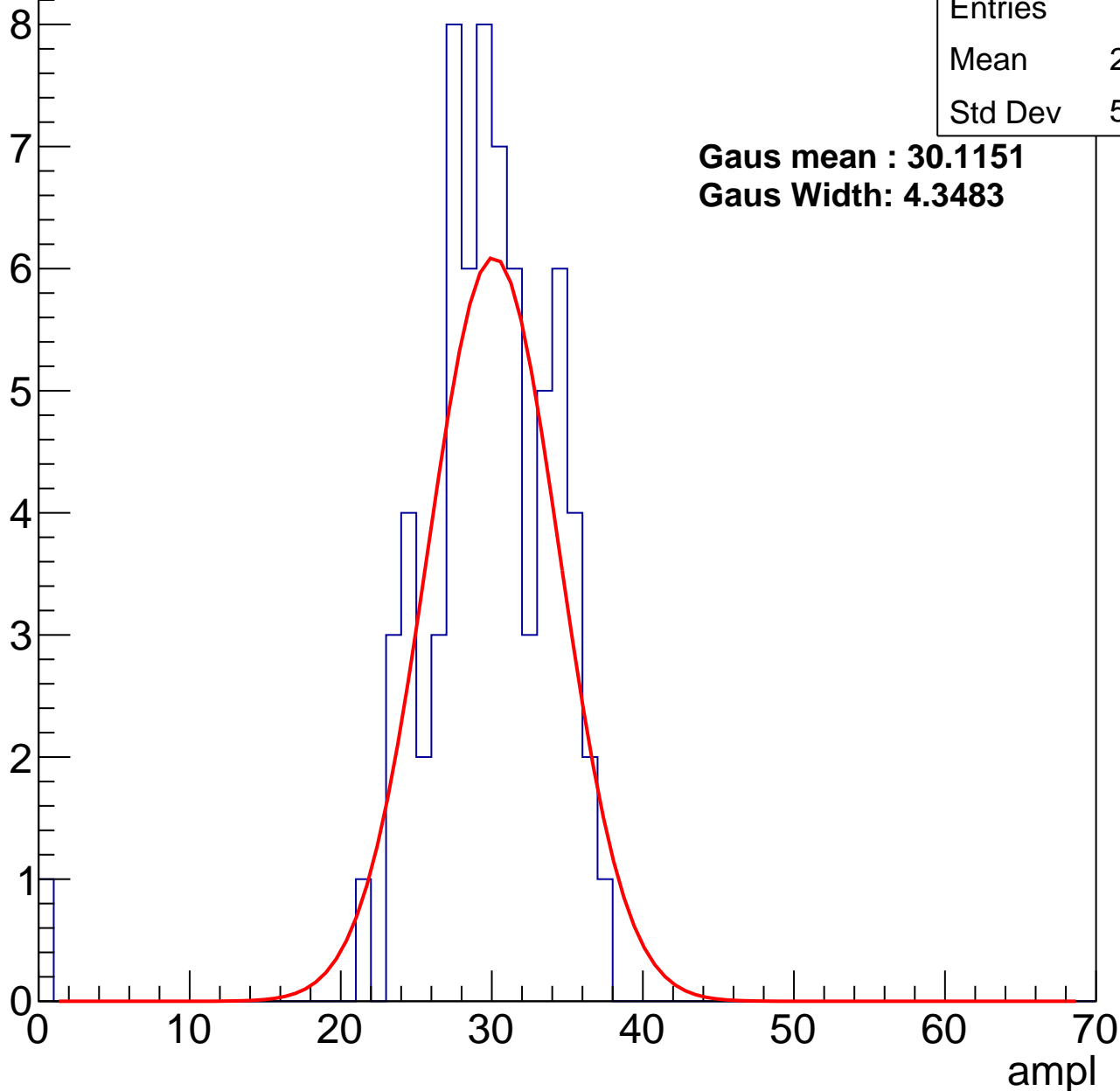
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.14
Std Dev	5.075

Gaus mean : 30.1151

Gaus Width: 4.3483



B1L103S, U20-ch67, adc1

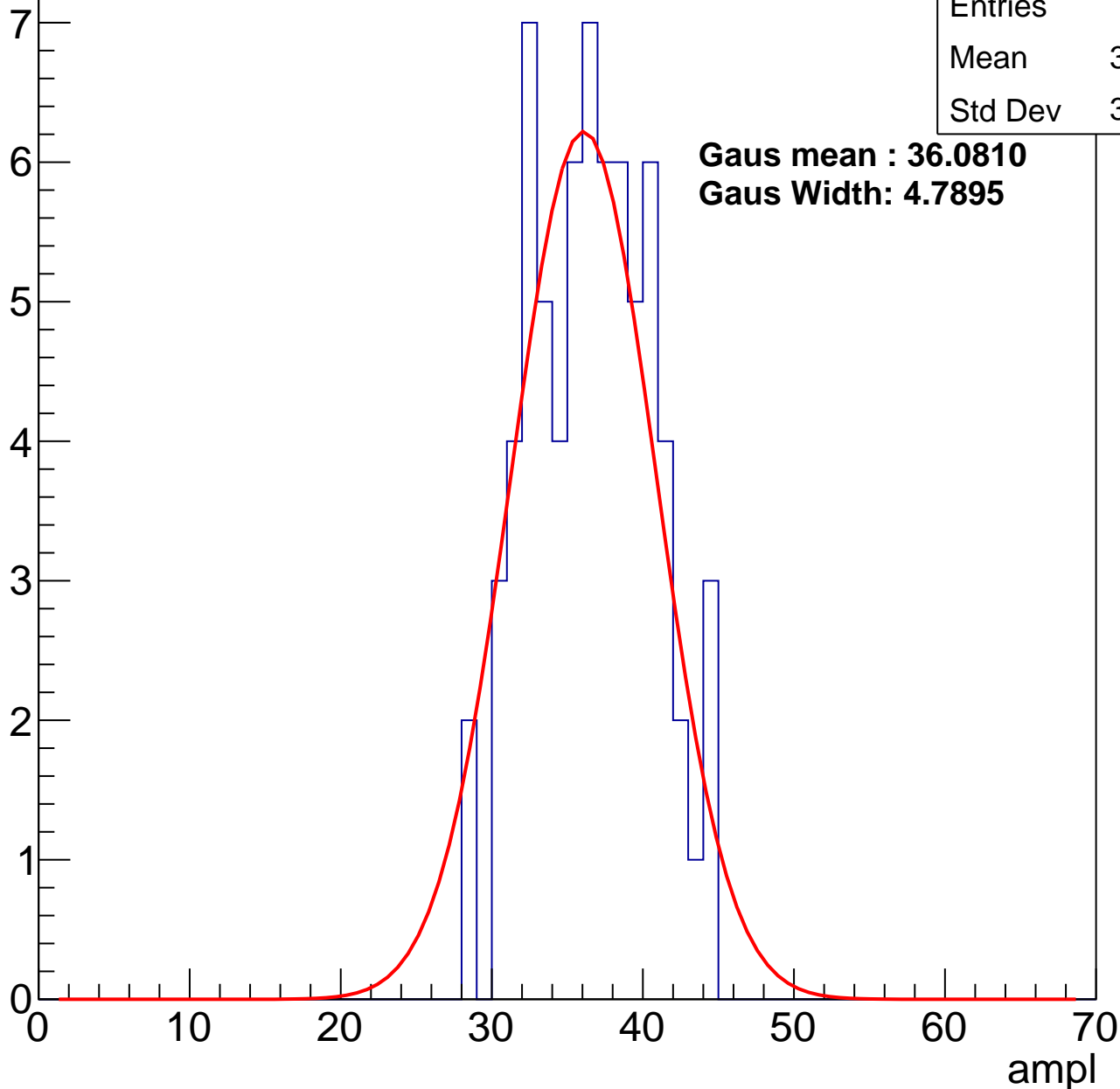
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.13
Std Dev	3.932

Gaus mean : 36.0810

Gaus Width: 4.7895



B1L103S, U20-ch67, adc2

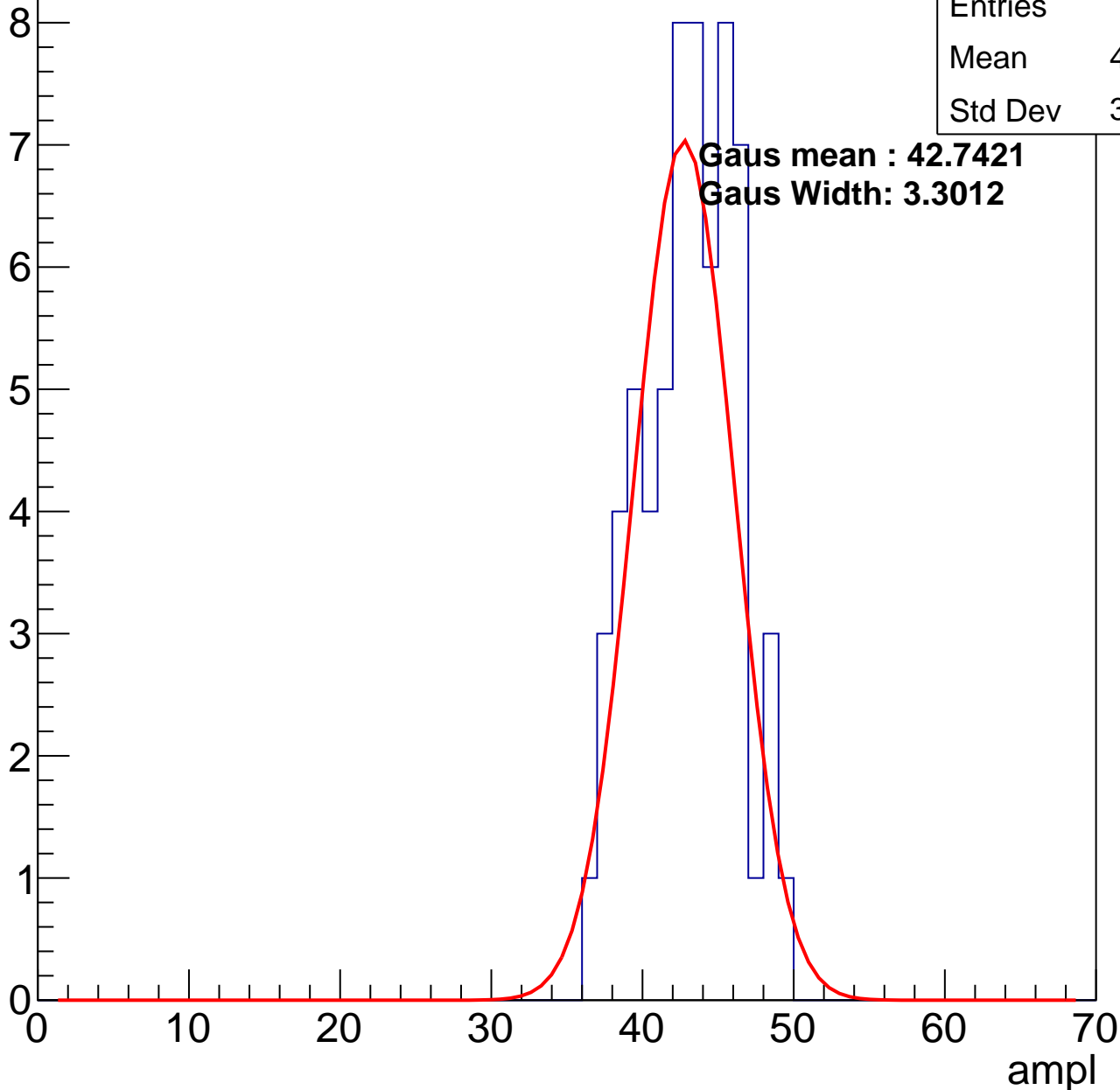
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.58
Std Dev	3.106

Gaus mean : 42.7421

Gaus Width: 3.3012

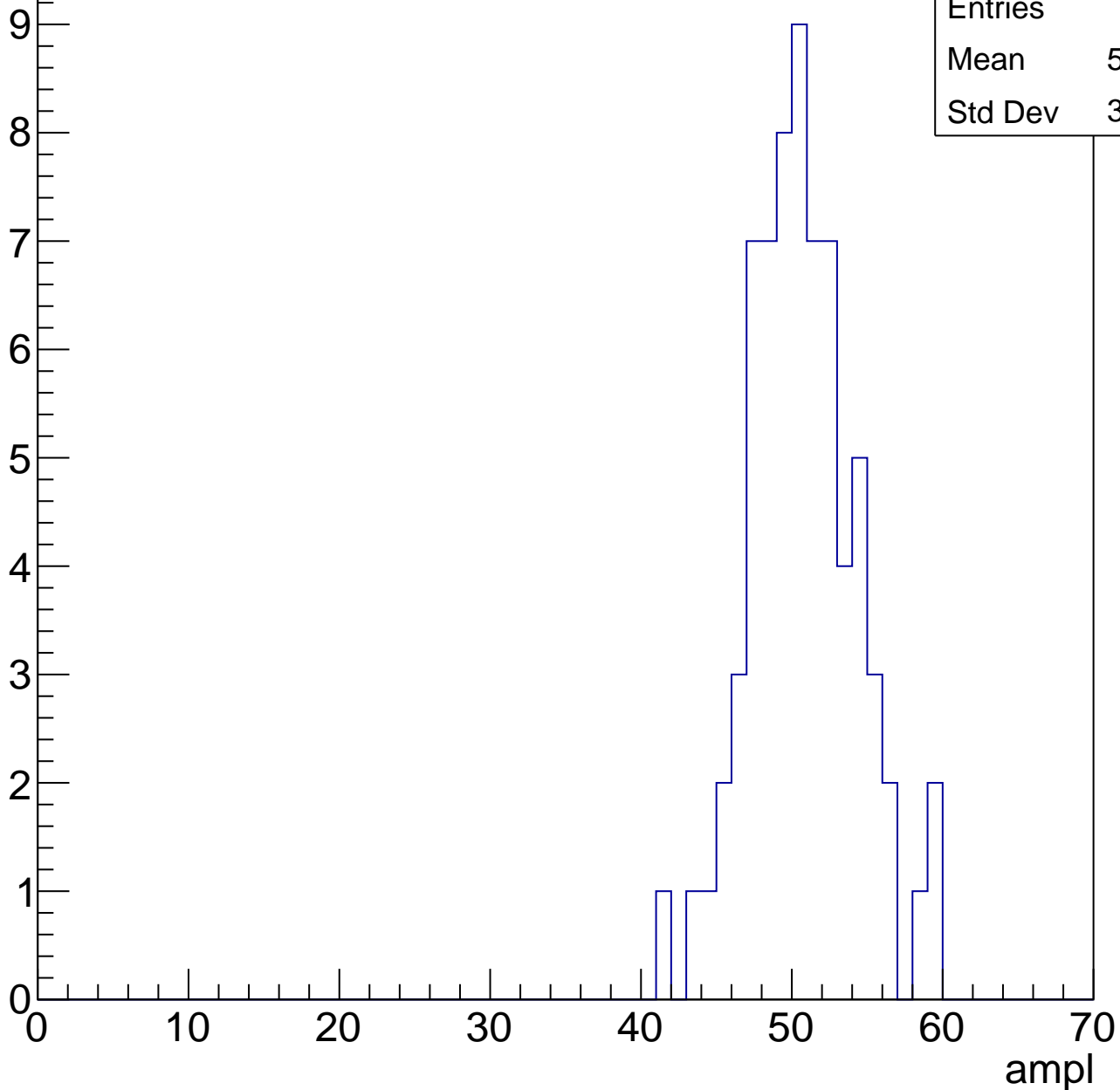


B1L103S, U20-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	50.27
Std Dev	3.549

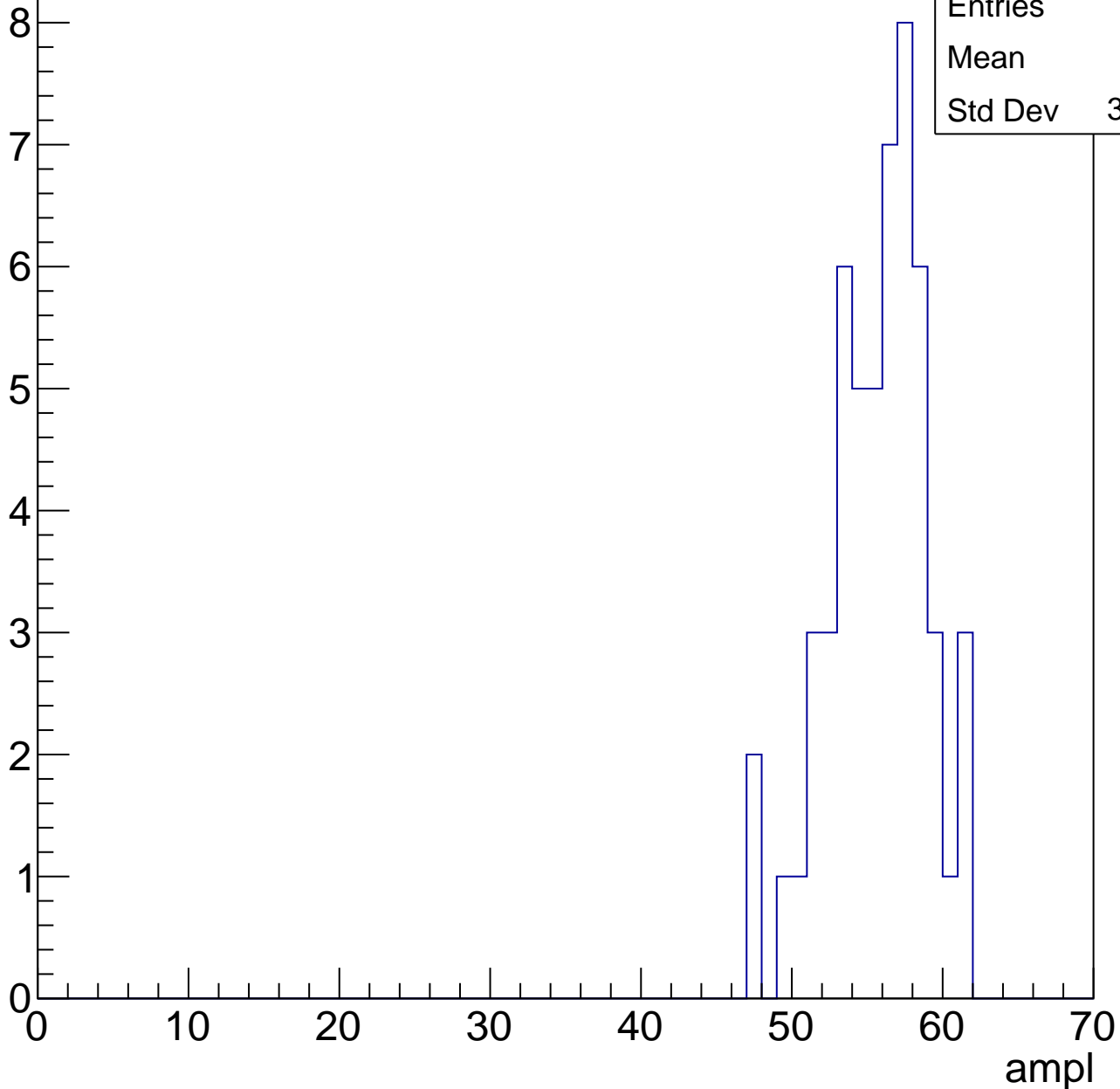


B1L103S, U20-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.2
Std Dev	3.234

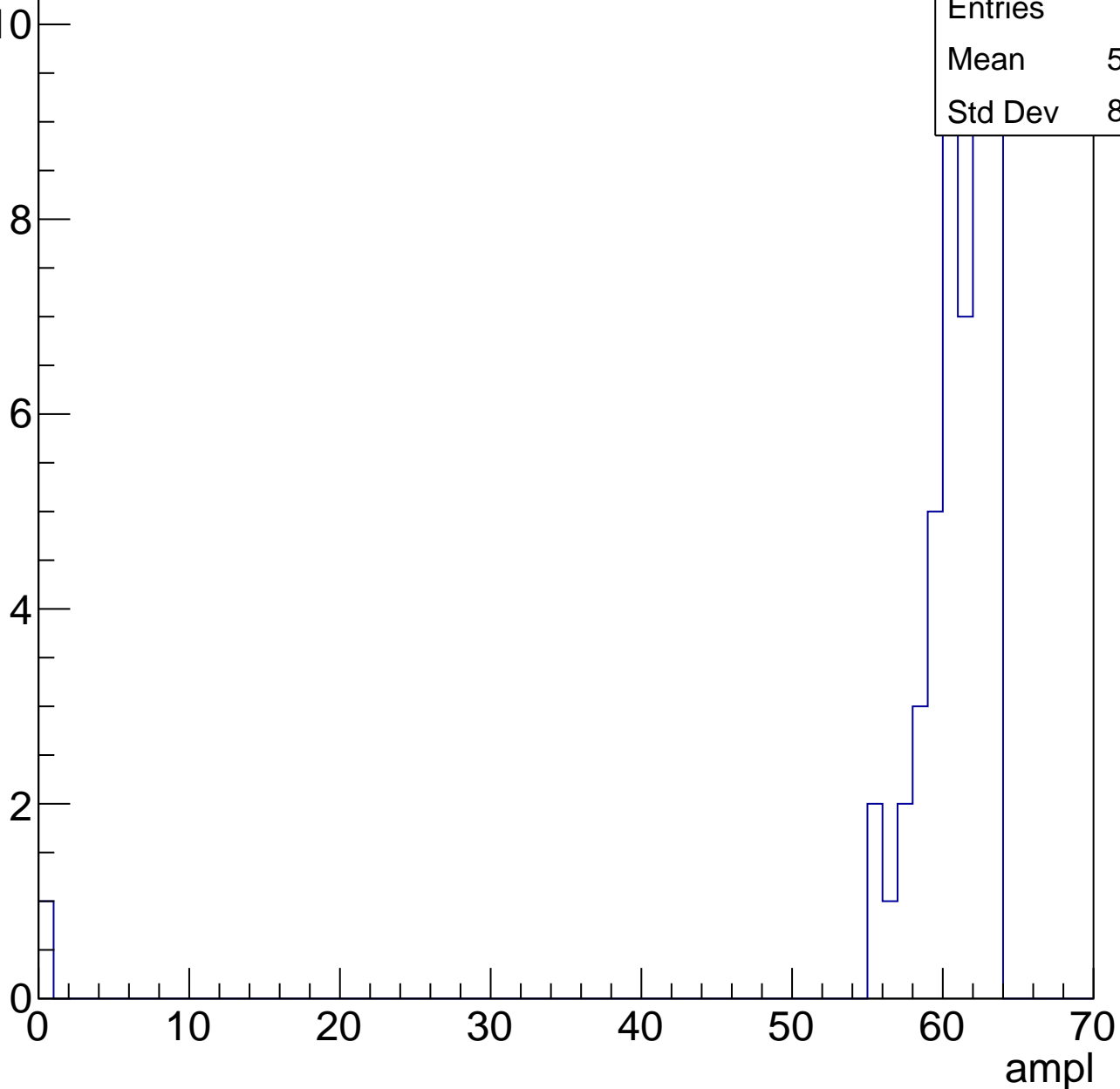


B1L103S, U20-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	59.32
Std Dev	8.735



B1L103S, U20-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U20-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L103S, U20-ch68, adc0

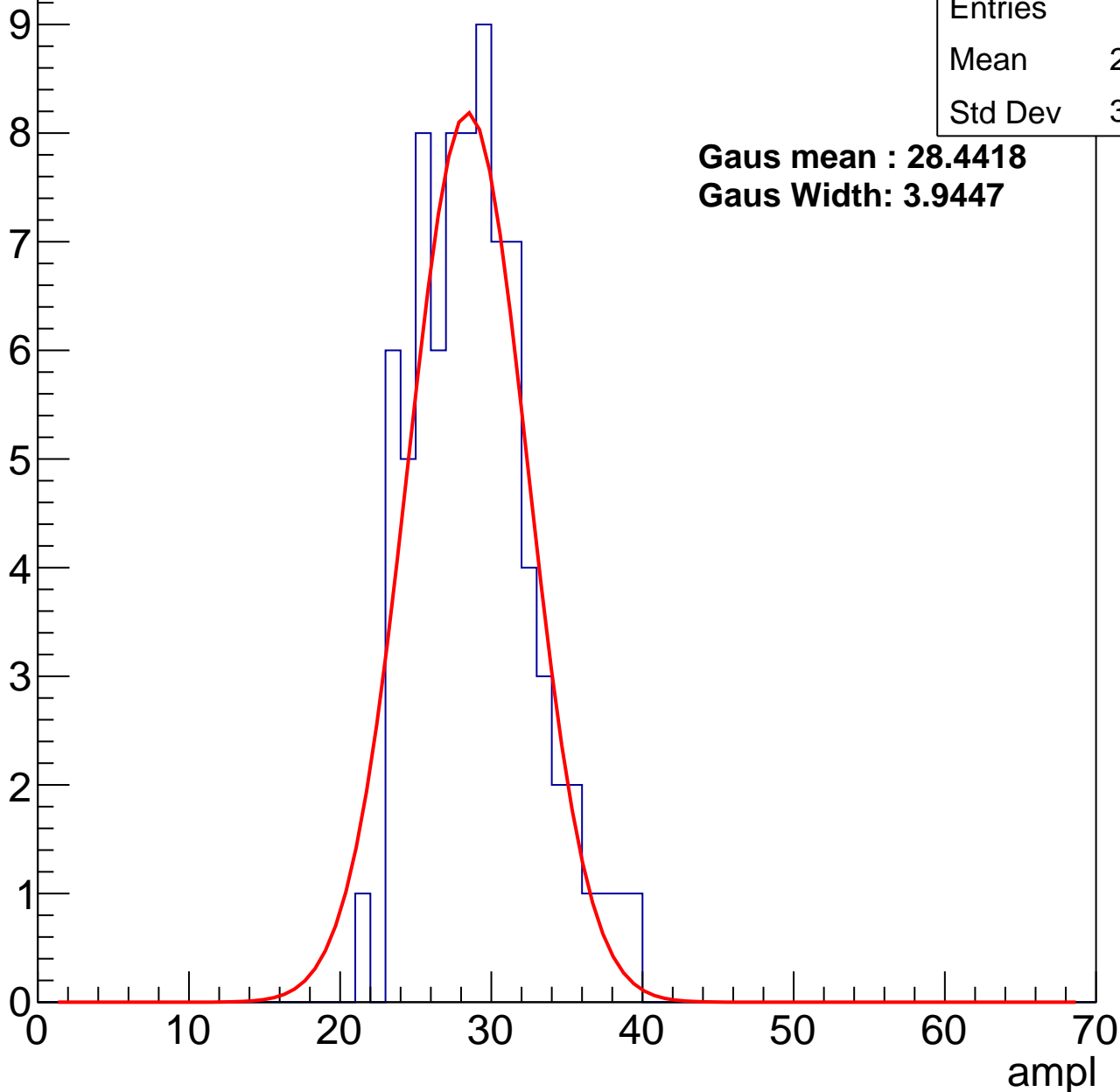
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	28.48
Std Dev	3.775

Gaus mean : 28.4418

Gaus Width: 3.9447



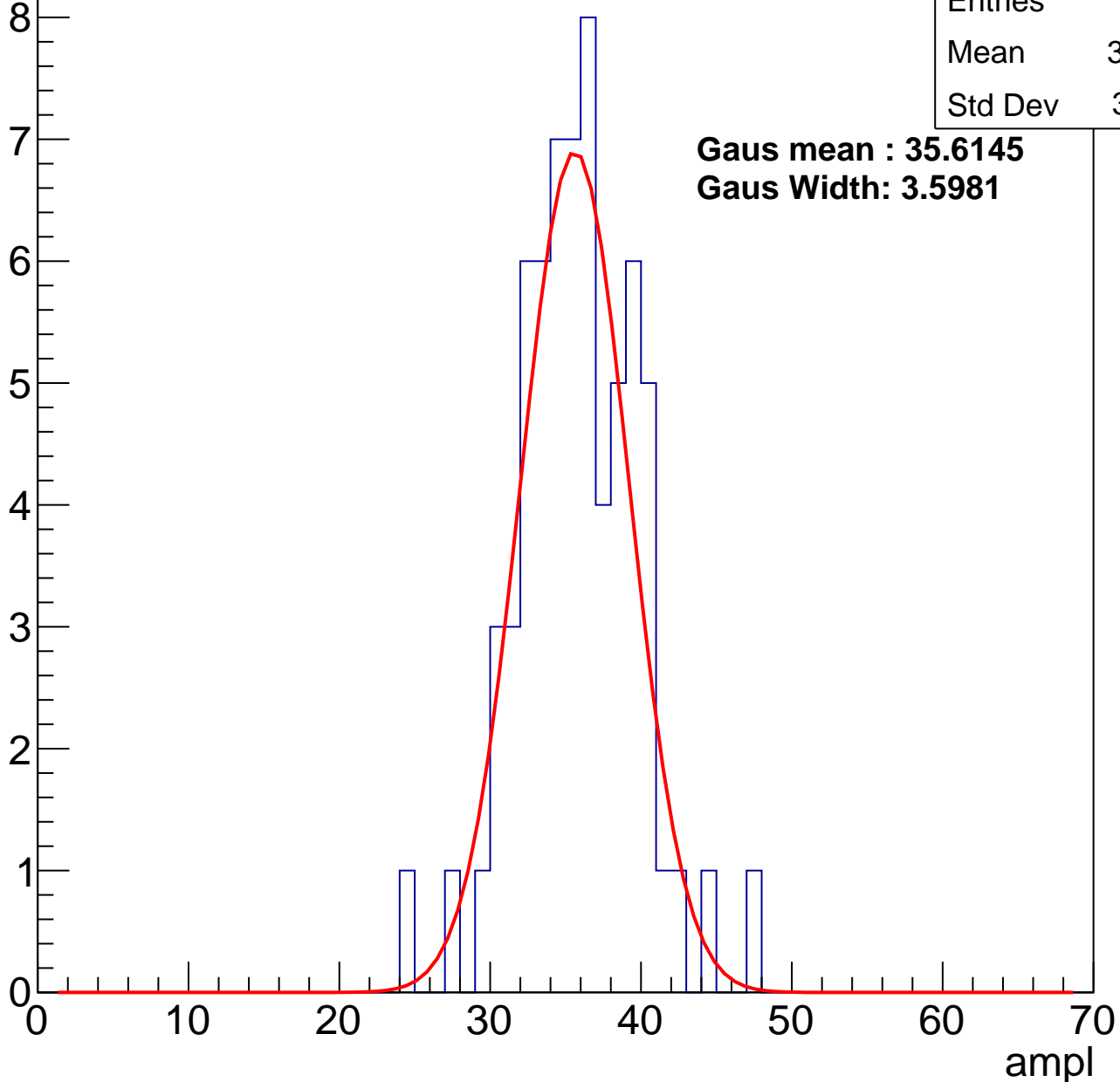
B1L103S, U20-ch68, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.37
Std Dev	3.901

Gaus mean : 35.6145
Gaus Width: 3.5981



B1L103S, U20-ch68, adc2

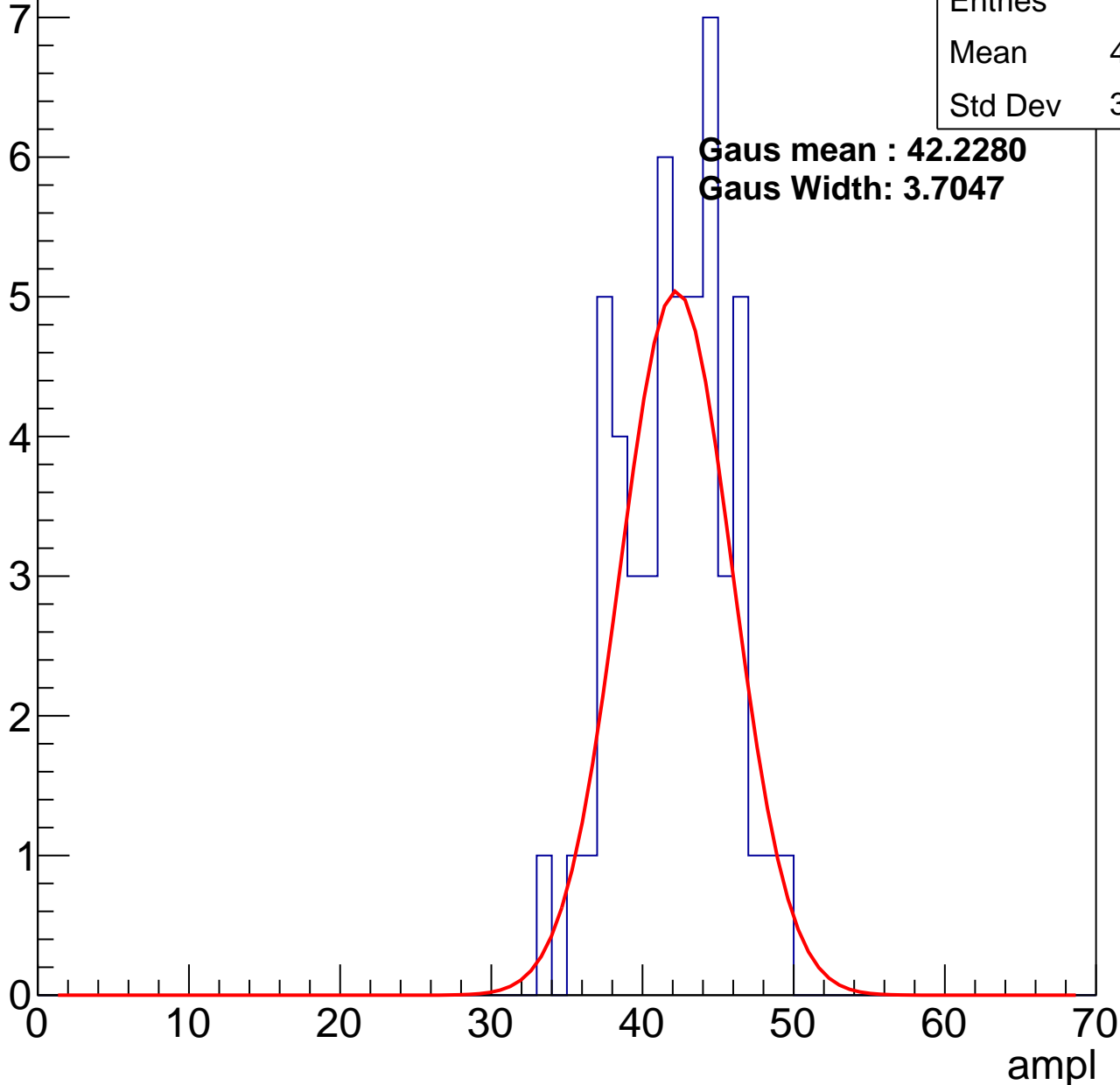
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.65
Std Dev	3.519

Gaus mean : 42.2280

Gaus Width: 3.7047

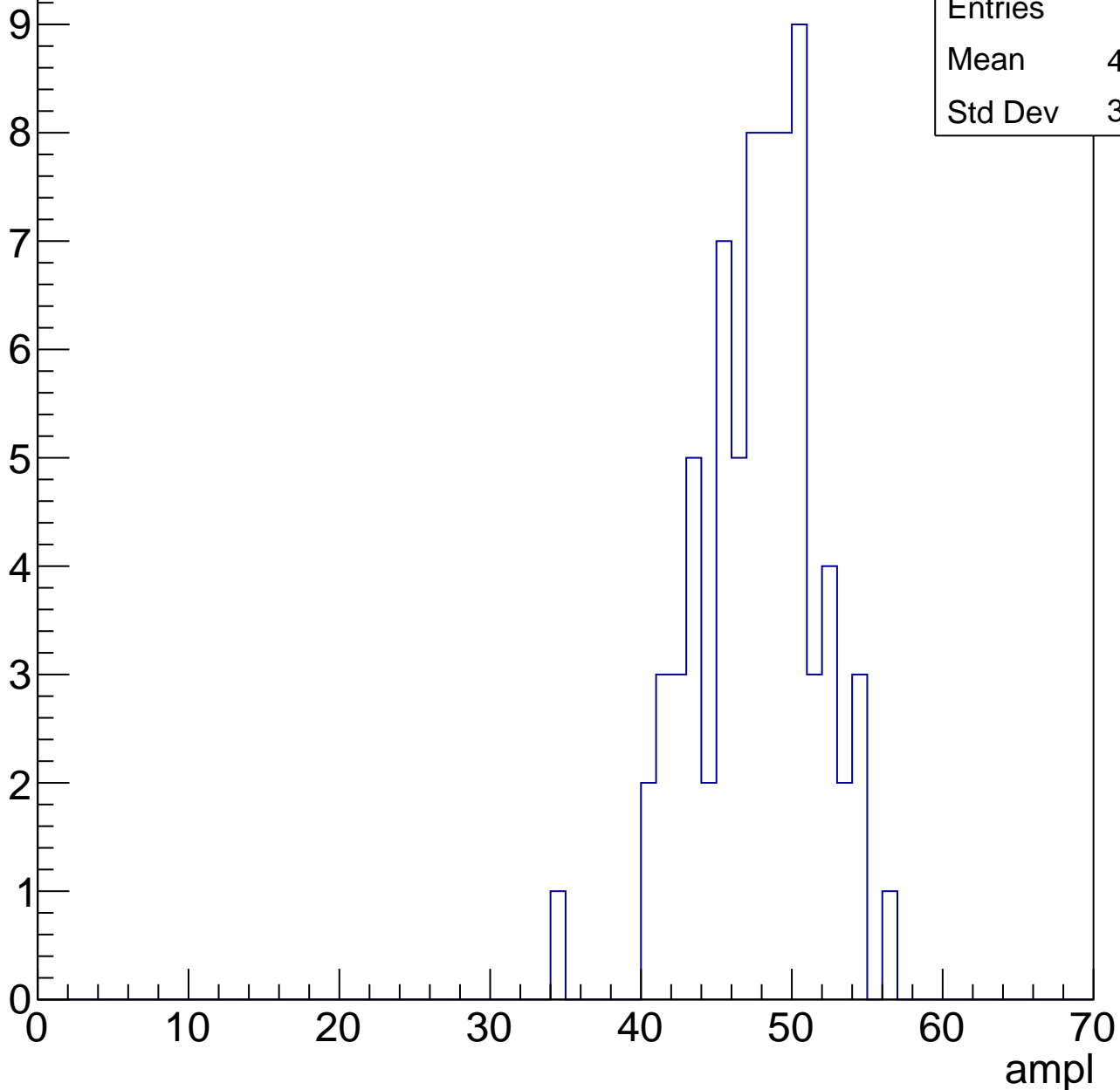


B1L103S, U20-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

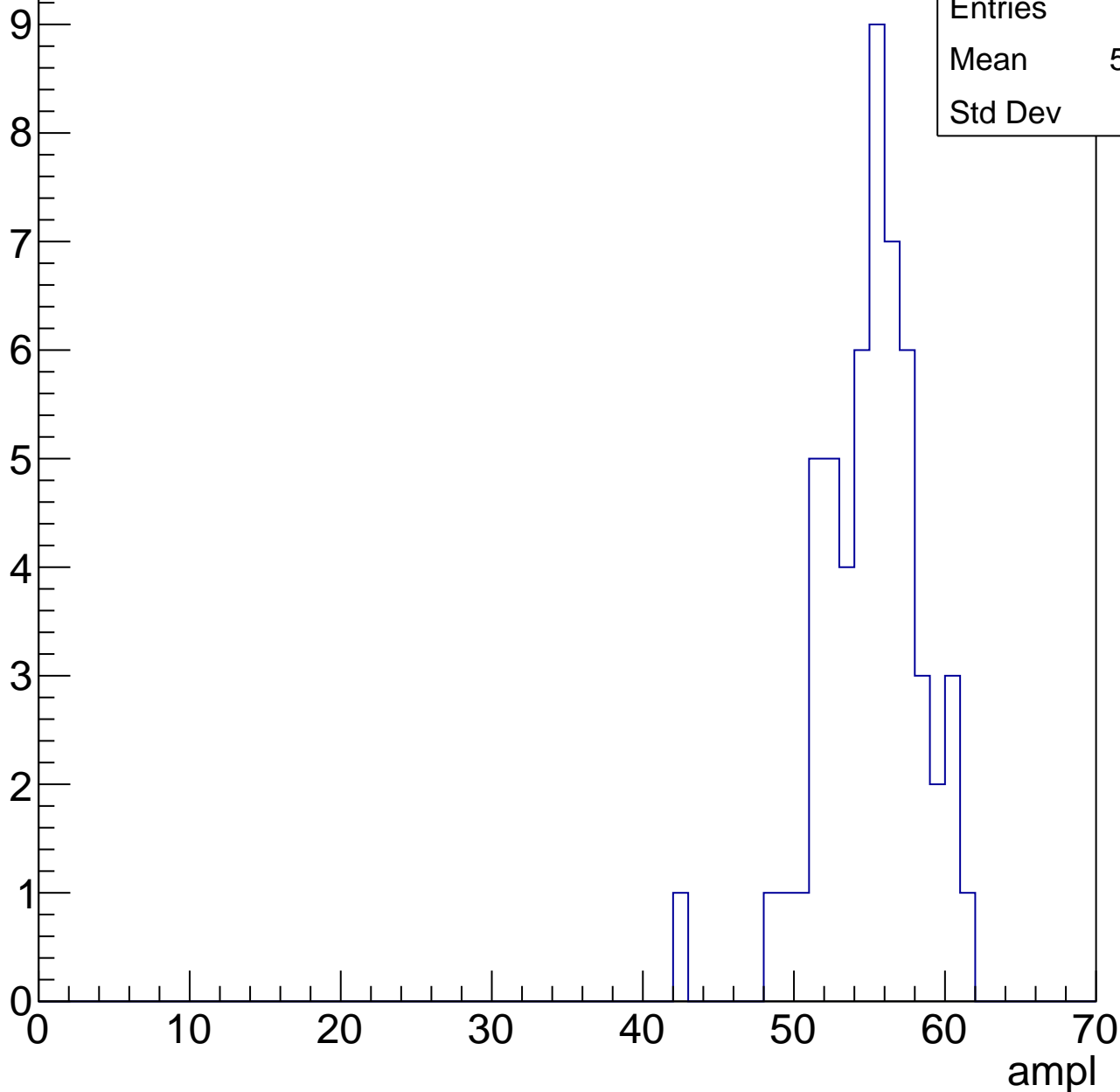
Entries	74
Mean	47.27
Std Dev	3.933



B1L103S, U20-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

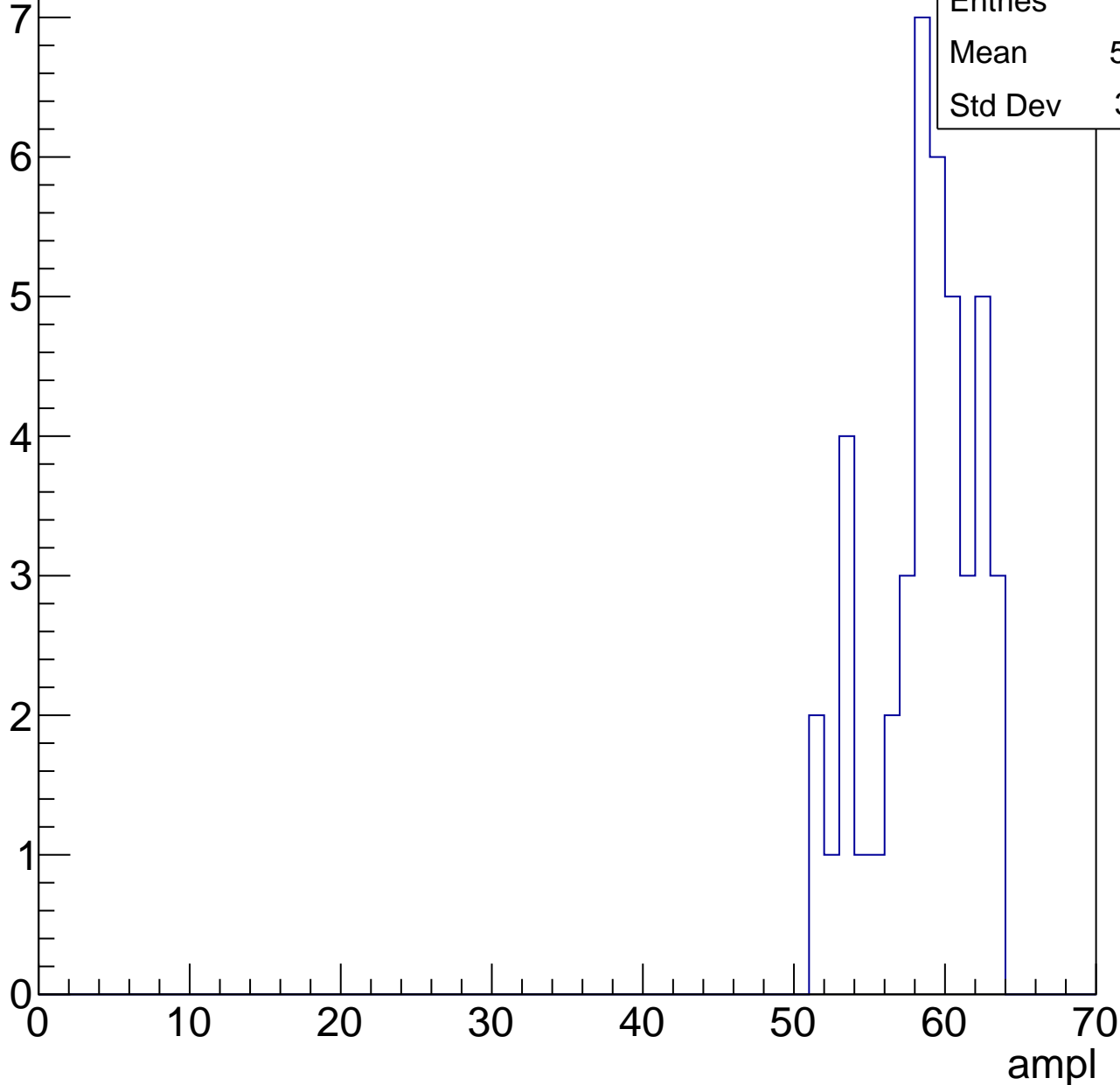


Entries	55
Mean	54.58
Std Dev	3.35

B1L103S, U20-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

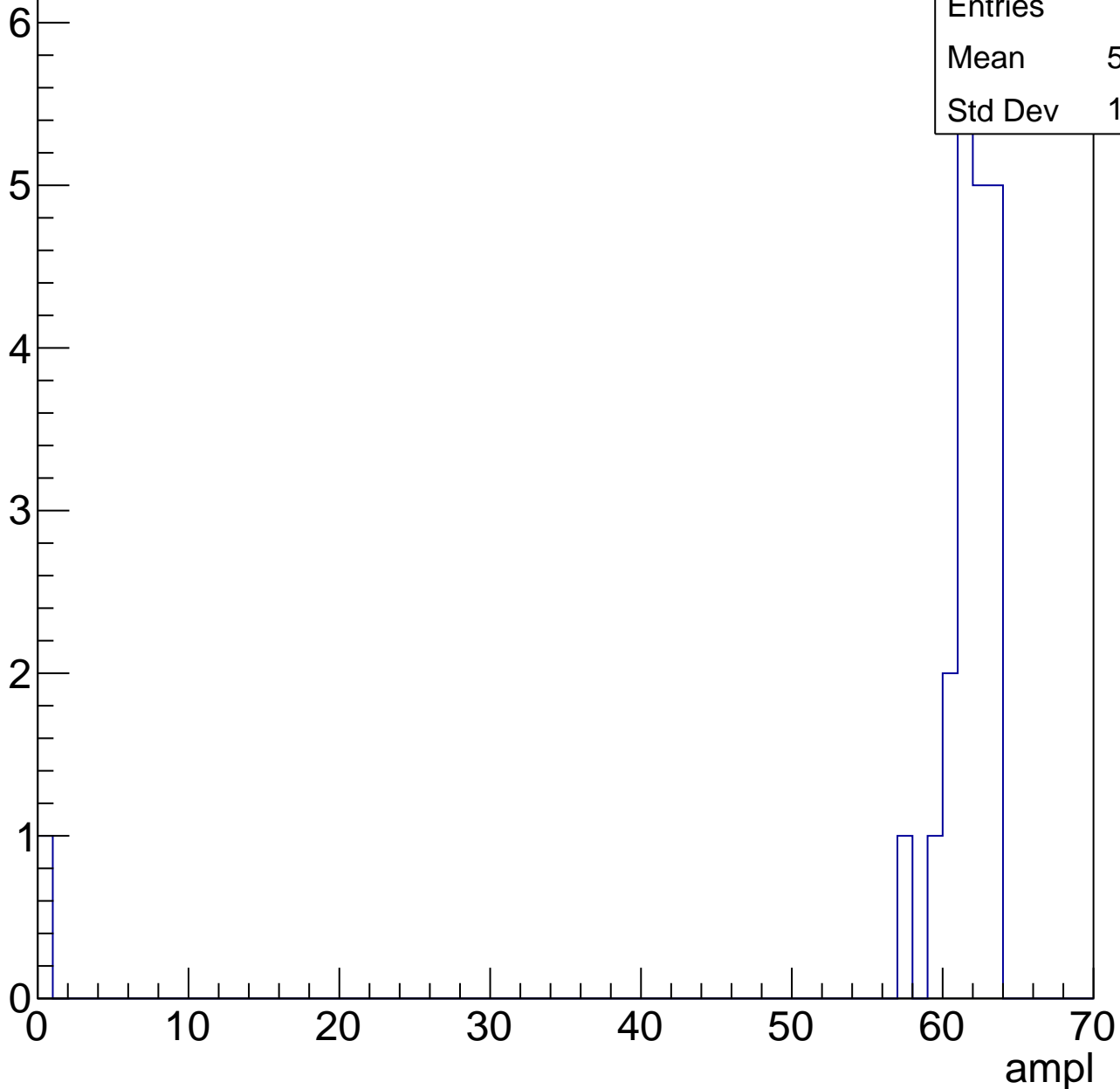


B1L103S, U20-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

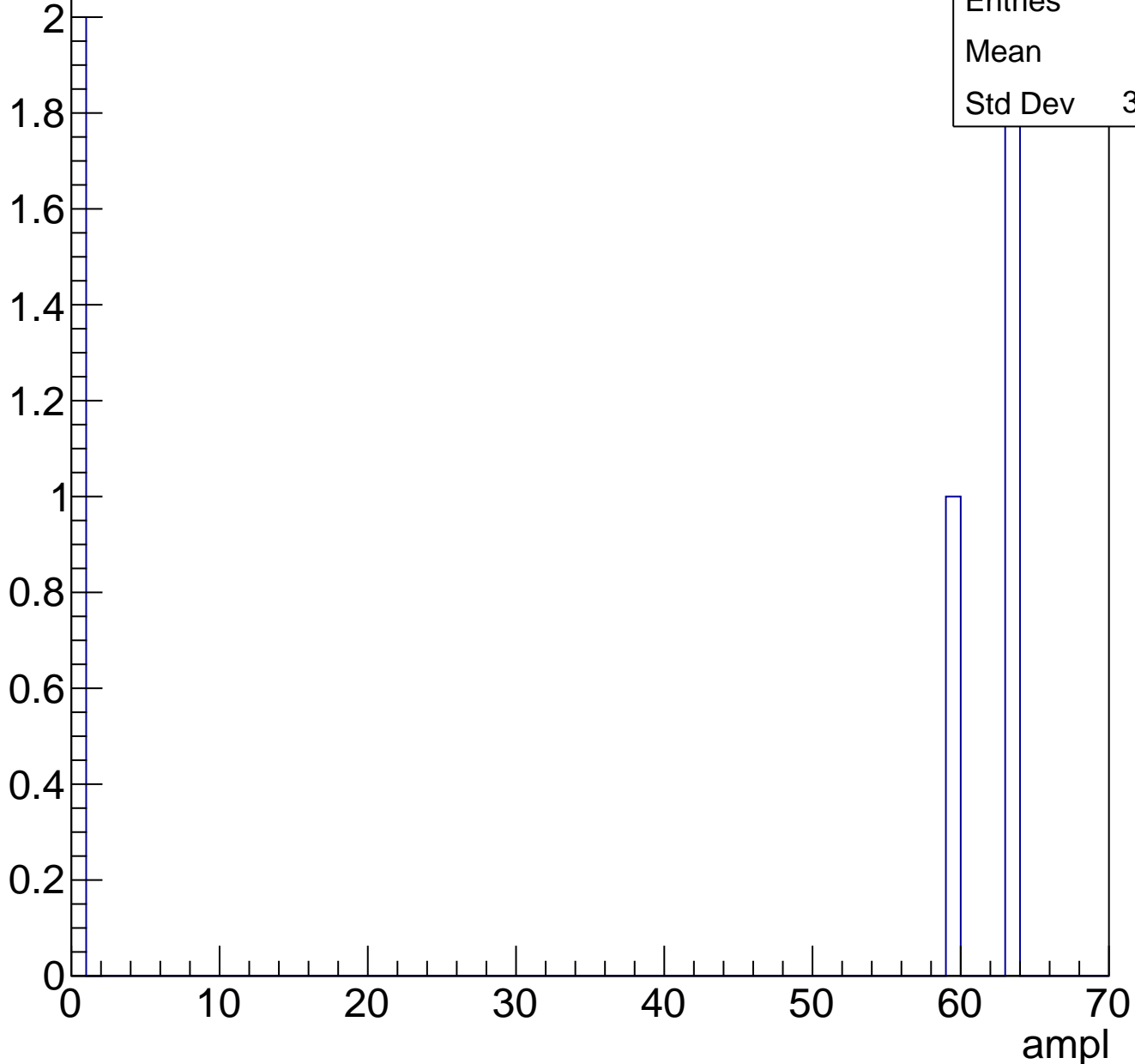
Entries	21
Mean	58.43
Std Dev	13.15



B1L103S, U20-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch69, adc0

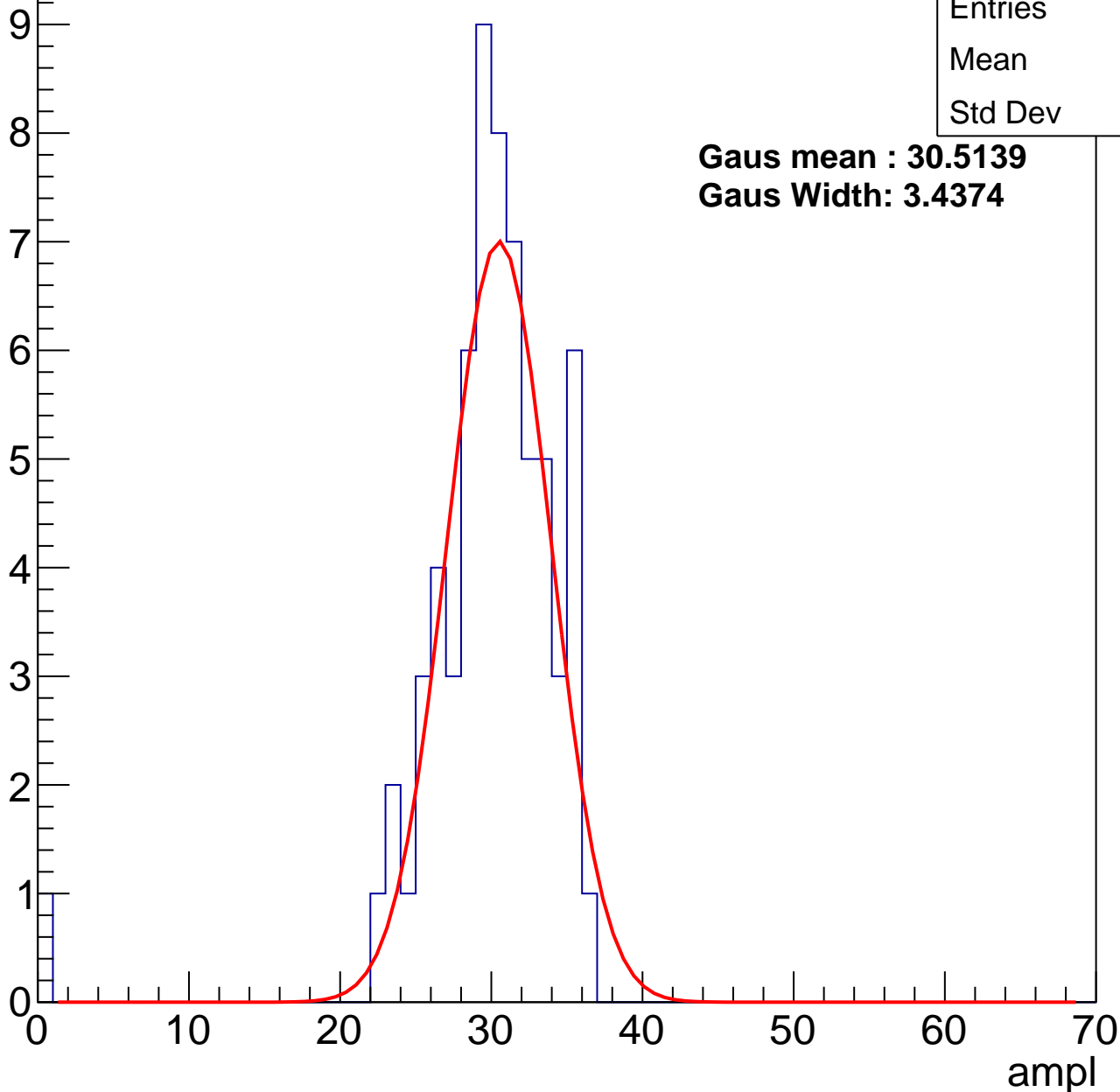
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.4
Std Dev	4.93

Gaus mean : 30.5139

Gaus Width: 3.4374



B1L103S, U20-ch69, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	36.51
Std Dev	3.429

Gaus mean : 37.1802

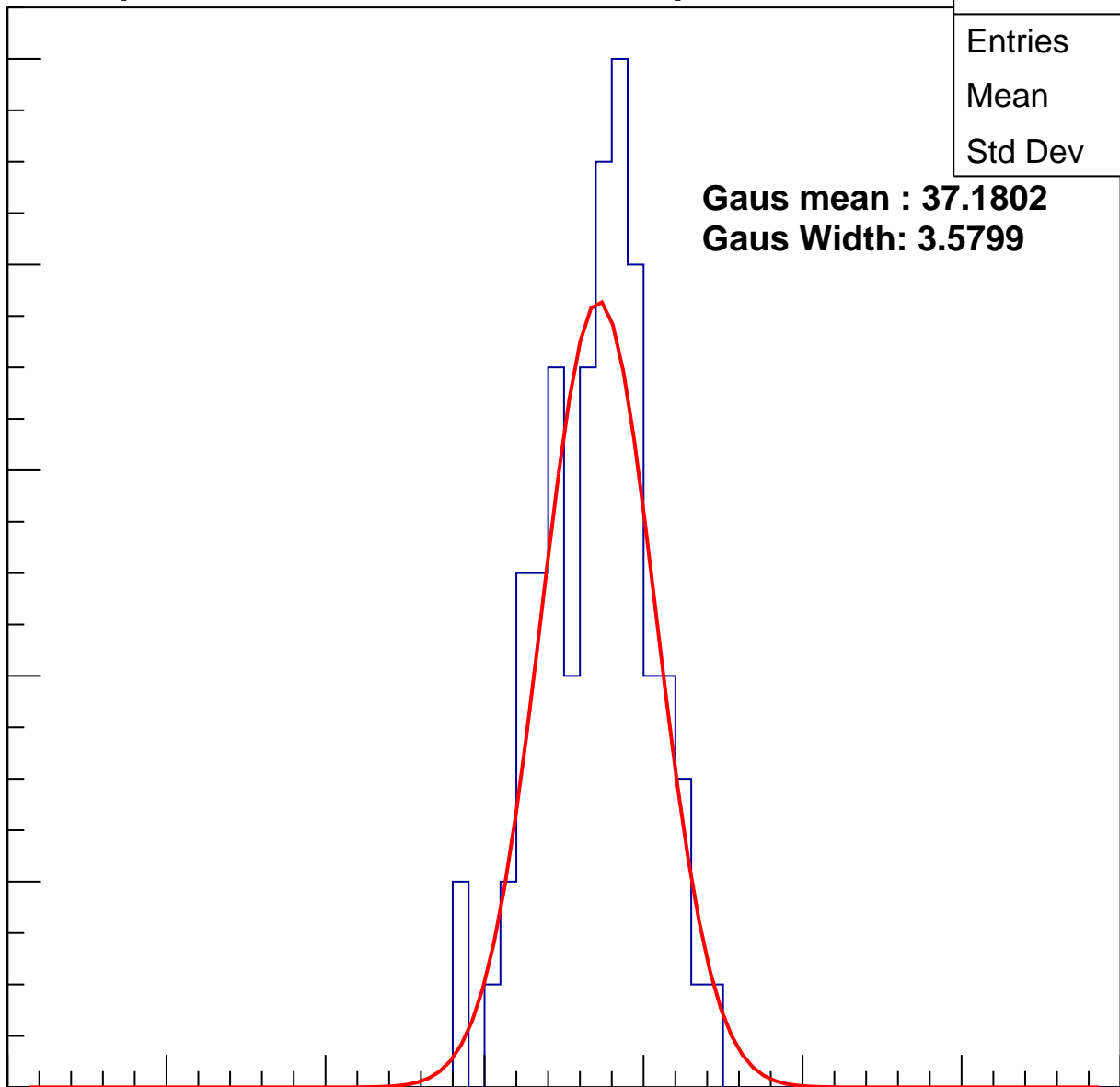
Gaus Width: 3.5799

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch69, adc2

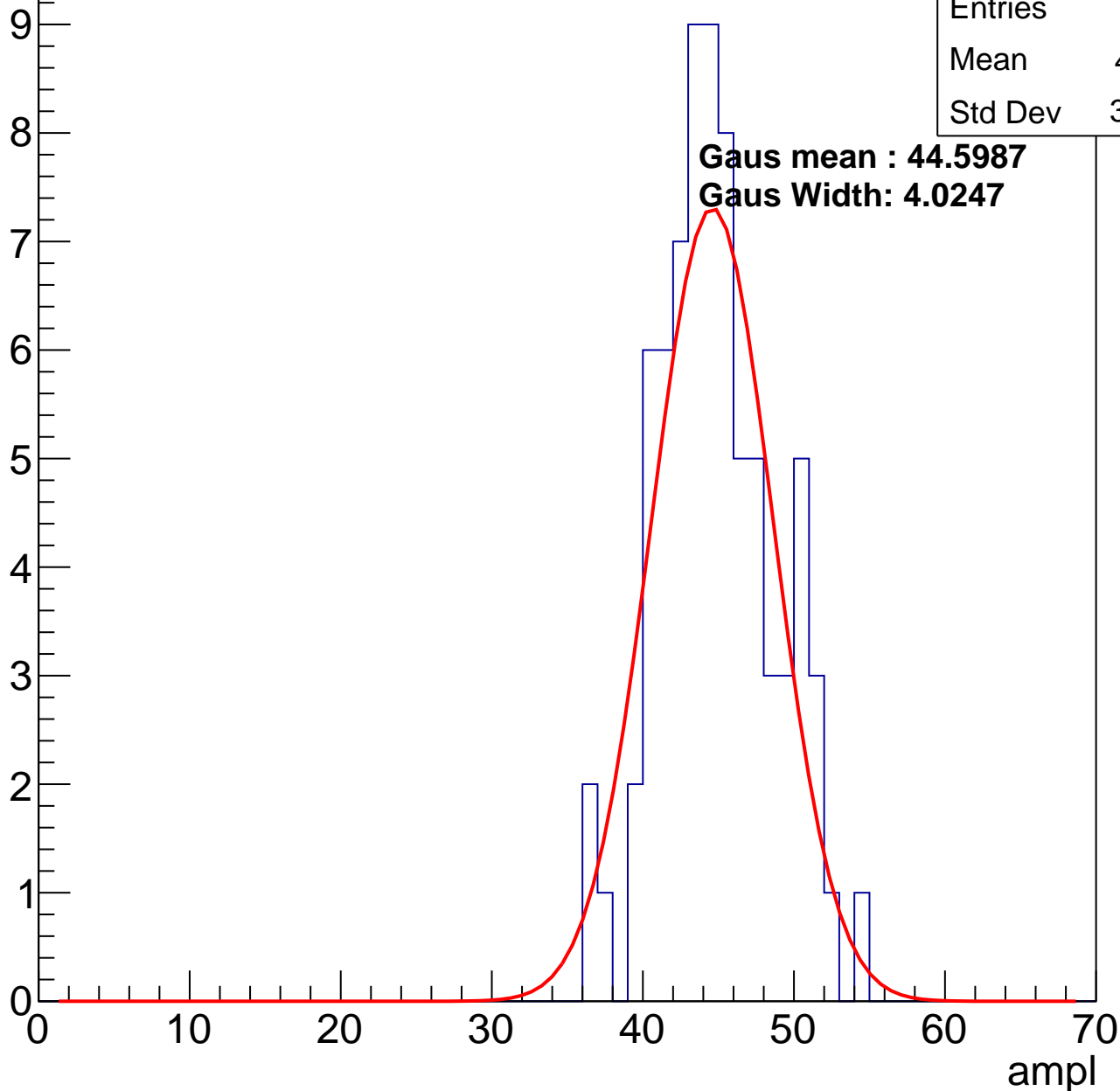
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	44.41
Std Dev	3.784

Gaus mean : 44.5987

Gaus Width: 4.0247

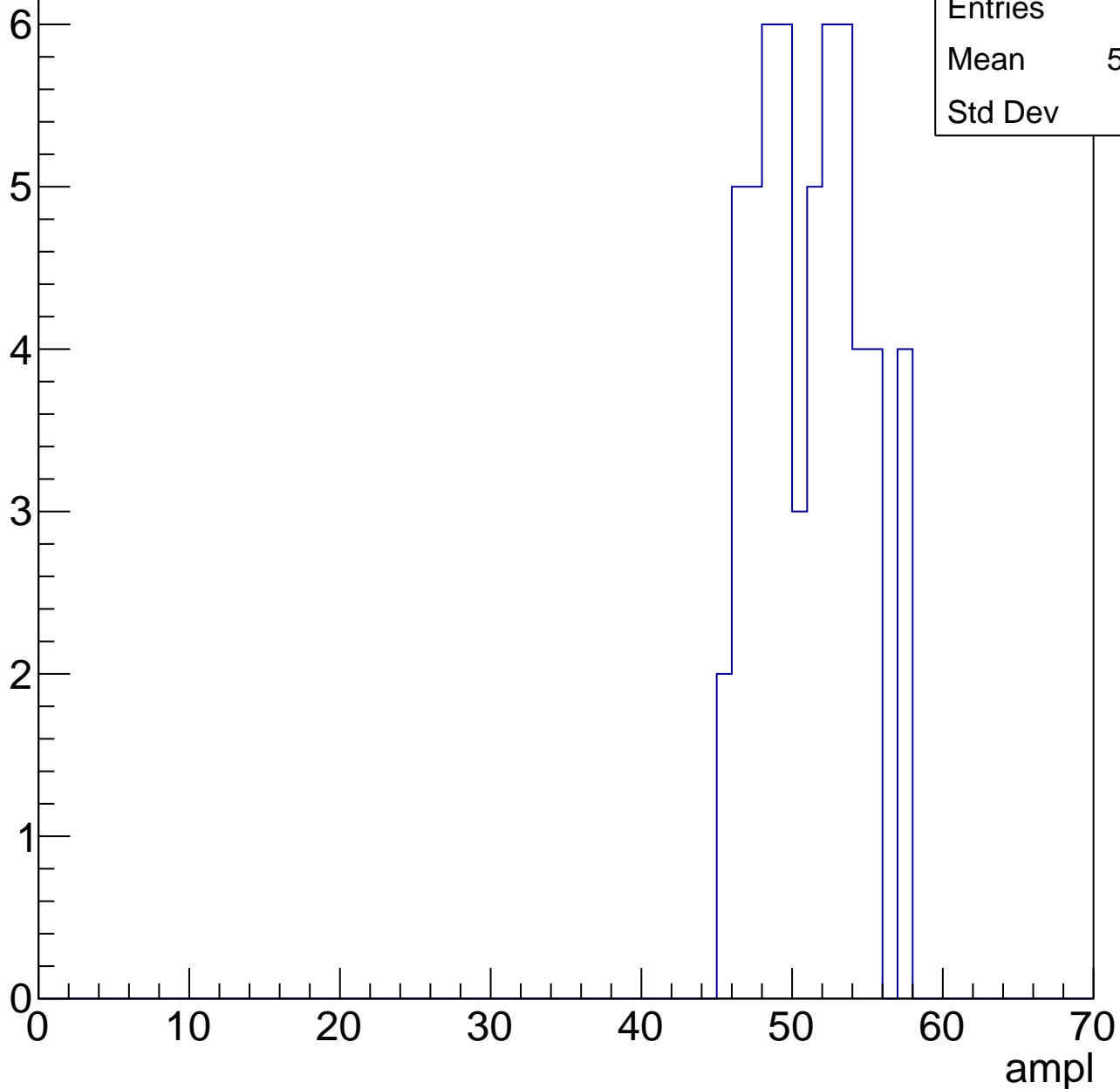


B1L103S, U20-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

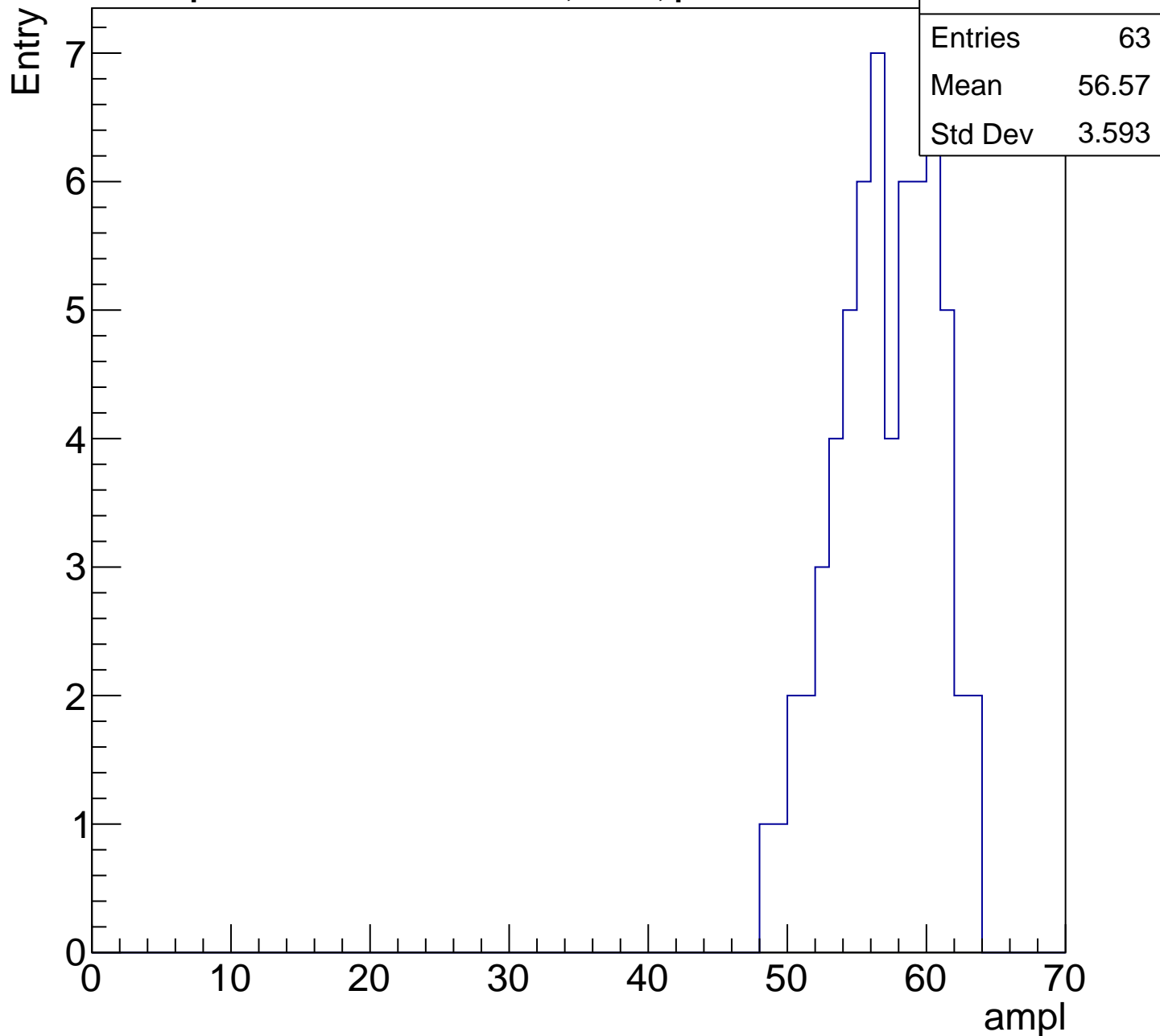
Entry

Entries	56
Mean	50.64
Std Dev	3.34



B1L103S, U20-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

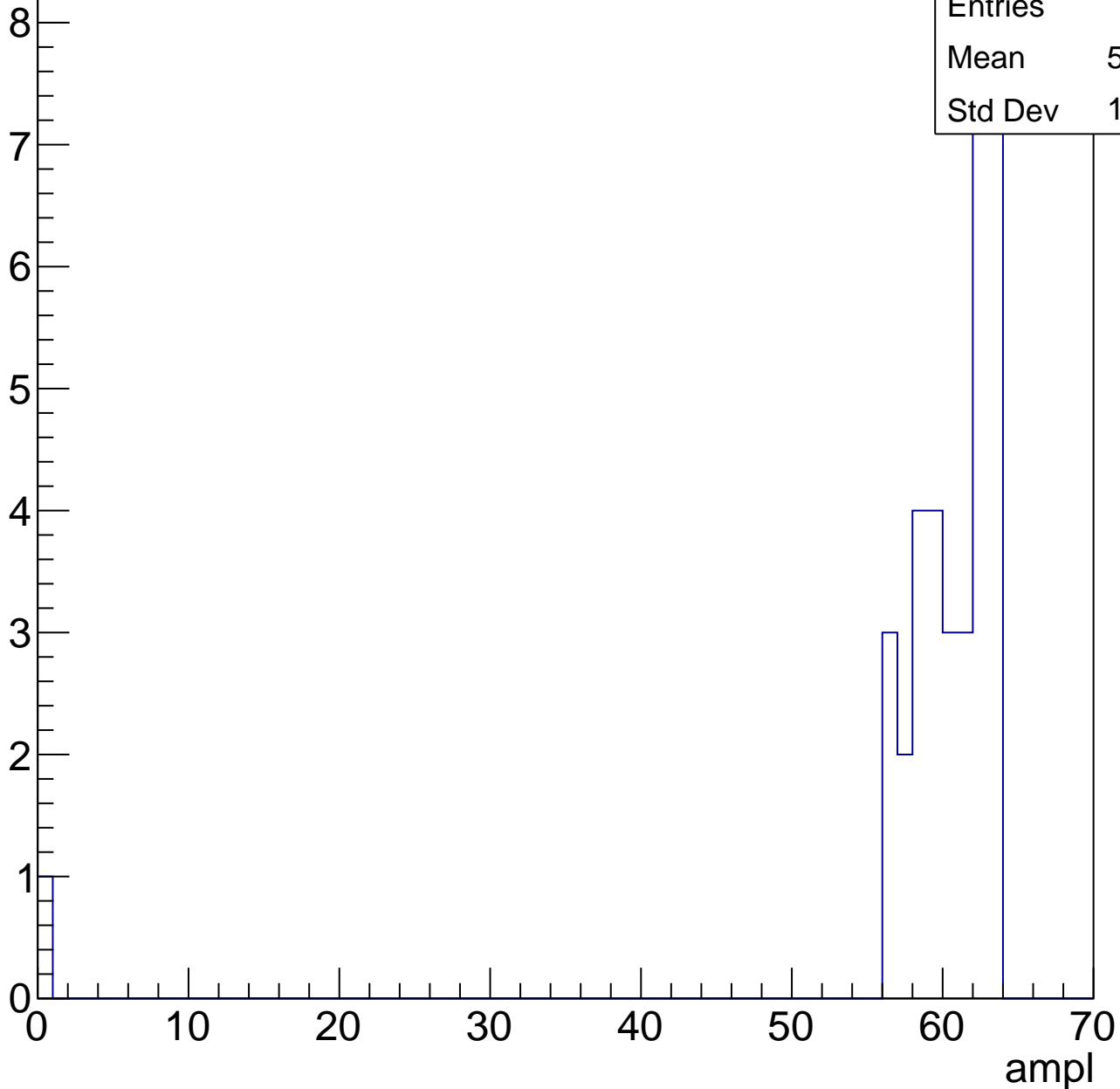


B1L103S, U20-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.69
Std Dev	10.18



B1L103S, U20-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	61
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L103S, U20-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U20-ch70, adc0

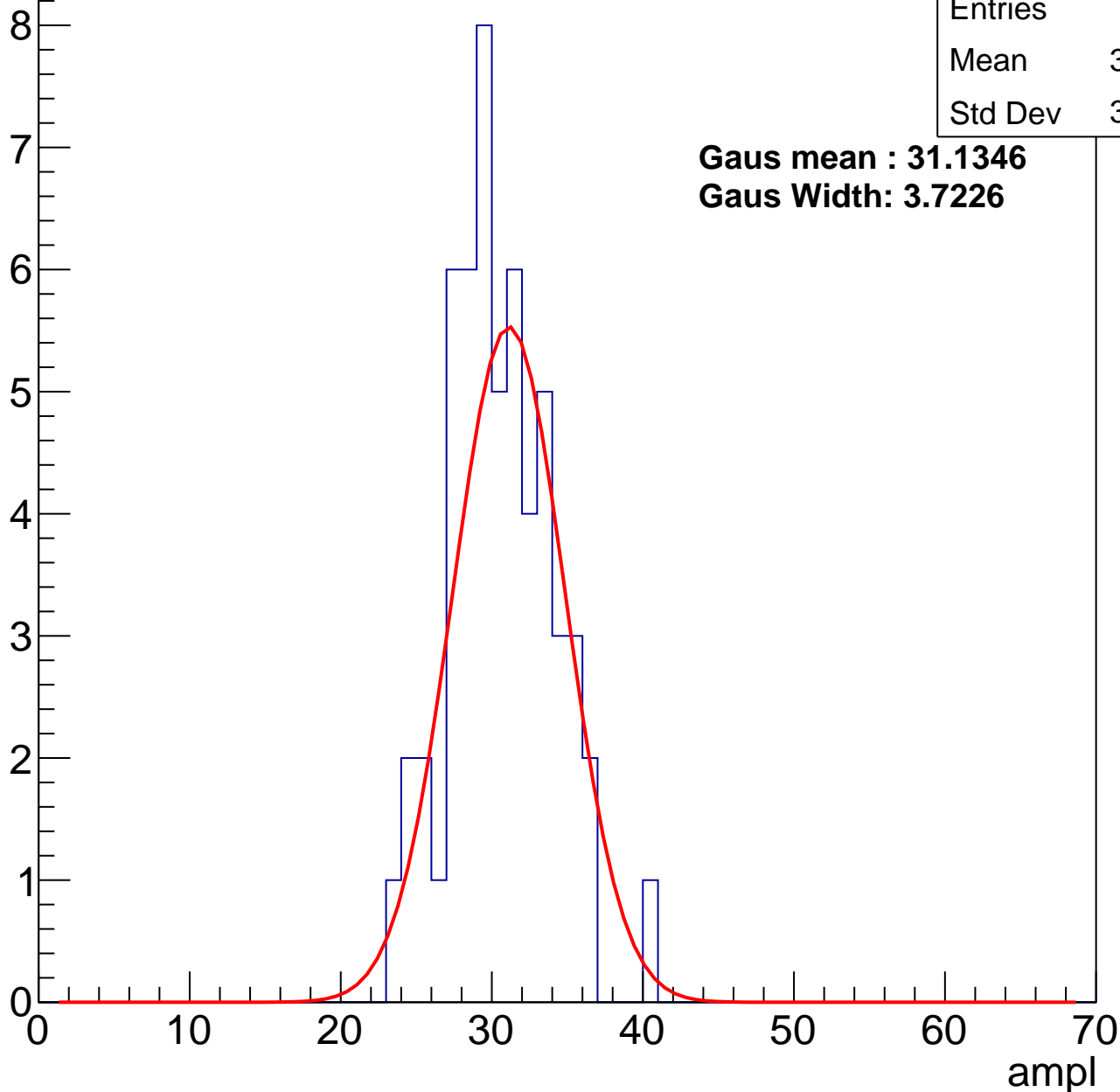
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	30.13
Std Dev	3.395

Gaus mean : 31.1346

Gaus Width: 3.7226



B1L103S, U20-ch70, adc1

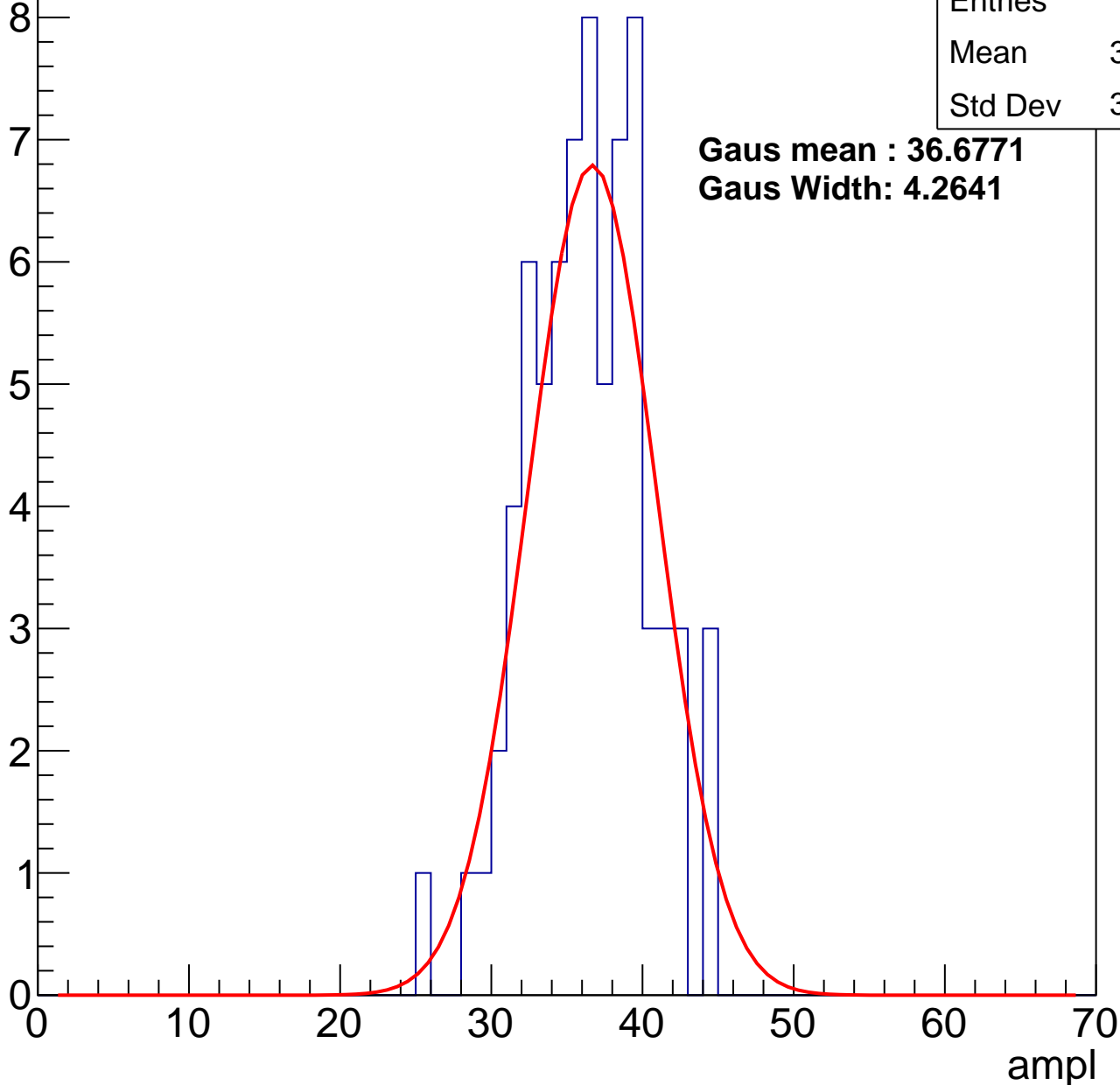
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.95
Std Dev	3.892

Gaus mean : 36.6771

Gaus Width: 4.2641



B1L103S, U20-ch70, adc2

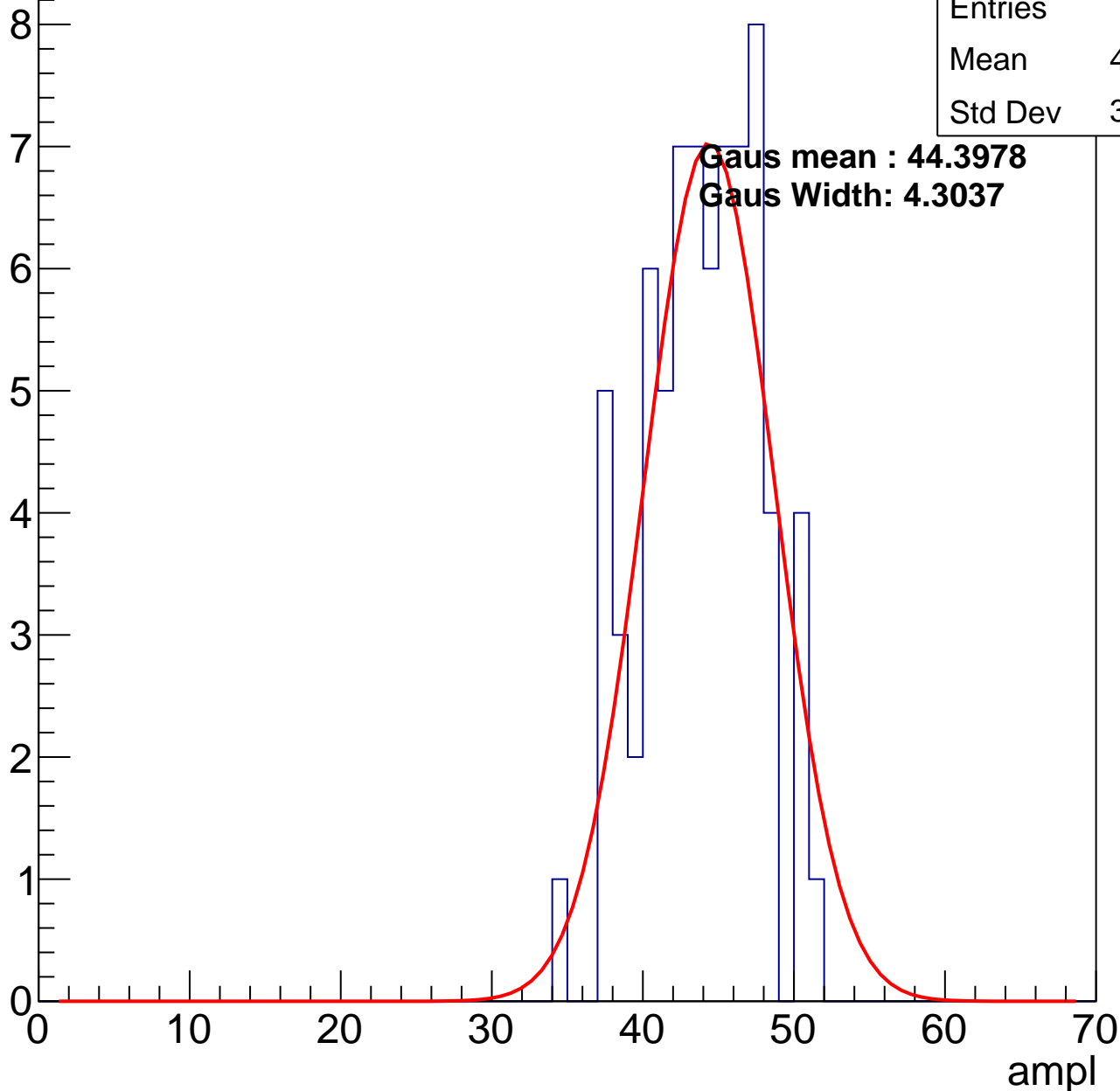
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.44
Std Dev	3.738

Gaus mean : 44.3978

Gaus Width: 4.3037

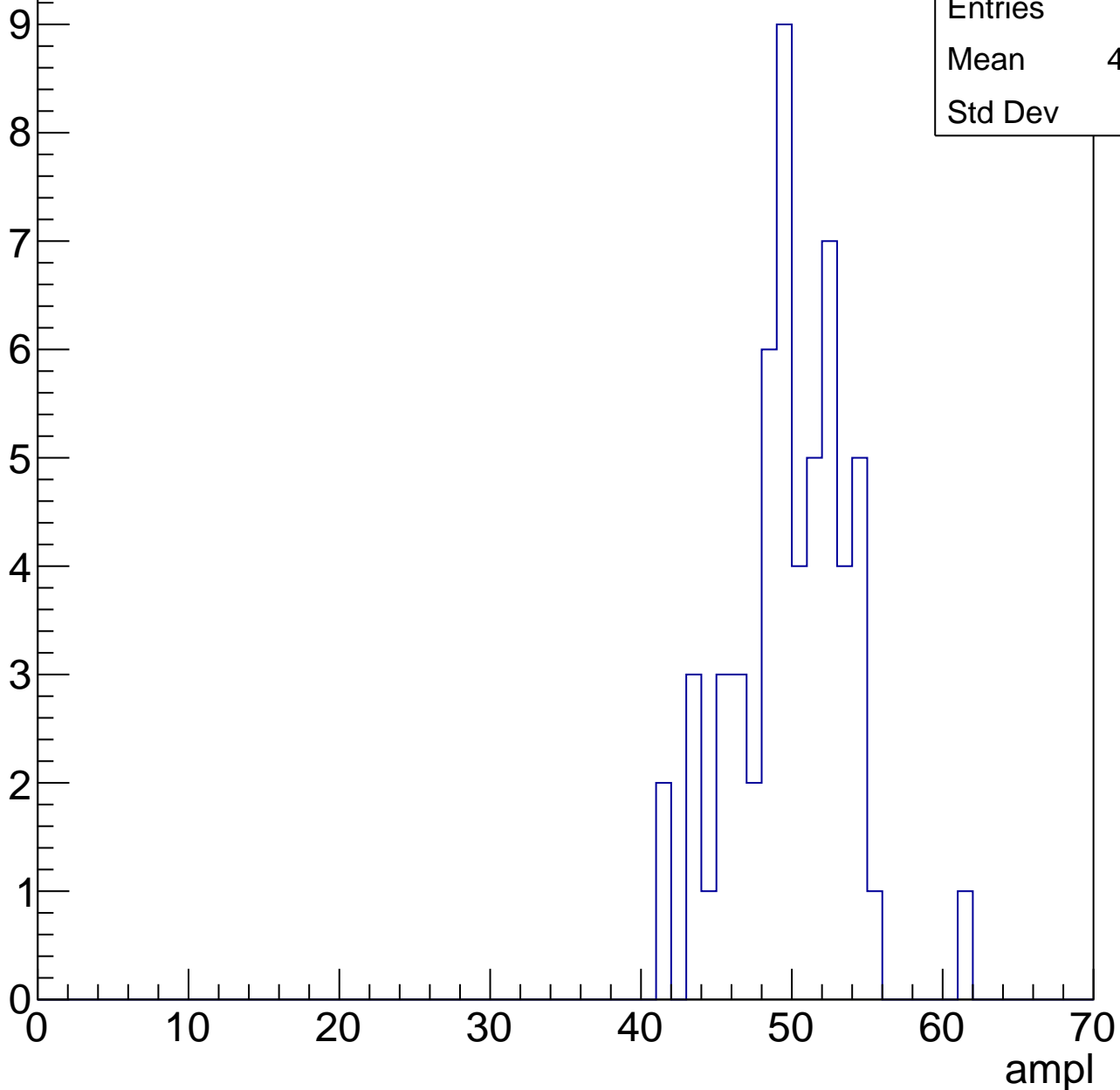


B1L103S, U20-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.43
Std Dev	3.77

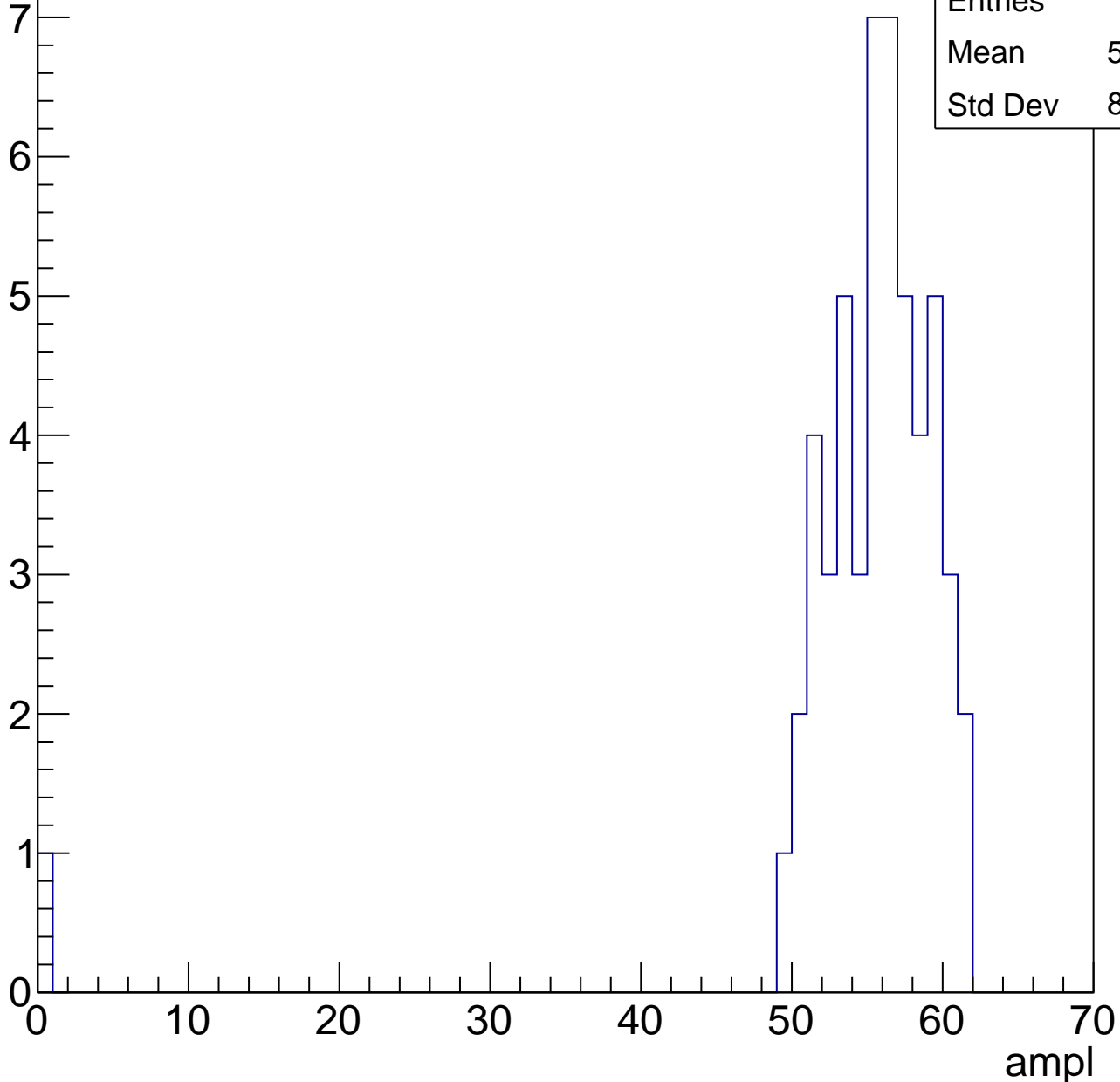


B1L103S, U20-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.37
Std Dev	8.197

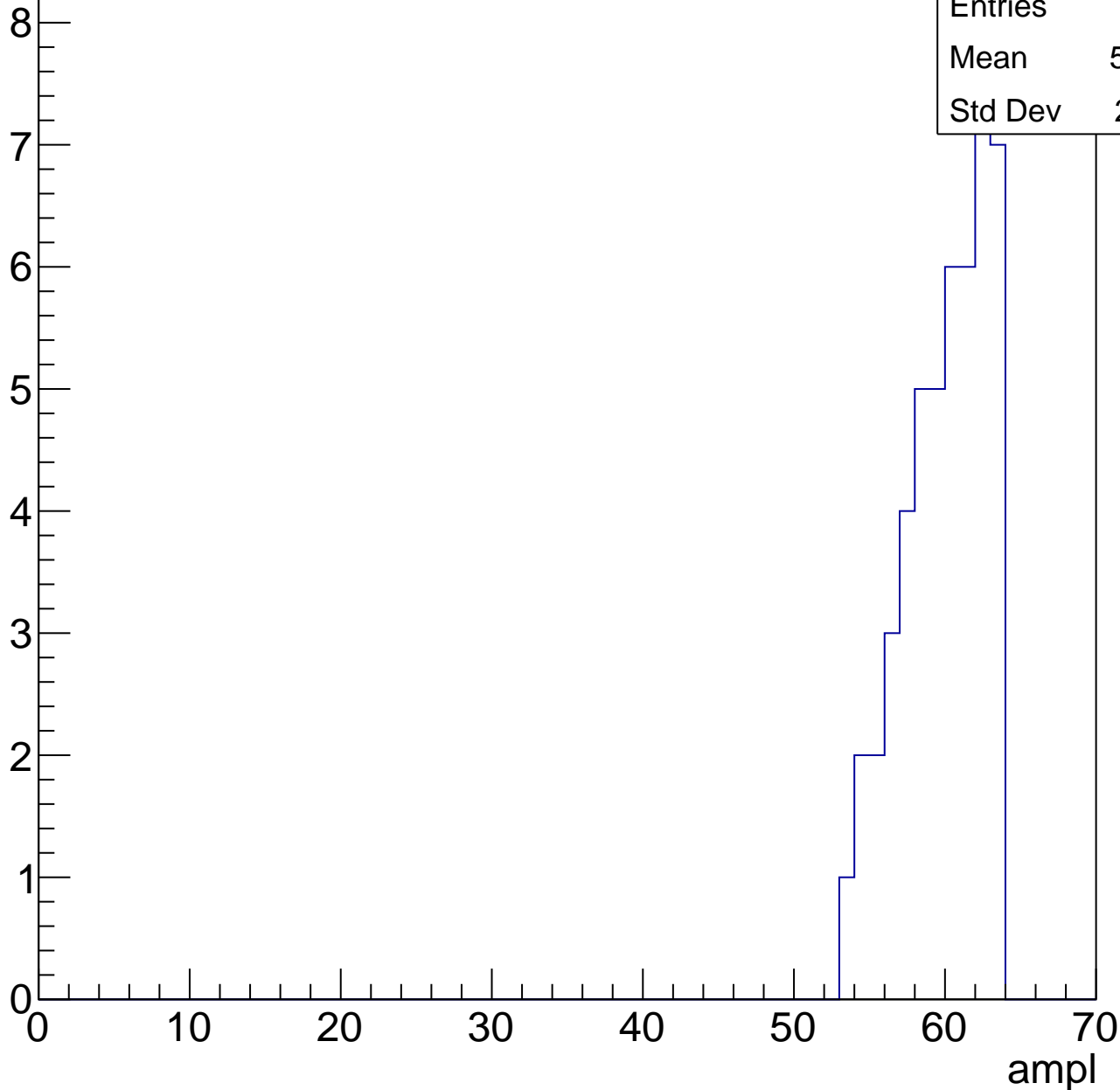


B1L103S, U20-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.49
Std Dev	2.741



B1L103S, U20-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

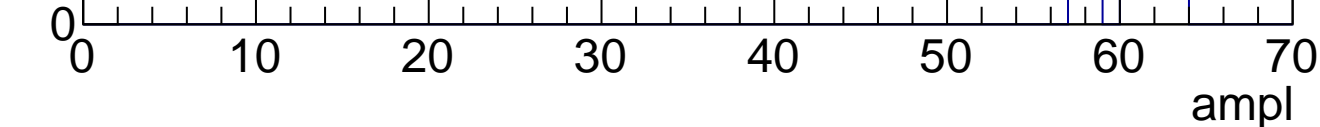
9

Mean

61

Std Dev

2.108



B1L103S, U20-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch71, adc0

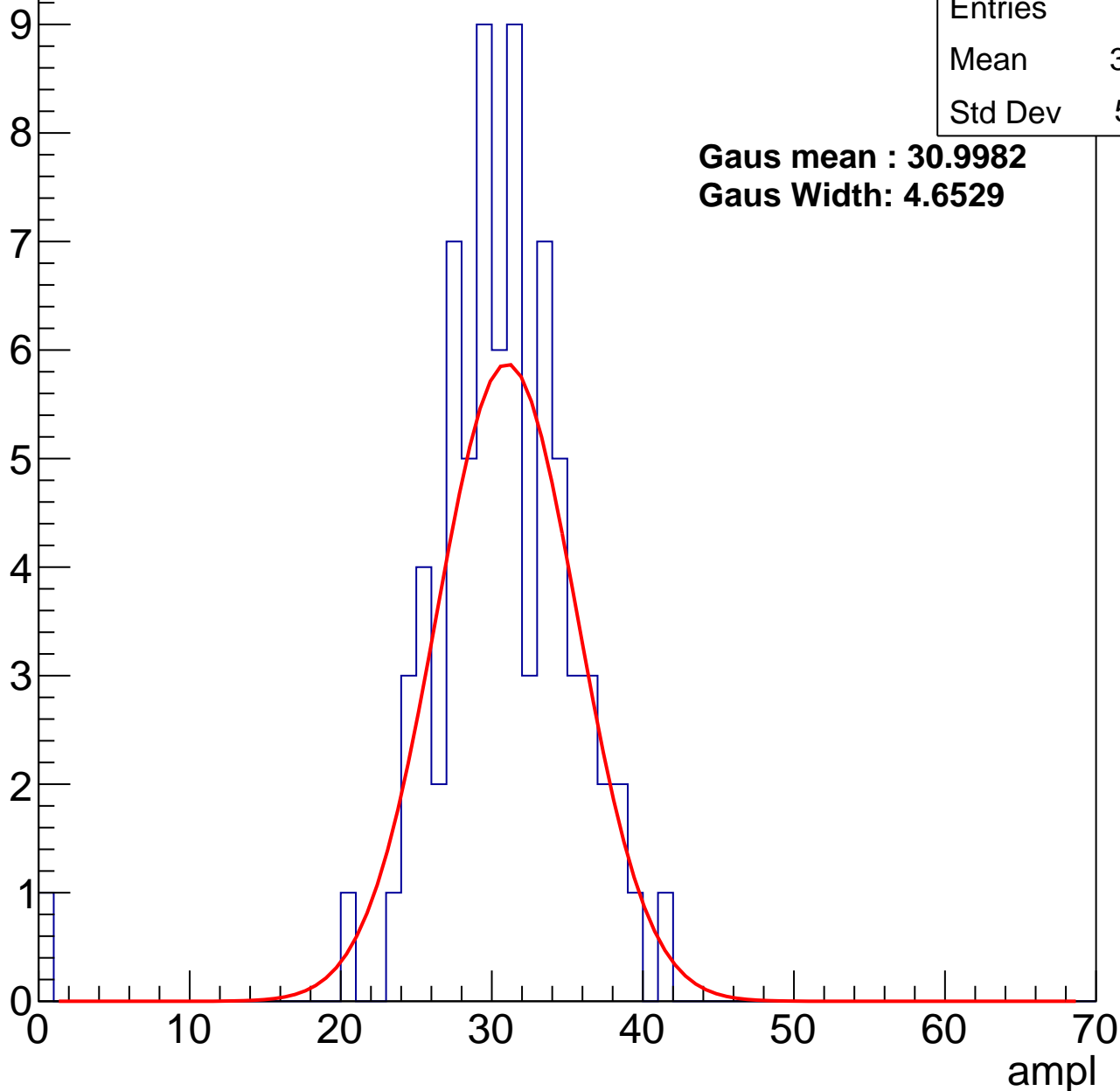
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	30.08
Std Dev	5.361

Gaus mean : 30.9982

Gaus Width: 4.6529



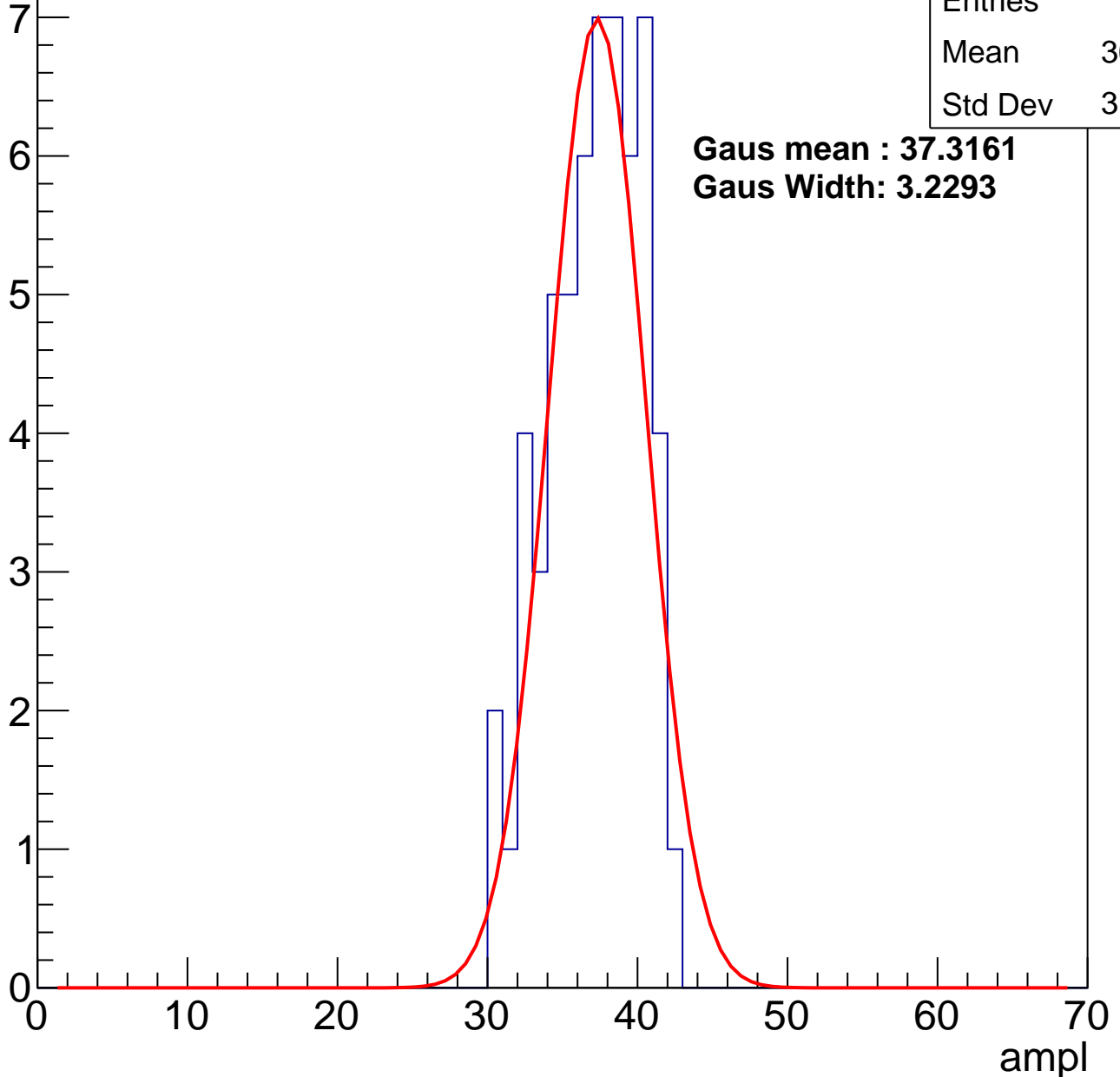
B1L103S, U20-ch71, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.62
Std Dev	3.016

Gaus mean : 37.3161
Gaus Width: 3.2293



B1L103S, U20-ch71, adc2

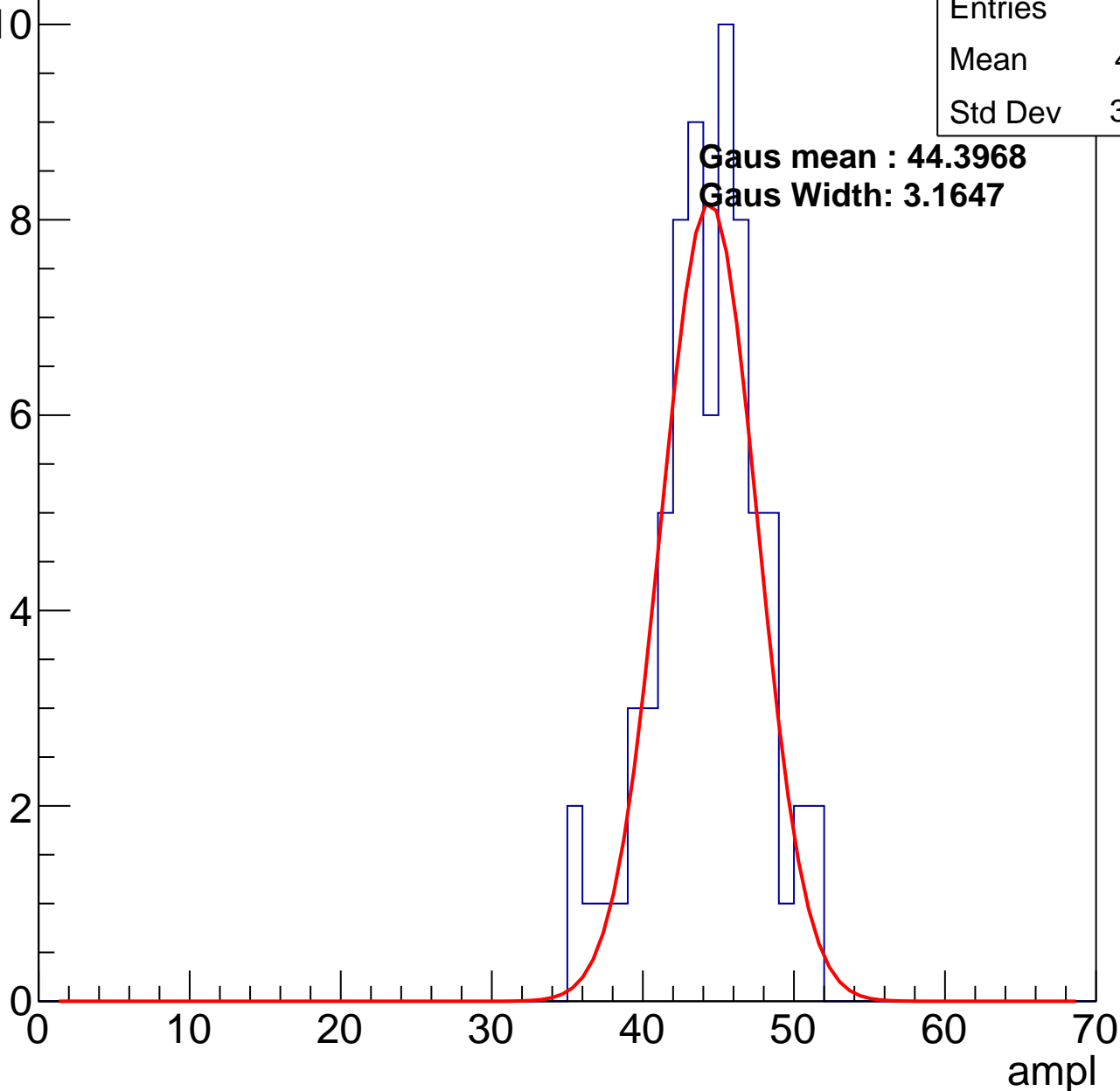
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.81
Std Dev	3.495

Gaus mean : 44.3968

Gaus Width: 3.1647

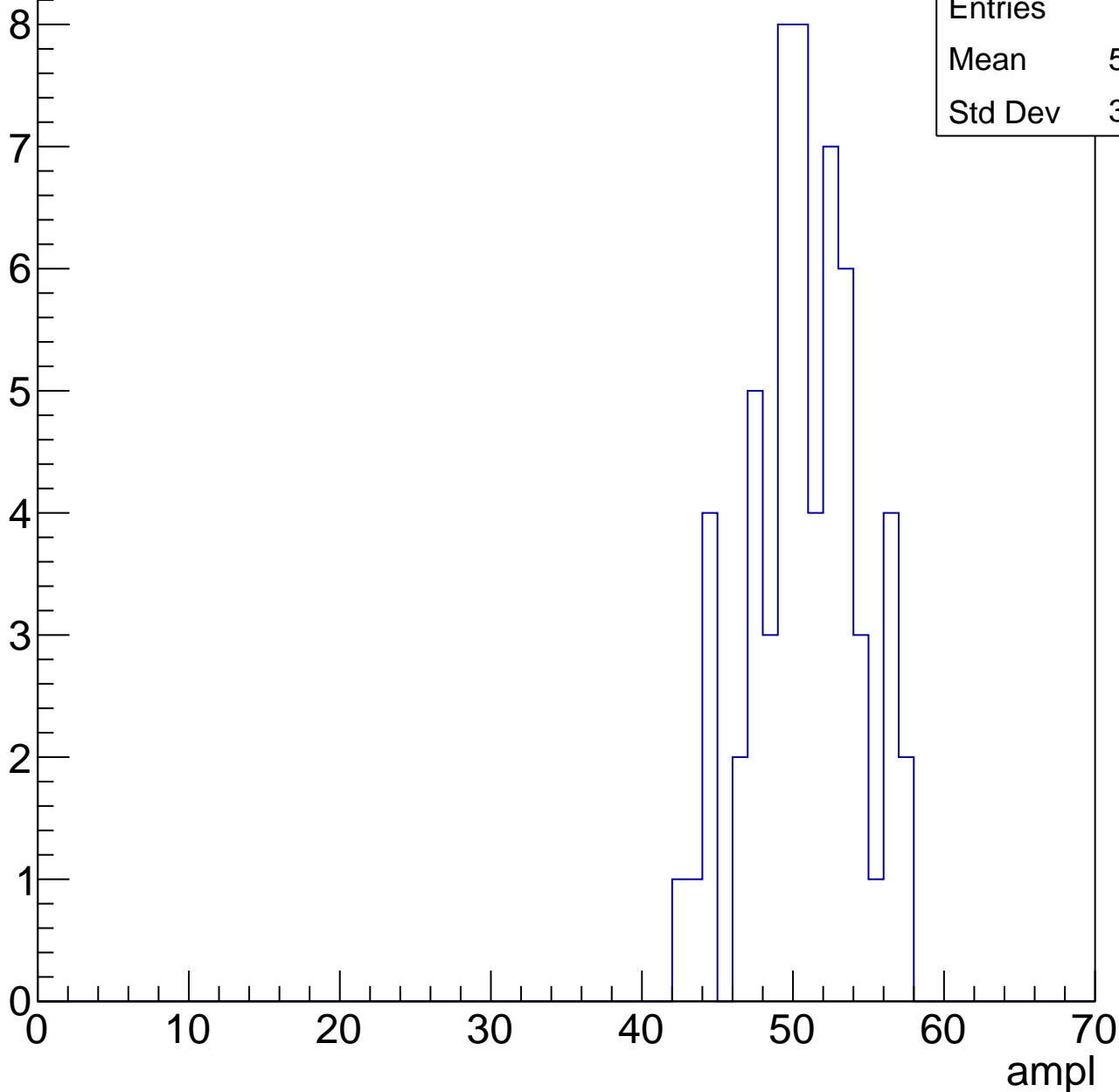


B1L103S, U20-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.25
Std Dev	3.568

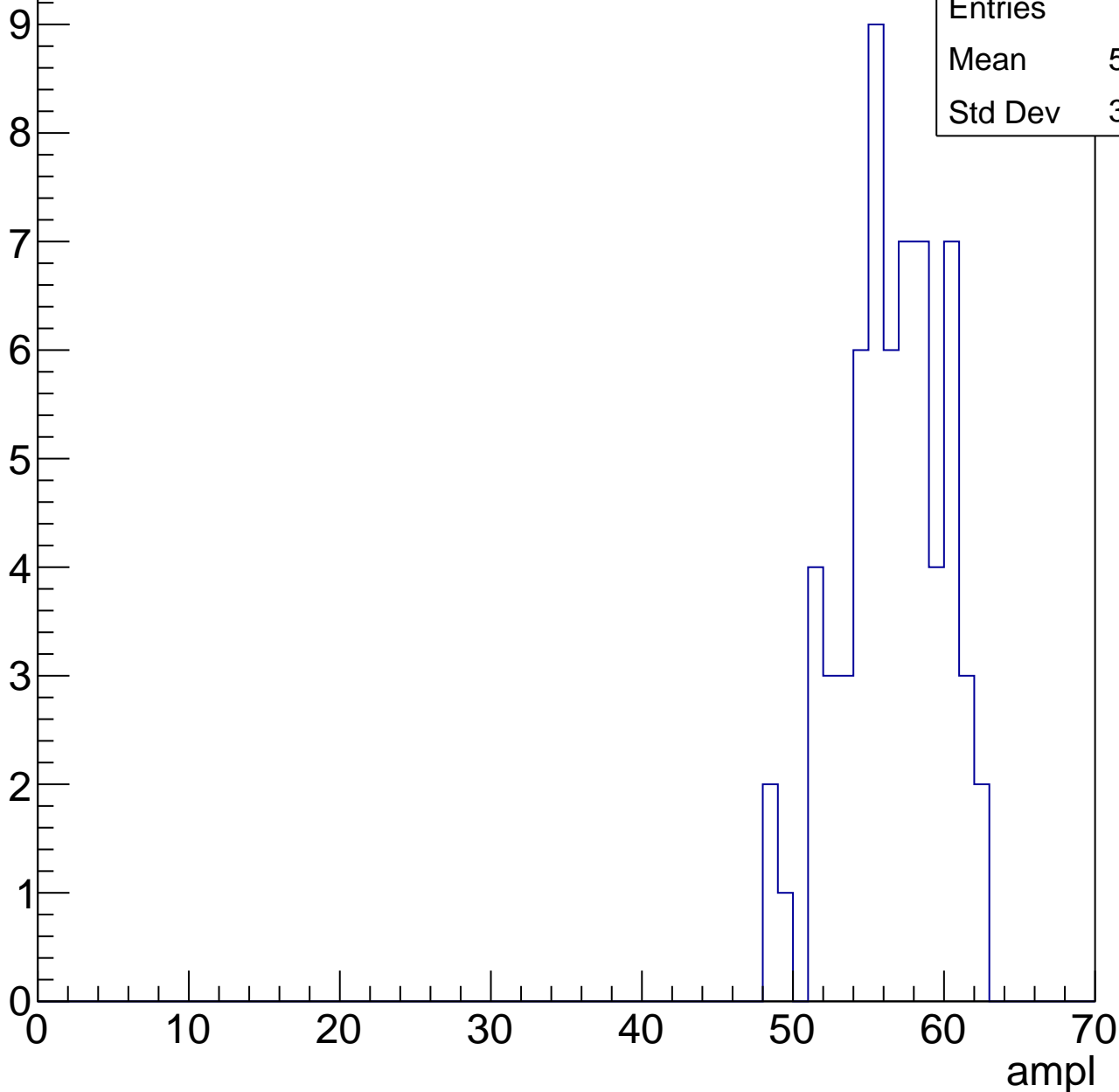


B1L103S, U20-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	56.05
Std Dev	3.342

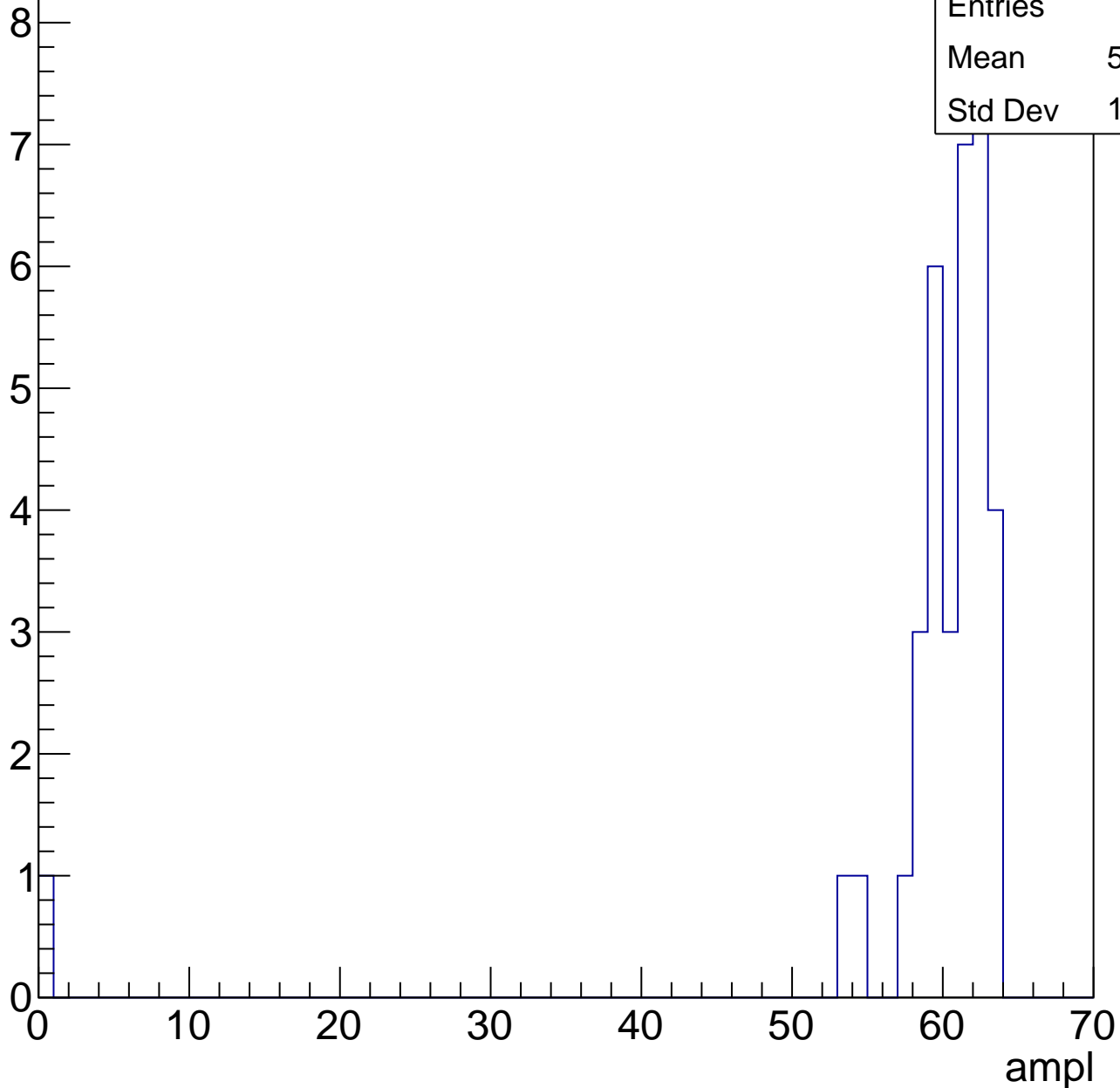


B1L103S, U20-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.49
Std Dev	10.29



B1L103S, U20-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U20-ch72, adc0

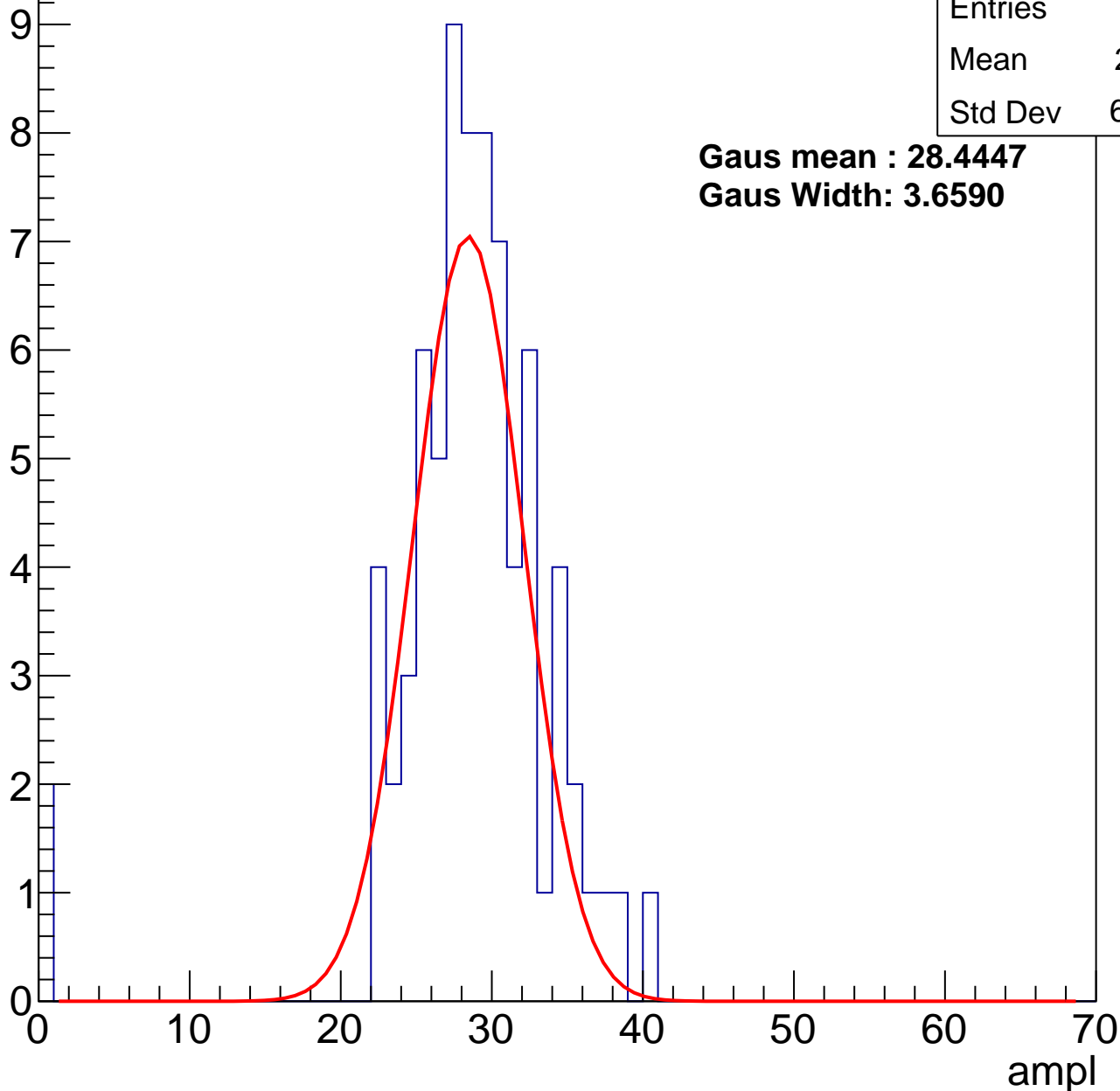
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.01
Std Dev	6.019

Gaus mean : 28.4447

Gaus Width: 3.6590



B1L103S, U20-ch72, adc1

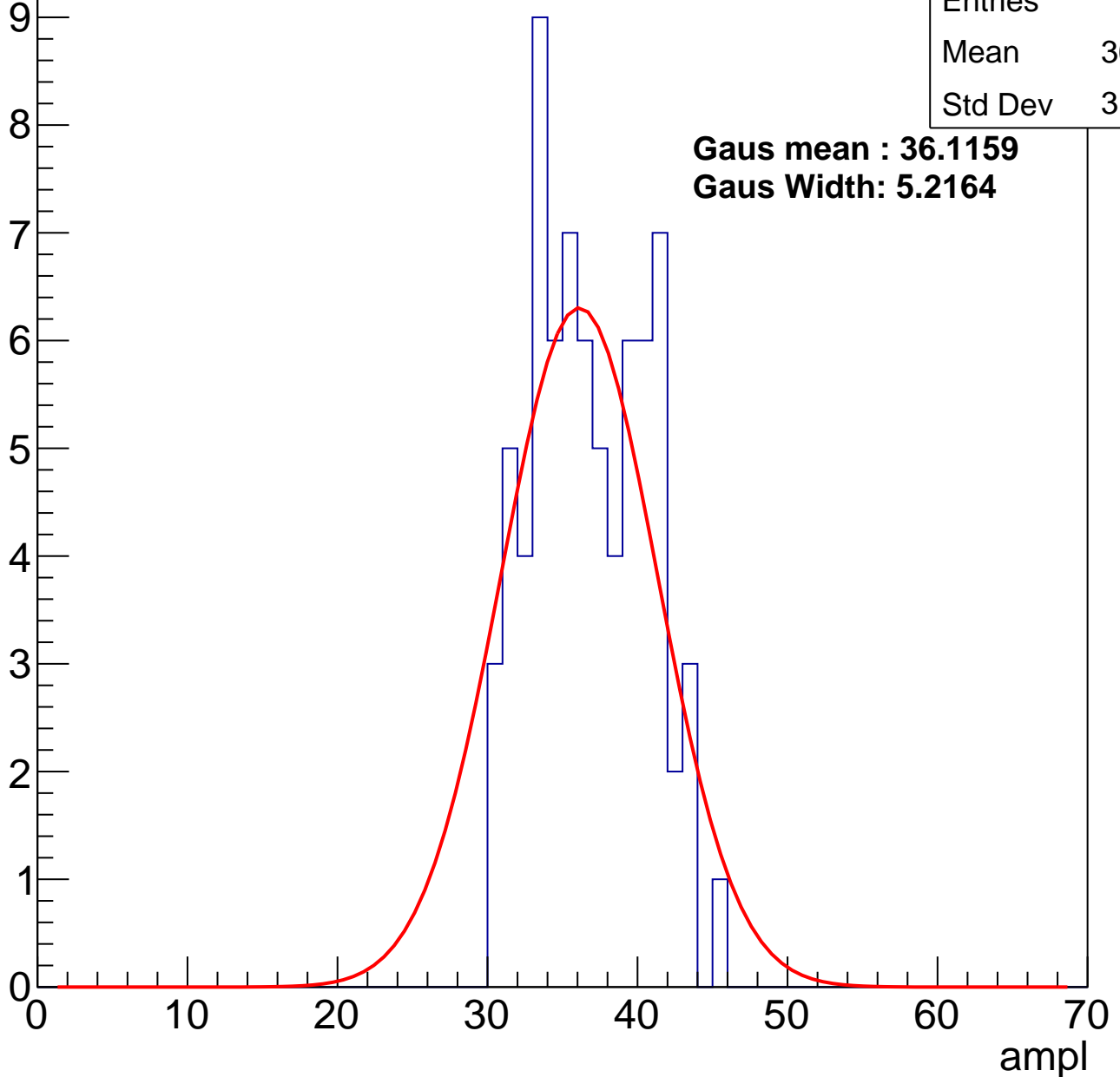
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.36
Std Dev	3.758

Gaus mean : 36.1159

Gaus Width: 5.2164



B1L103S, U20-ch72, adc2

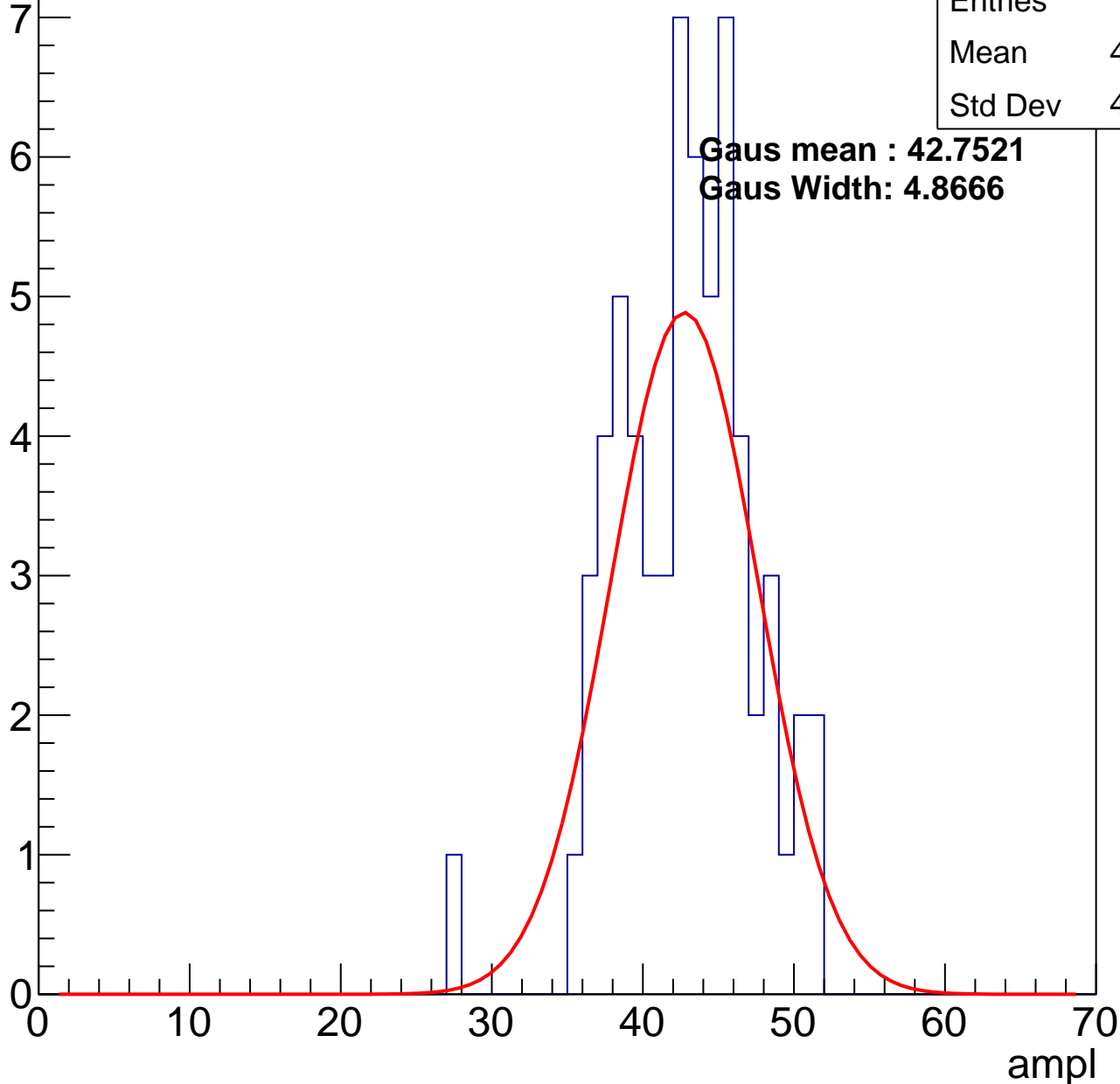
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.33
Std Dev	4.465

Gaus mean : 42.7521

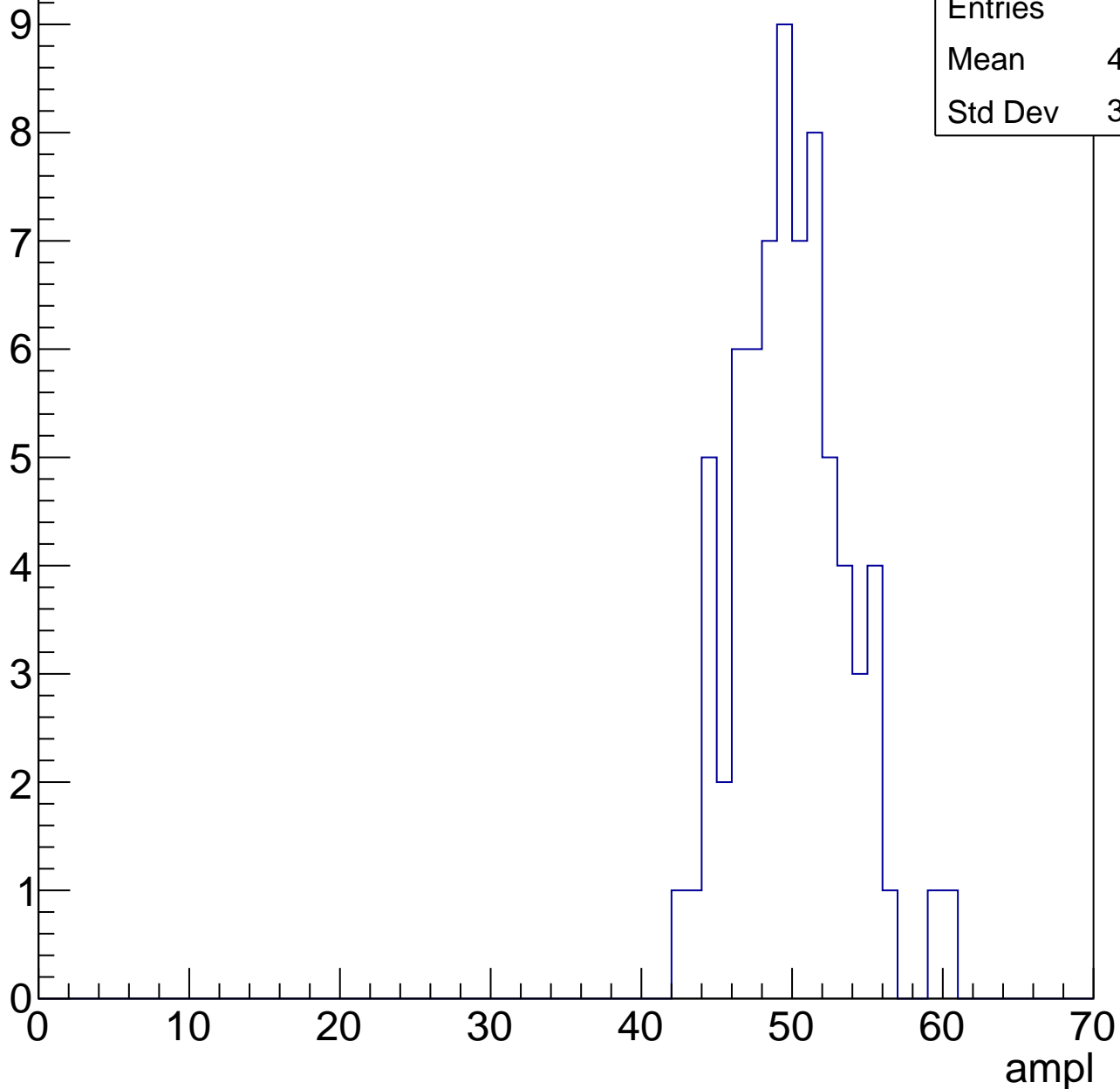
Gaus Width: 4.8666



B1L103S, U20-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

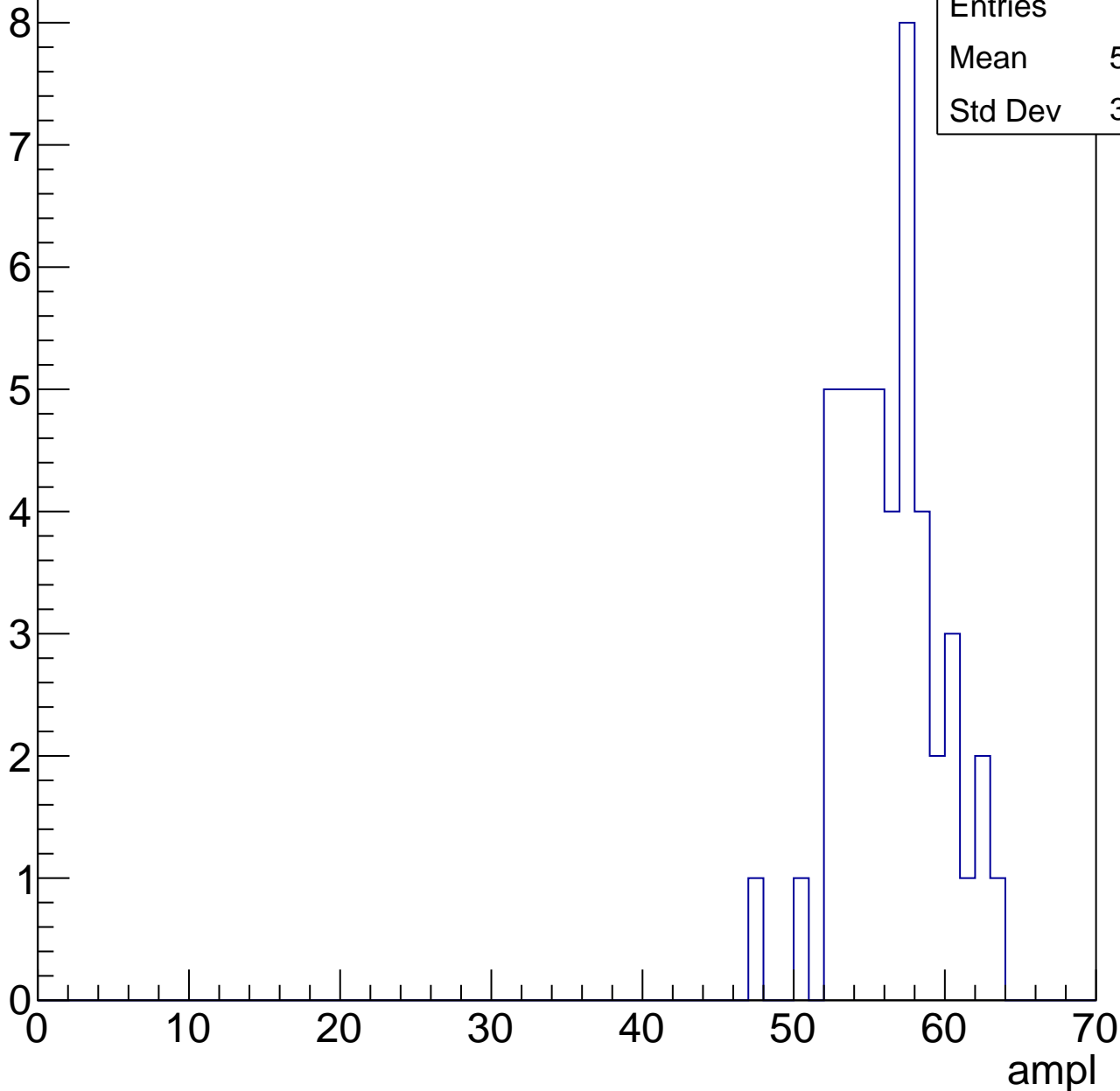


B1L103S, U20-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

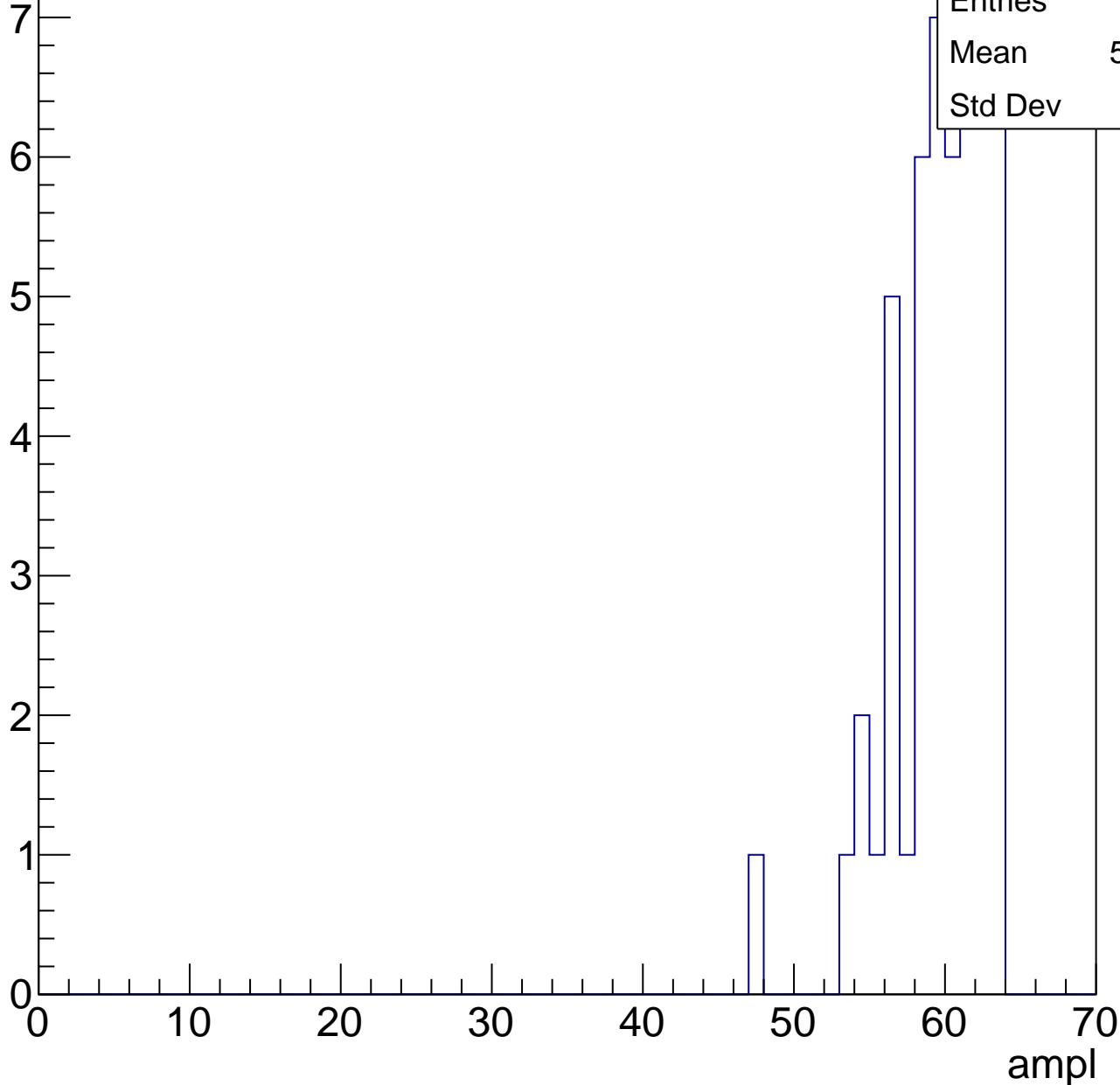
Entries	47
Mean	55.85
Std Dev	3.268



B1L103S, U20-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

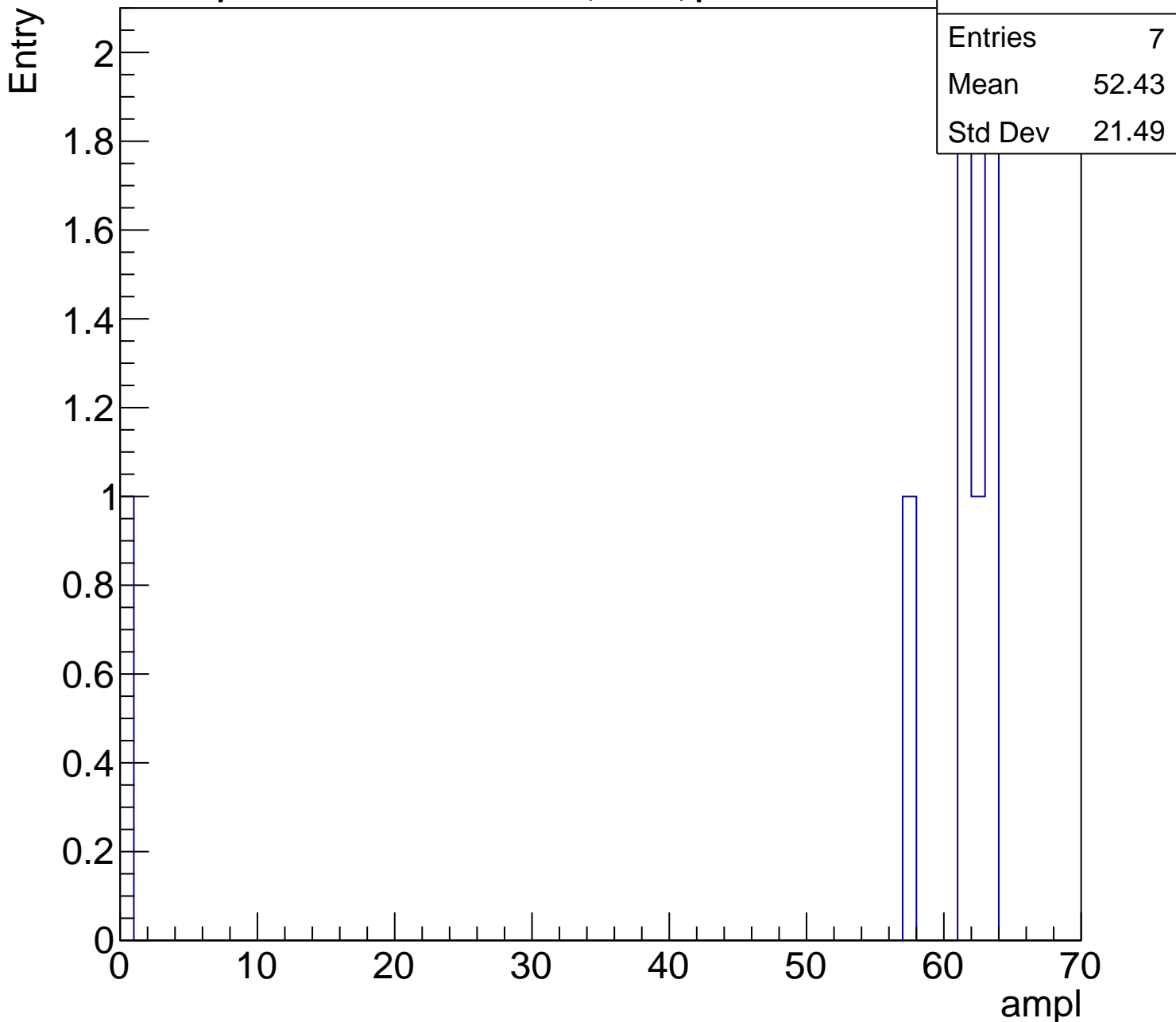
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.43
Std Dev	21.49

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch73, adc0

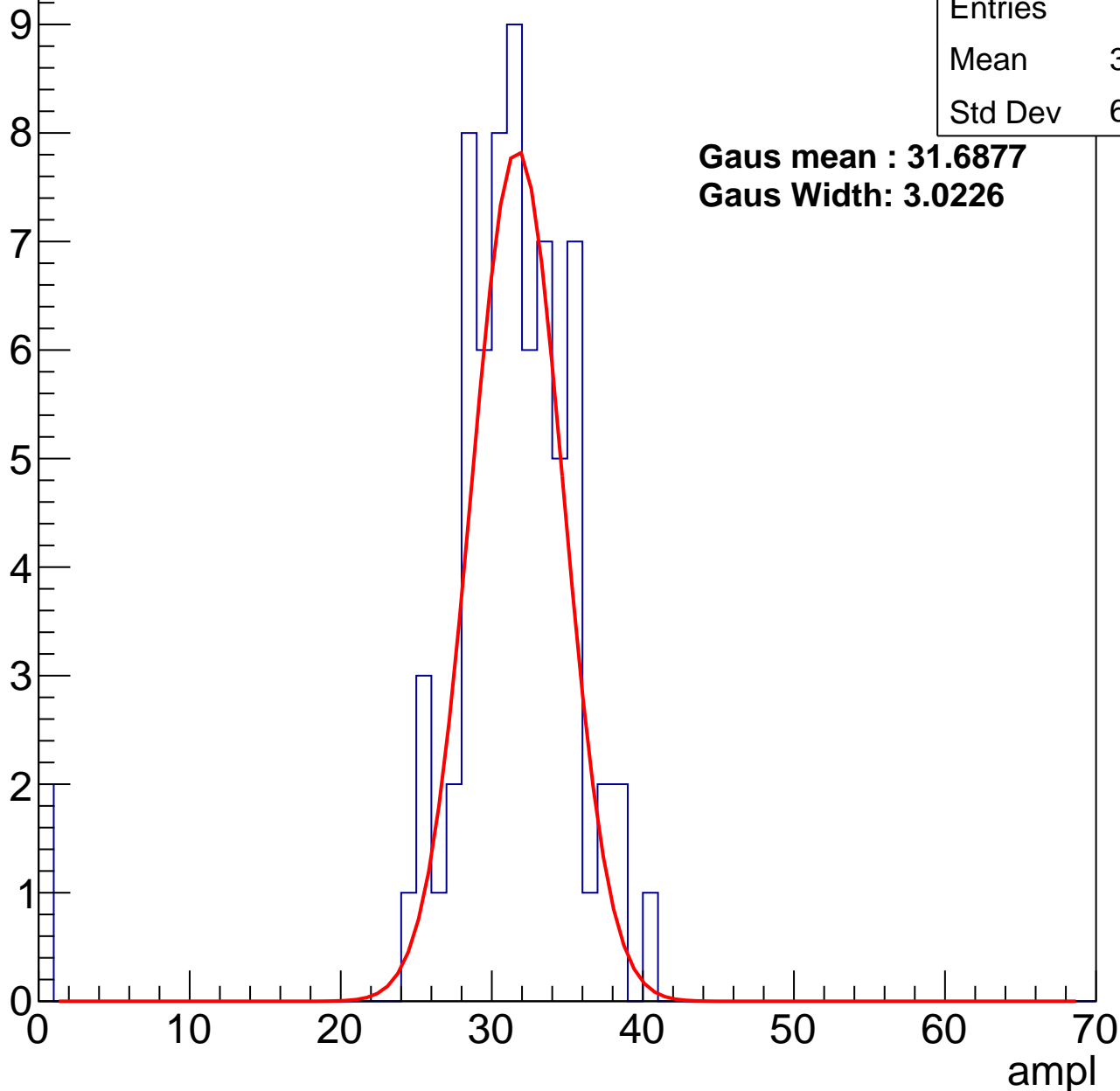
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.42
Std Dev	6.148

Gaus mean : 31.6877

Gaus Width: 3.0226



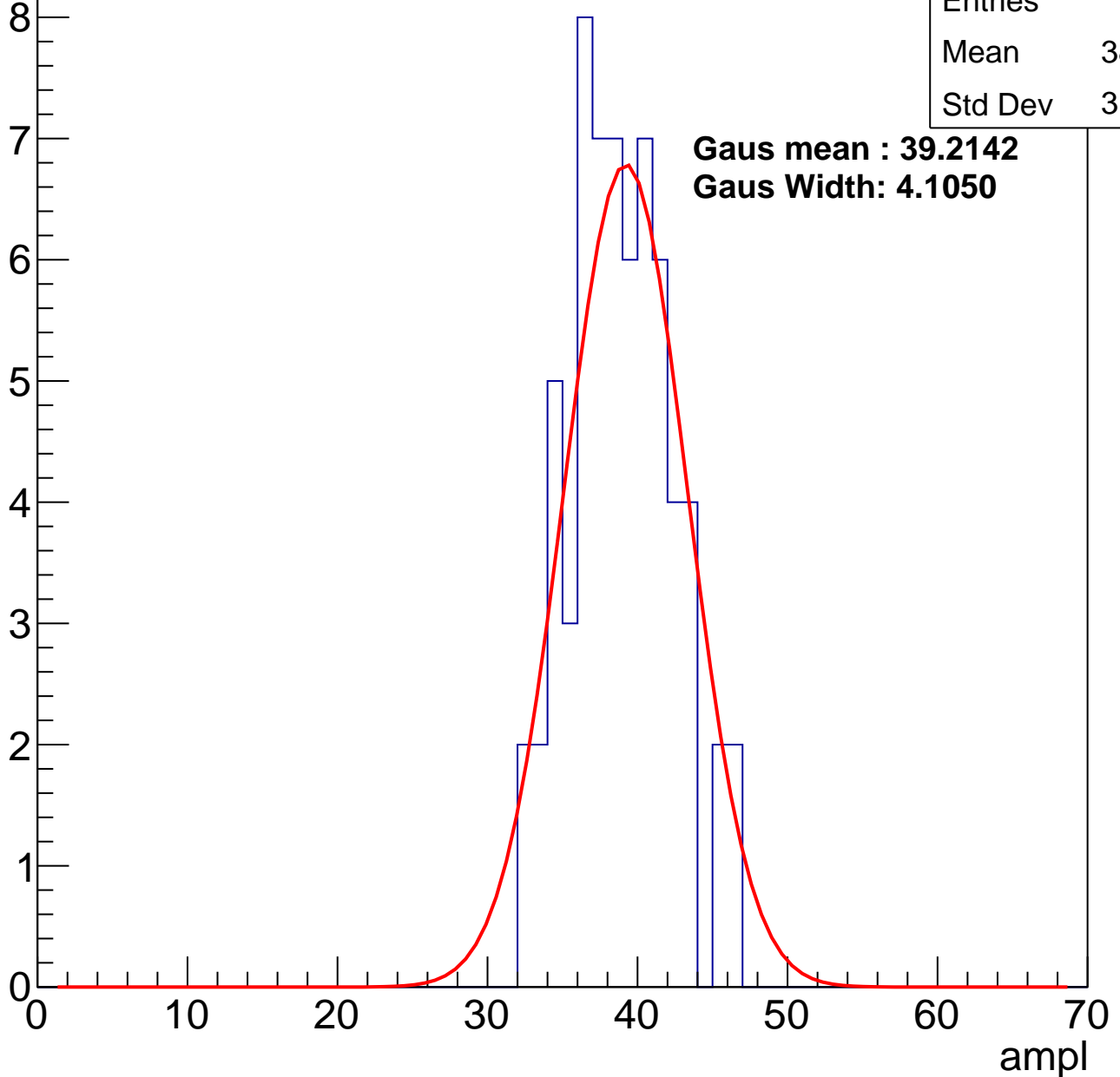
B1L103S, U20-ch73, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	38.46
Std Dev	3.356

Gaus mean : 39.2142
Gaus Width: 4.1050



B1L103S, U20-ch73, adc2

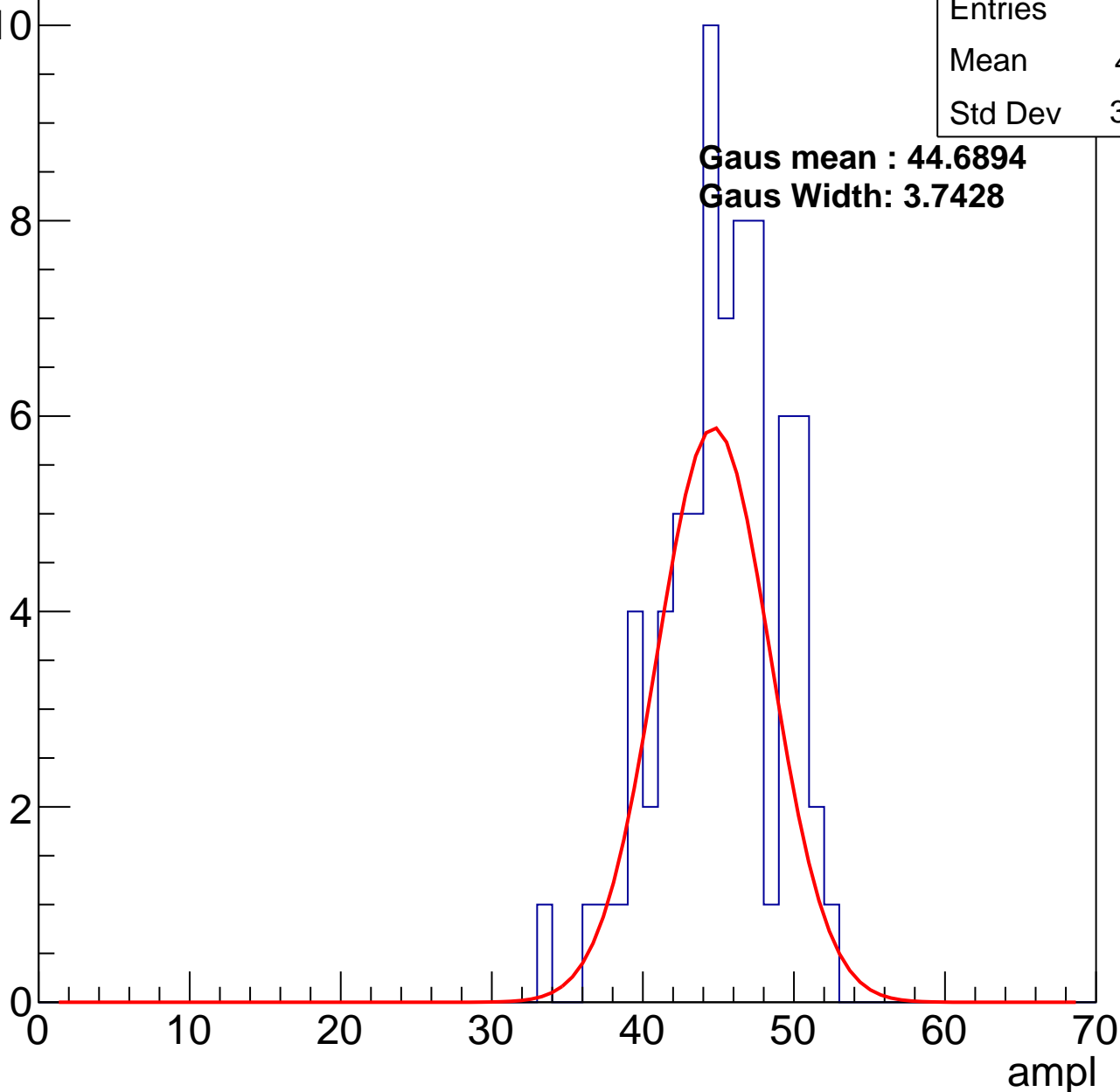
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	44.71
Std Dev	3.848

Gaus mean : 44.6894

Gaus Width: 3.7428

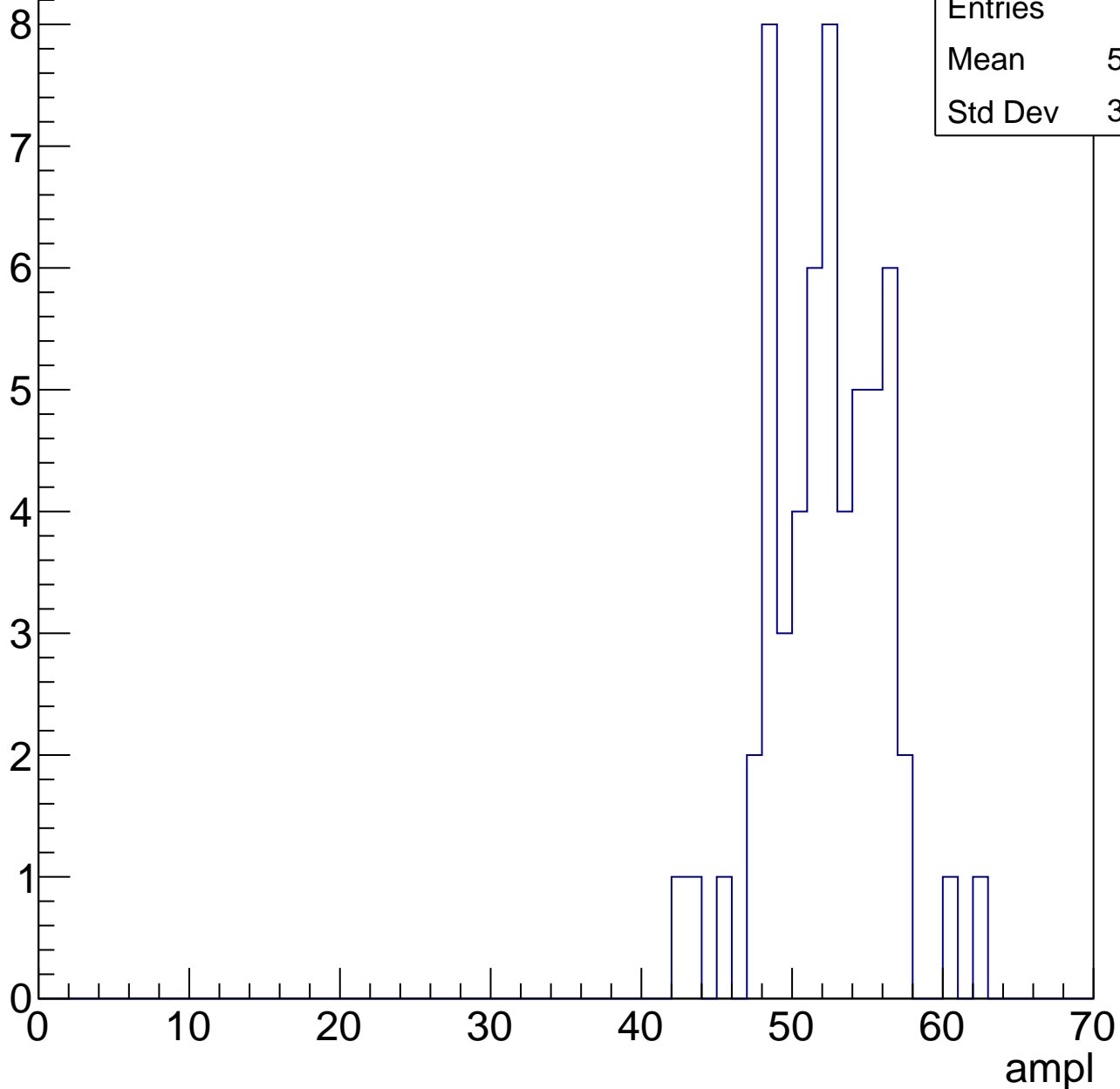


B1L103S, U20-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	51.83
Std Dev	3.793

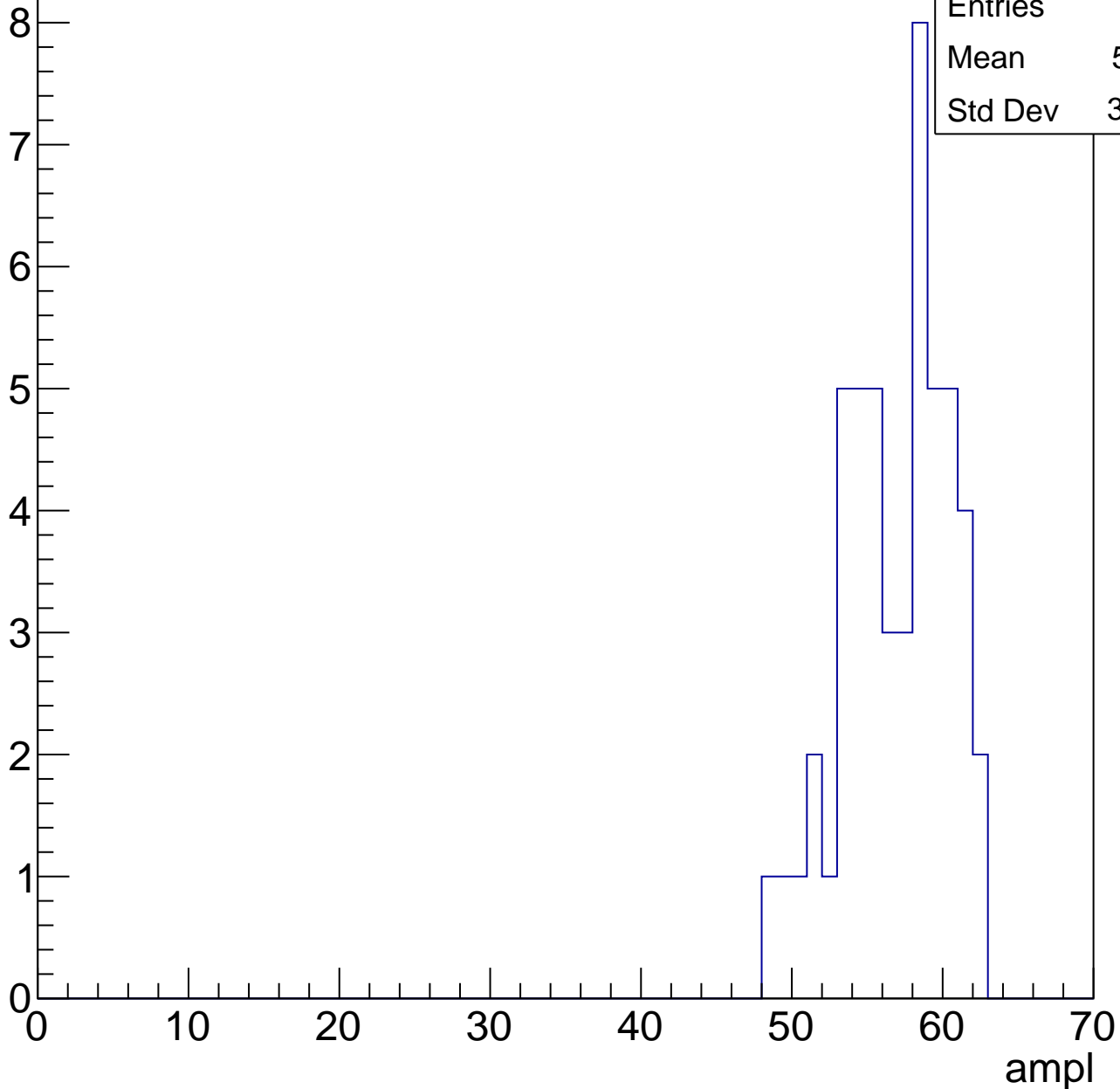


B1L103S, U20-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	56.41
Std Dev	3.454

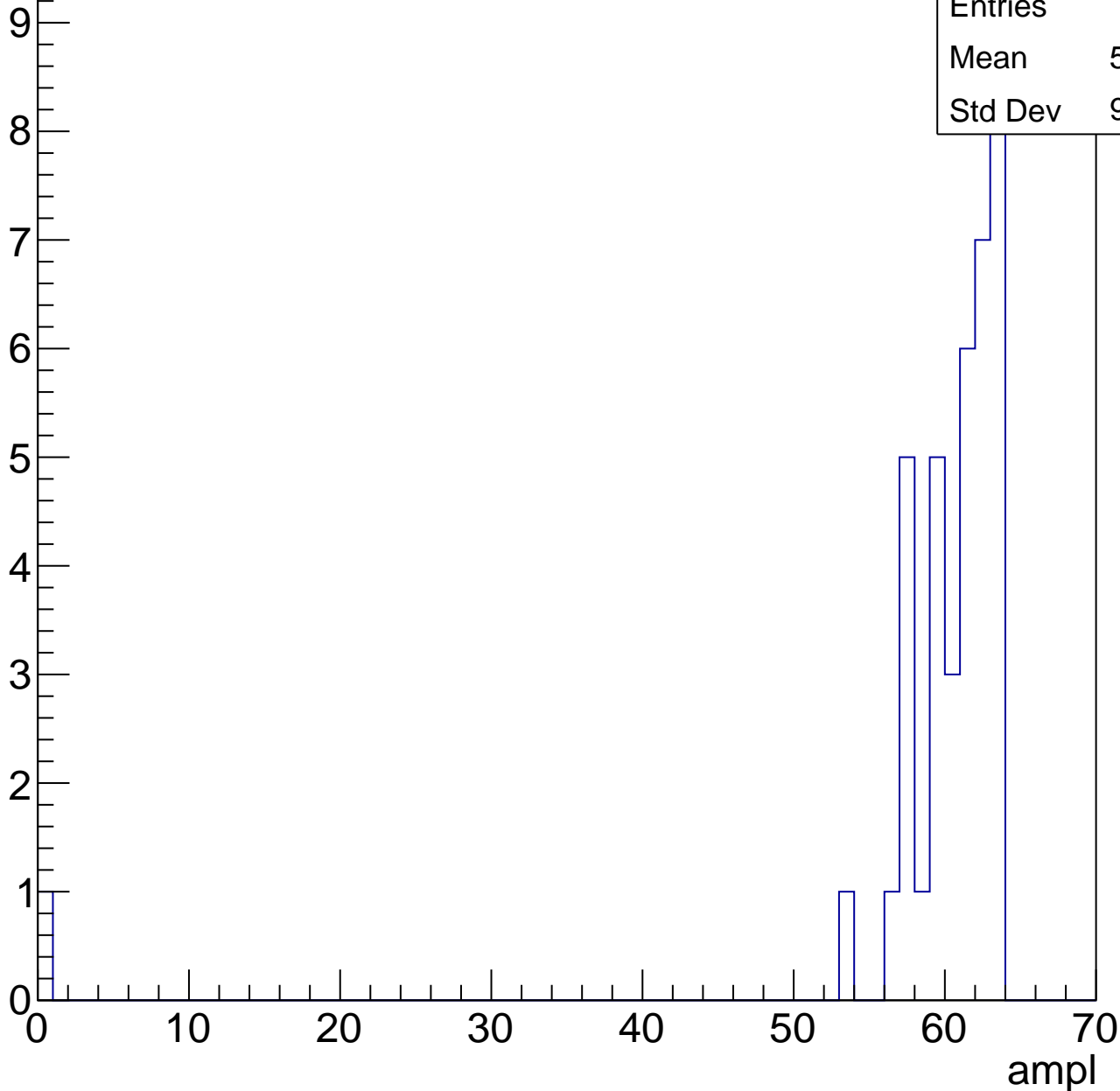


B1L103S, U20-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.82
Std Dev	9.845



B1L103S, U20-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.33
Std Dev	2.055

0 10 20 30 40 50 60 70

ampl

B1L103S, U20-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch74, adc0

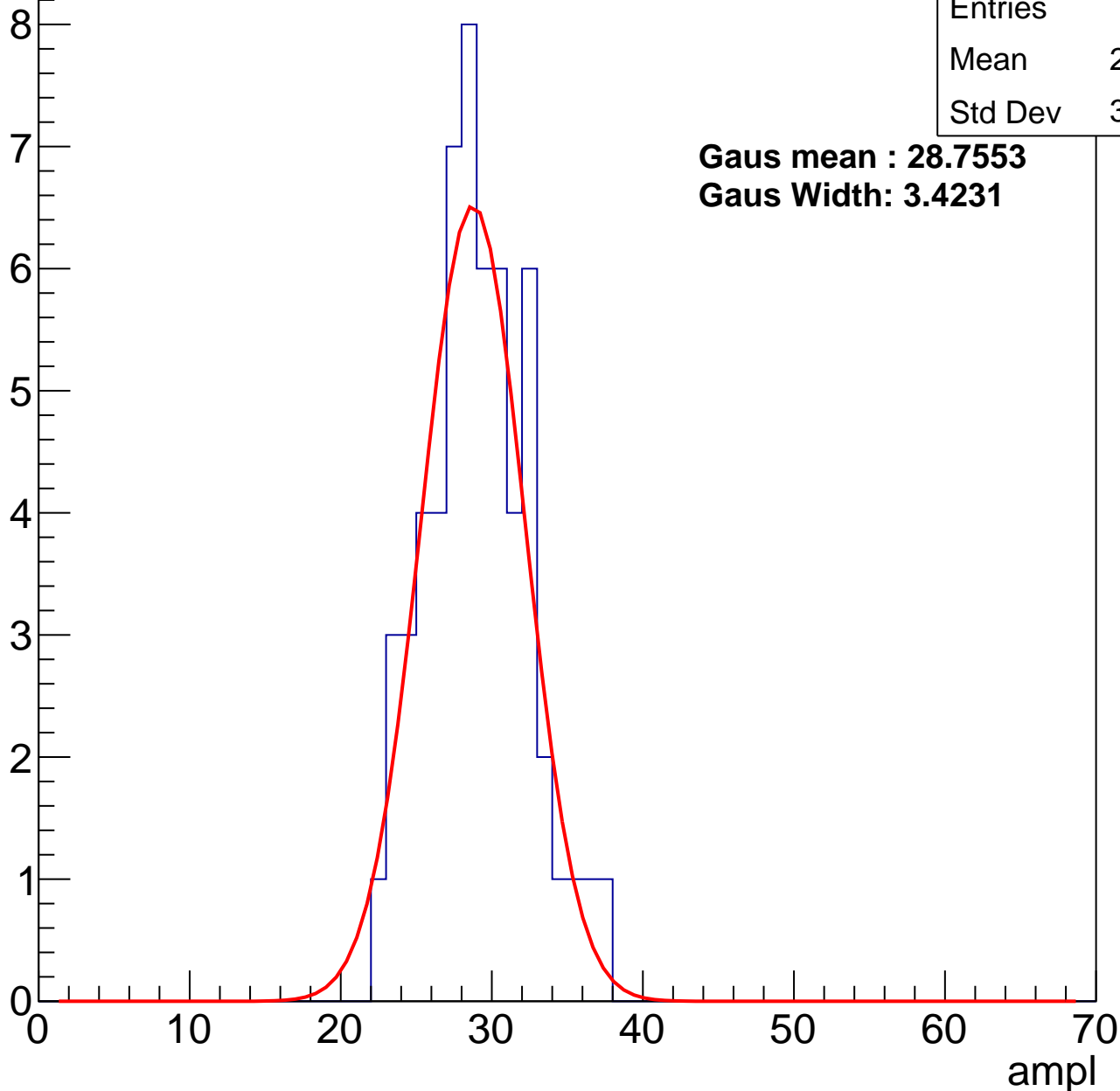
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	28.59
Std Dev	3.322

Gaus mean : 28.7553

Gaus Width: 3.4231



B1L103S, U20-ch74, adc1

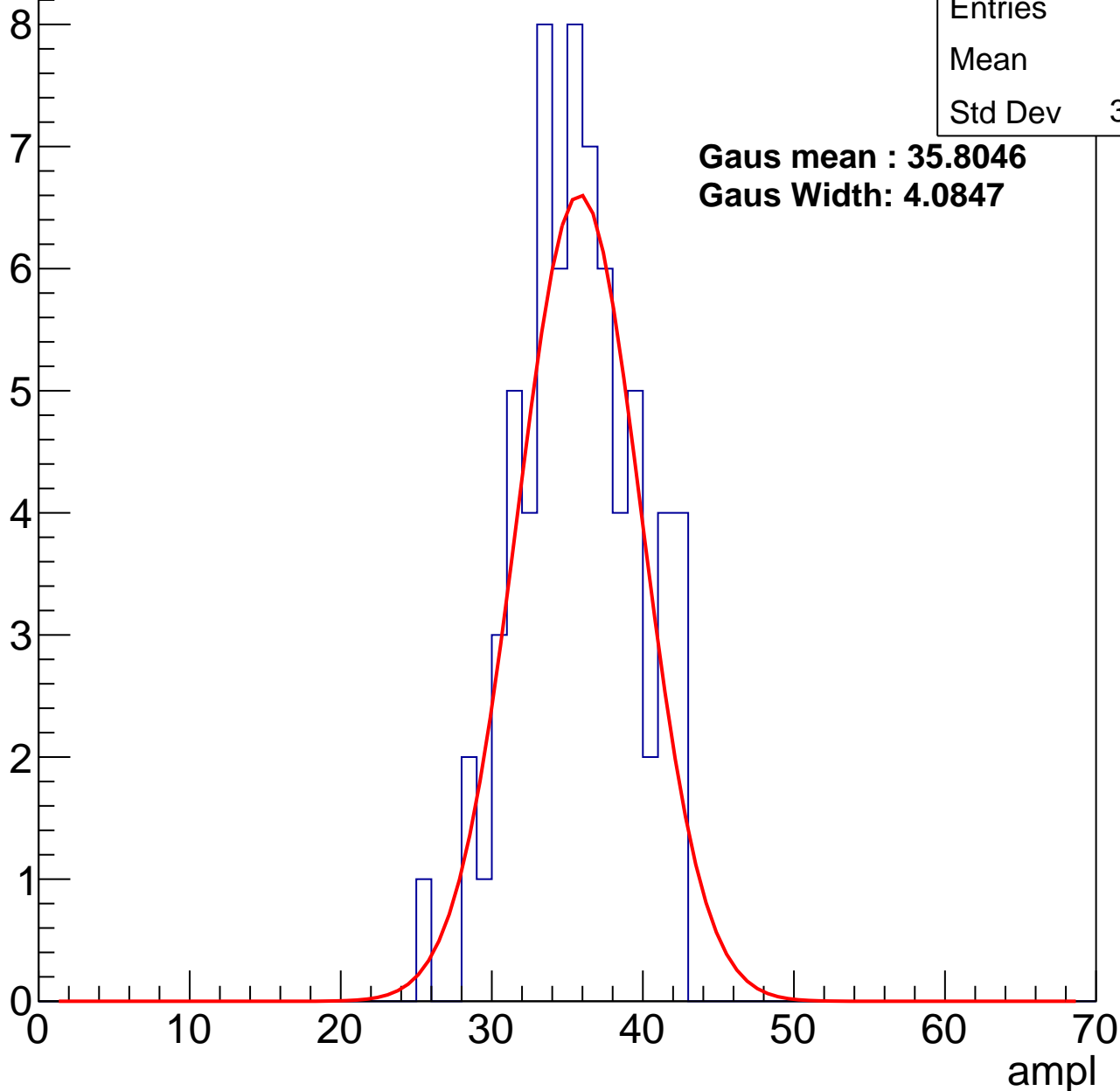
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.2
Std Dev	3.774

Gaus mean : 35.8046

Gaus Width: 4.0847



B1L103S, U20-ch74, adc2

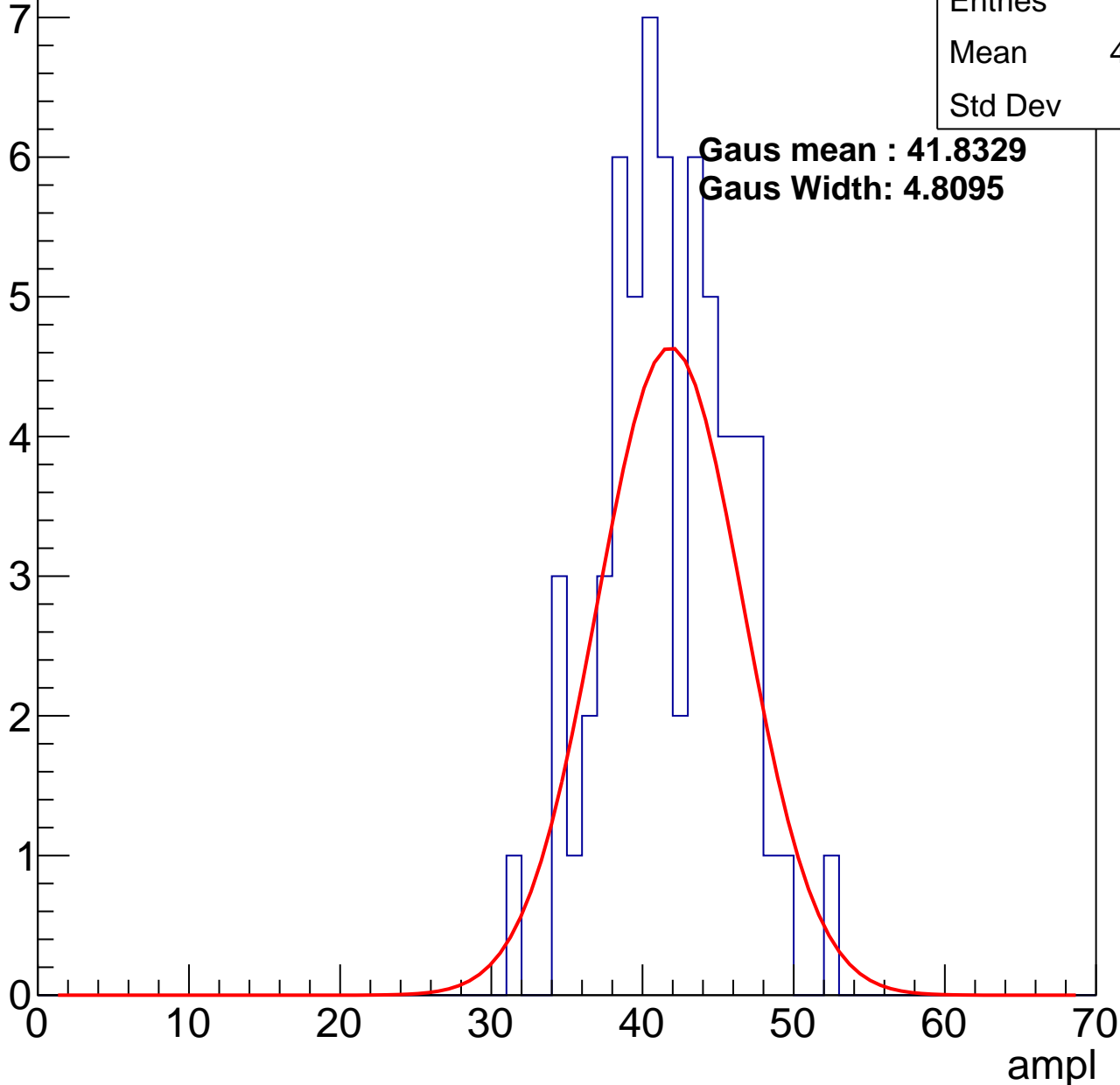
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.34
Std Dev	4.15

Gaus mean : 41.8329

Gaus Width: 4.8095

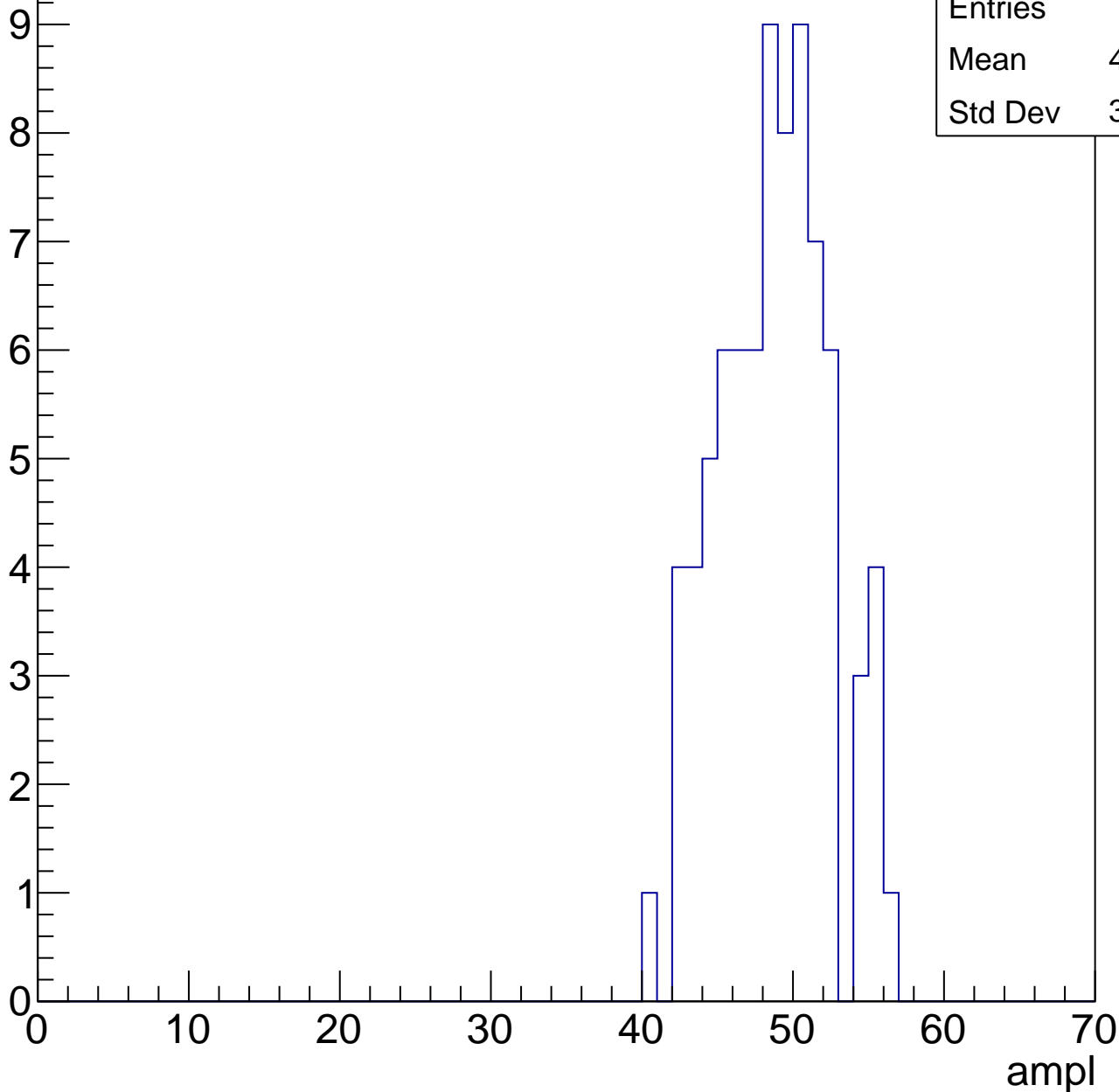


B1L103S, U20-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	48.22
Std Dev	3.627

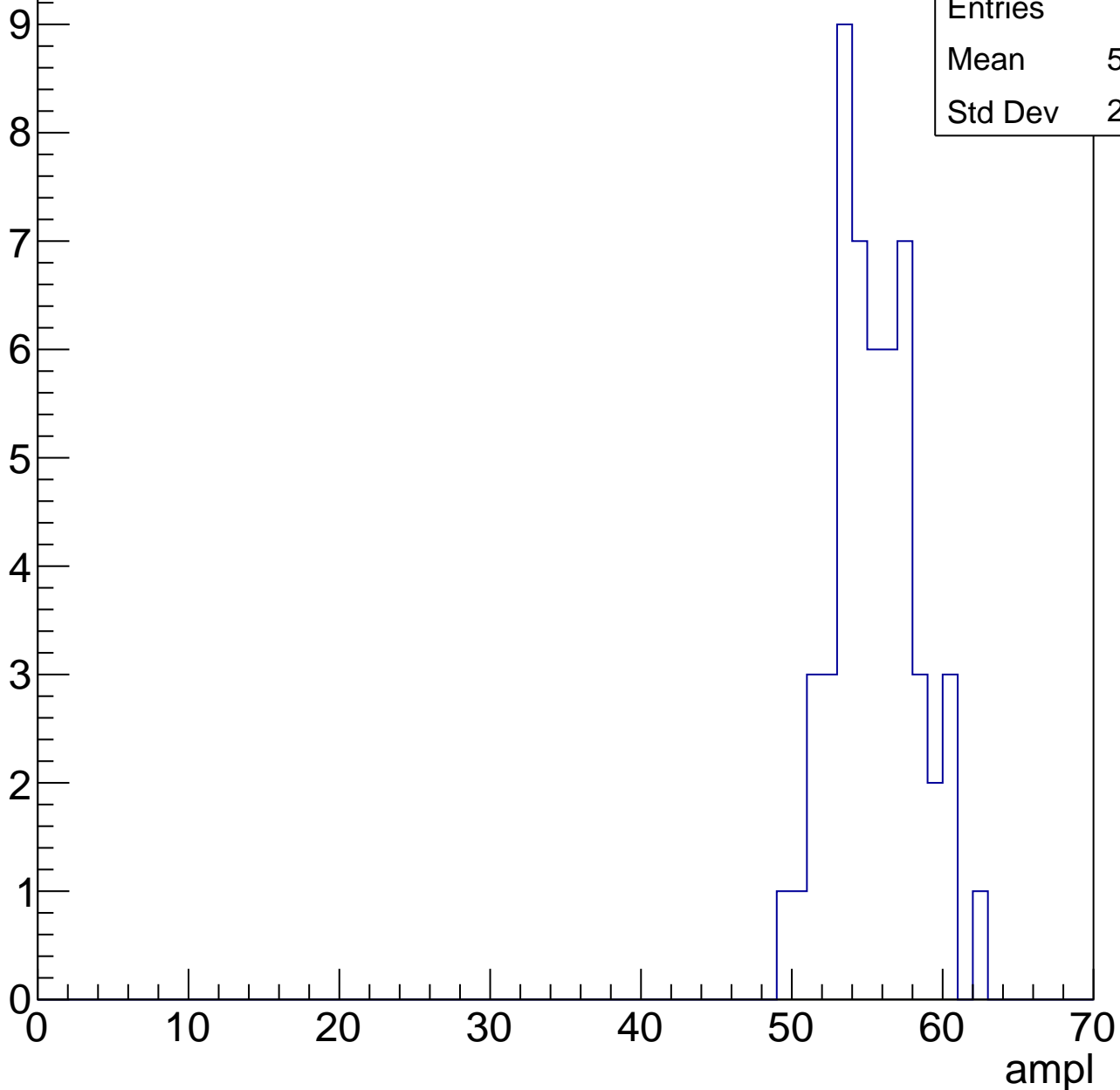


B1L103S, U20-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.04
Std Dev	2.759

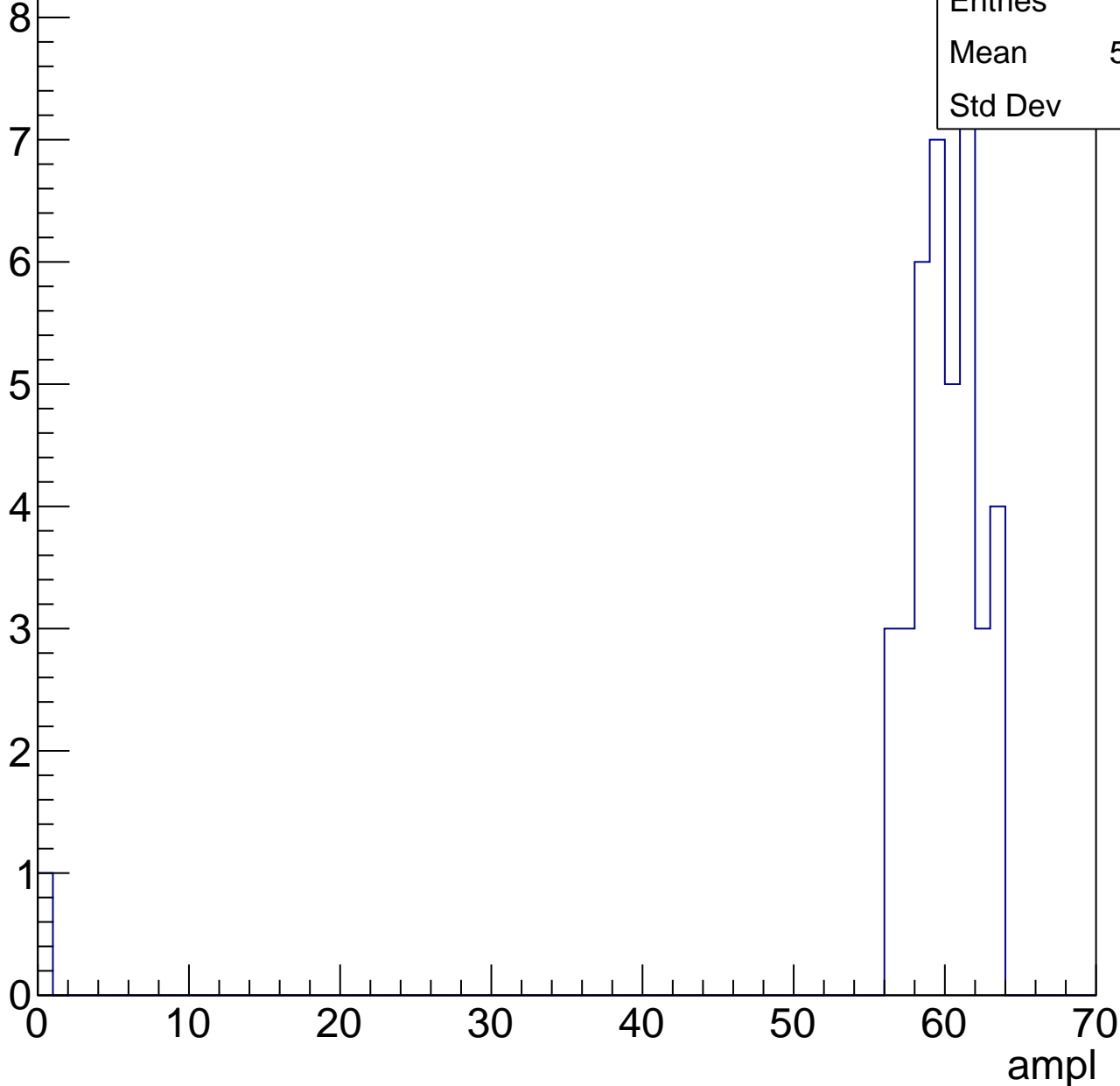


B1L103S, U20-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.15
Std Dev	9.52

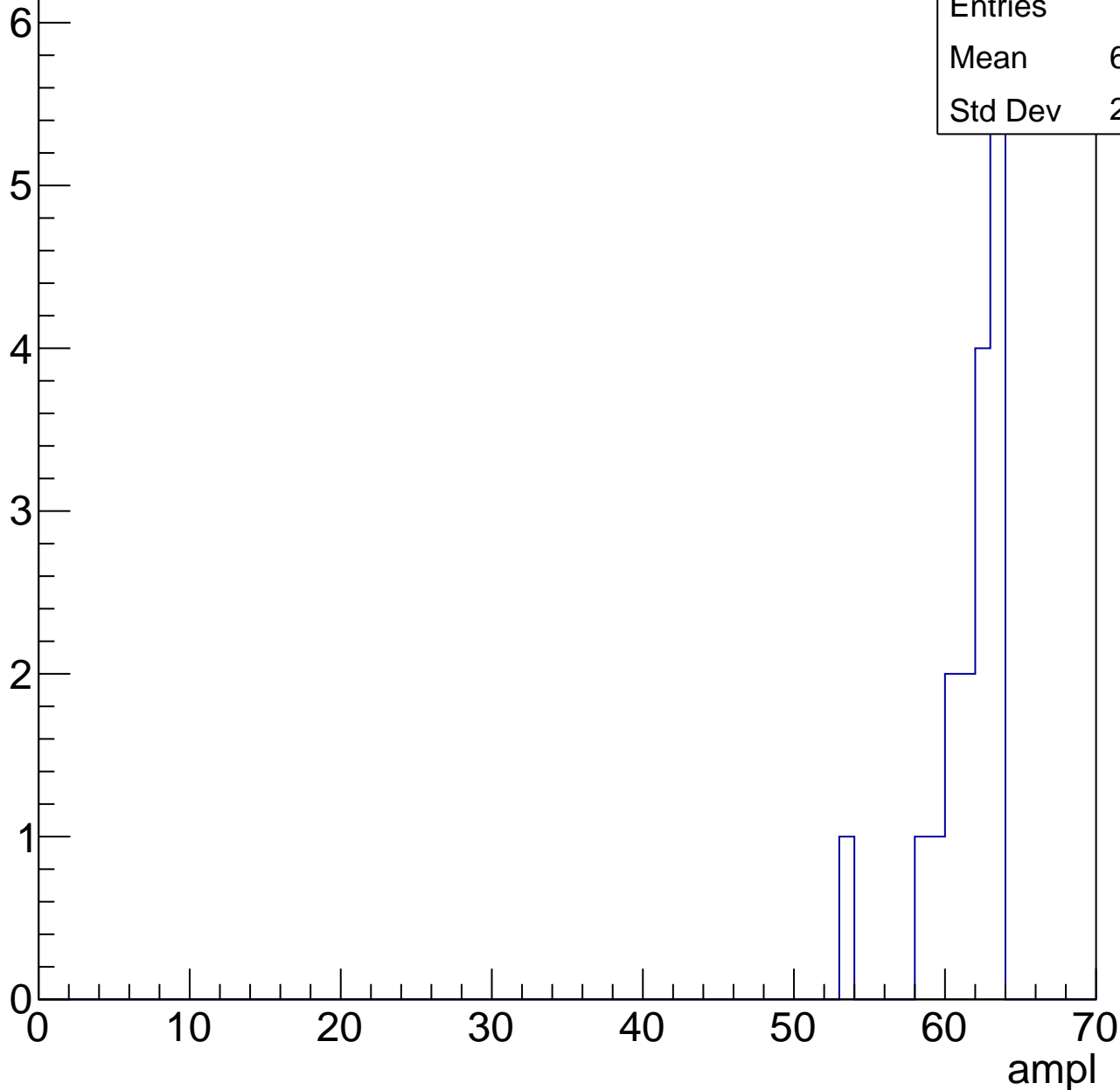


B1L103S, U20-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

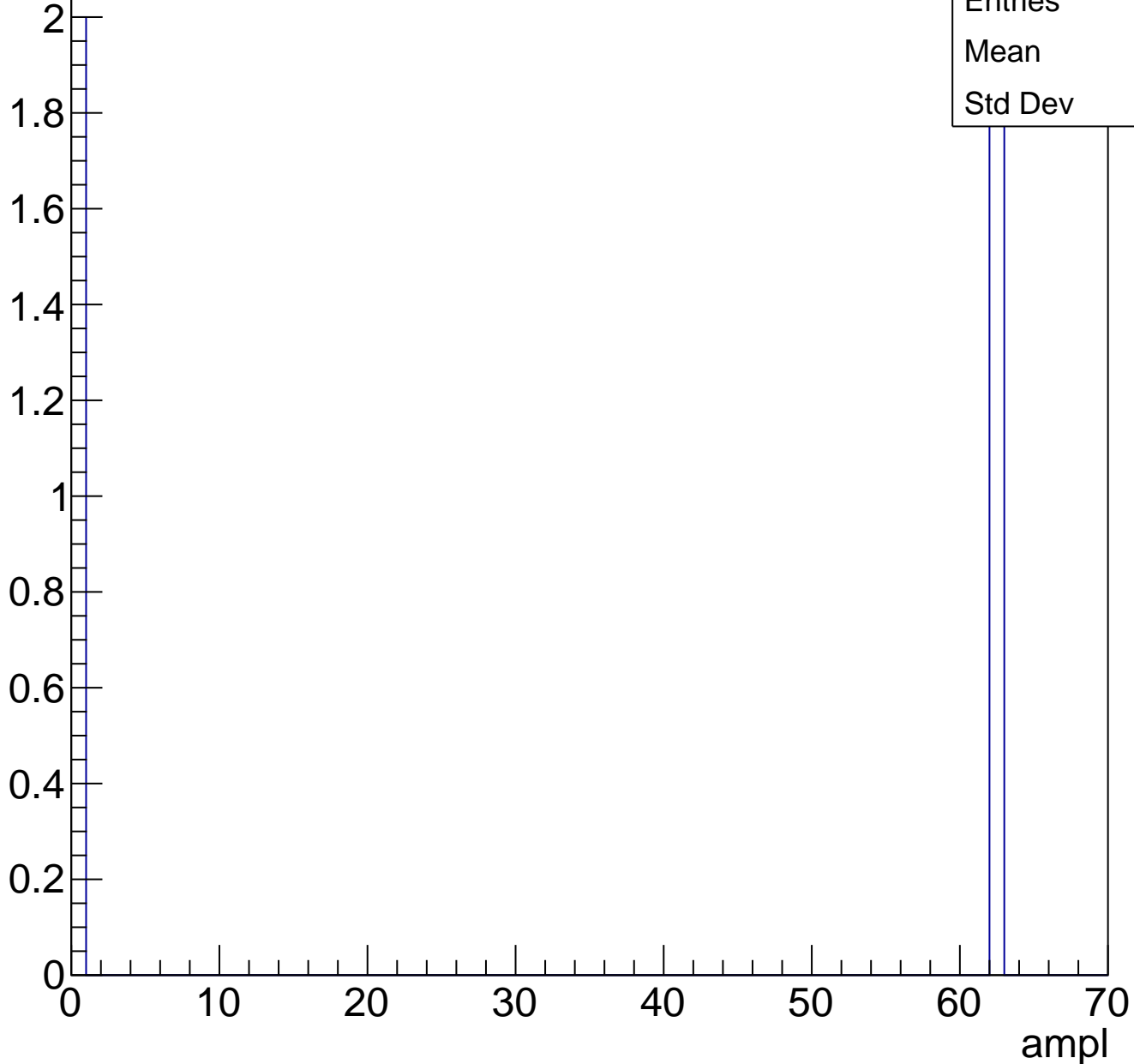
Entries	17
Mean	61.06
Std Dev	2.508



B1L103S, U20-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch75, adc0

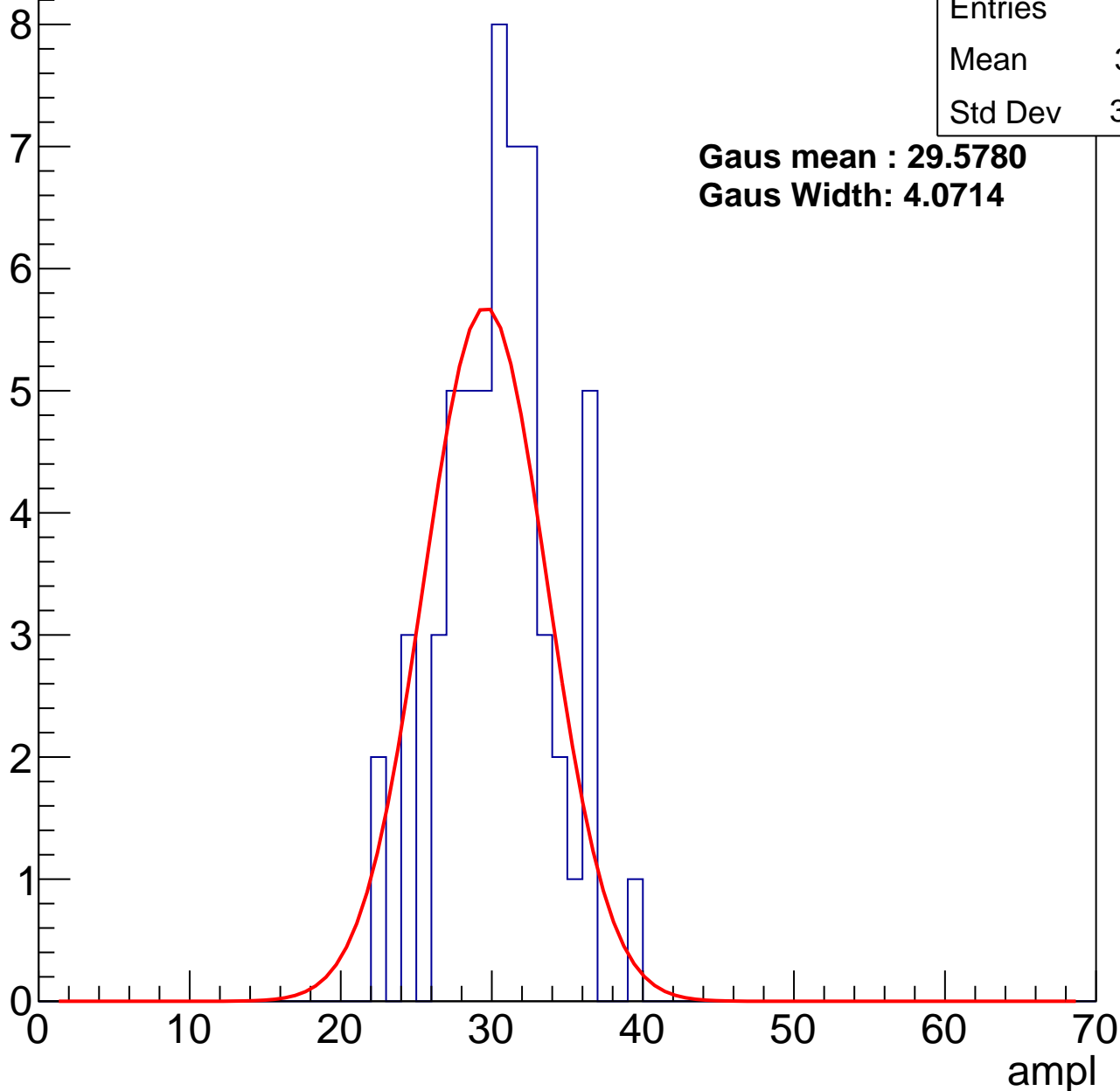
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	30.11
Std Dev	3.587

Gaus mean : 29.5780

Gaus Width: 4.0714



B1L103S, U20-ch75, adc1

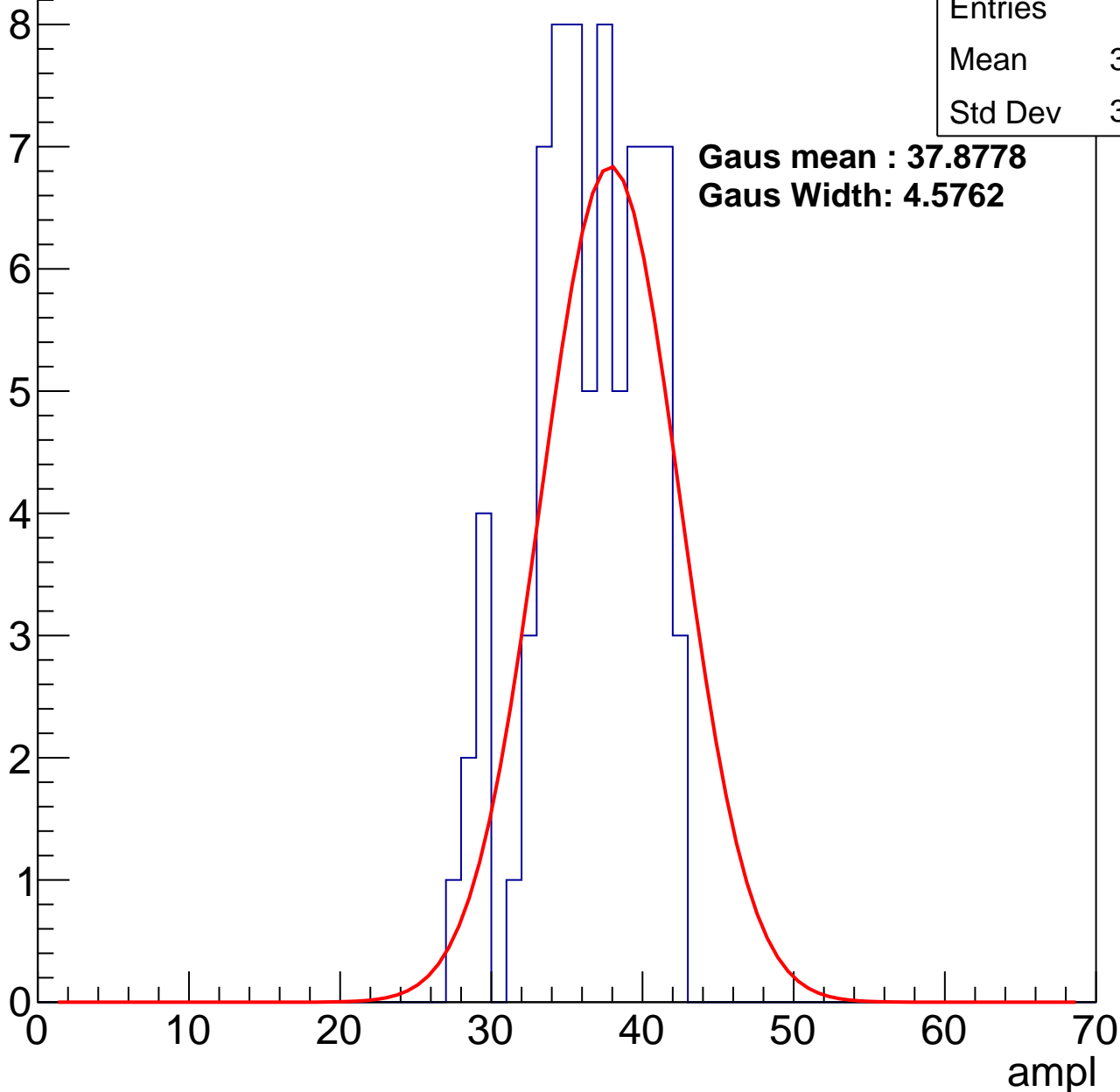
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.07
Std Dev	3.746

Gaus mean : 37.8778

Gaus Width: 4.5762



B1L103S, U20-ch75, adc2

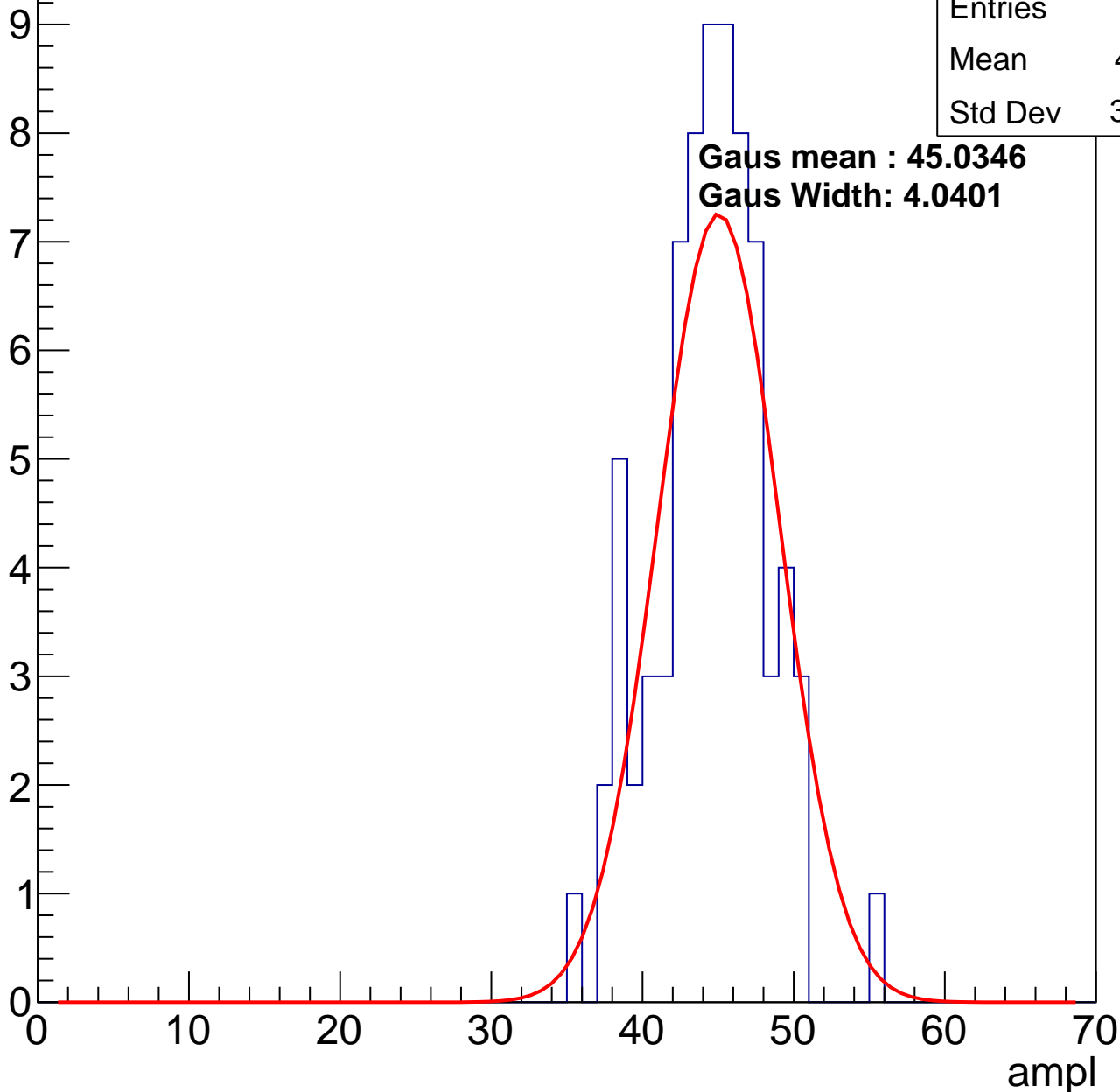
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	44.01
Std Dev	3.653

Gaus mean : 45.0346

Gaus Width: 4.0401

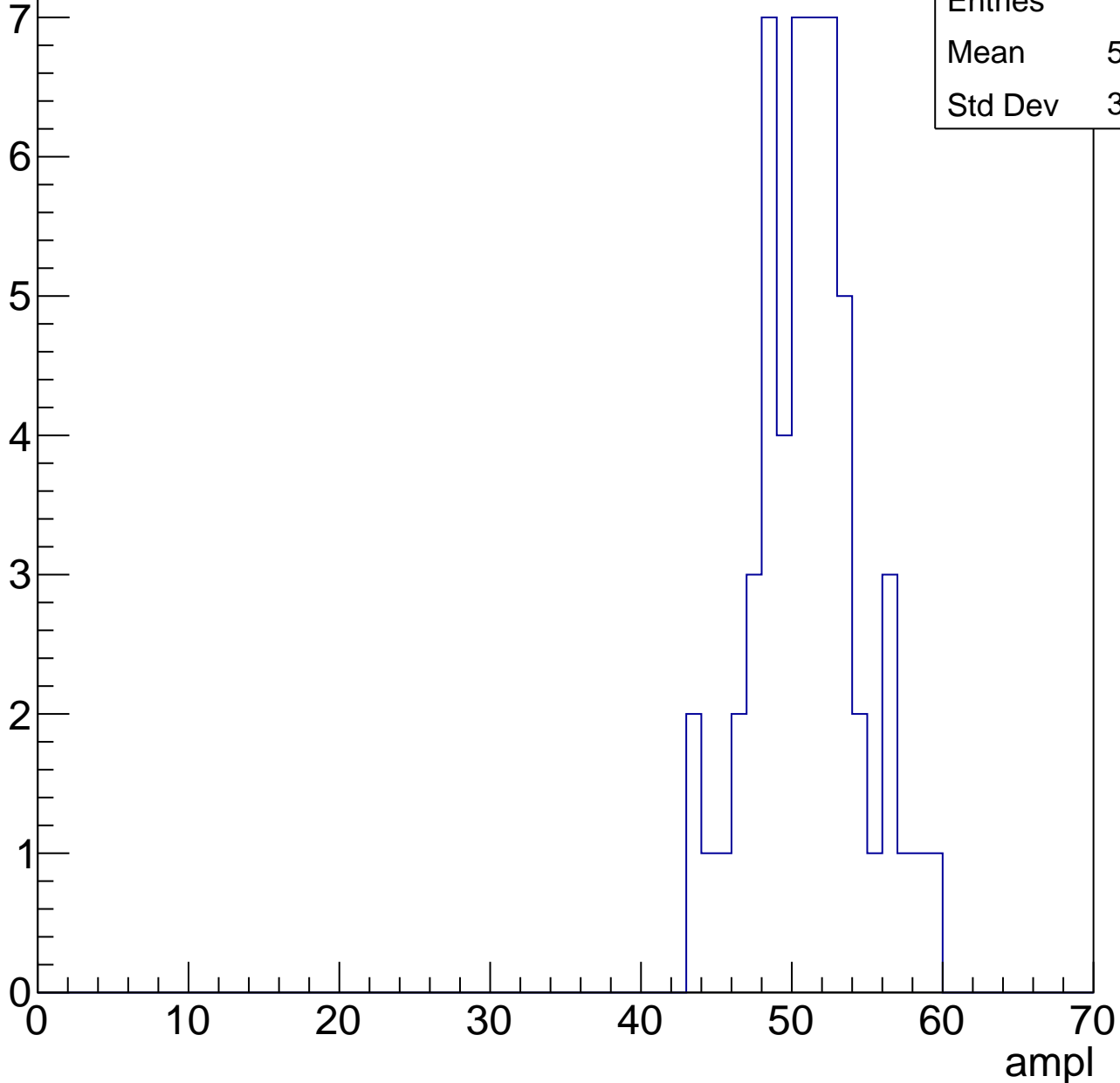


B1L103S, U20-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	50.56
Std Dev	3.494

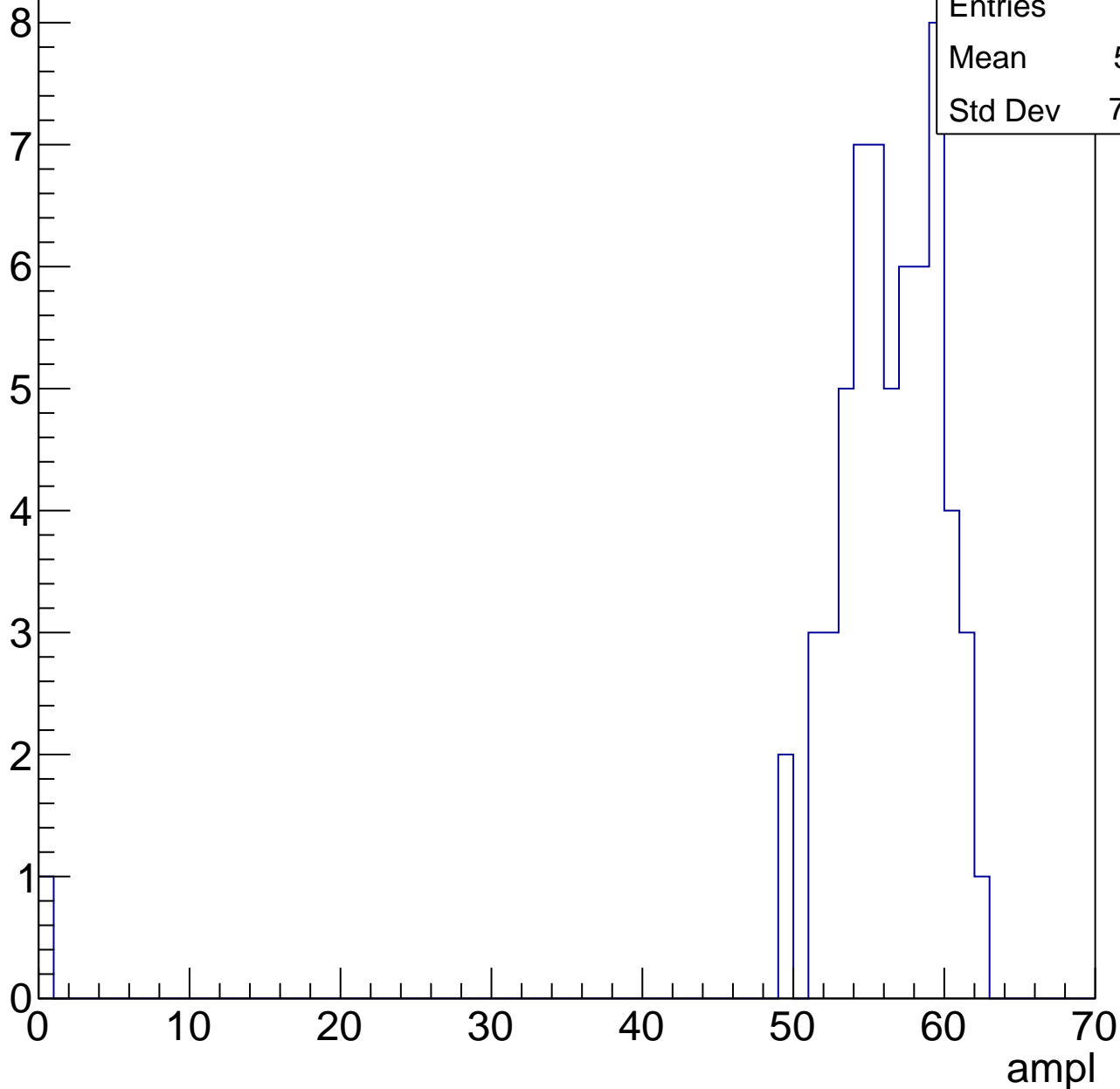


B1L103S, U20-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.11
Std Dev	7.753

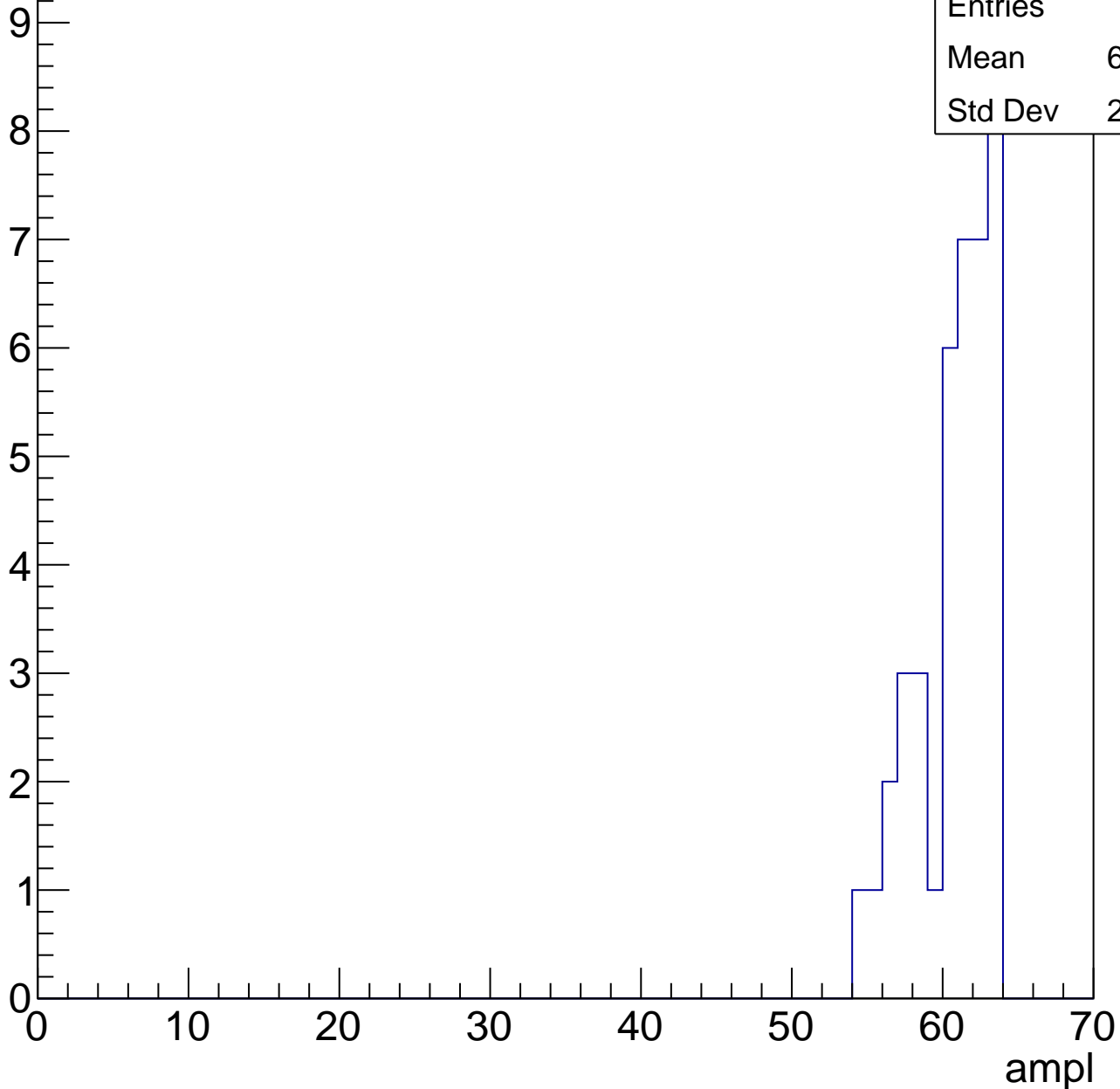


B1L103S, U20-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	60.33
Std Dev	2.474



B1L103S, U20-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch76, adc0

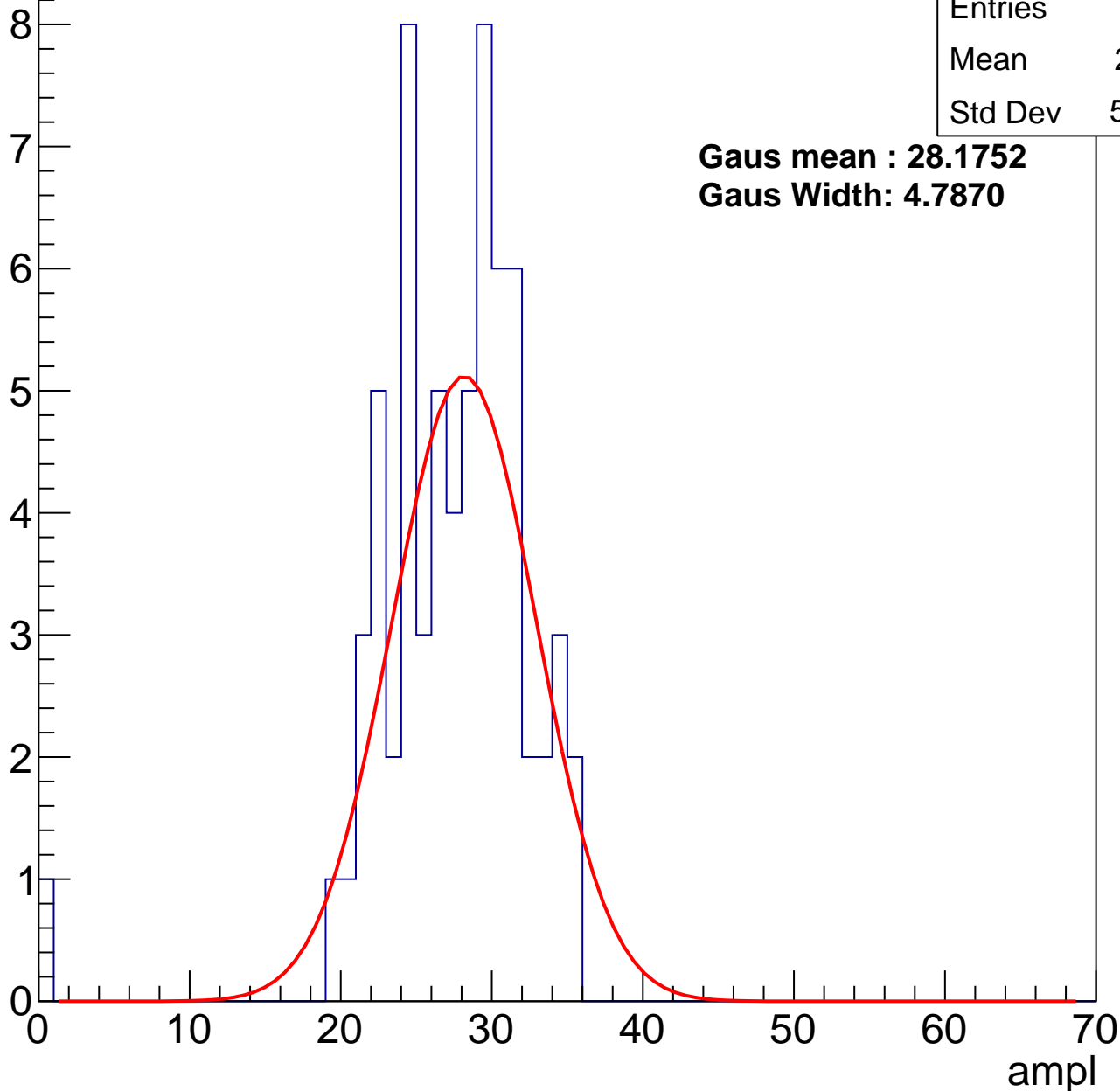
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	26.91
Std Dev	5.154

Gaus mean : 28.1752

Gaus Width: 4.7870



B1L103S, U20-ch76, adc1

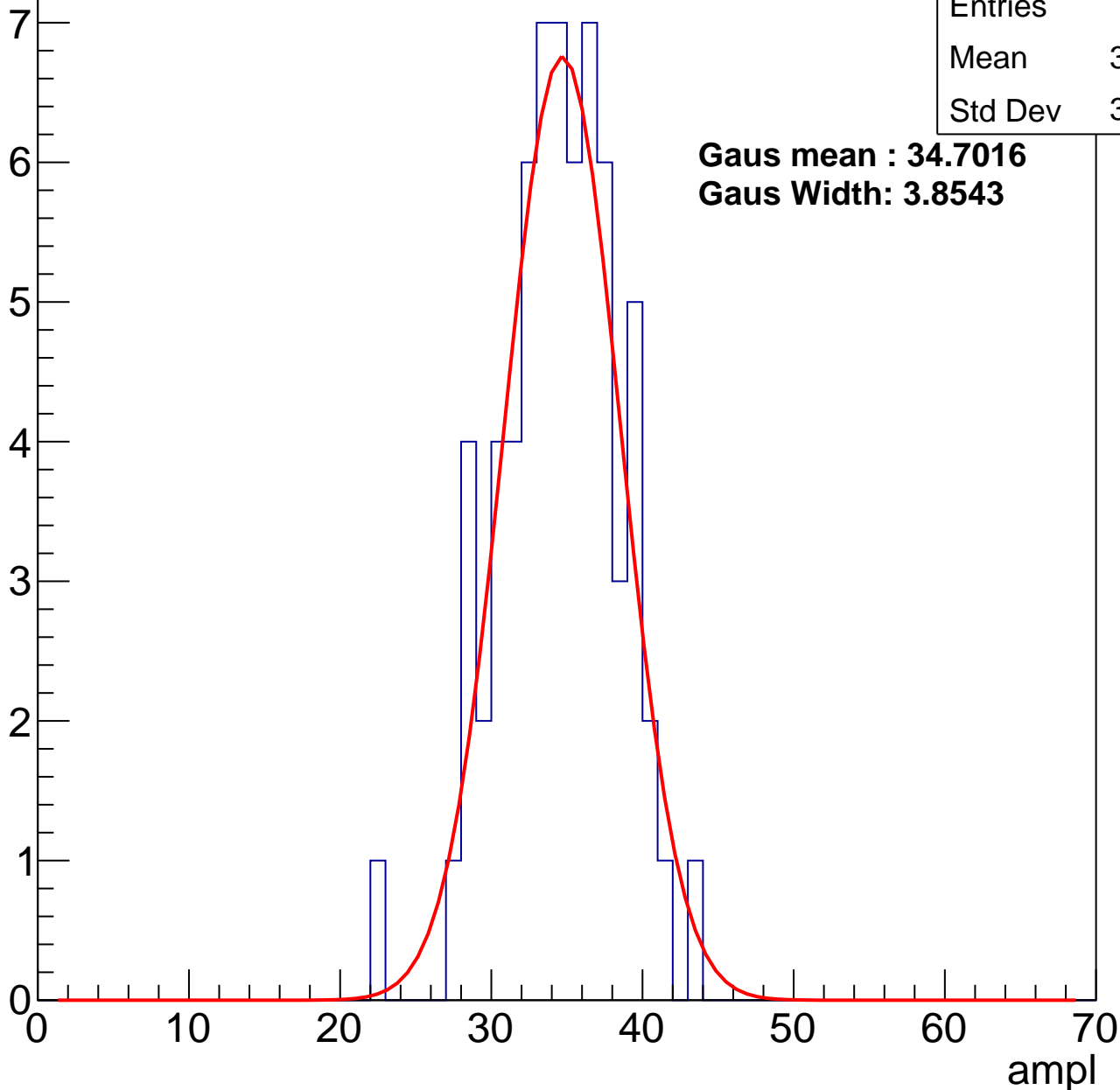
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	34.04
Std Dev	3.826

Gaus mean : 34.7016

Gaus Width: 3.8543



B1L103S, U20-ch76, adc2

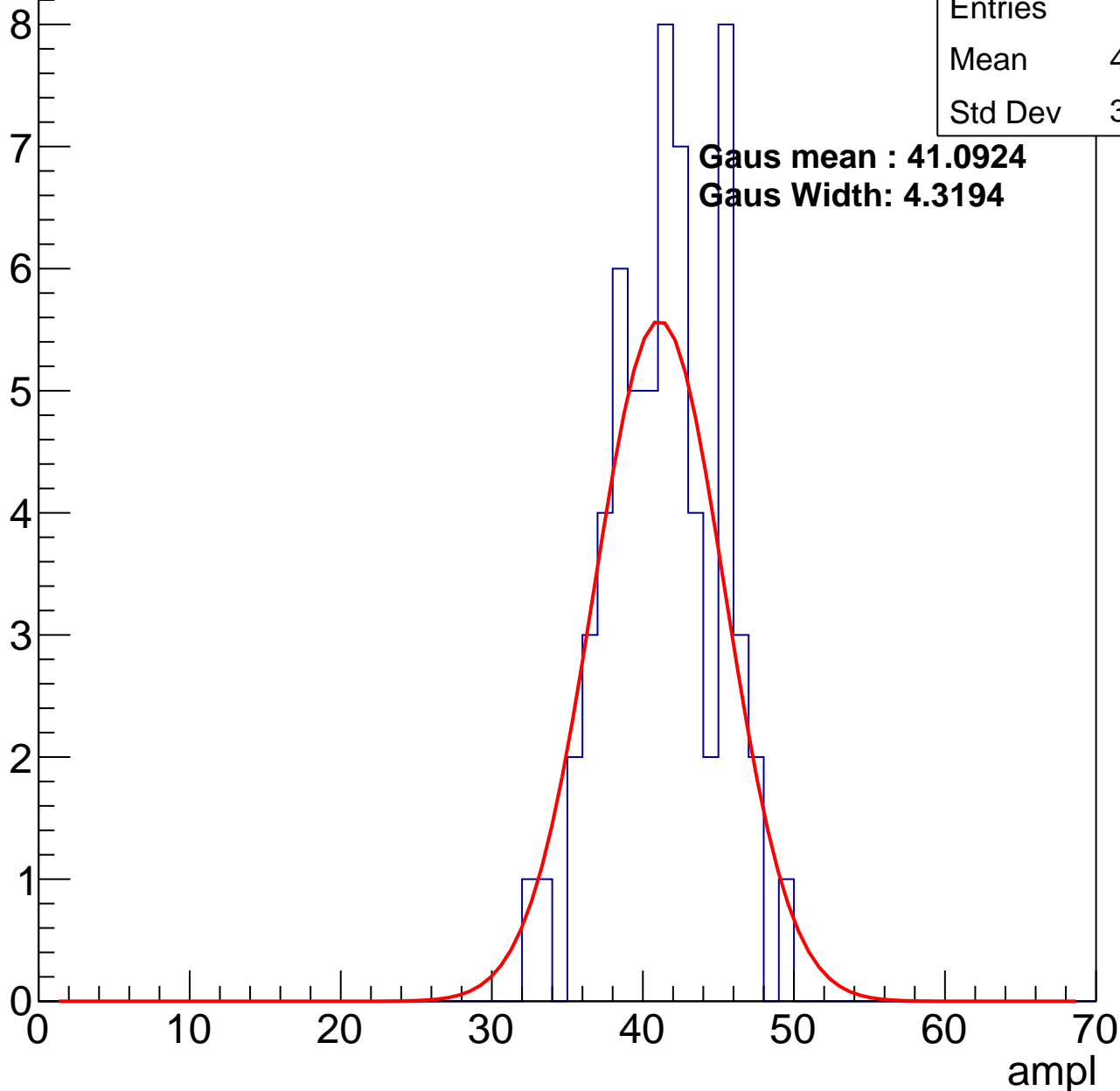
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	40.92
Std Dev	3.625

Gaus mean : 41.0924

Gaus Width: 4.3194

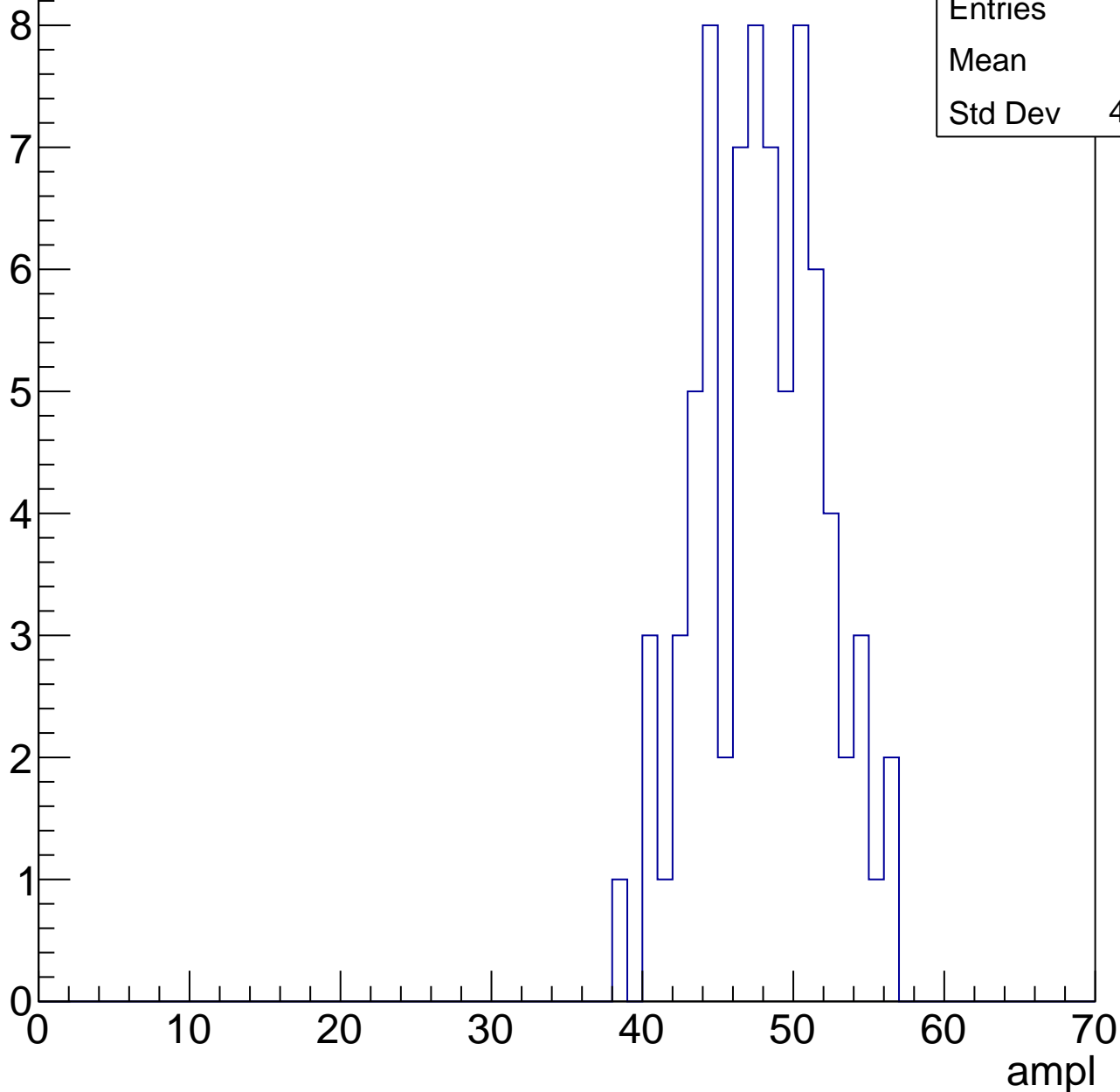


B1L103S, U20-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	47.5
Std Dev	4.018

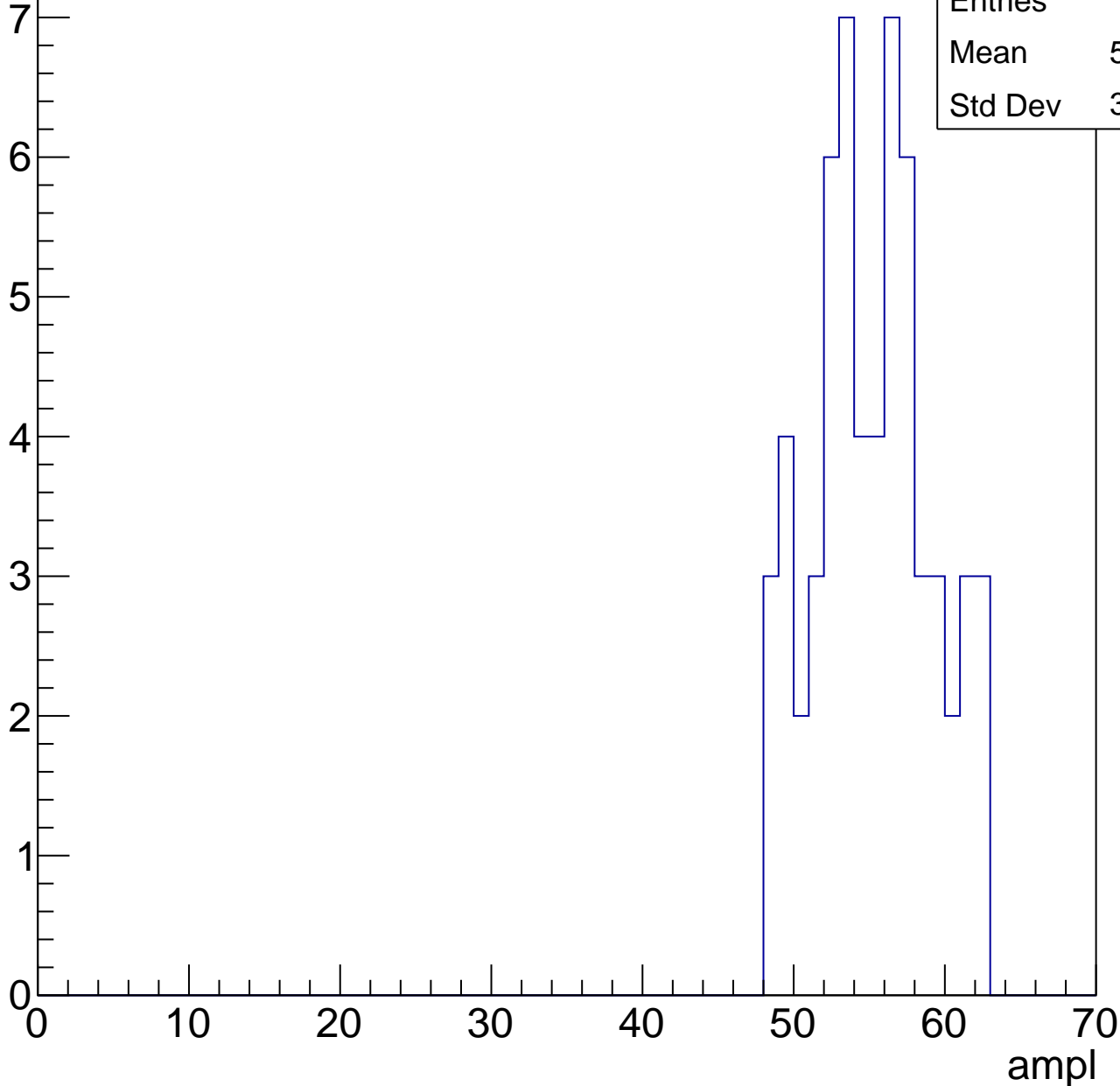


B1L103S, U20-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.77
Std Dev	3.836

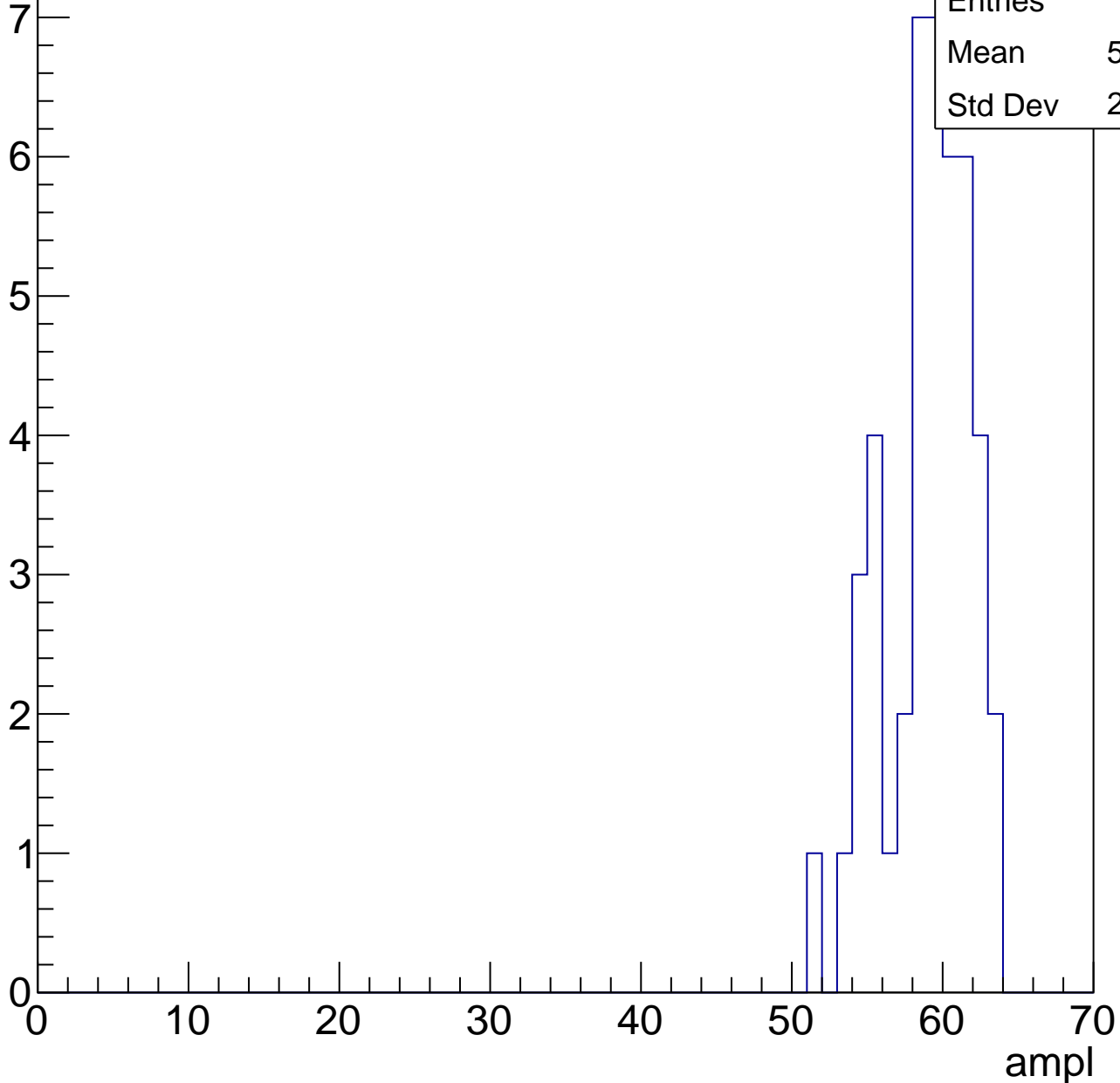


B1L103S, U20-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.52
Std Dev	2.824

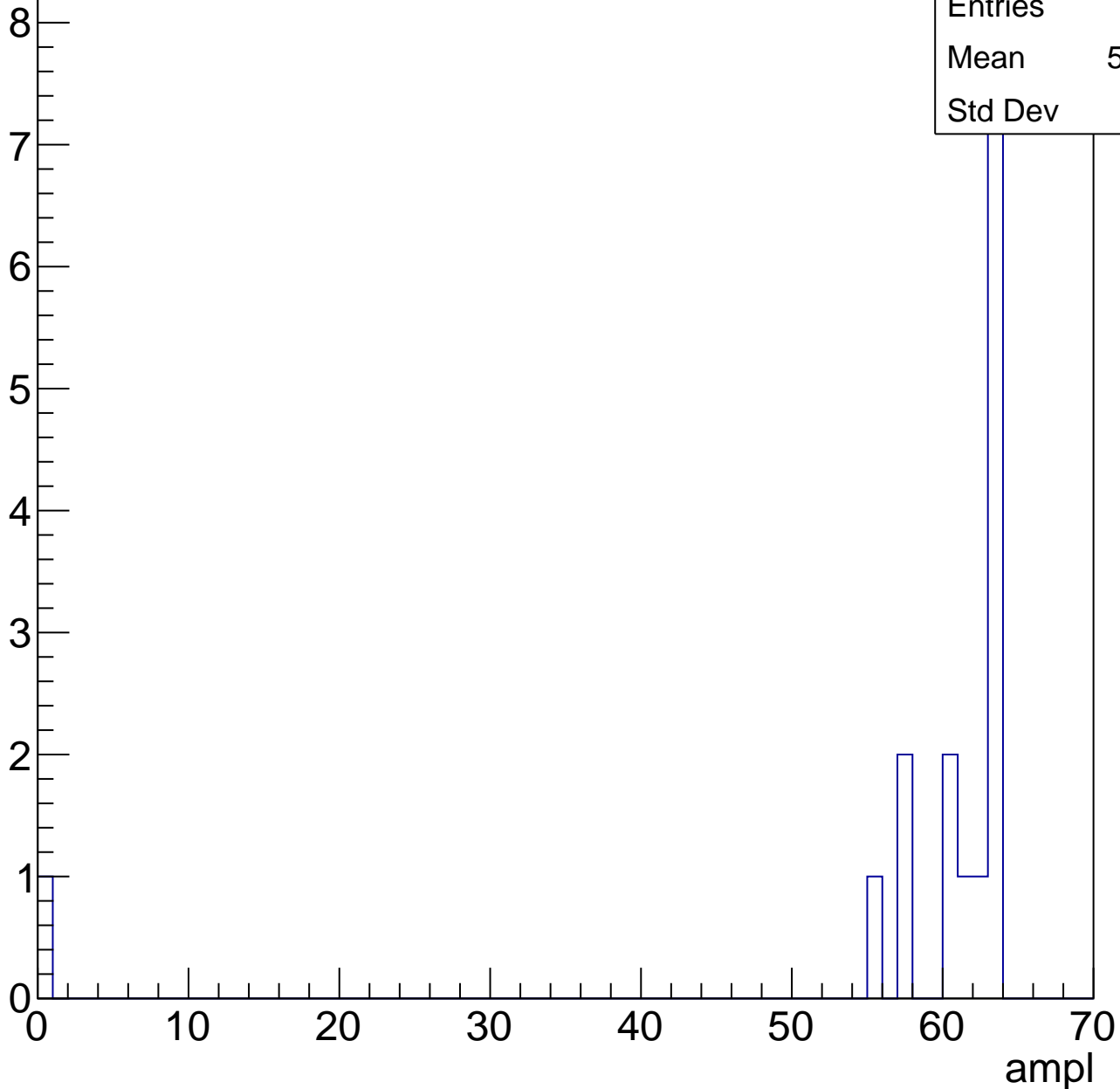


B1L103S, U20-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

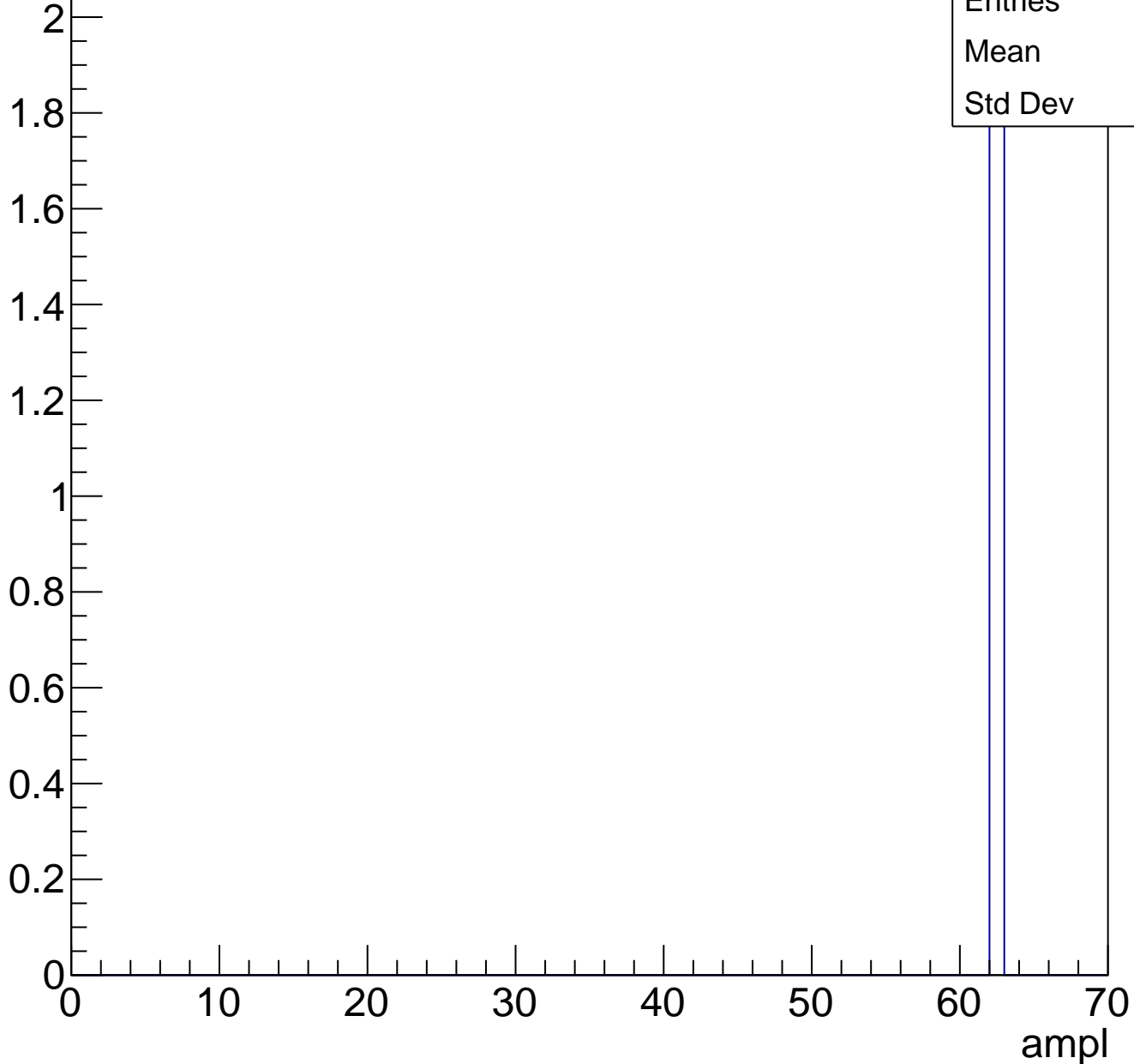
Entries	16
Mean	57.25
Std Dev	15



B1L103S, U20-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch77, adc0

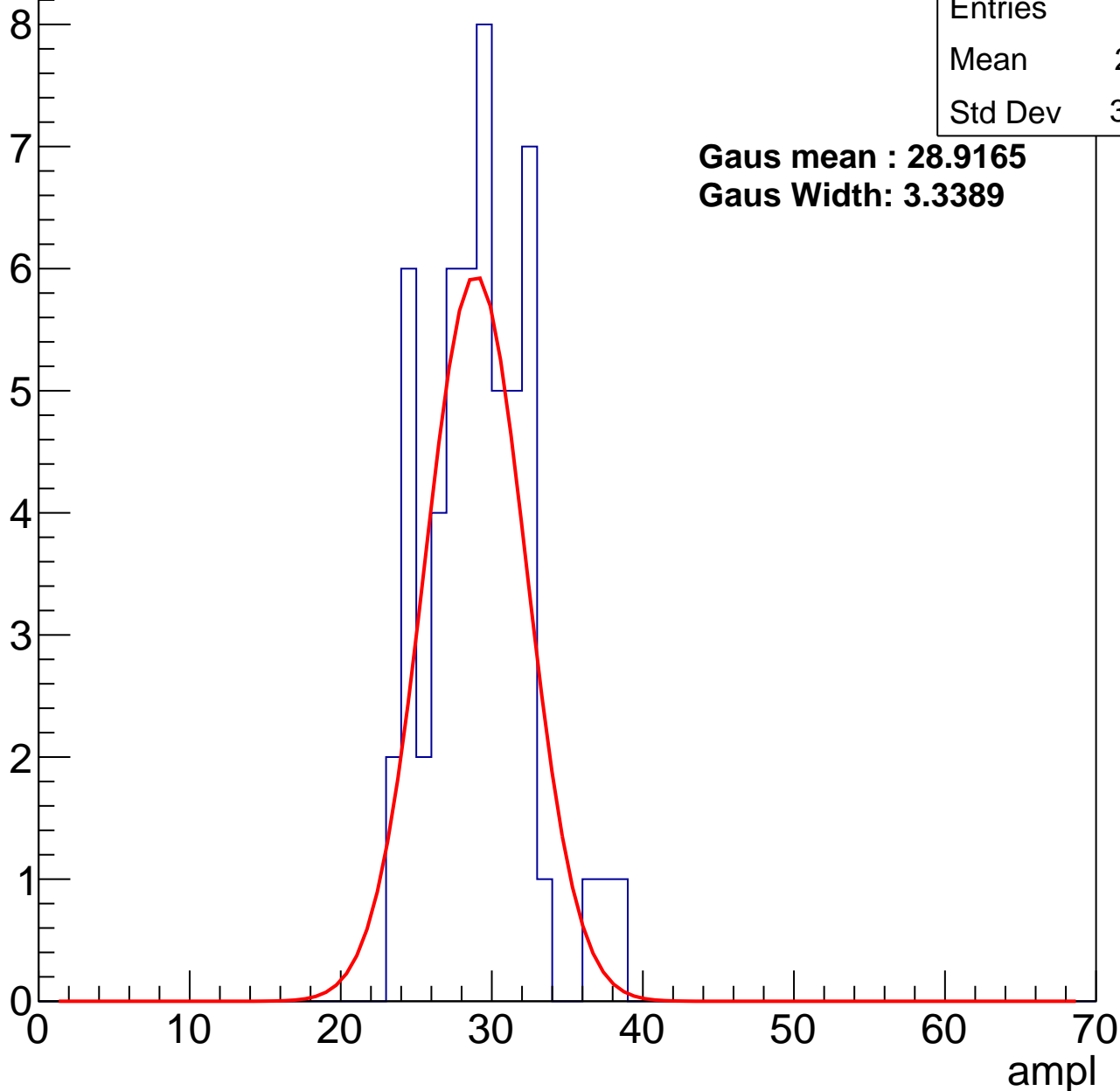
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	28.71
Std Dev	3.339

Gaus mean : 28.9165

Gaus Width: 3.3389



B1L103S, U20-ch77, adc1

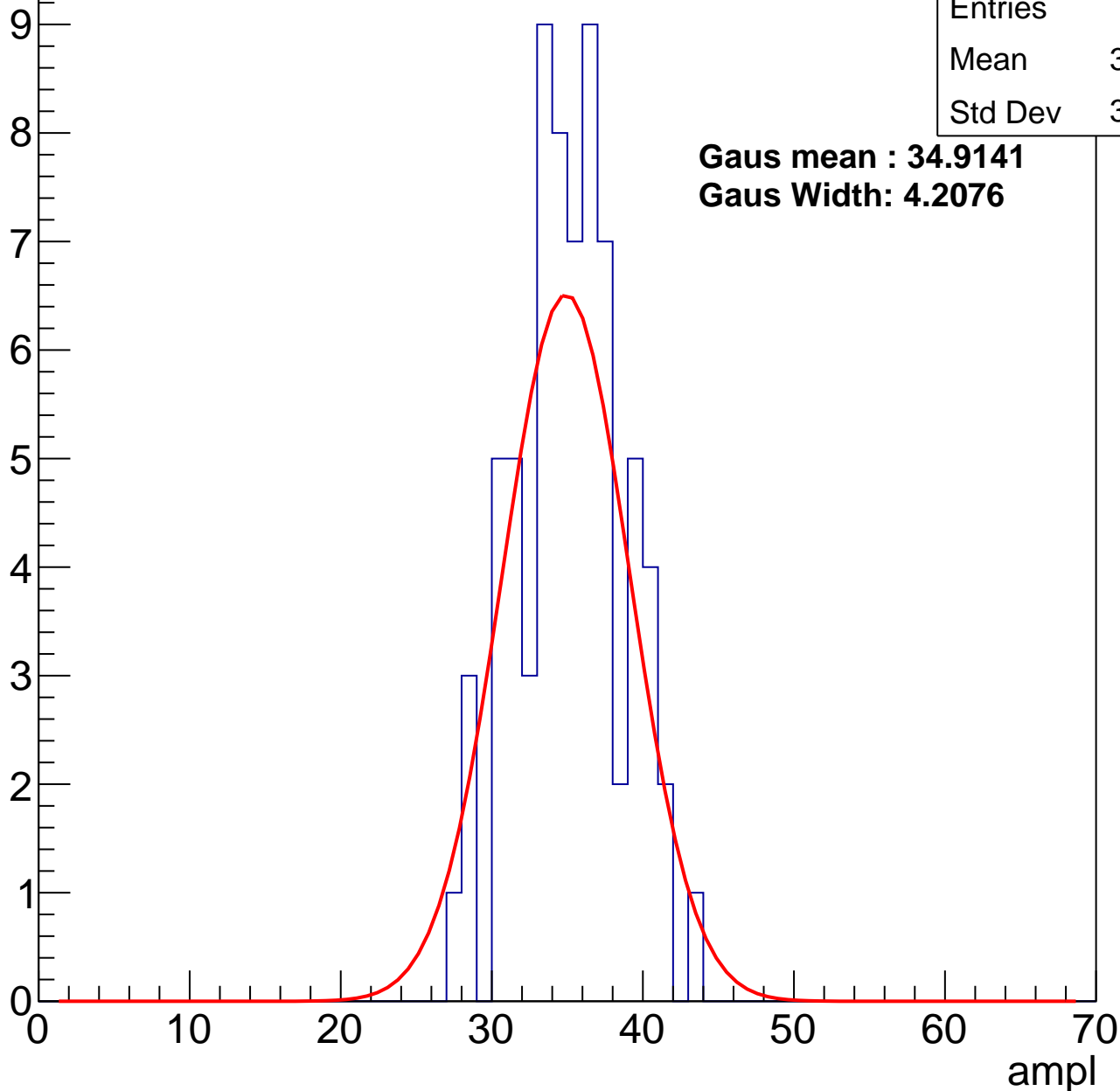
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	34.72
Std Dev	3.465

Gaus mean : 34.9141

Gaus Width: 4.2076



B1L103S, U20-ch77, adc2

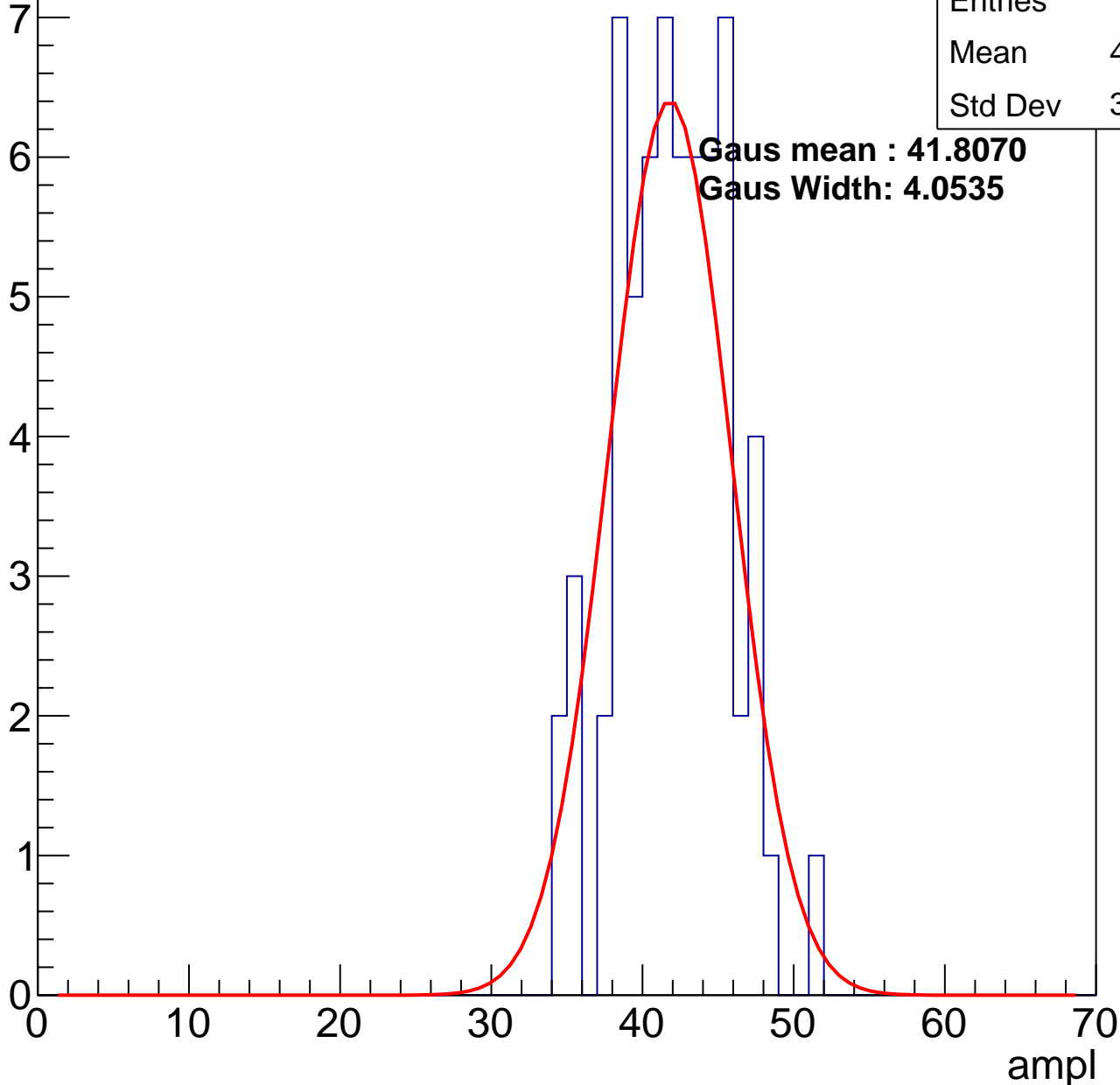
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.58
Std Dev	3.603

Gaus mean : 41.8070

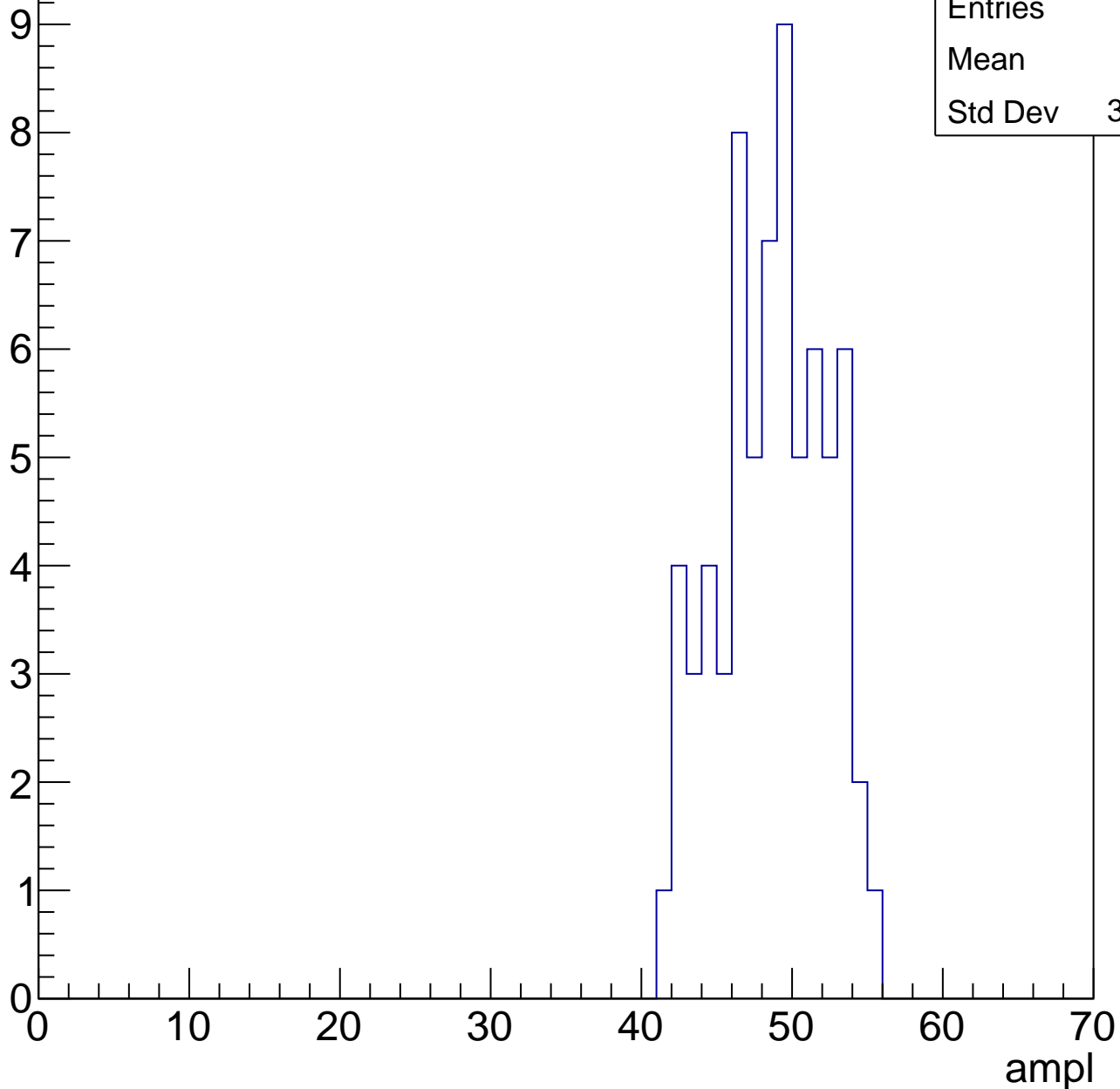
Gaus Width: 4.0535



B1L103S, U20-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

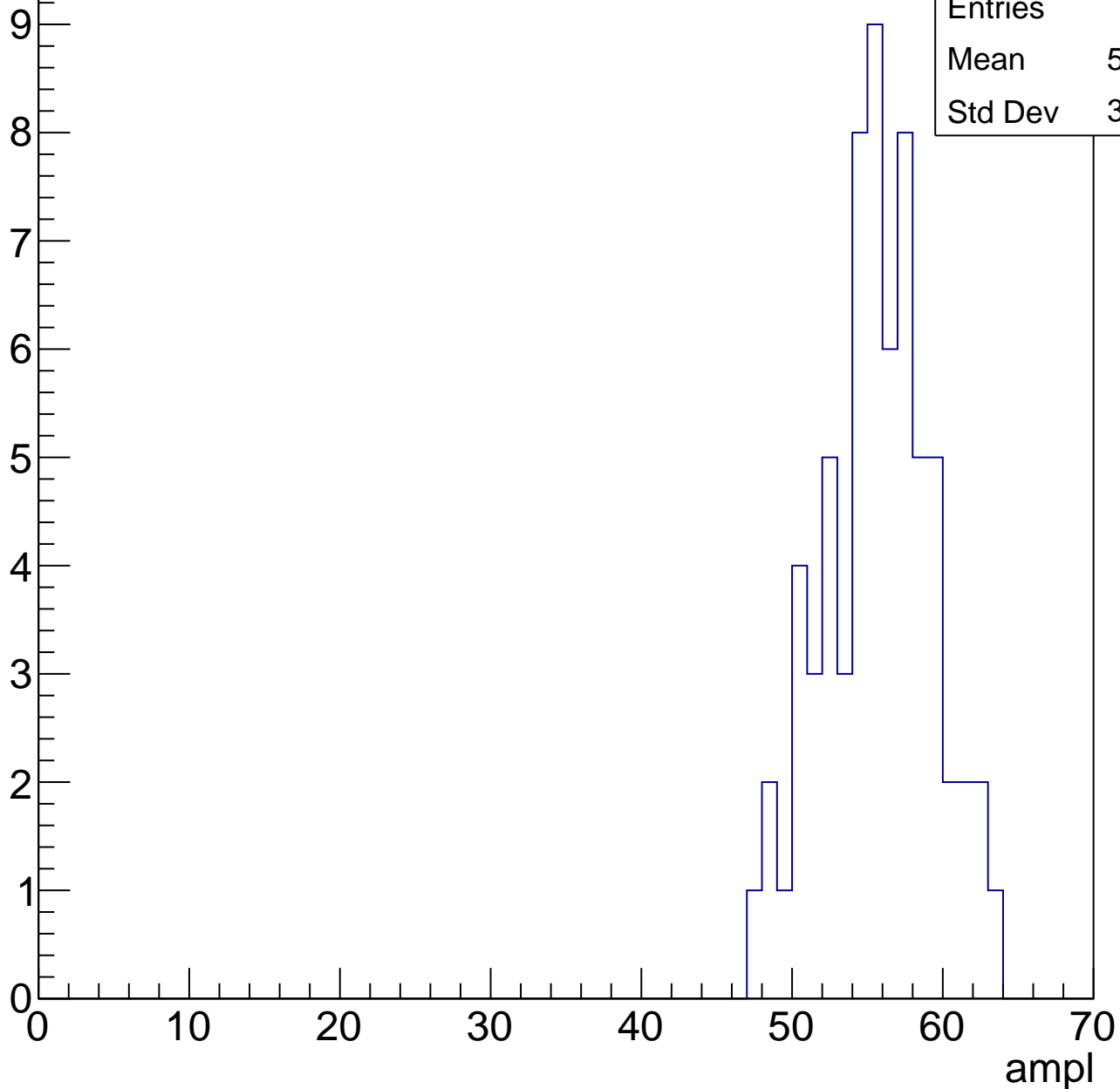
Entry



B1L103S, U20-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

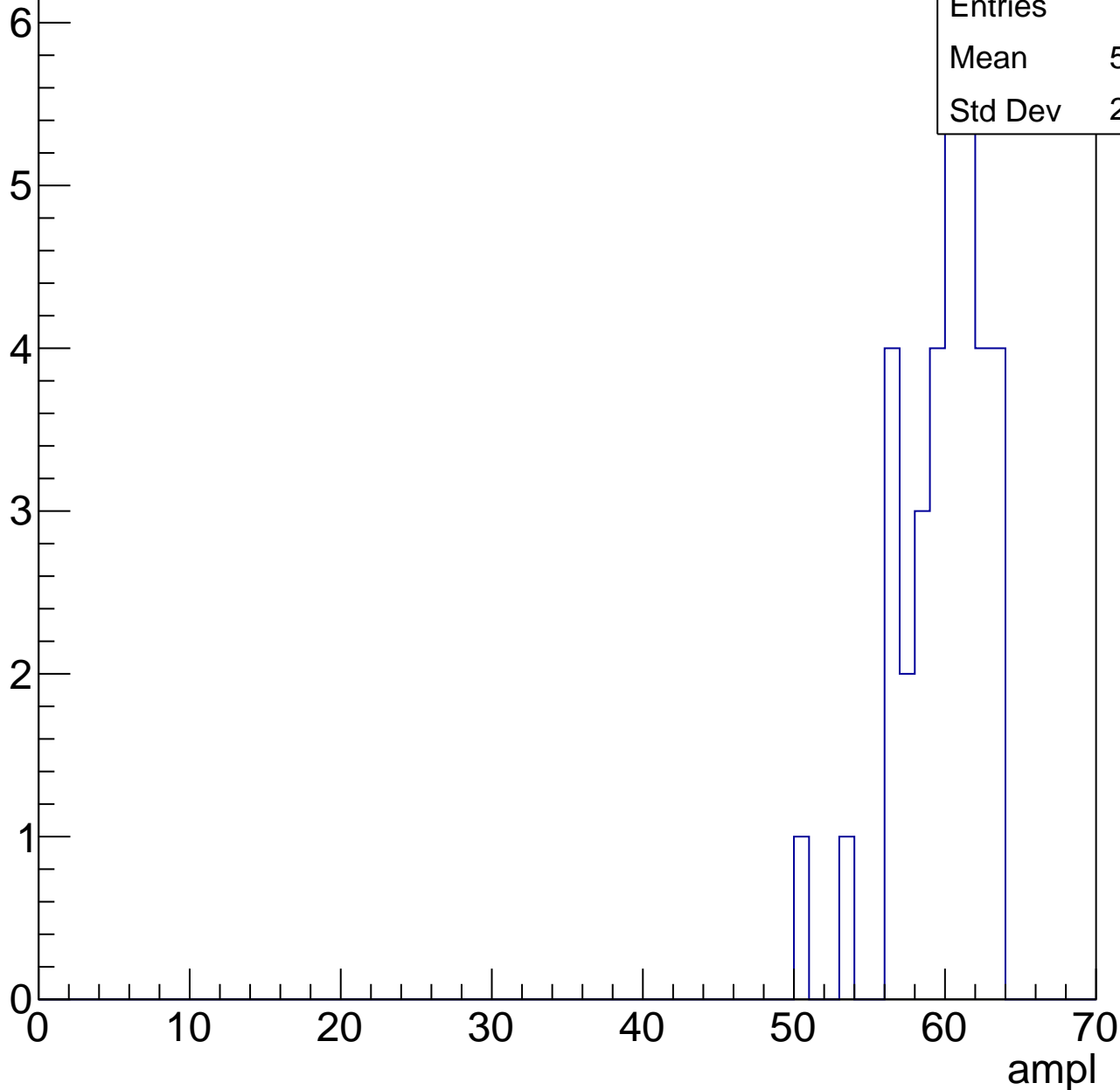


B1L103S, U20-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	59.34
Std Dev	2.878

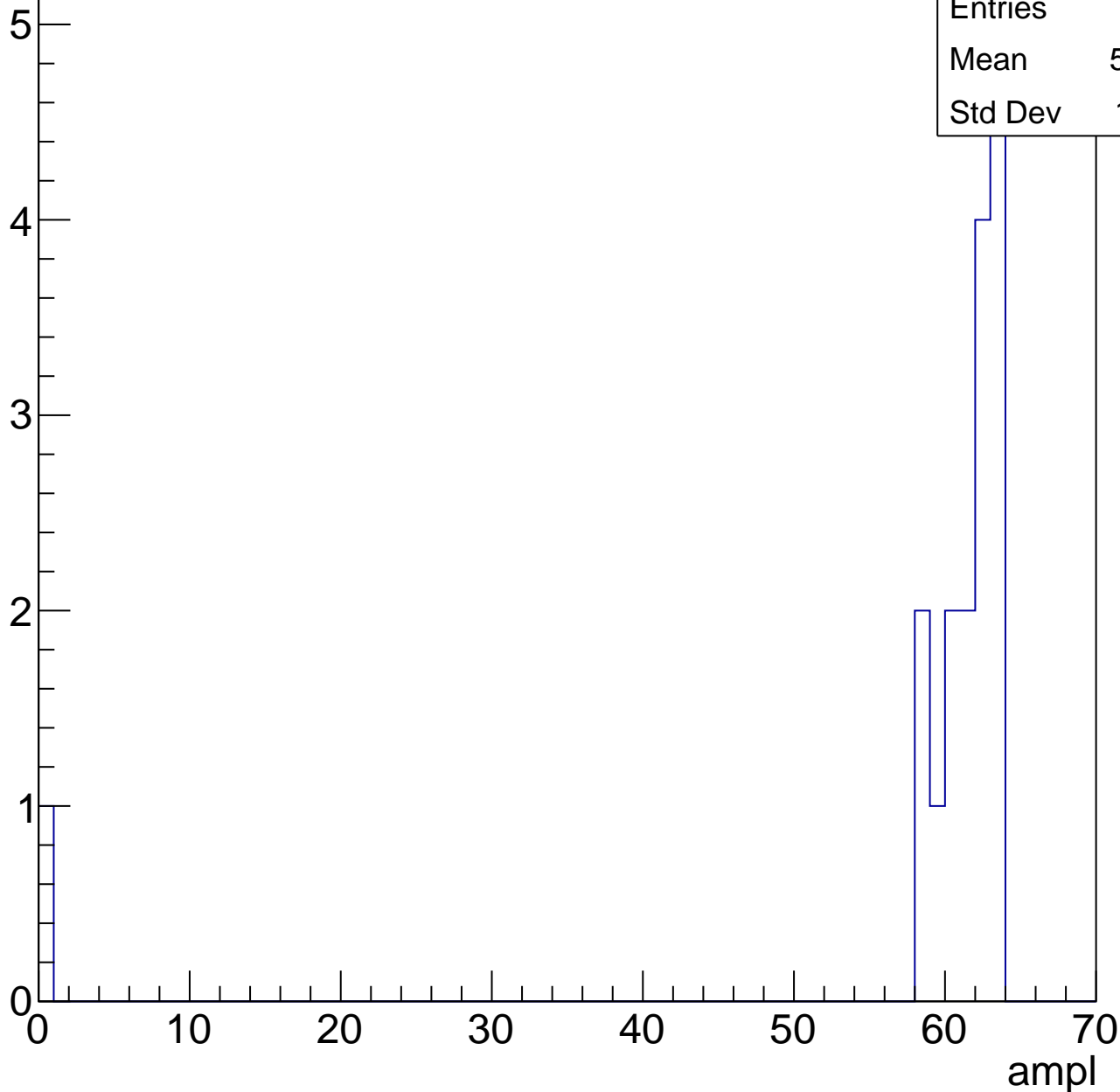


B1L103S, U20-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.65
Std Dev	14.51



B1L103S, U20-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch78, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	26.96
Std Dev	5.746

Gaus mean : 28.3668

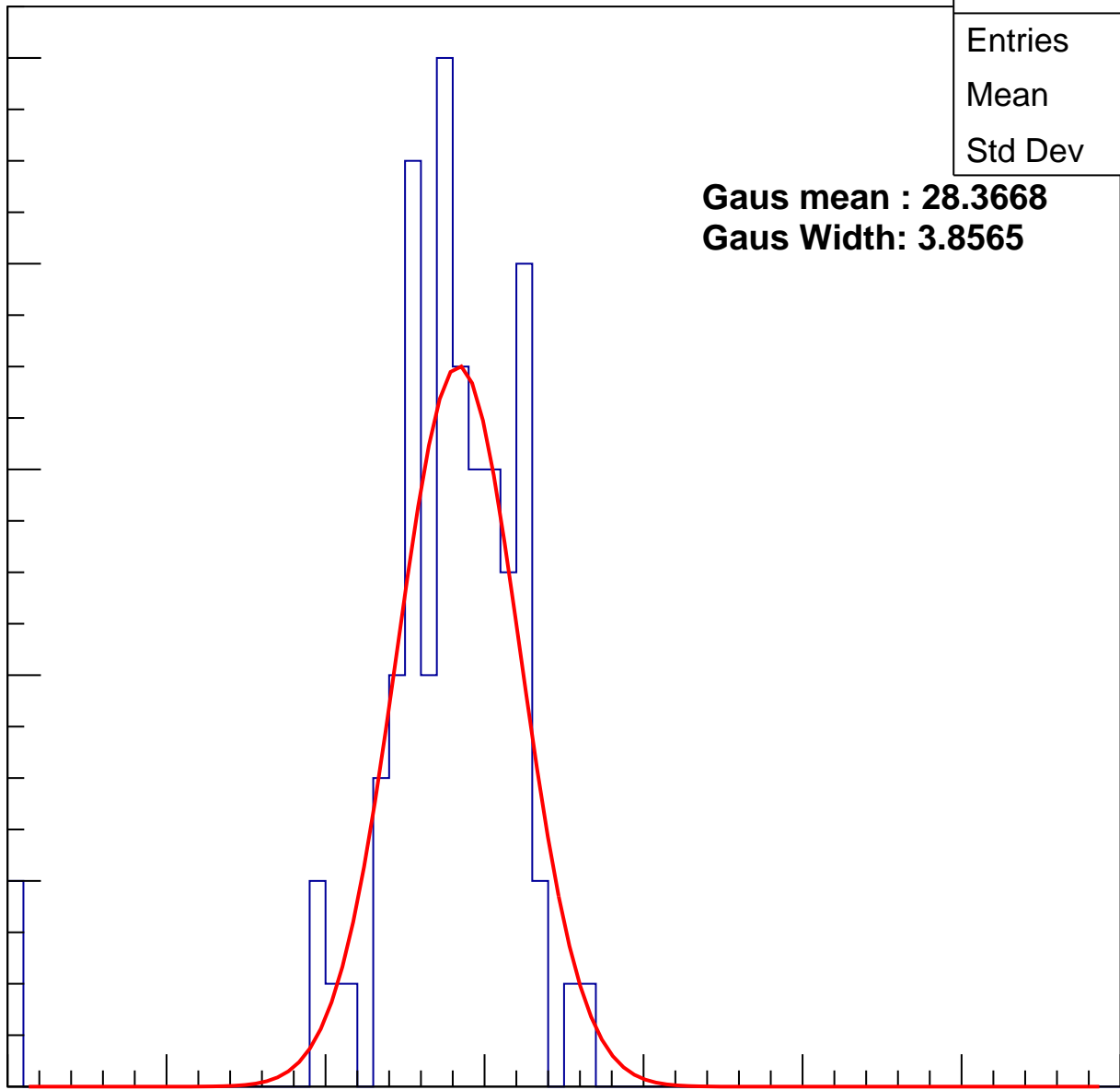
Gaus Width: 3.8565

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch78, adc1

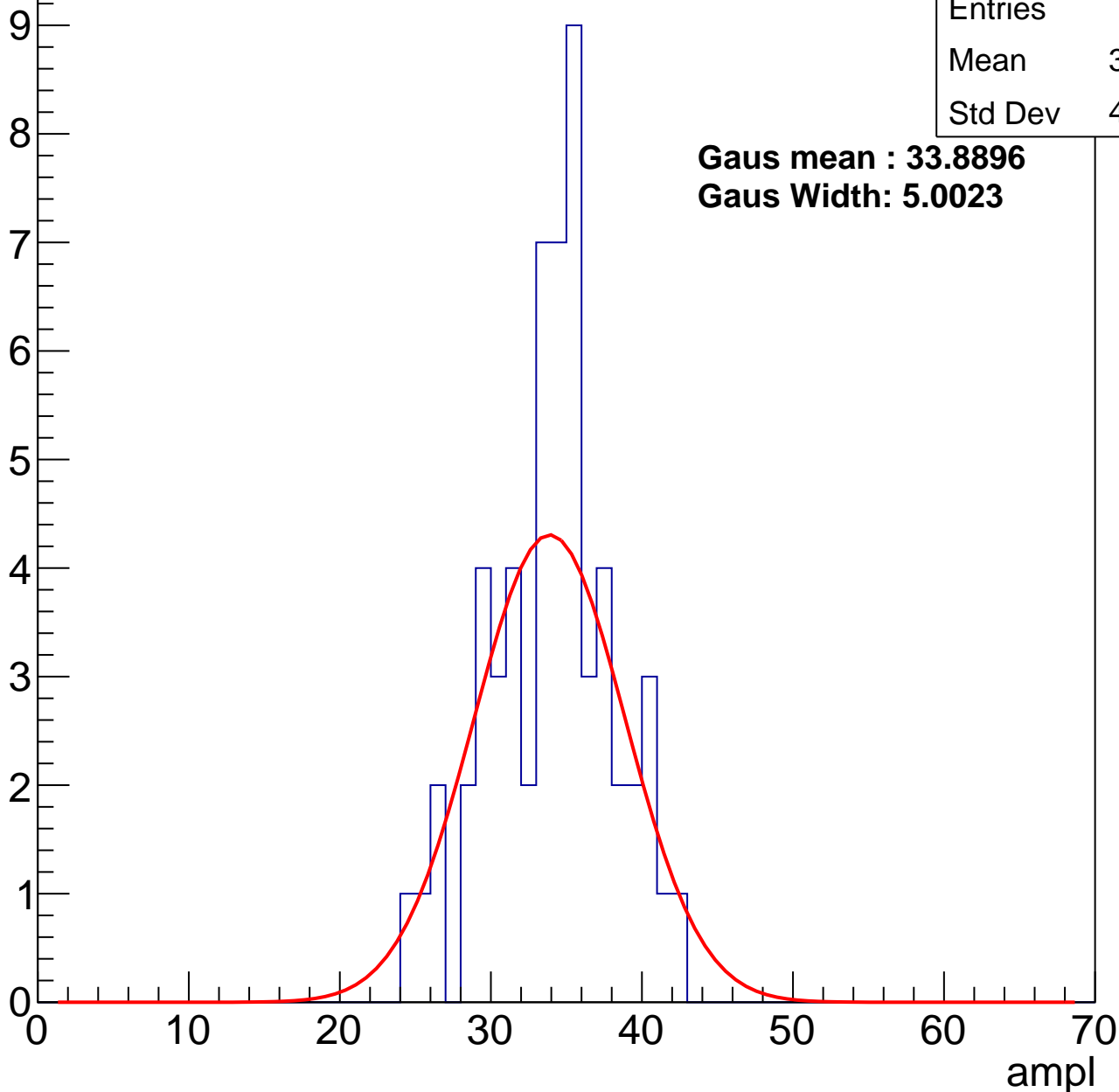
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	33.59
Std Dev	4.004

Gaus mean : 33.8896

Gaus Width: 5.0023



B1L103S, U20-ch78, adc2

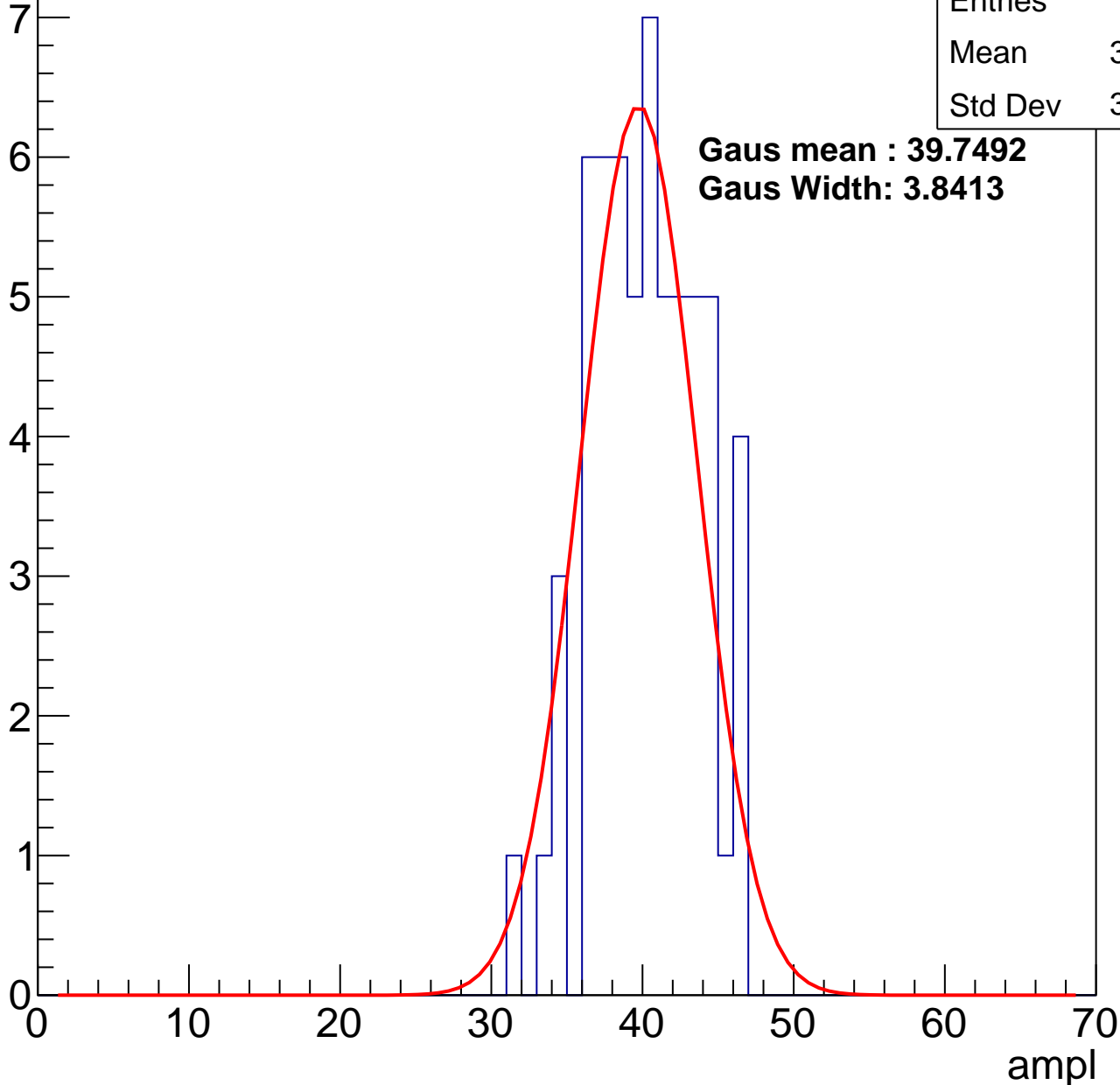
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	39.77
Std Dev	3.495

Gaus mean : 39.7492

Gaus Width: 3.8413

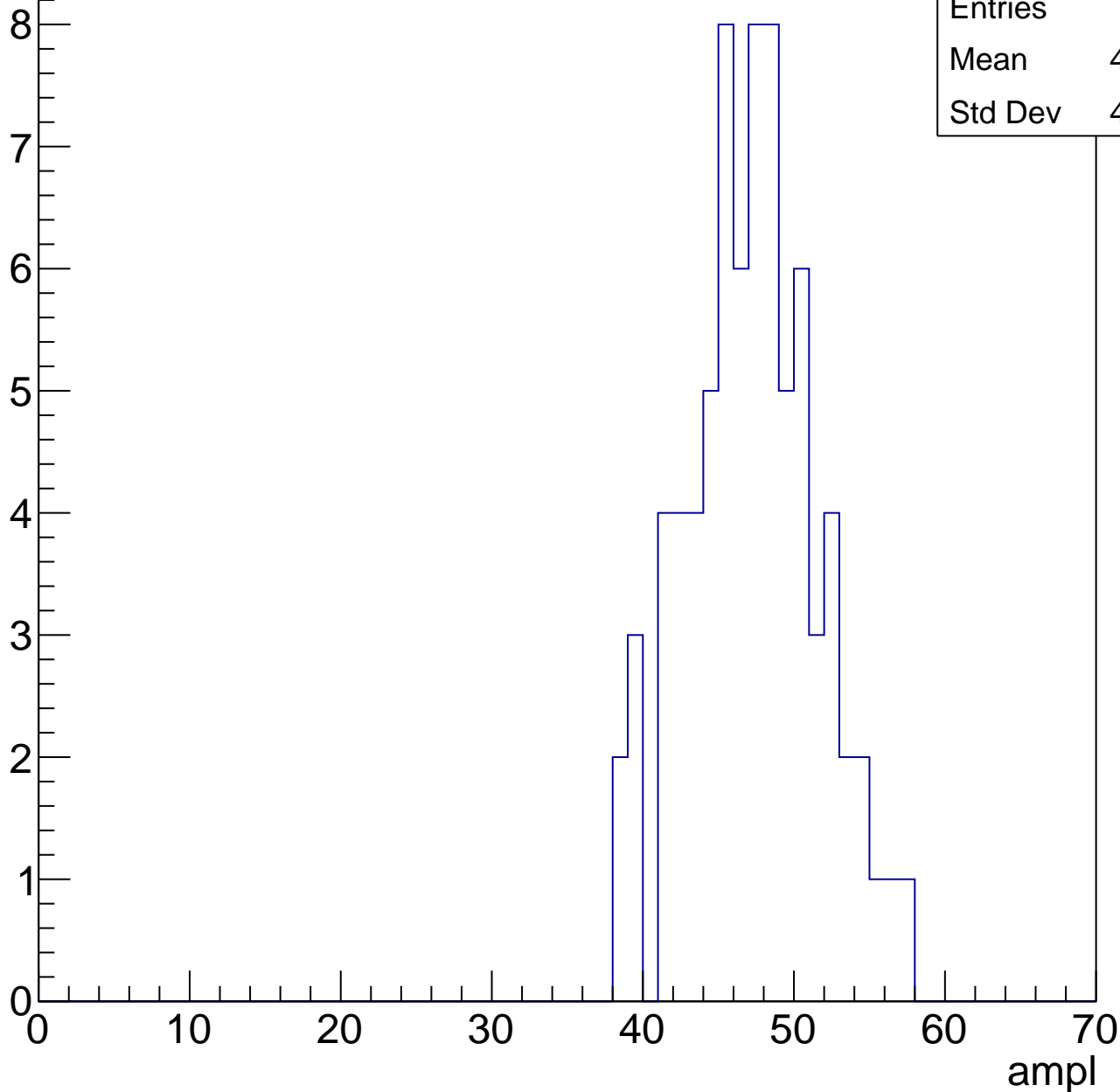


B1L103S, U20-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	46.77
Std Dev	4.242

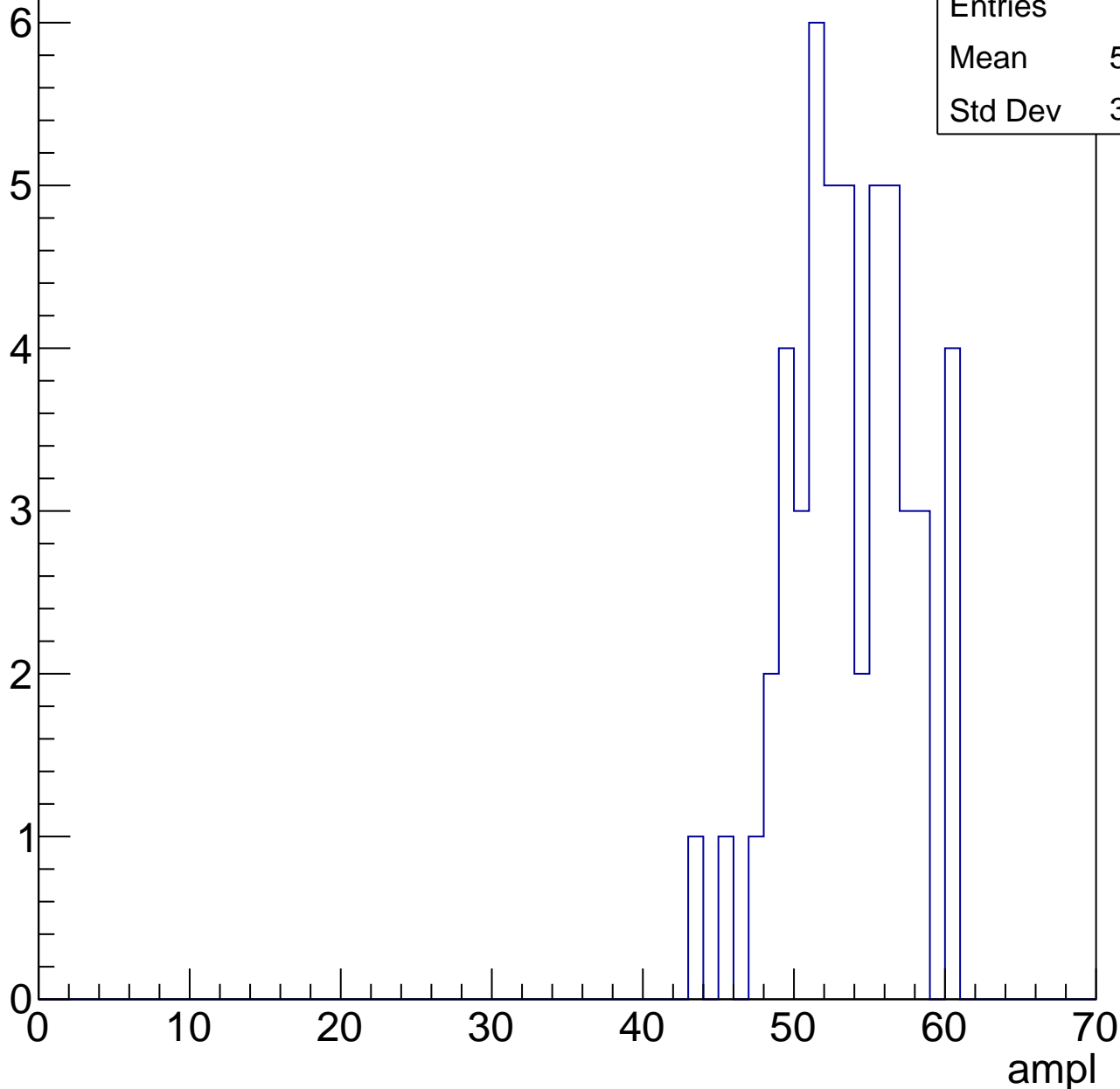


B1L103S, U20-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.12
Std Dev	3.887



B1L103S, U20-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

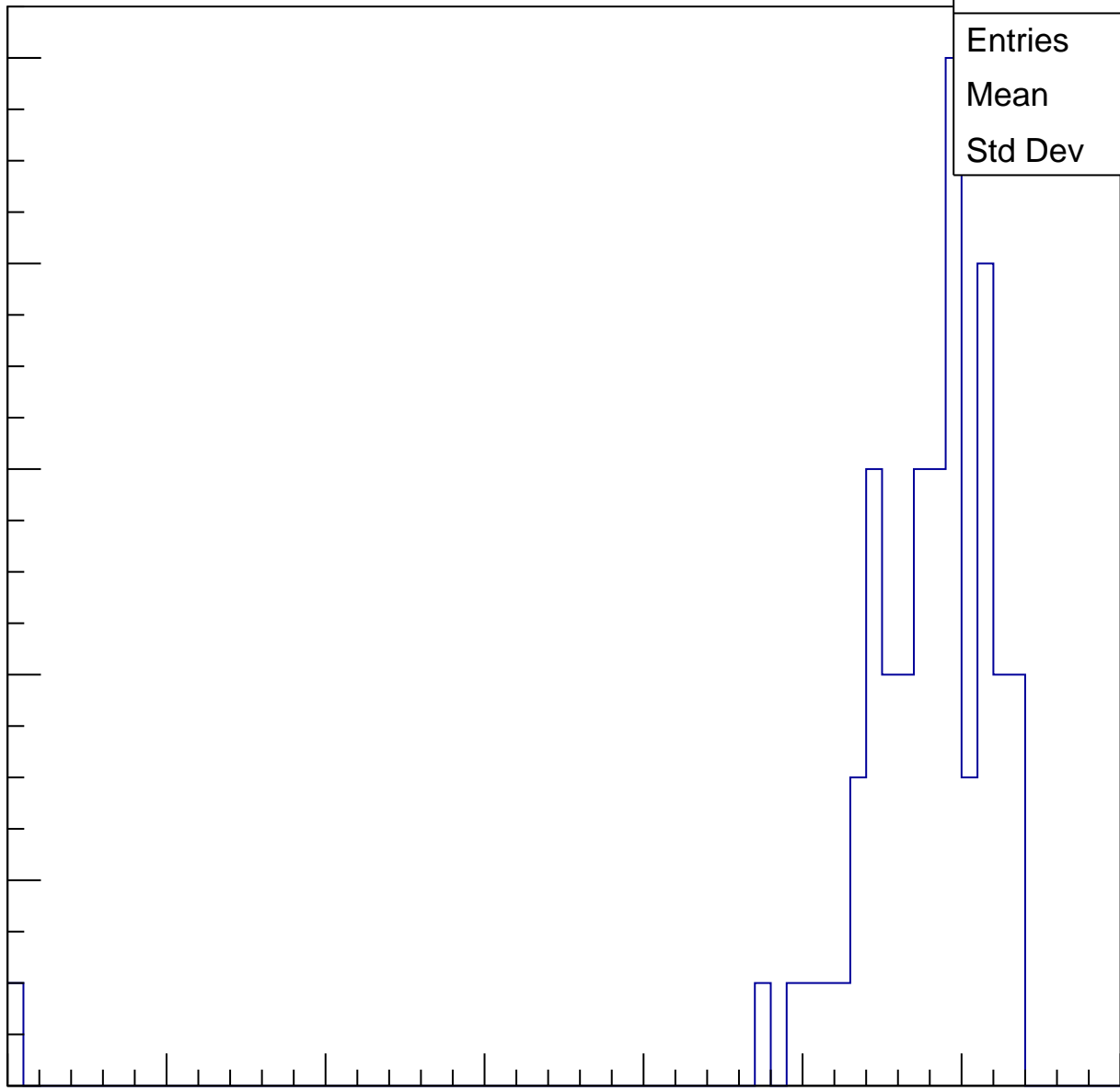
Entries	64
Mean	56.62
Std Dev	7.985

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

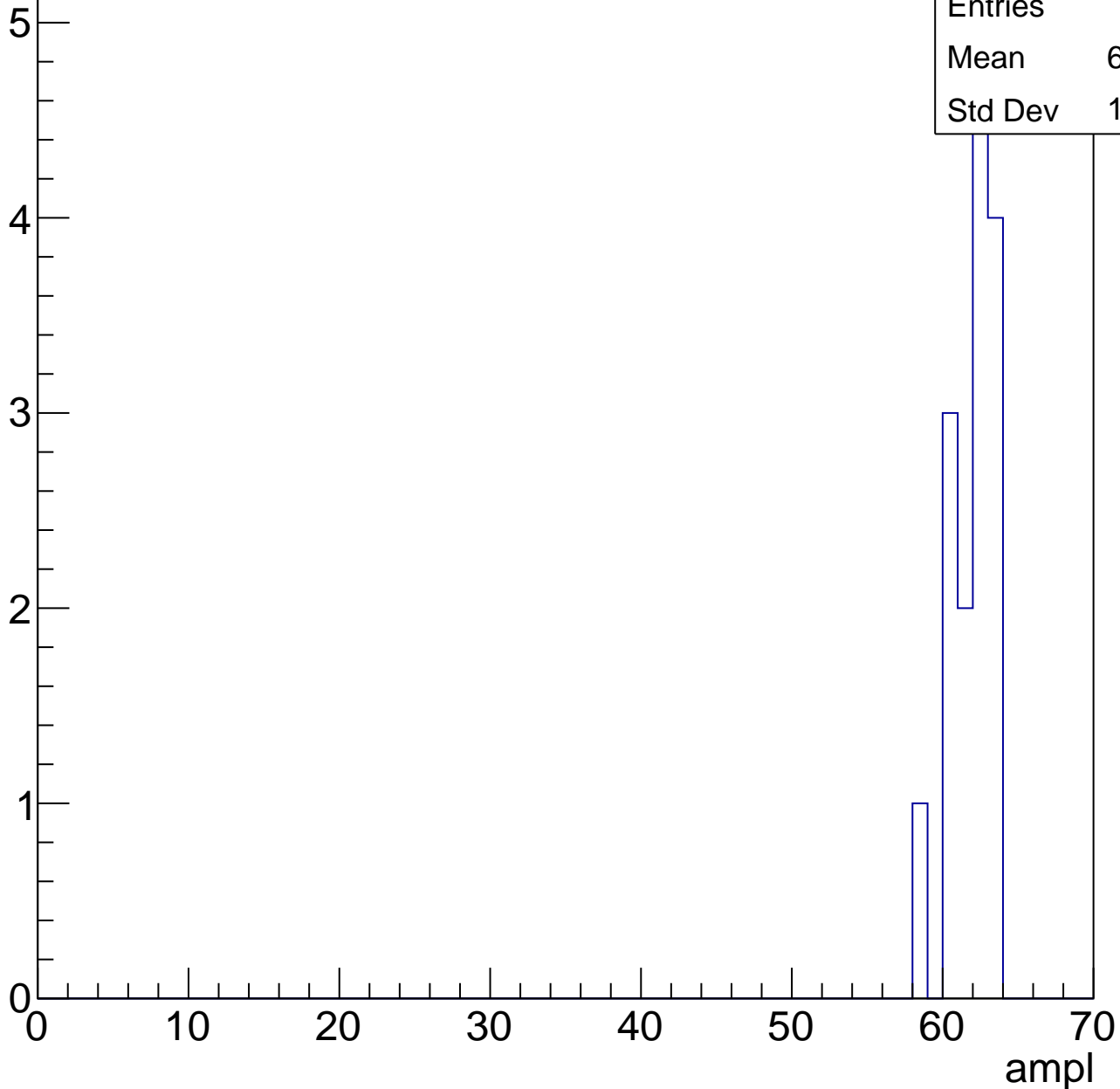


B1L103S, U20-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.47
Std Dev	1.408



B1L103S, U20-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch79, adc0

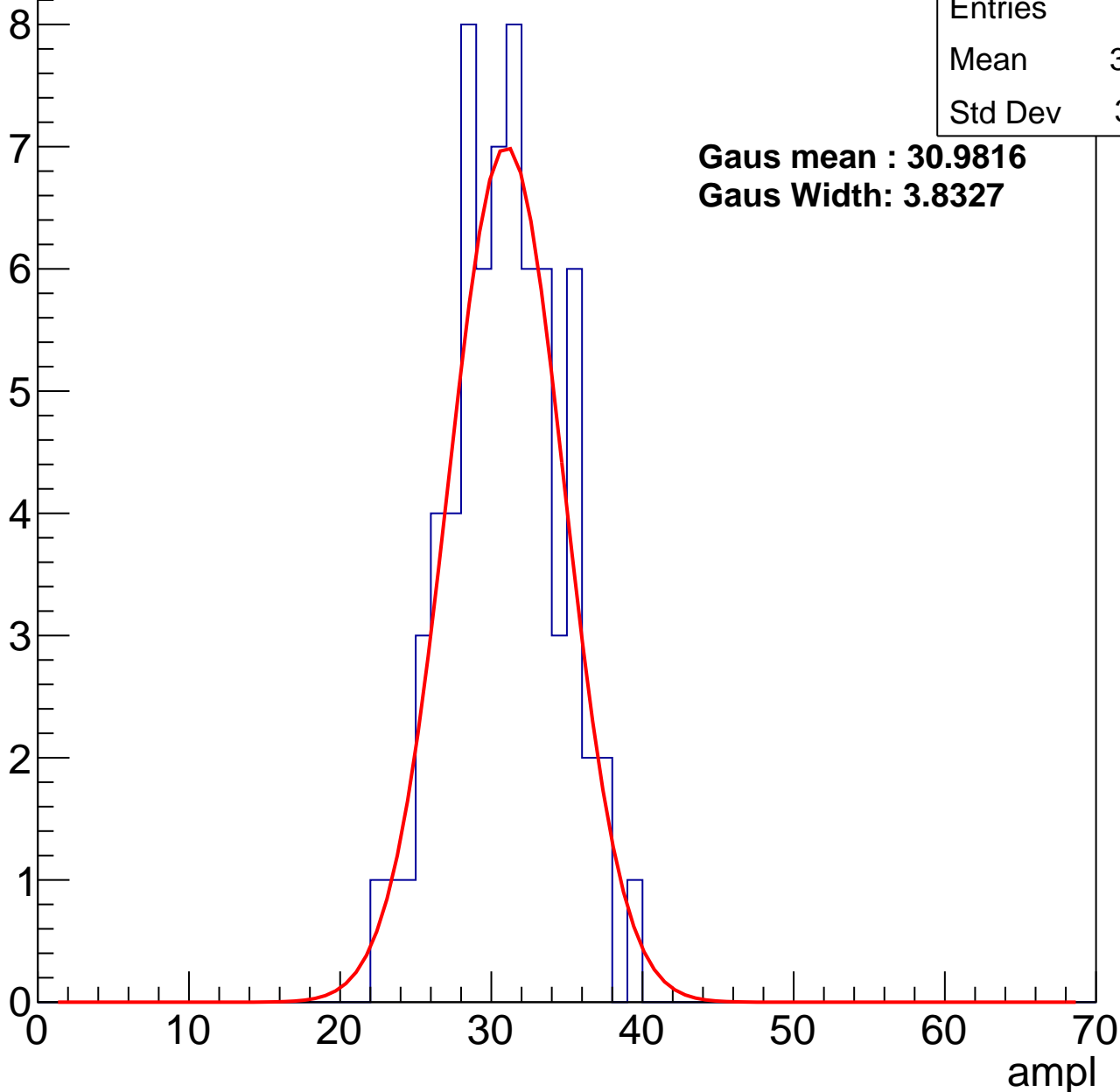
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	30.42
Std Dev	3.581

Gaus mean : 30.9816

Gaus Width: 3.8327



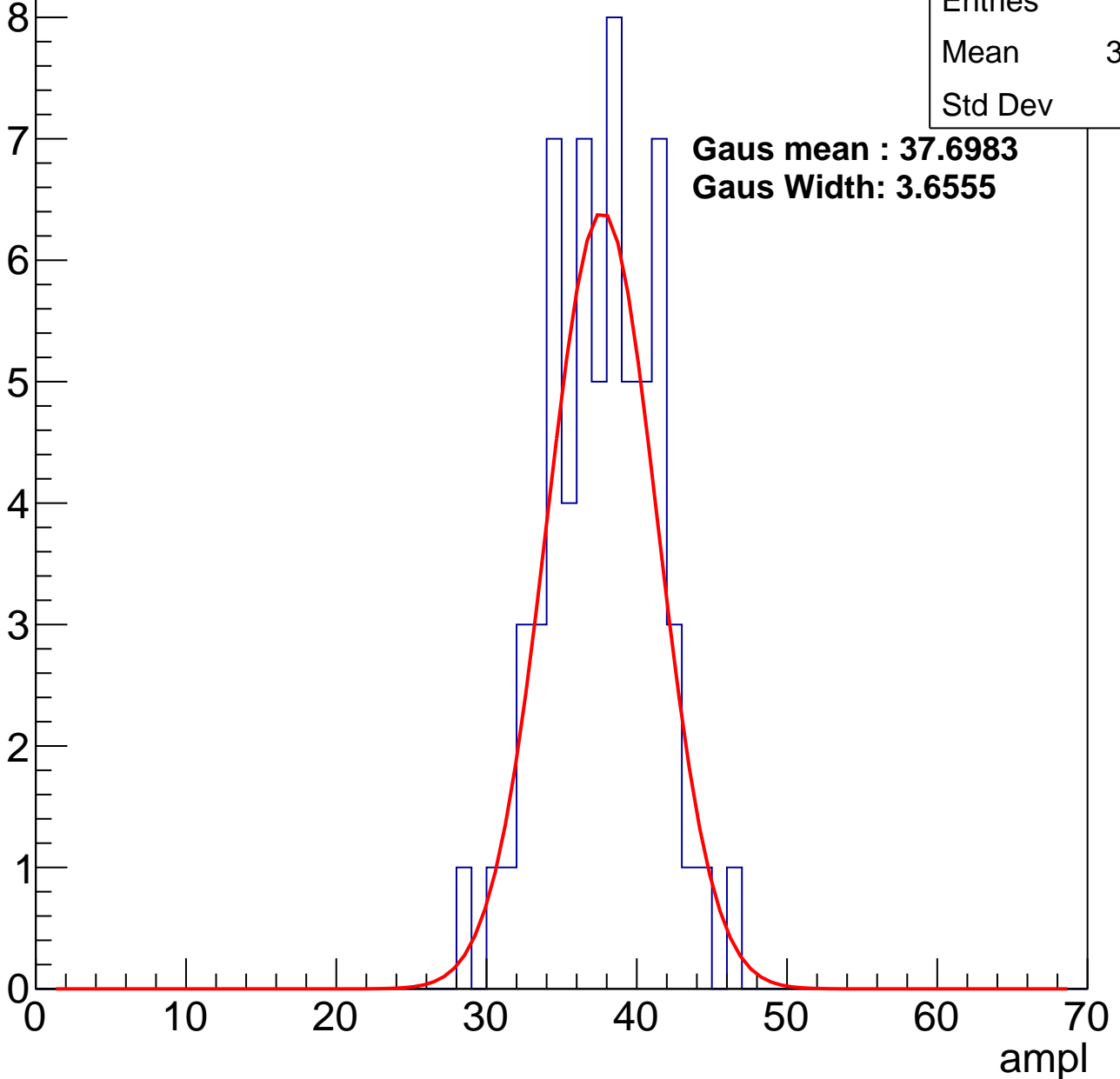
B1L103S, U20-ch79, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.21
Std Dev	3.56

Gaus mean : 37.6983
Gaus Width: 3.6555



B1L103S, U20-ch79, adc2

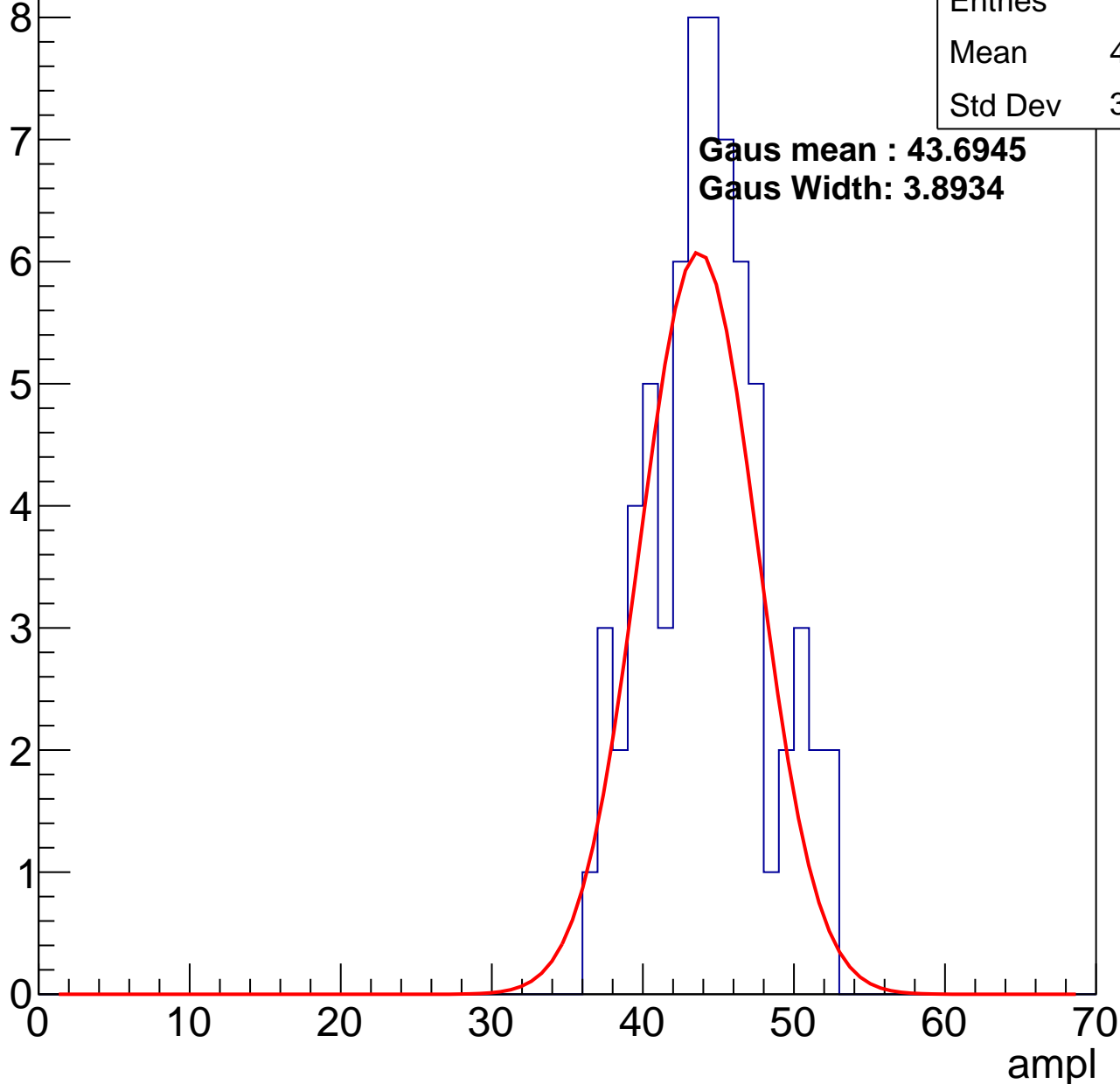
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.79
Std Dev	3.825

Gaus mean : 43.6945

Gaus Width: 3.8934

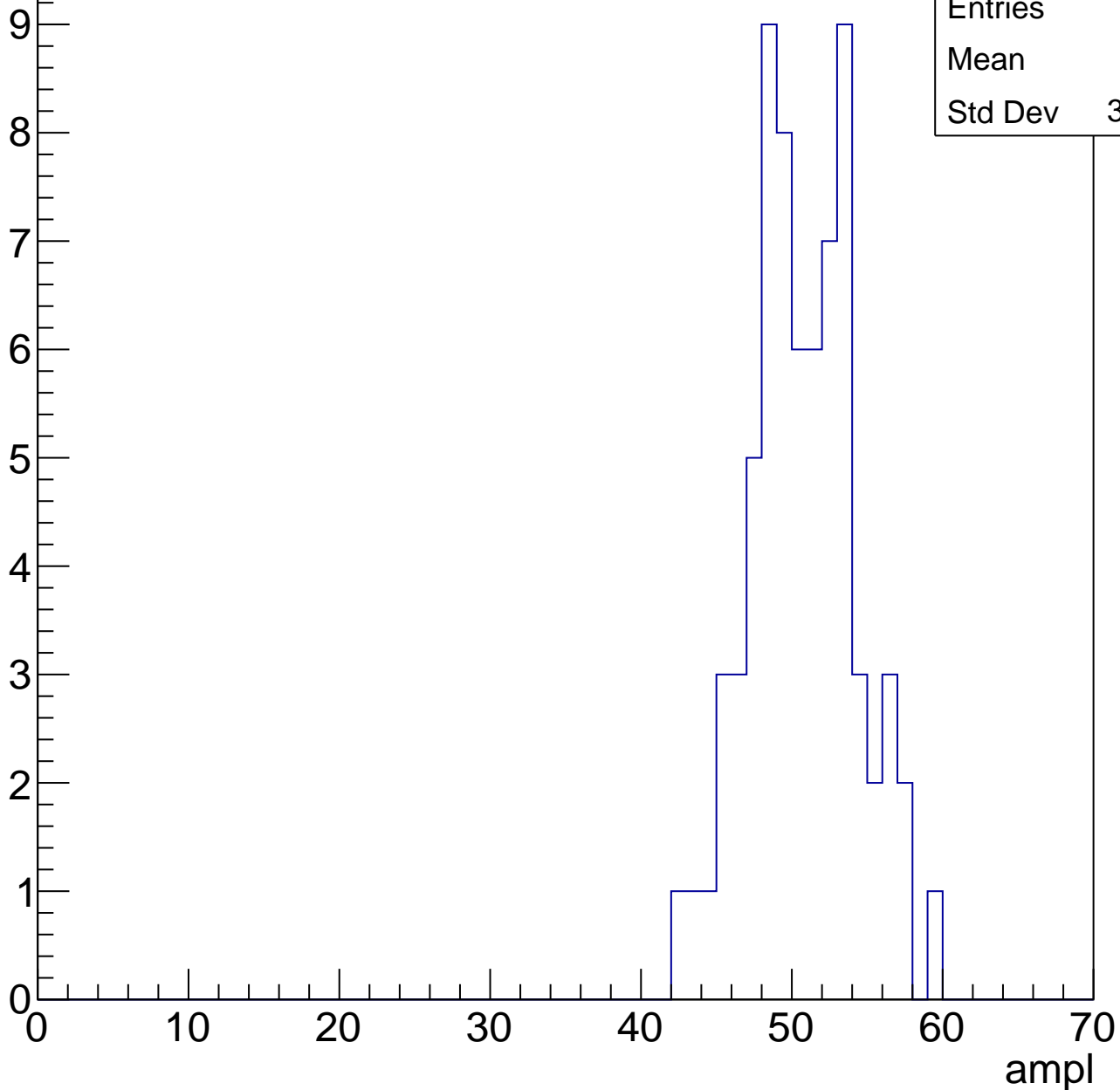


B1L103S, U20-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	50.3
Std Dev	3.494

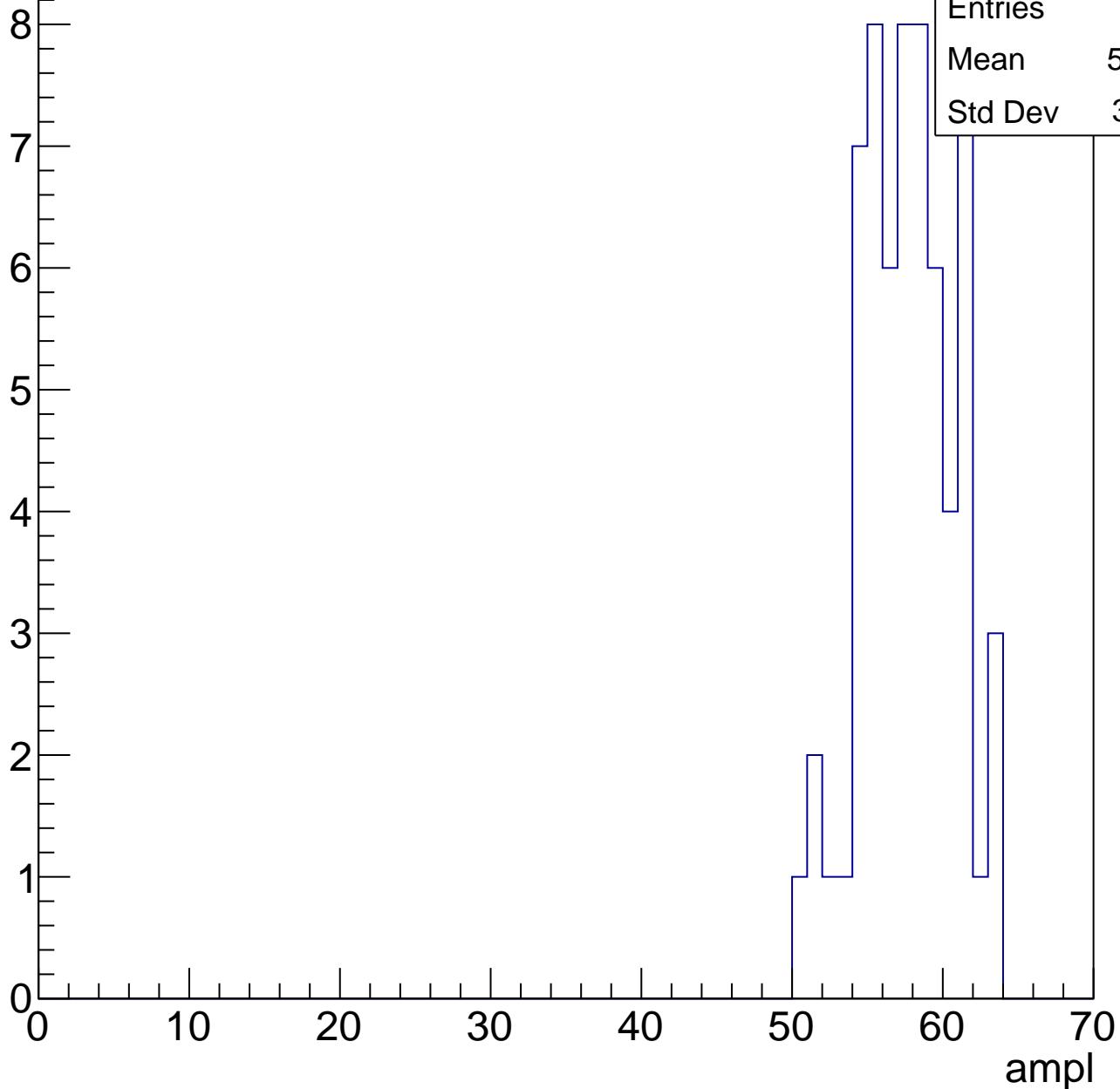


B1L103S, U20-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	57.25
Std Dev	3.031

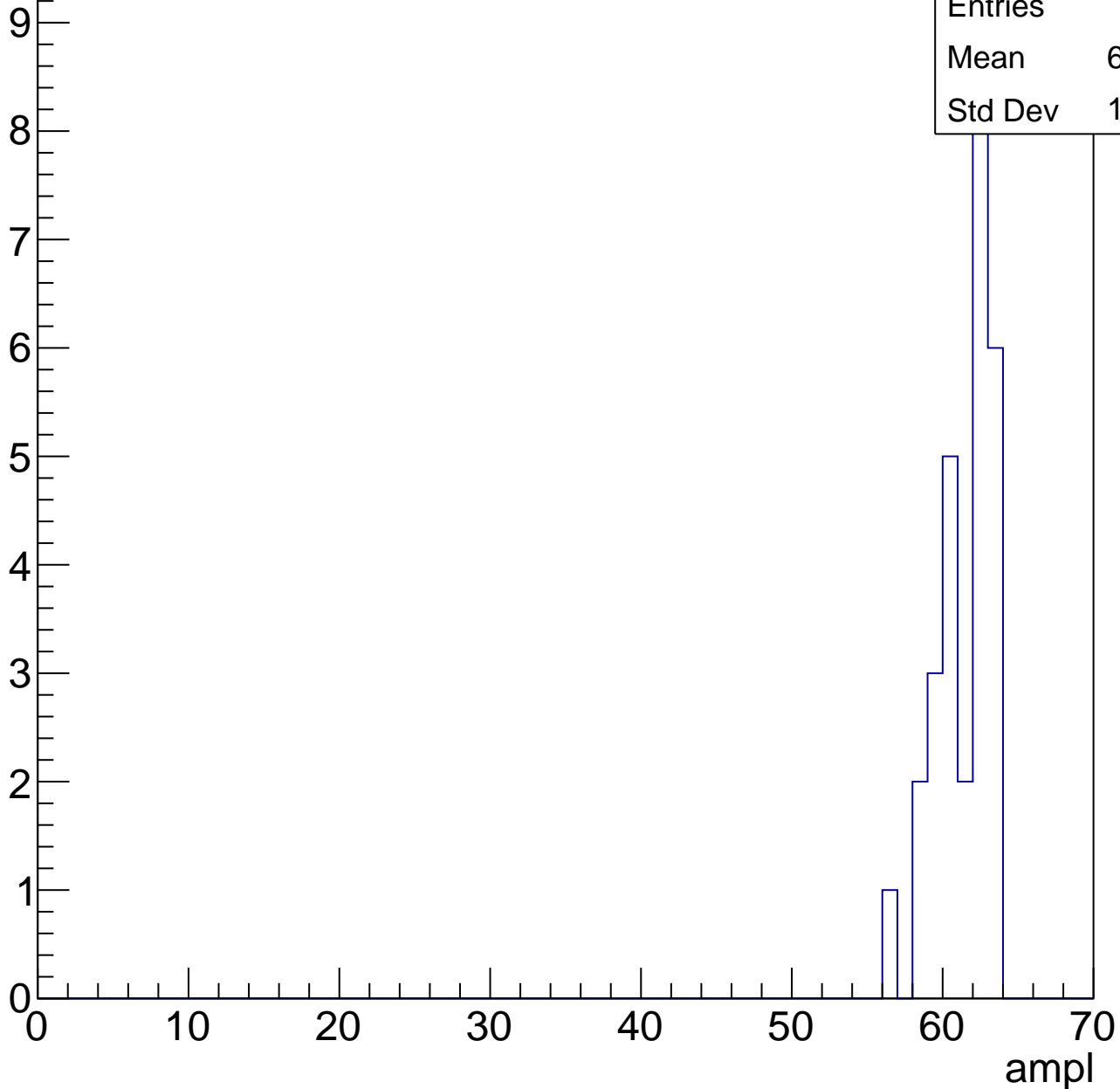


B1L103S, U20-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

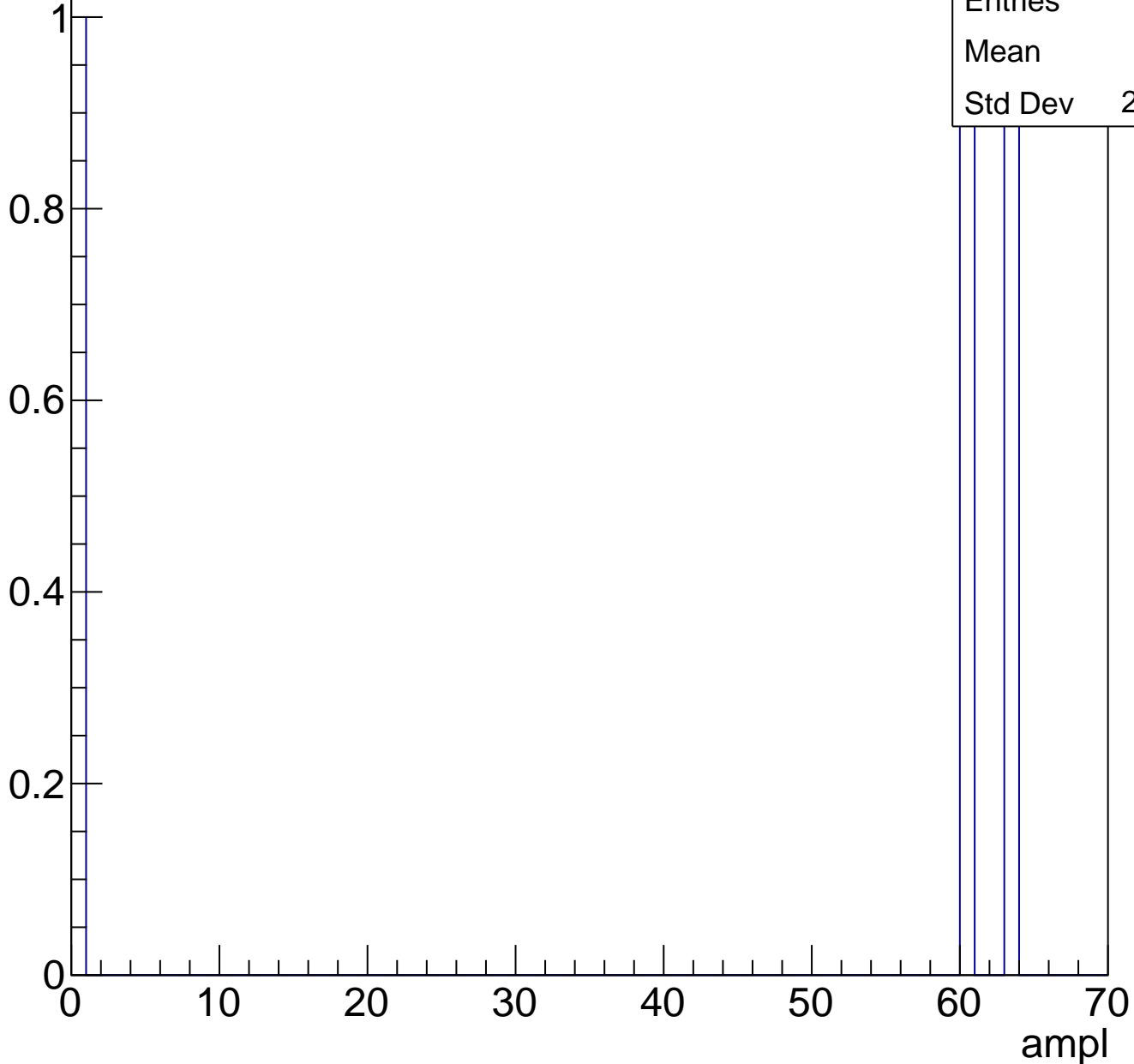
Entries	28
Mean	60.96
Std Dev	1.822



B1L103S, U20-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch80, adc0

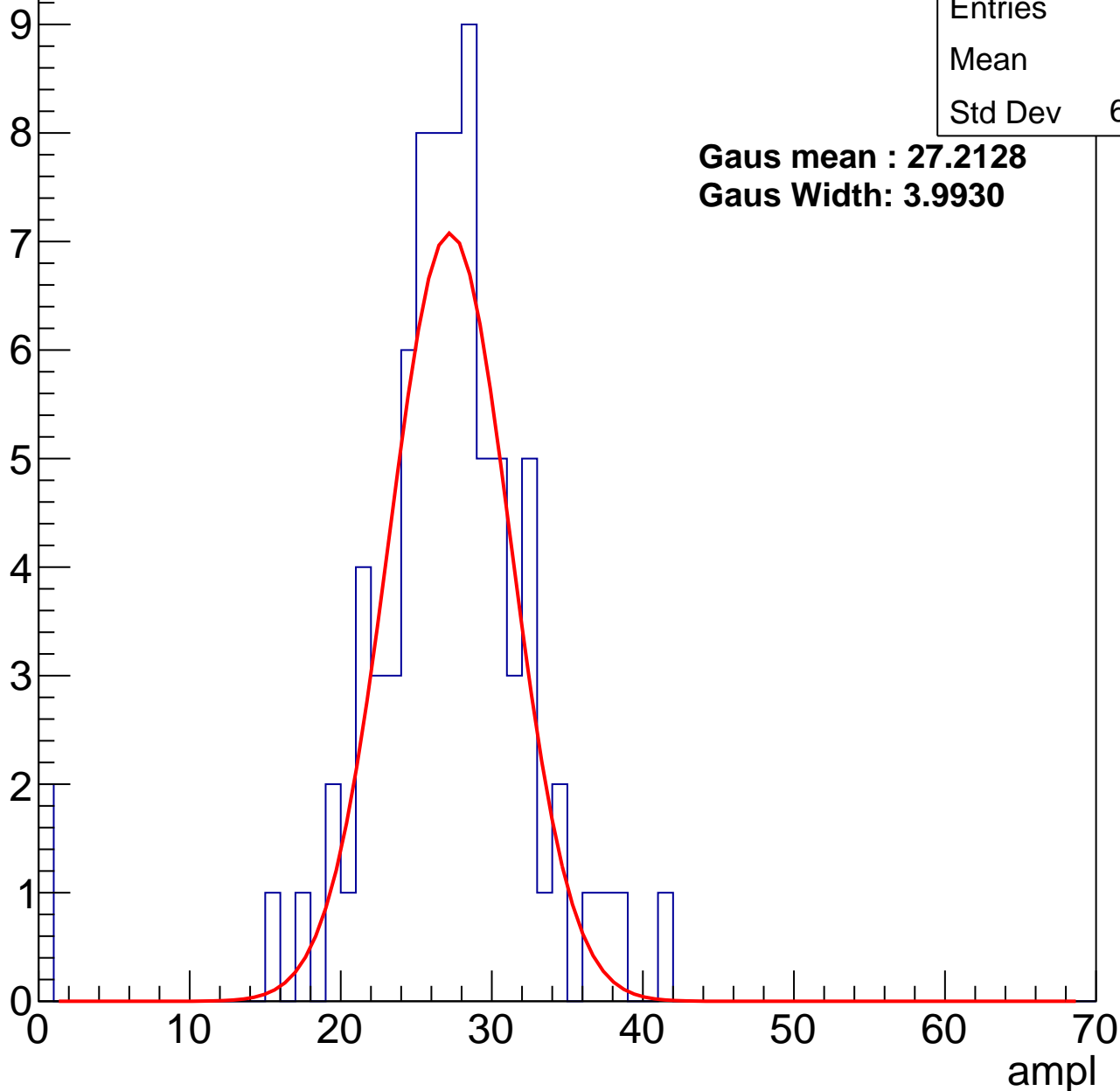
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	26.3
Std Dev	6.165

Gaus mean : 27.2128

Gaus Width: 3.9930



B1L103S, U20-ch80, adc1

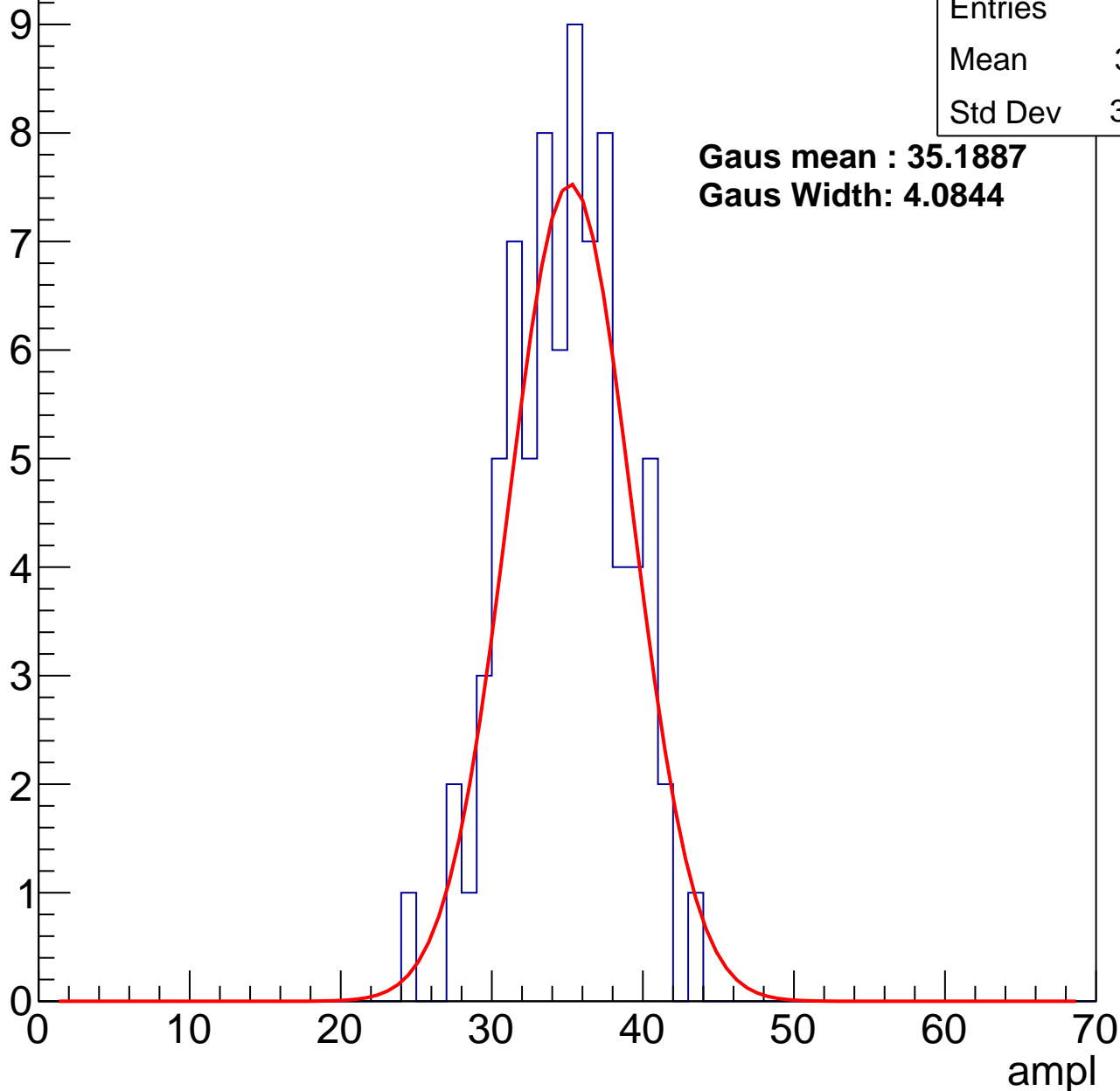
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.41
Std Dev	3.753

Gaus mean : 35.1887

Gaus Width: 4.0844



B1L103S, U20-ch80, adc2

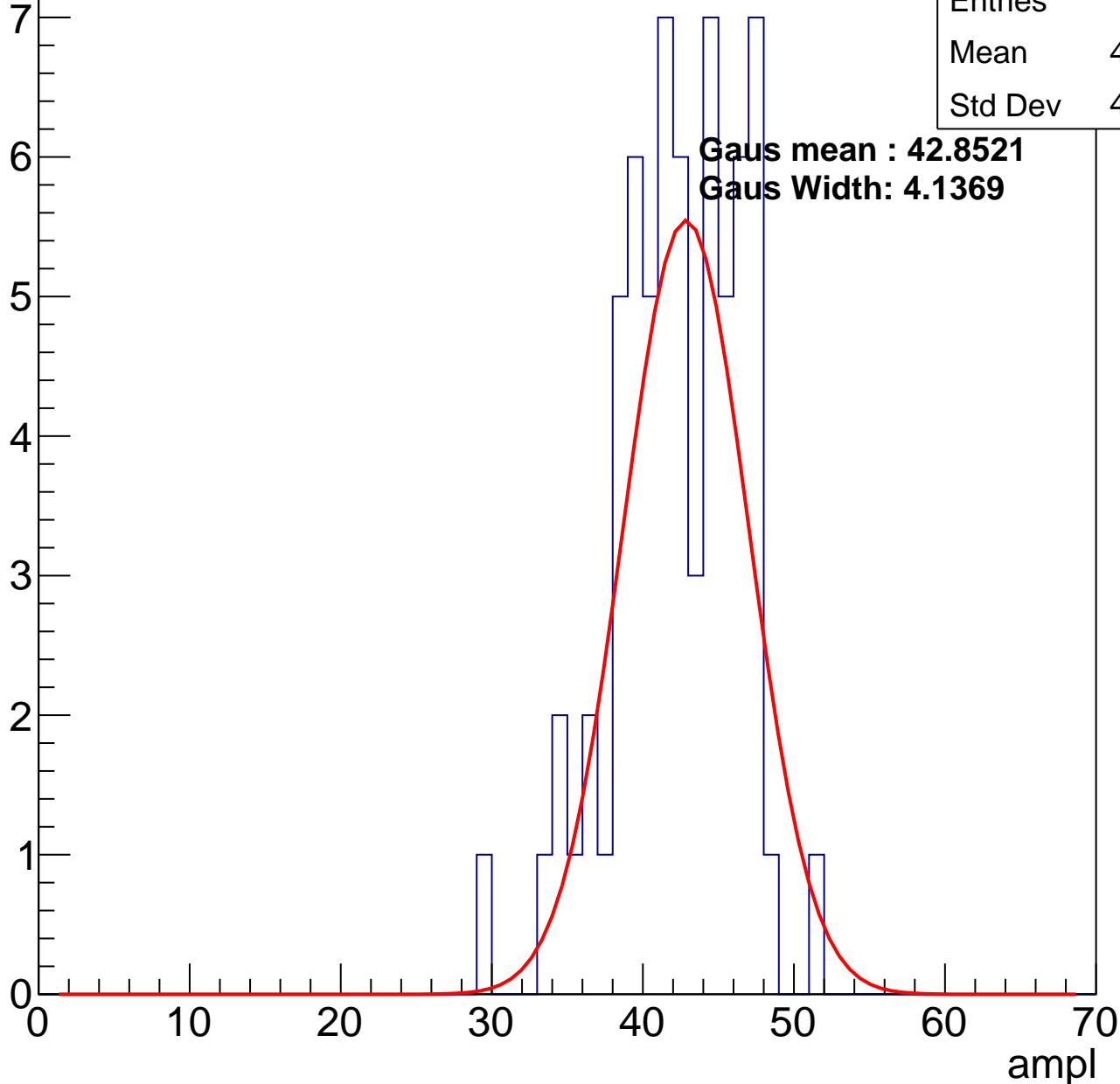
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	41.84
Std Dev	4.138

Gaus mean : 42.8521

Gaus Width: 4.1369

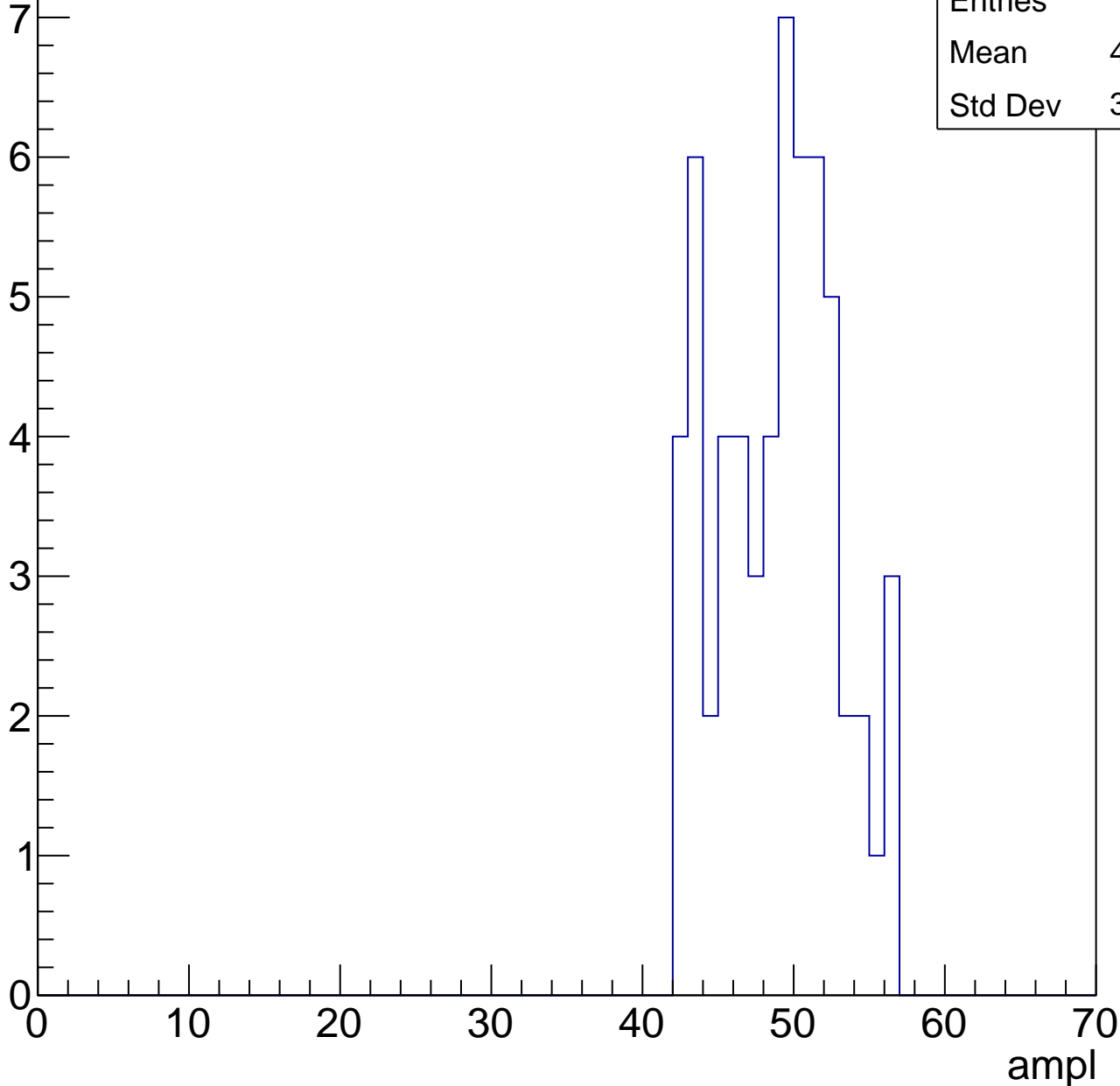


B1L103S, U20-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.42
Std Dev	3.902

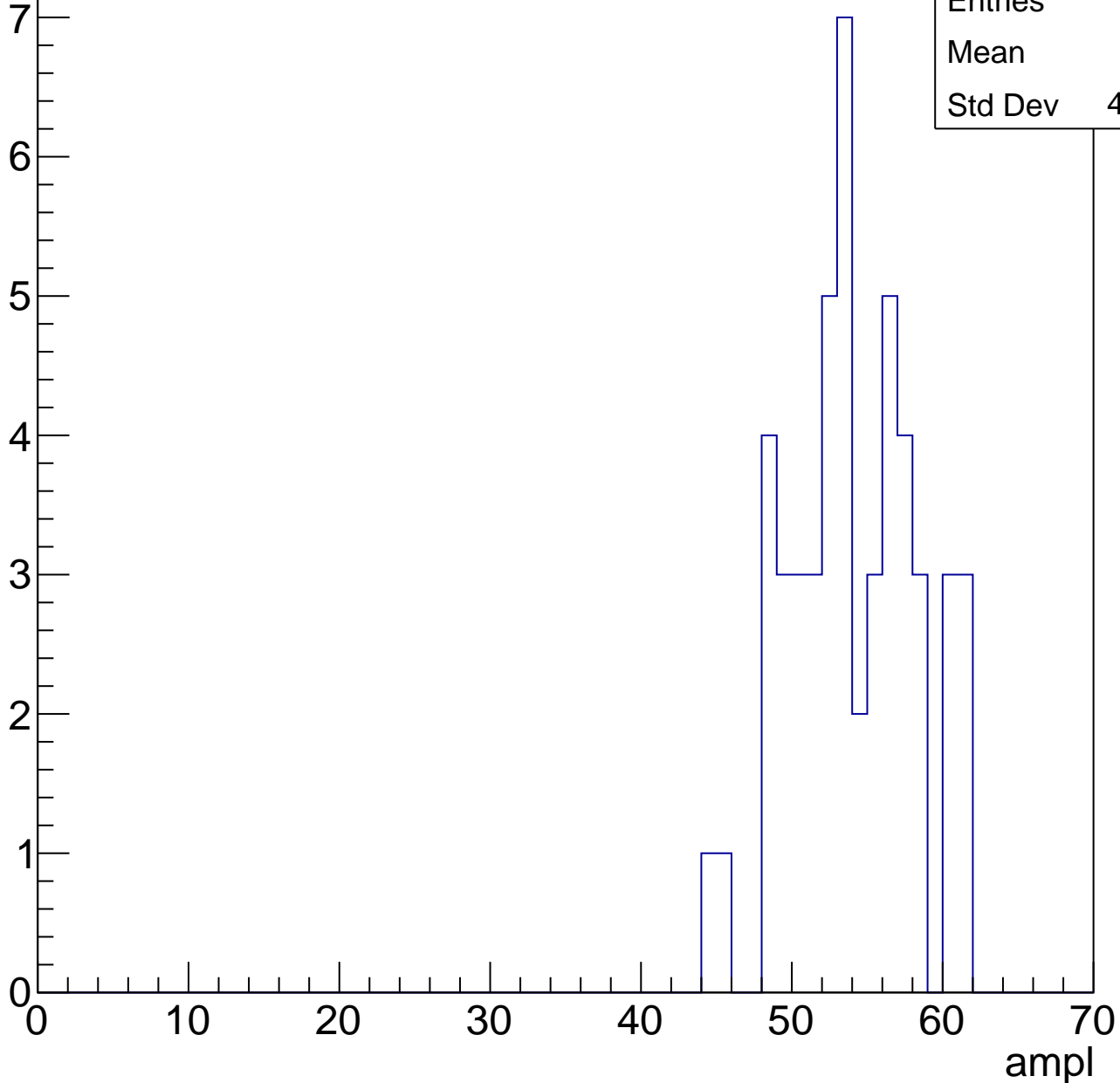


B1L103S, U20-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.6
Std Dev	4.138

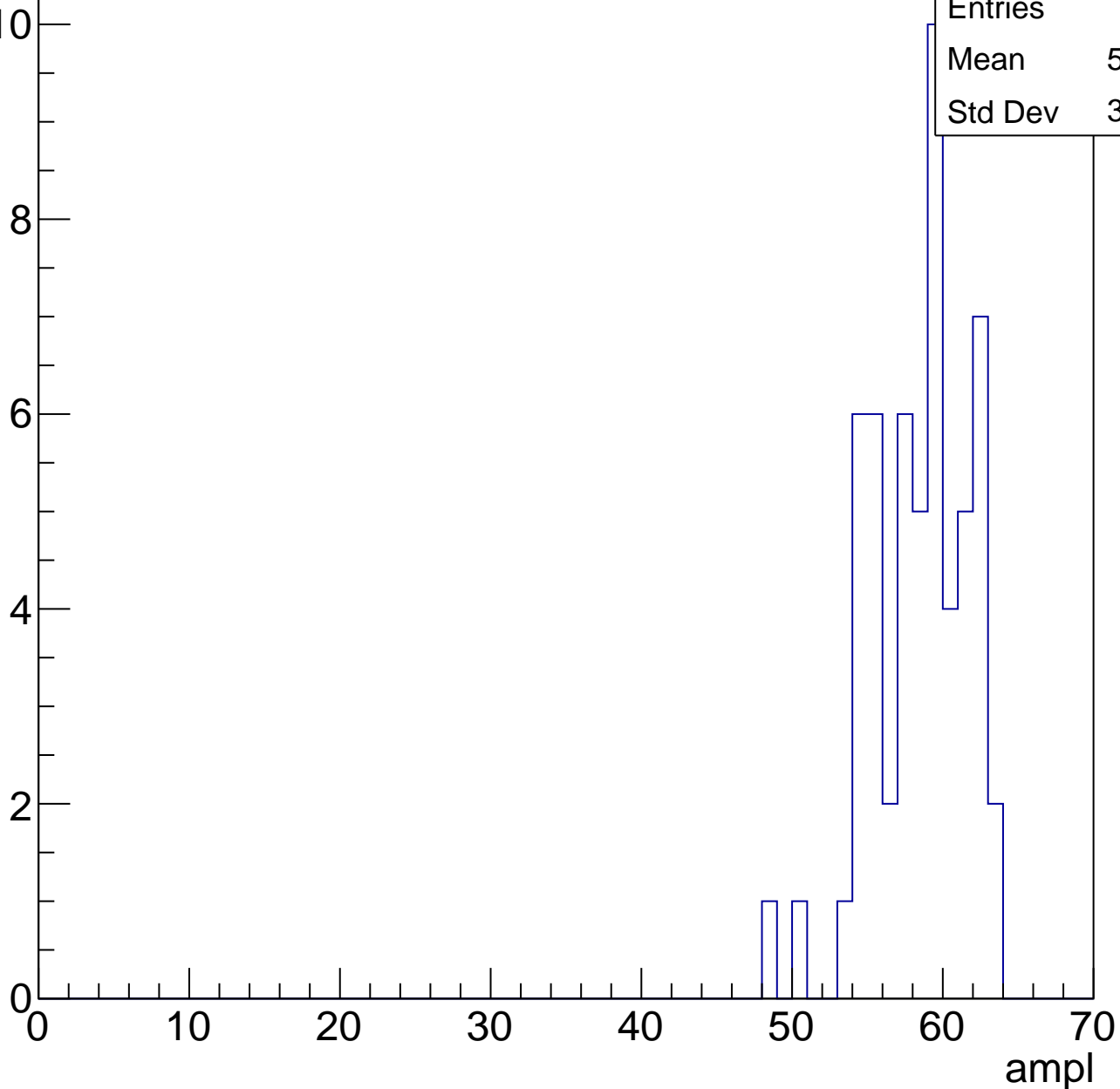


B1L103S, U20-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	57.93
Std Dev	3.234

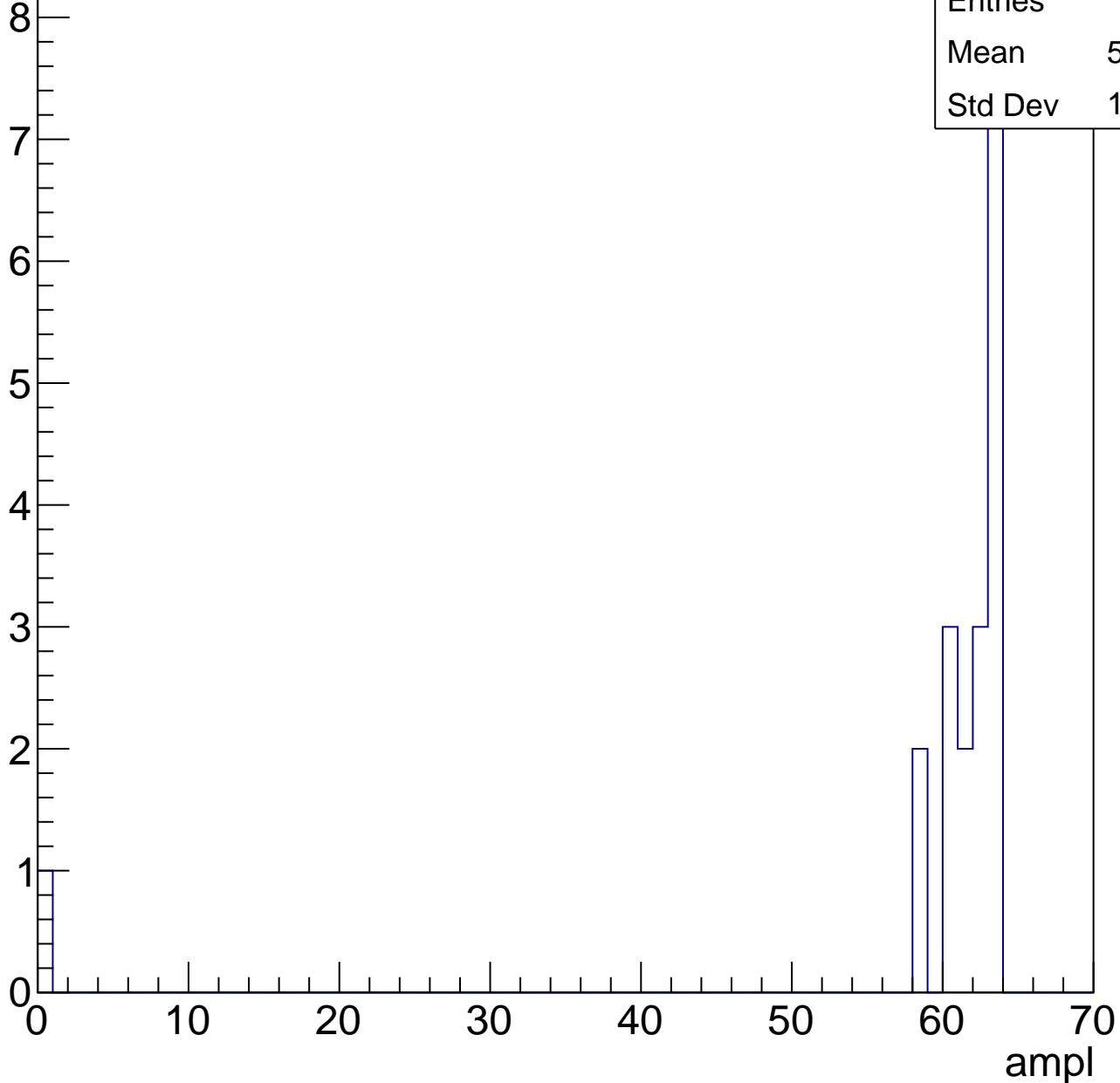


B1L103S, U20-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	58.32
Std Dev	13.84



B1L103S, U20-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch81, adc0

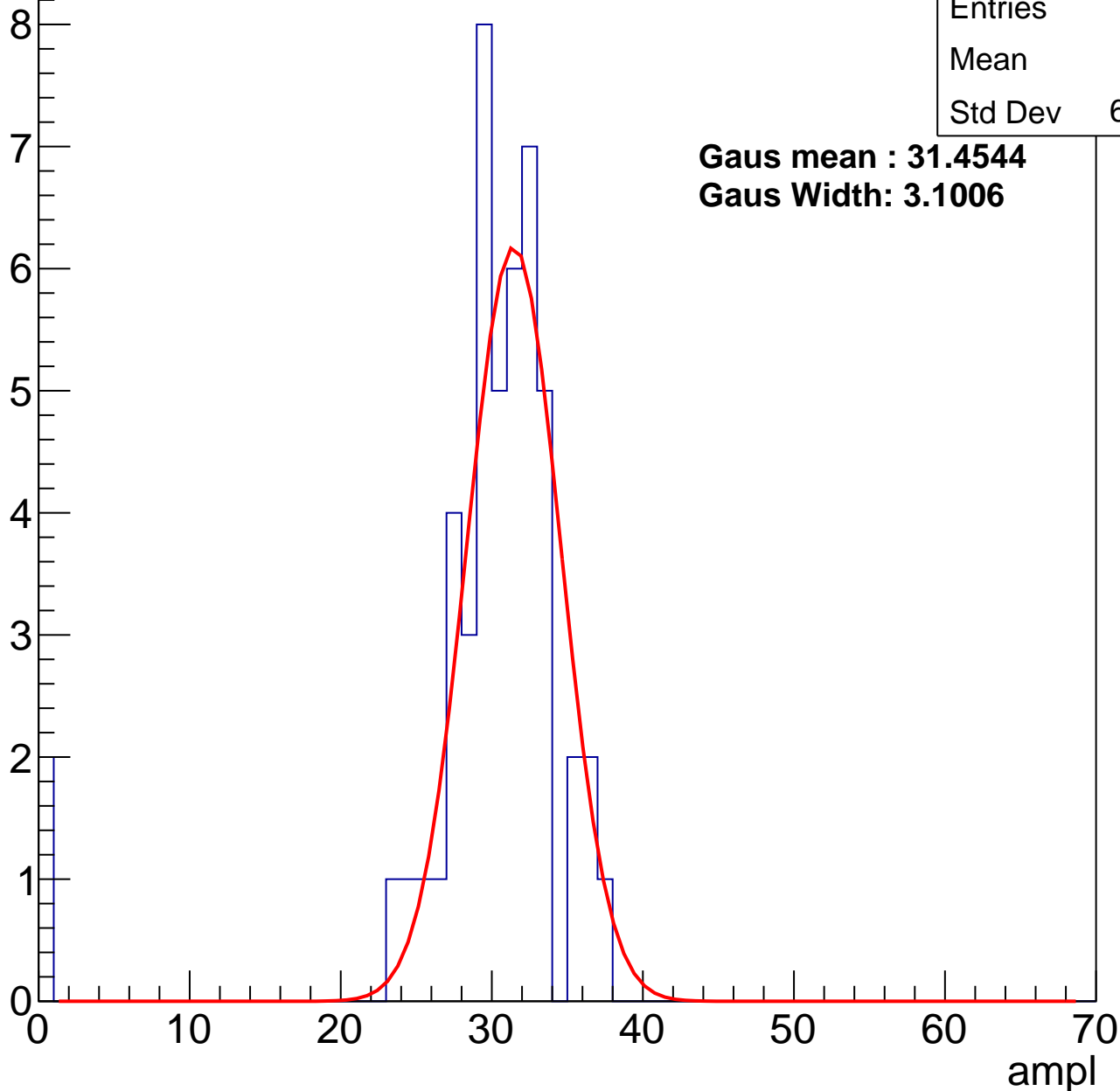
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	29.1
Std Dev	6.689

Gaus mean : 31.4544

Gaus Width: 3.1006



B1L103S, U20-ch81, adc1

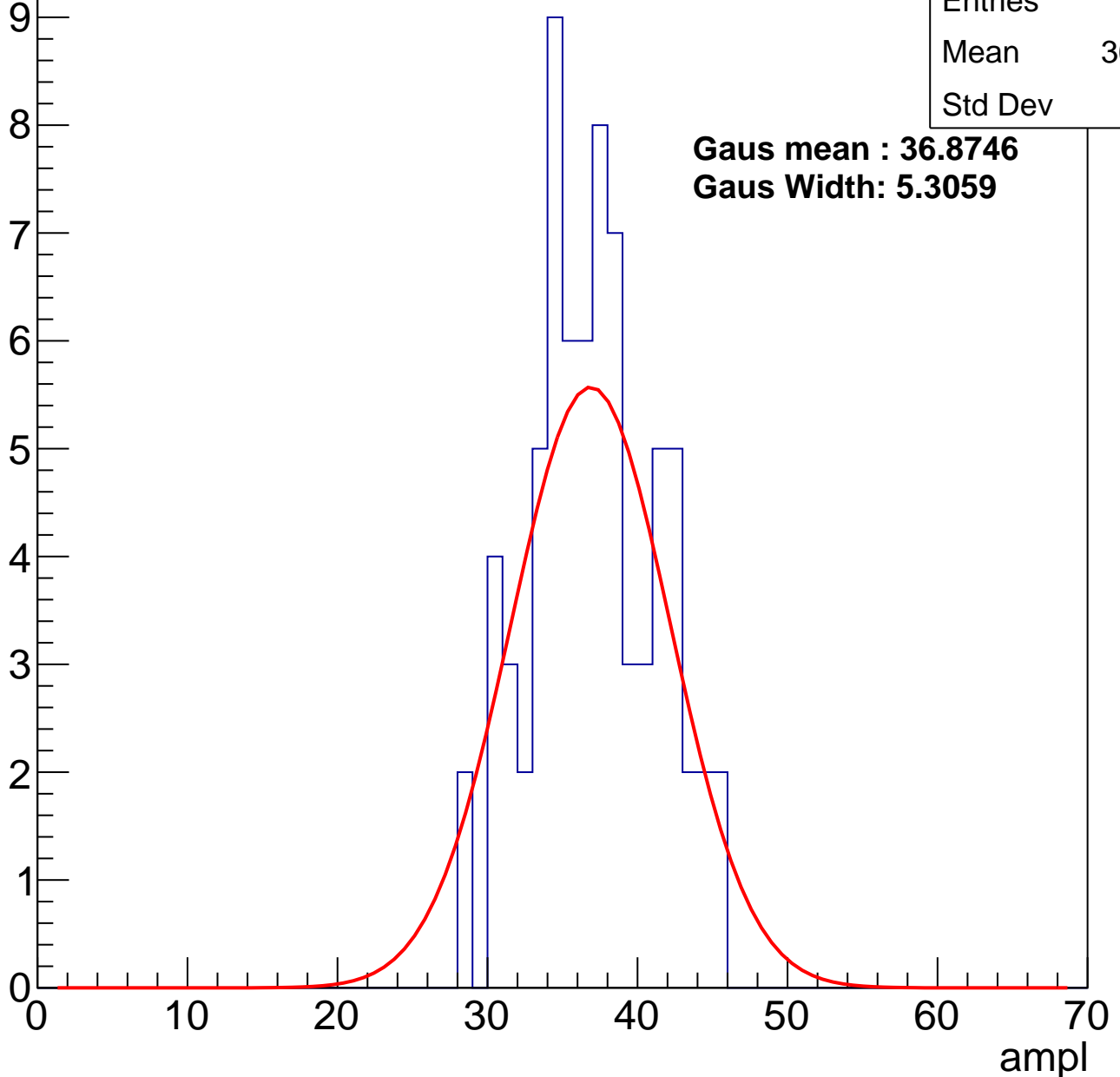
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.59
Std Dev	4.1

Gaus mean : 36.8746

Gaus Width: 5.3059



B1L103S, U20-ch81, adc2

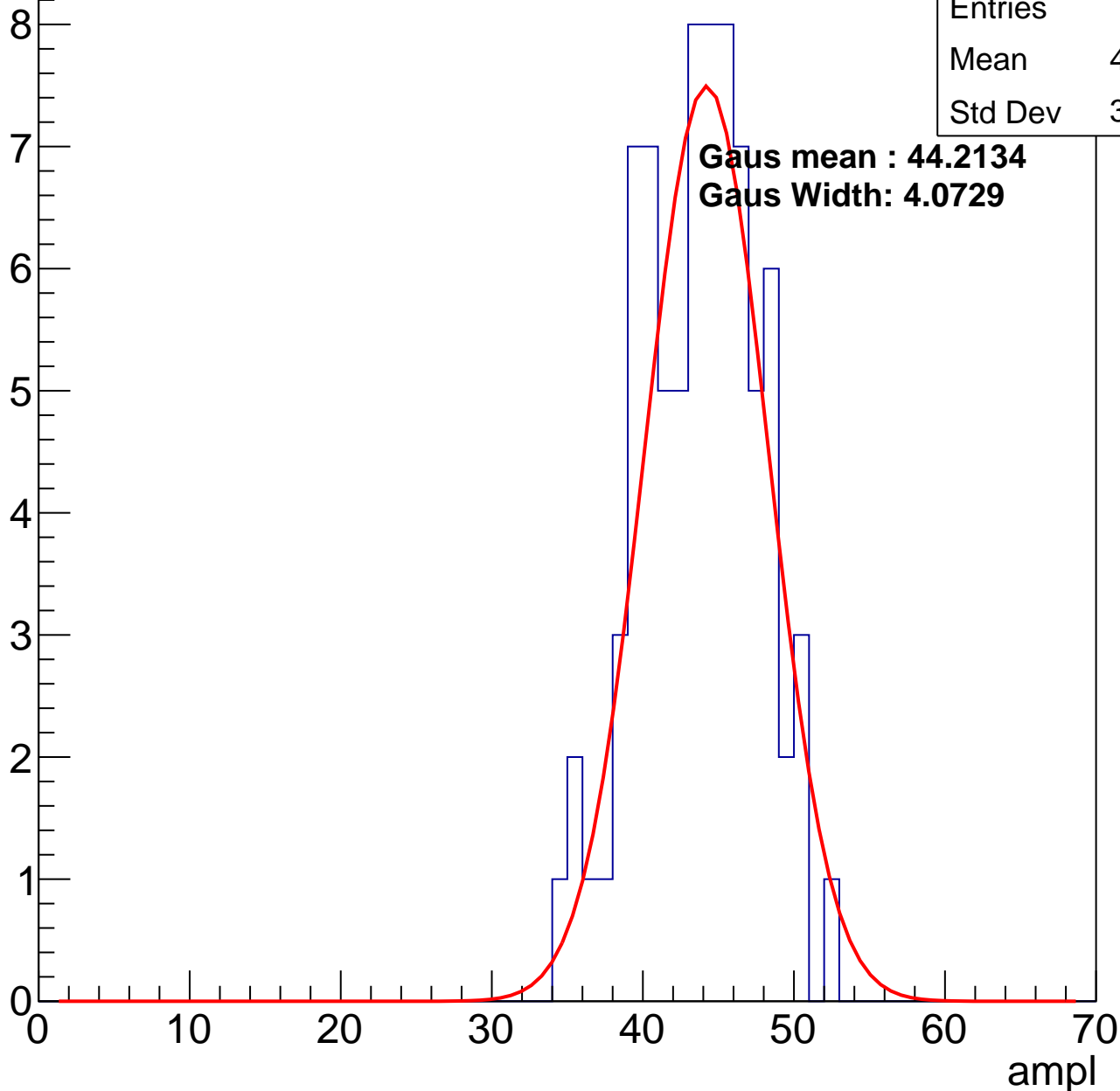
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	43.25
Std Dev	3.868

Gaus mean : 44.2134

Gaus Width: 4.0729



B1L103S, U20-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

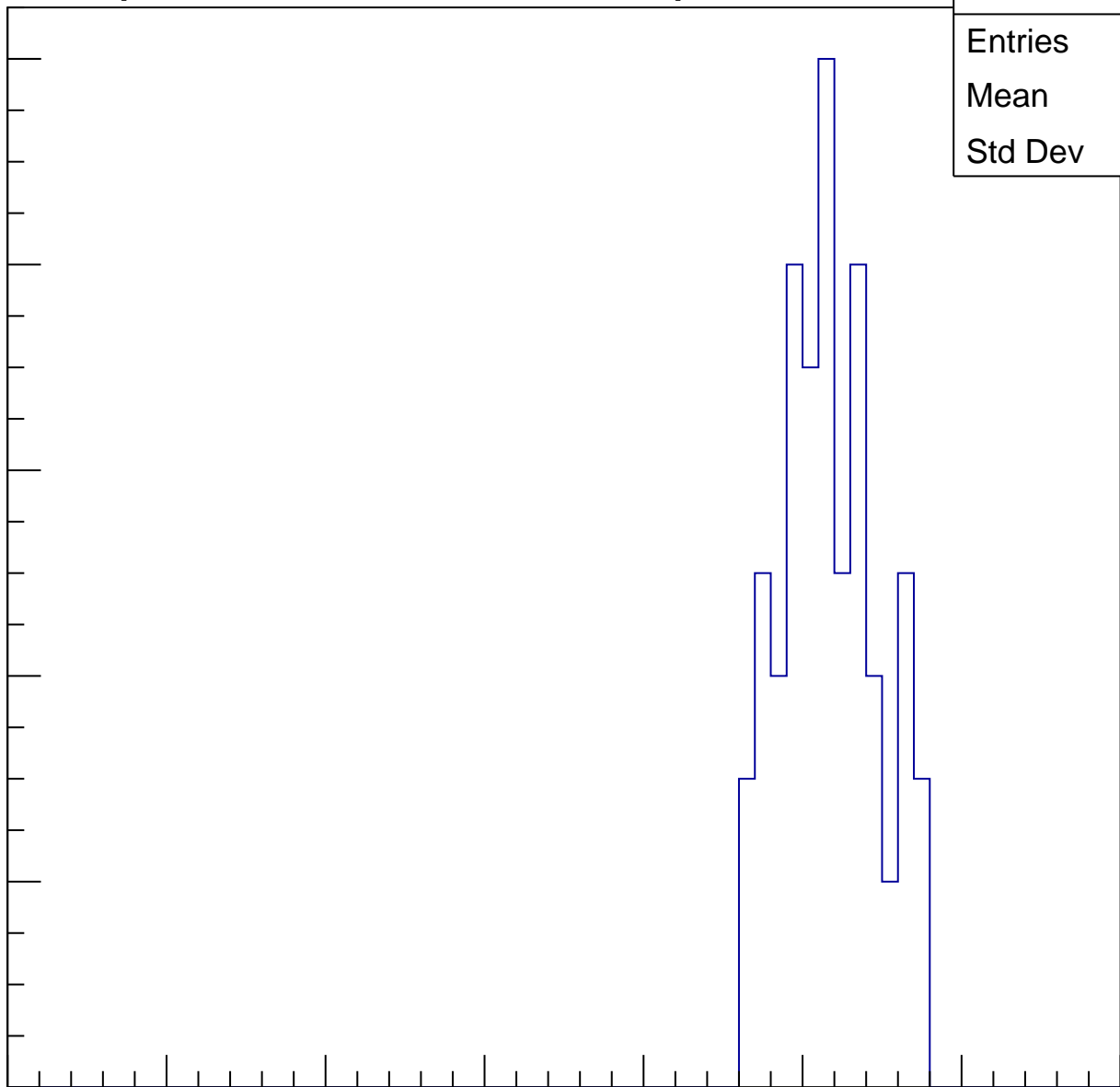
Entries	64
Mean	51.22
Std Dev	2.971

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

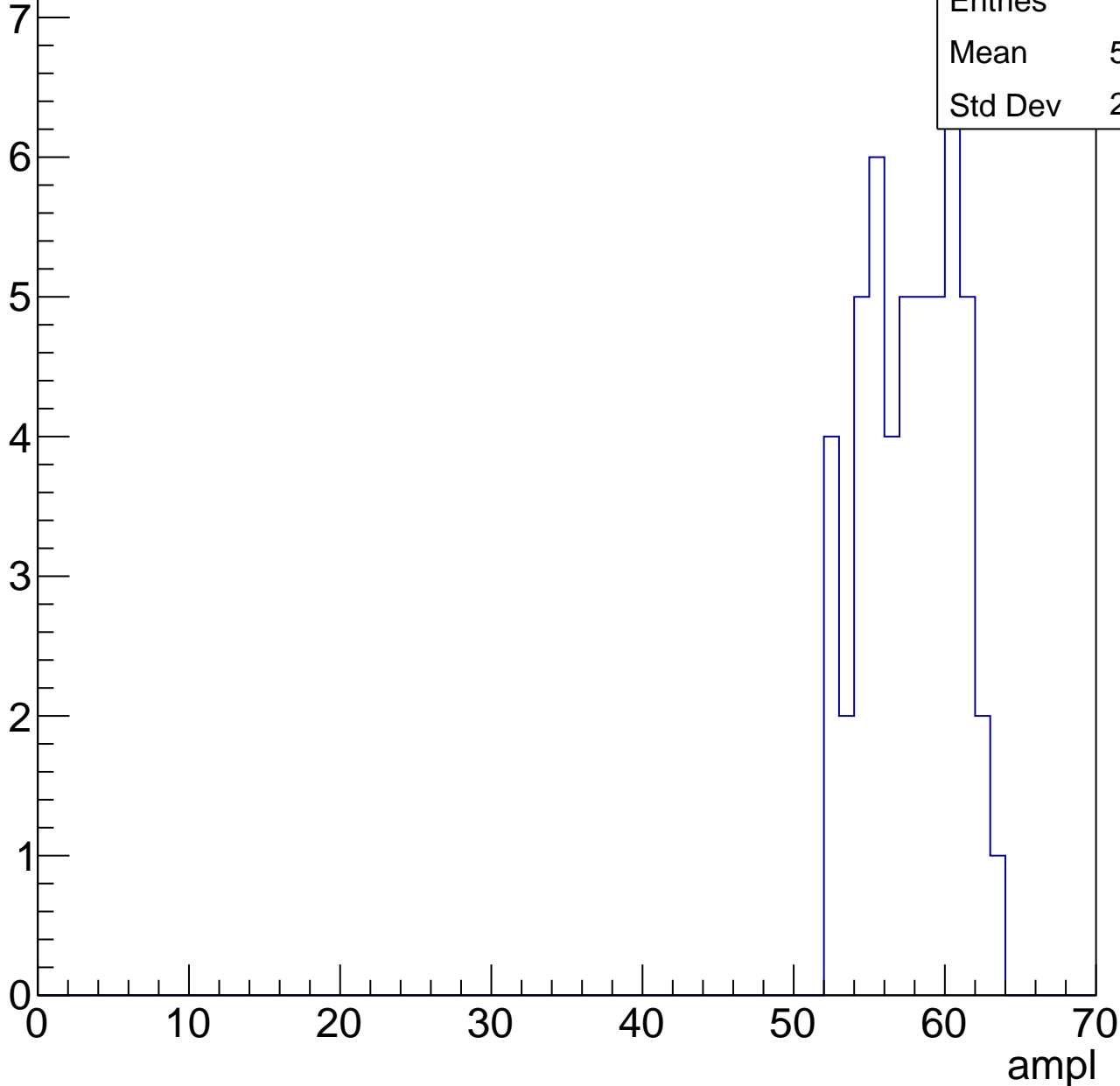


B1L103S, U20-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	57.25
Std Dev	2.989

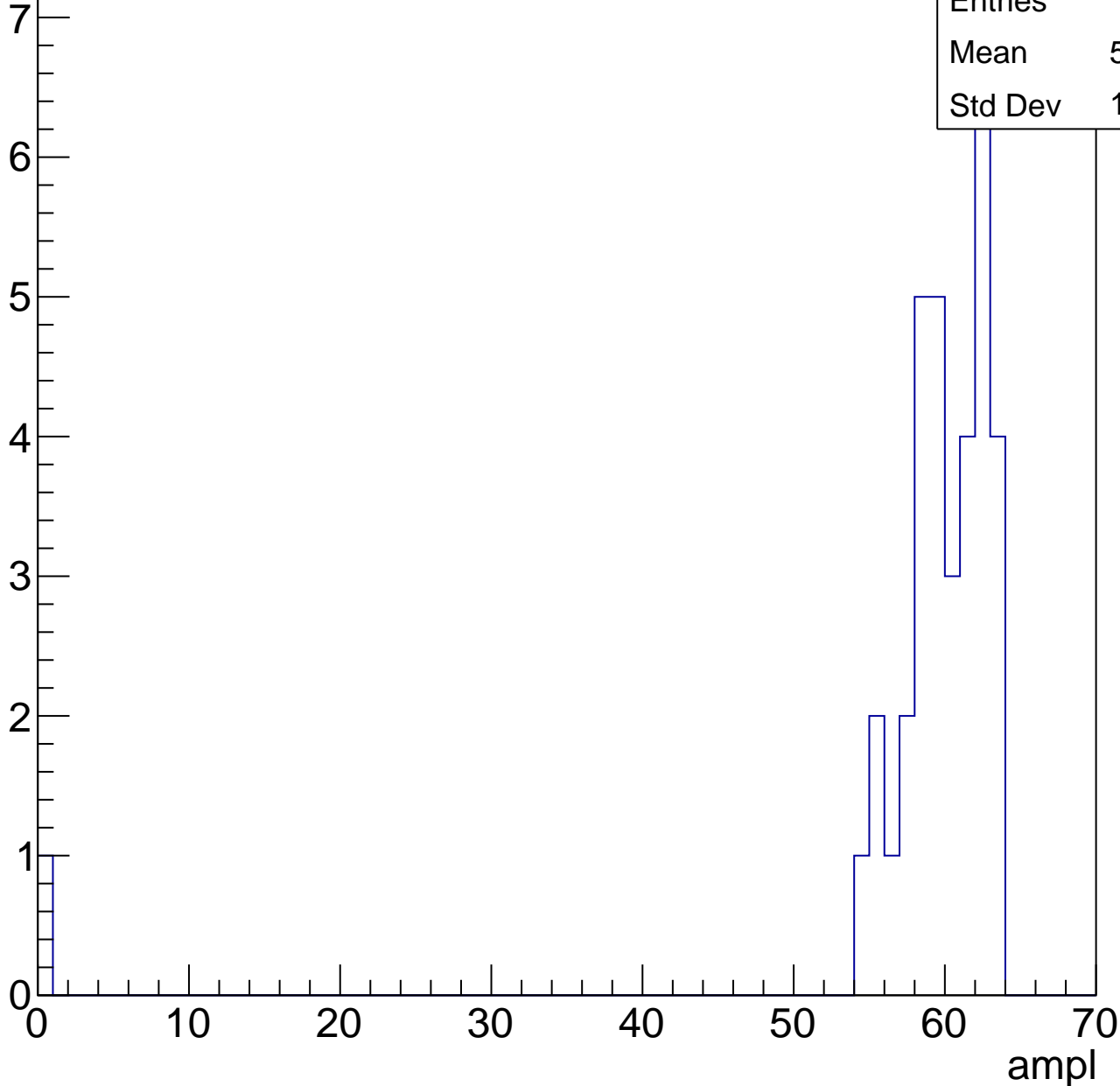


B1L103S, U20-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	57.97
Std Dev	10.24

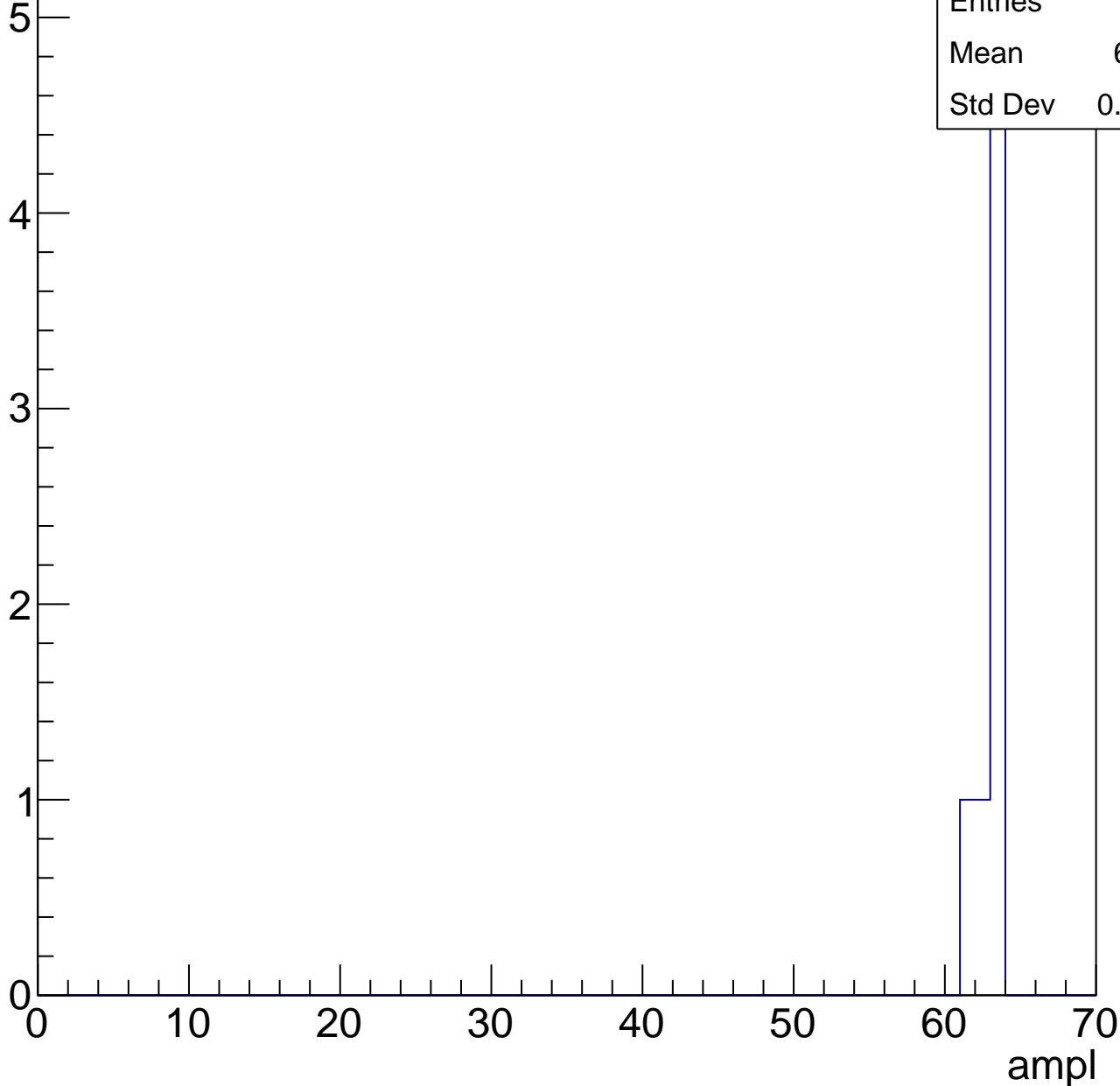


B1L103S, U20-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B1L103S, U20-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch82, adc0

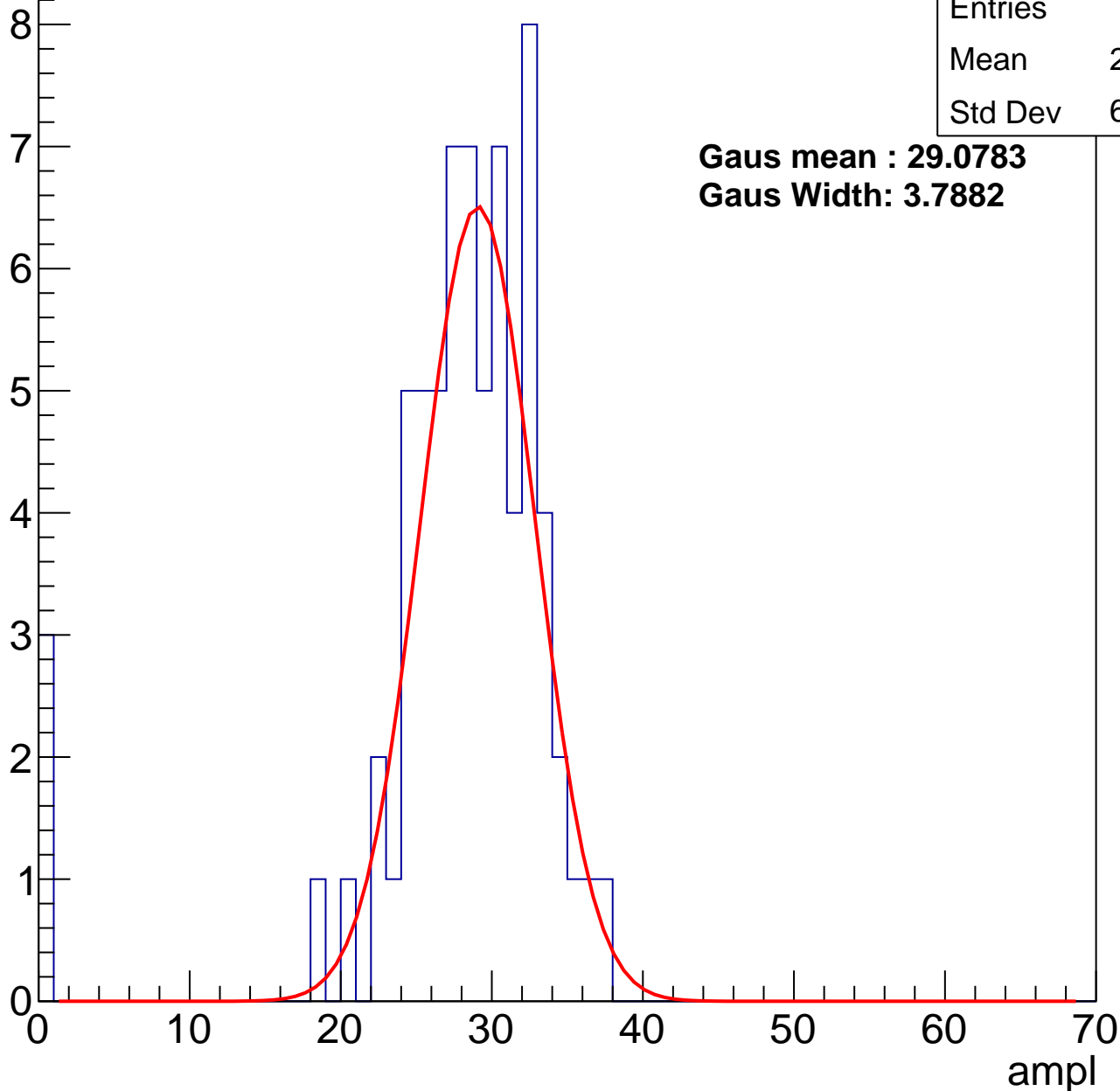
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	27.26
Std Dev	6.855

Gaus mean : 29.0783

Gaus Width: 3.7882



B1L103S, U20-ch82, adc1

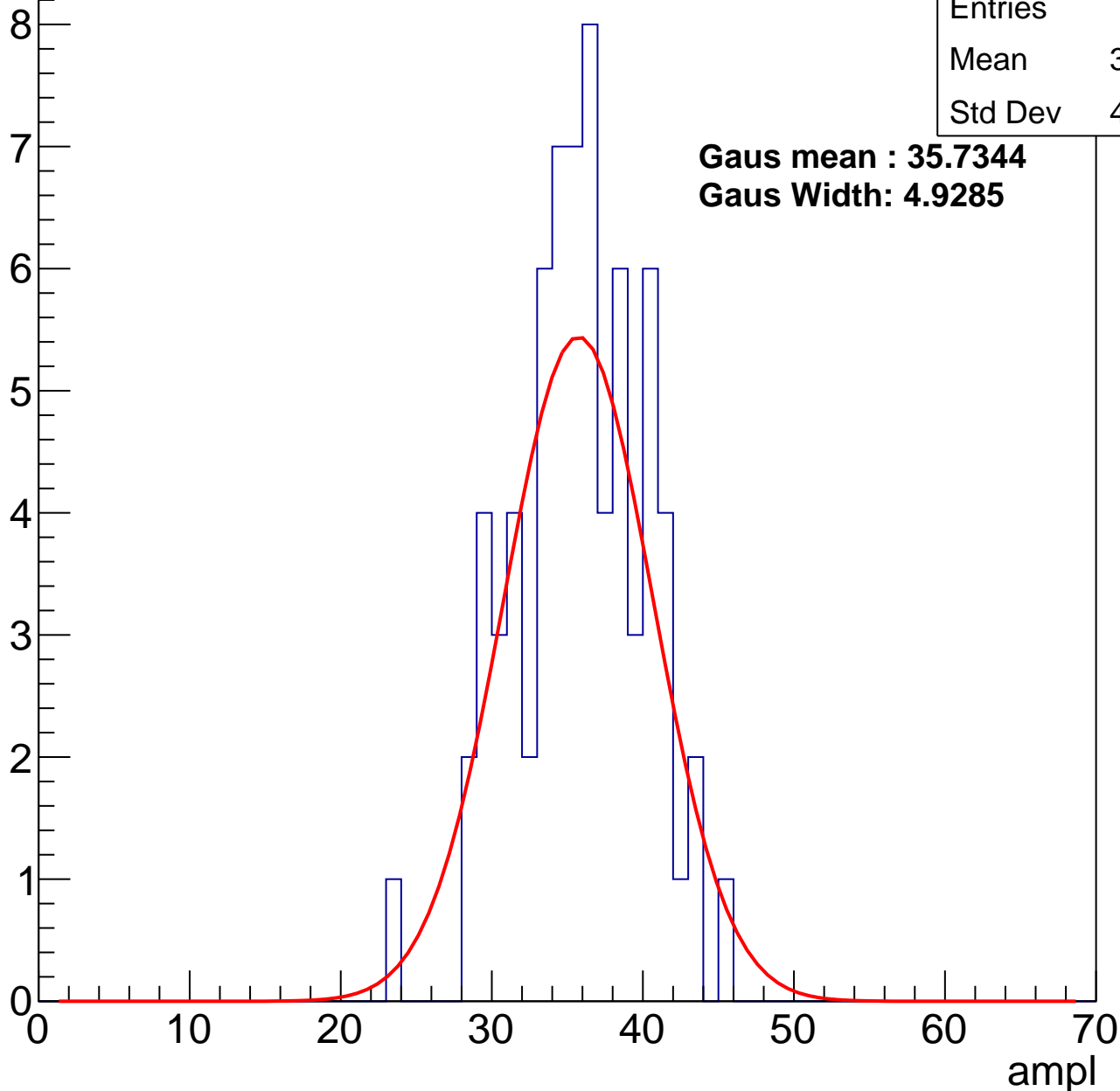
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.38
Std Dev	4.214

Gaus mean : 35.7344

Gaus Width: 4.9285



B1L103S, U20-ch82, adc2

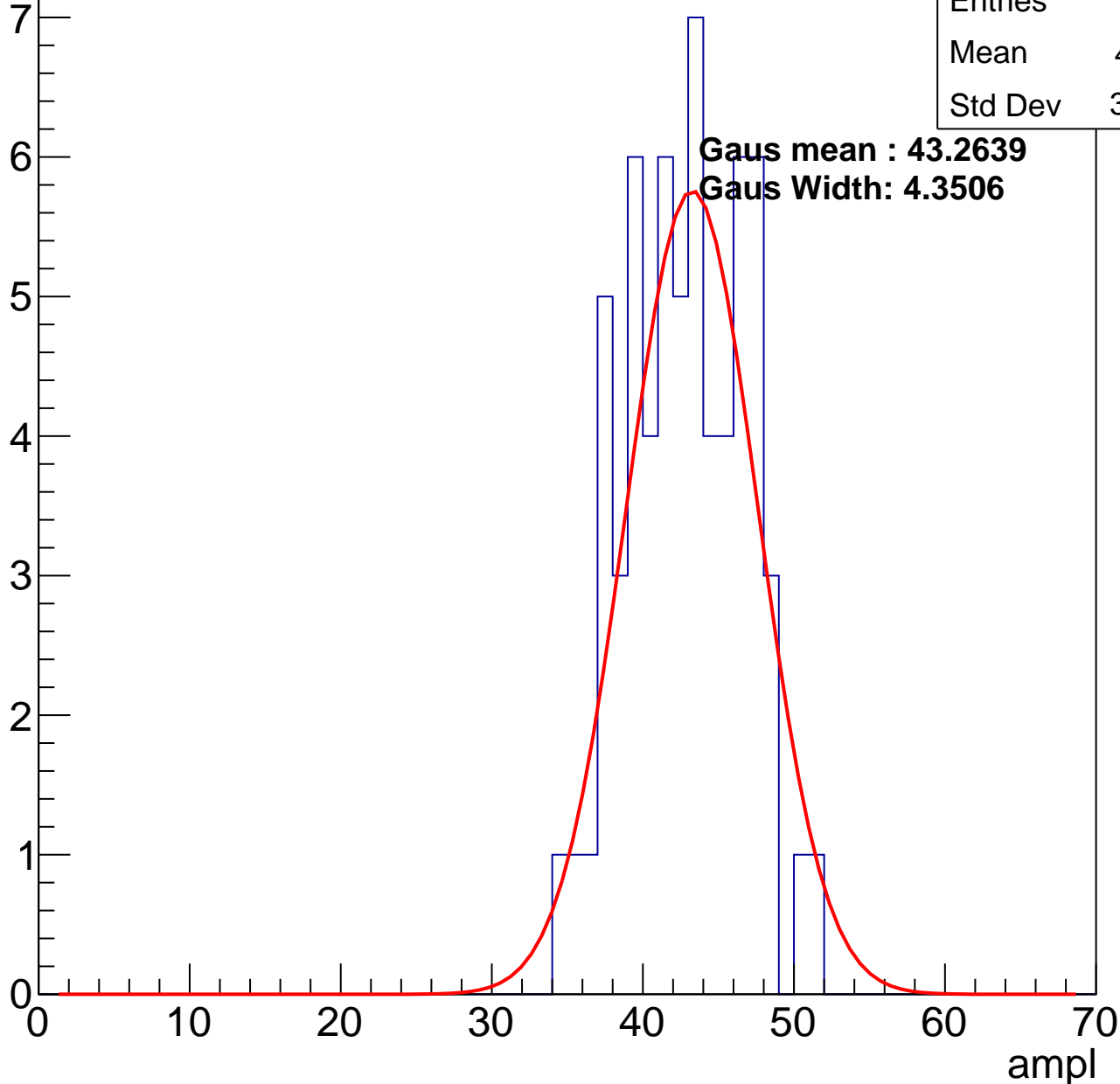
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.41
Std Dev	3.843

Gaus mean : 43.2639

Gaus Width: 4.3506

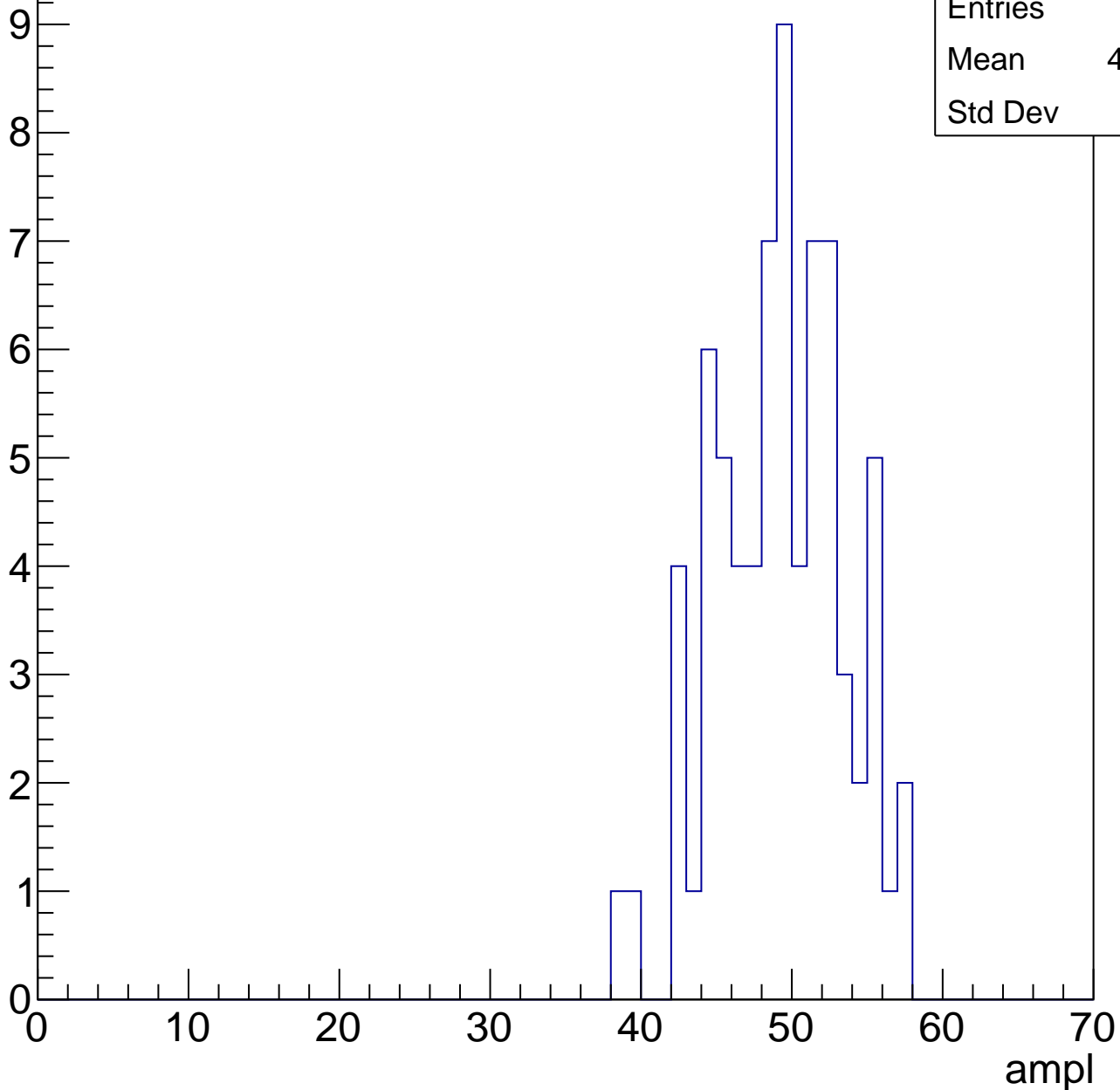


B1L103S, U20-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	48.75
Std Dev	4.2

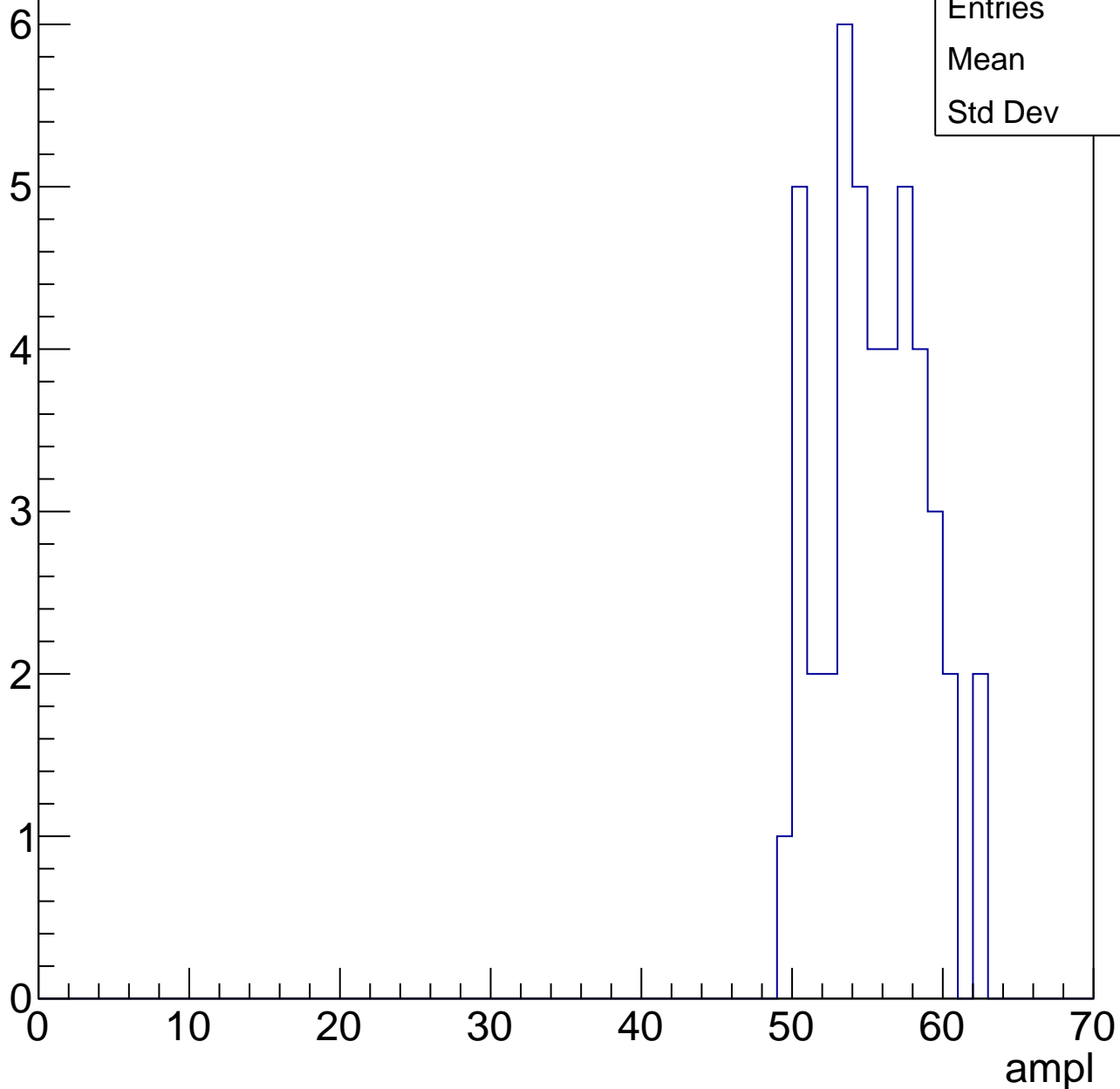


B1L103S, U20-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55
Std Dev	3.32

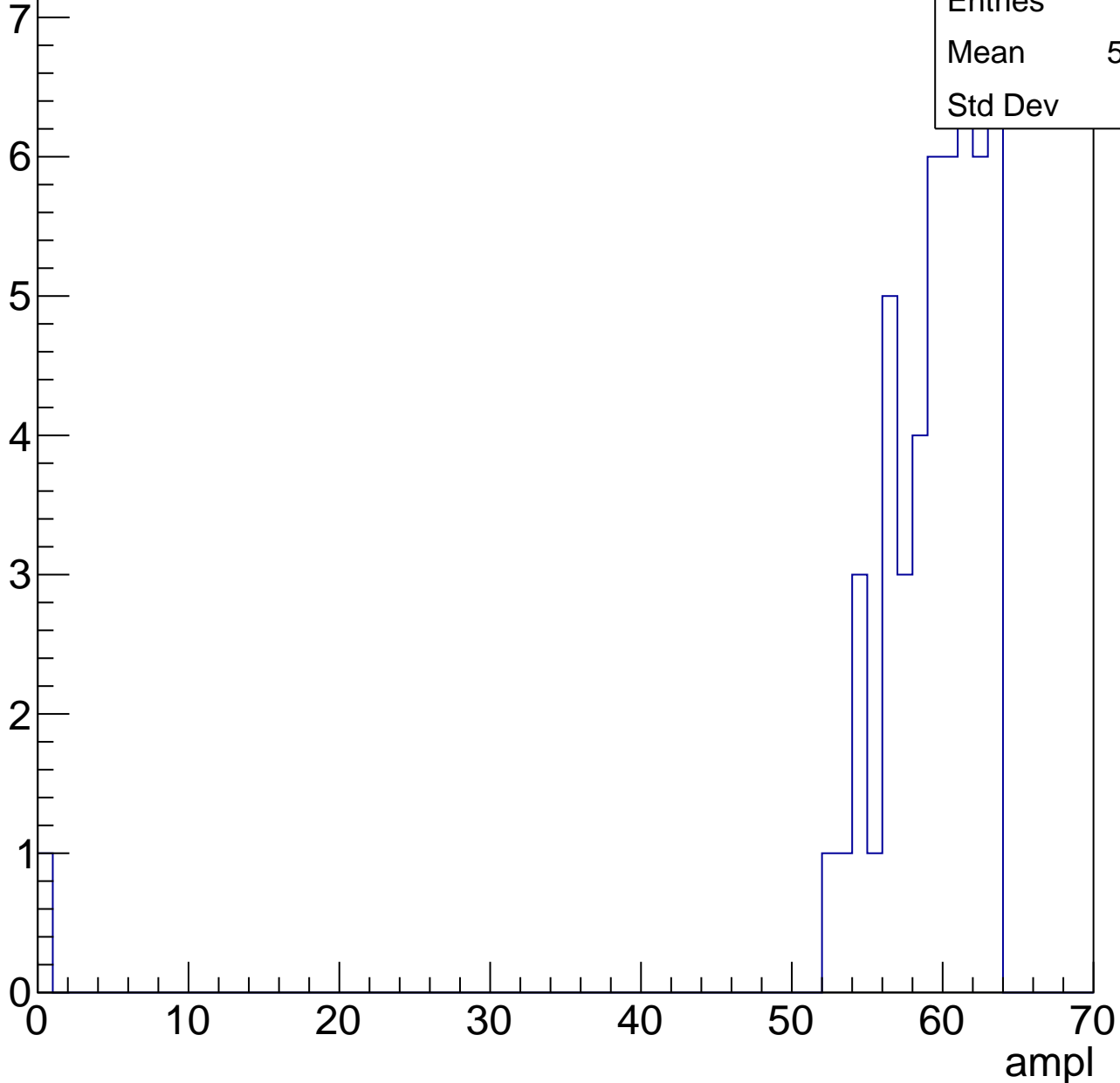


B1L103S, U20-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.02
Std Dev	8.71



B1L103S, U20-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

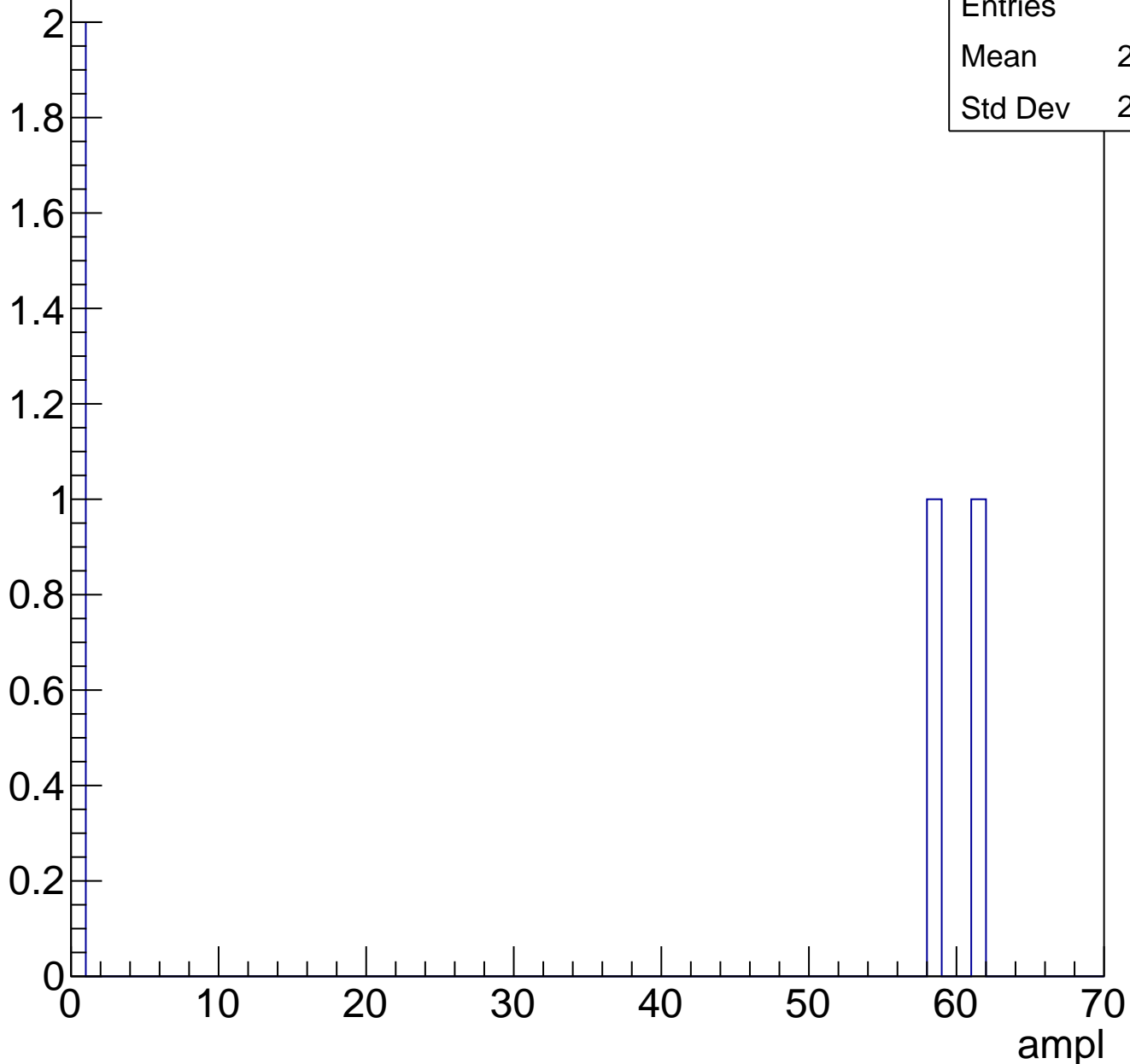
ampl

Entries	11
Mean	61.09
Std Dev	1.621

B1L103S, U20-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	29.75
Std Dev	29.77

B1L103S, U20-ch83, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	29.17
Std Dev	5.047

Gaus mean : 29.5253

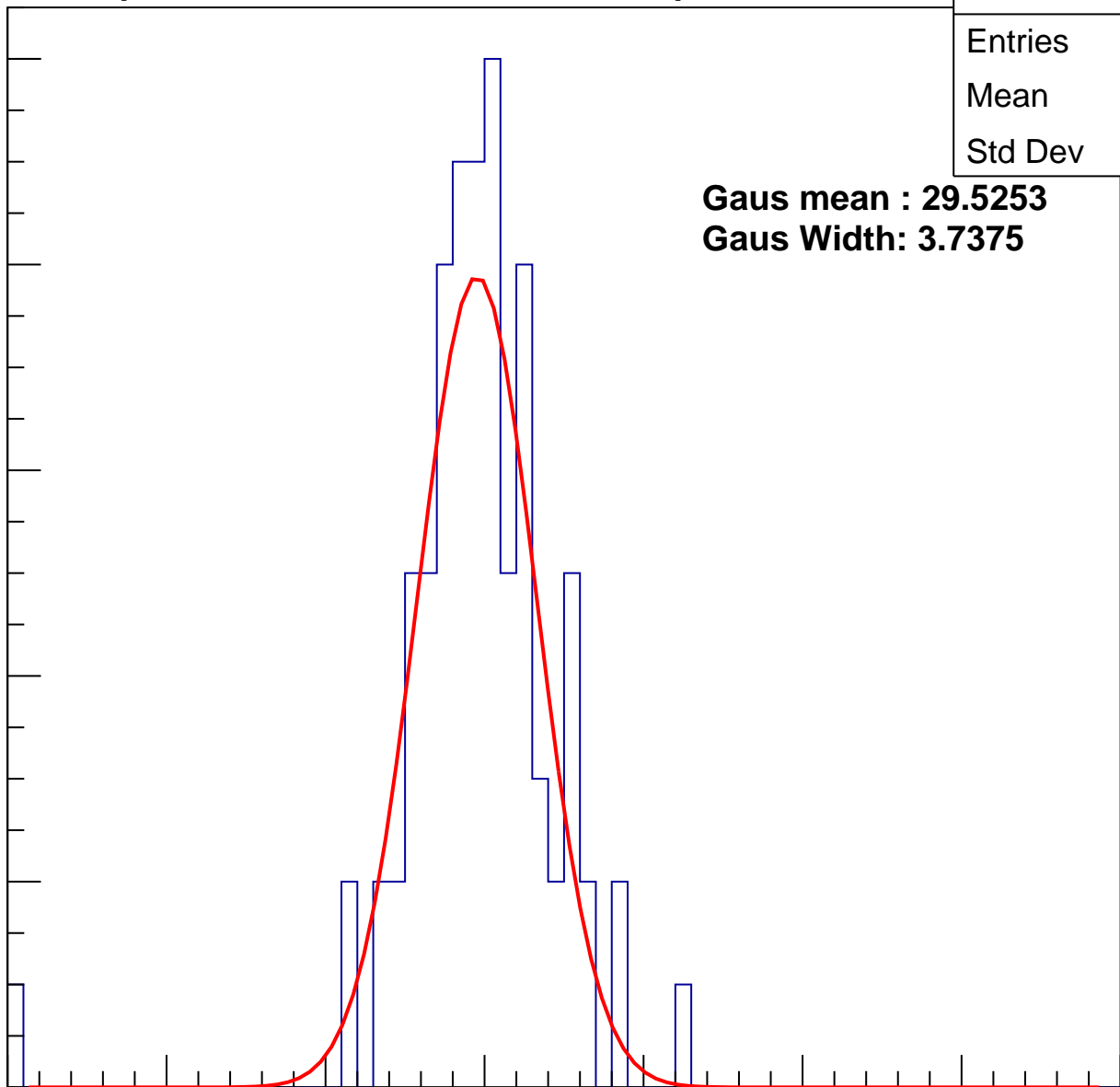
Gaus Width: 3.7375

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch83, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	37.07
Std Dev	3.496

Gaus mean : 37.6322

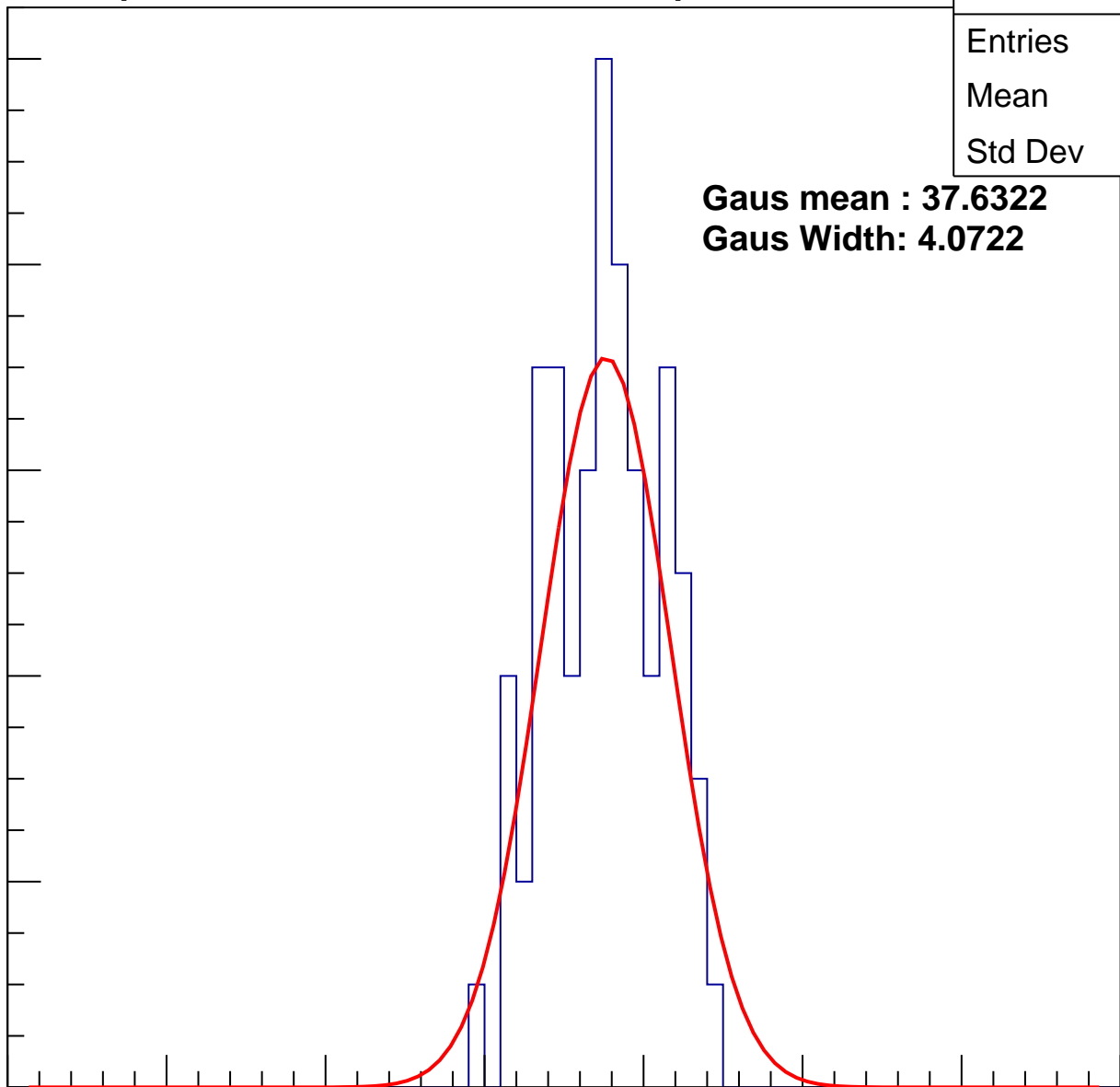
Gaus Width: 4.0722

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch83, adc2

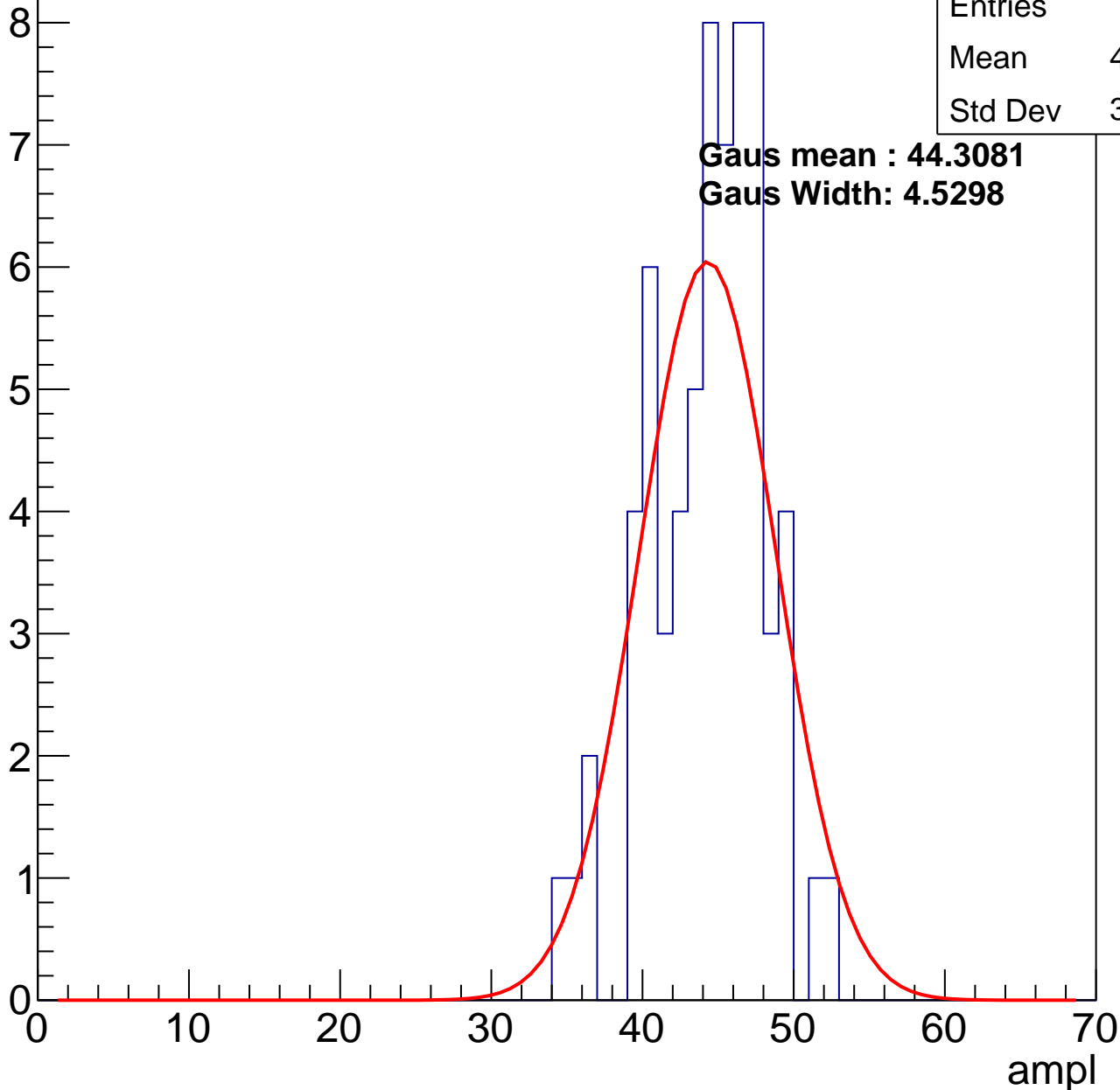
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	43.89
Std Dev	3.742

Gaus mean : 44.3081

Gaus Width: 4.5298

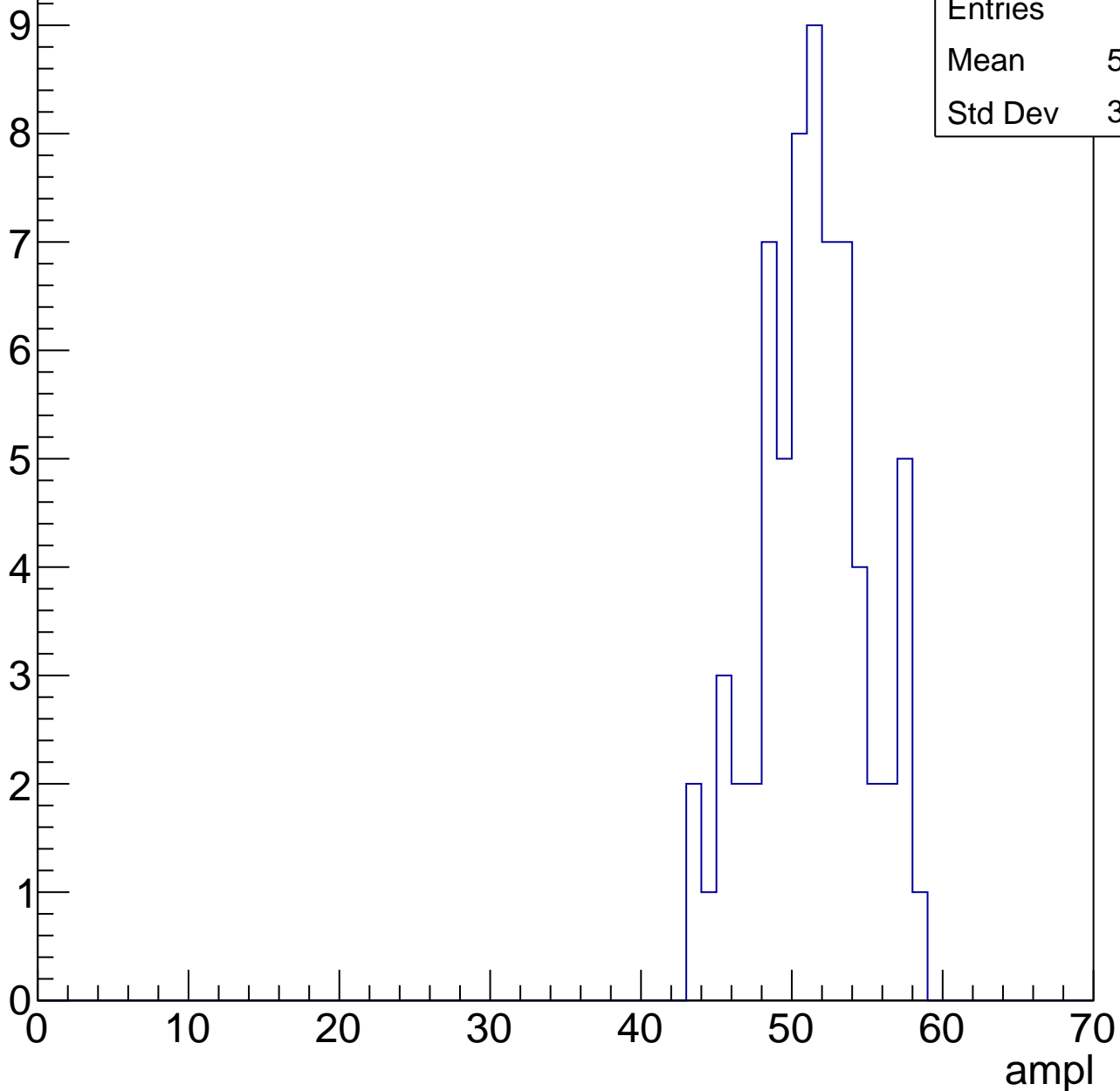


B1L103S, U20-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	50.85
Std Dev	3.538

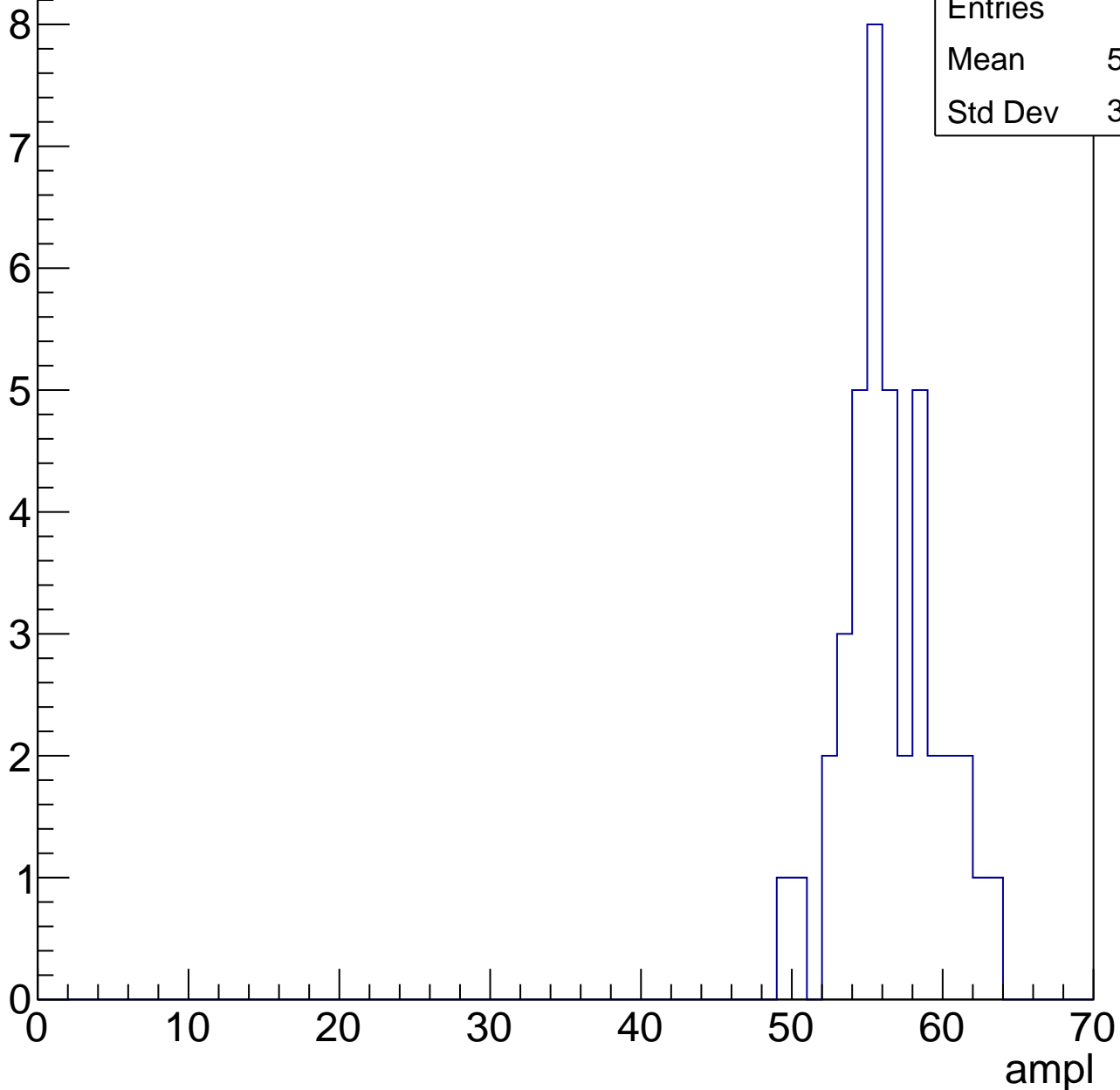


B1L103S, U20-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	56.02
Std Dev	3.078

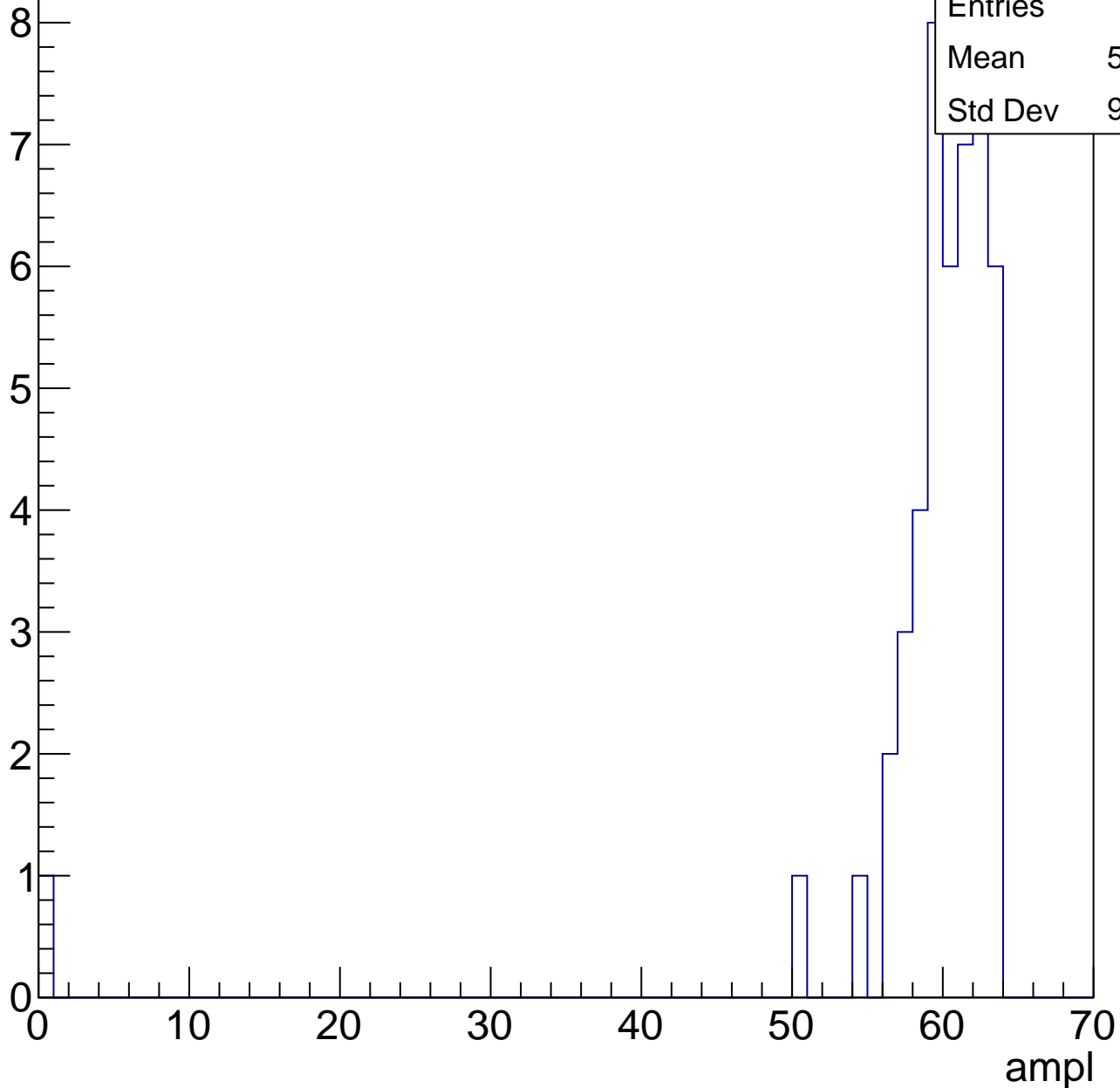


B1L103S, U20-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.55
Std Dev	9.008



B1L103S, U20-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

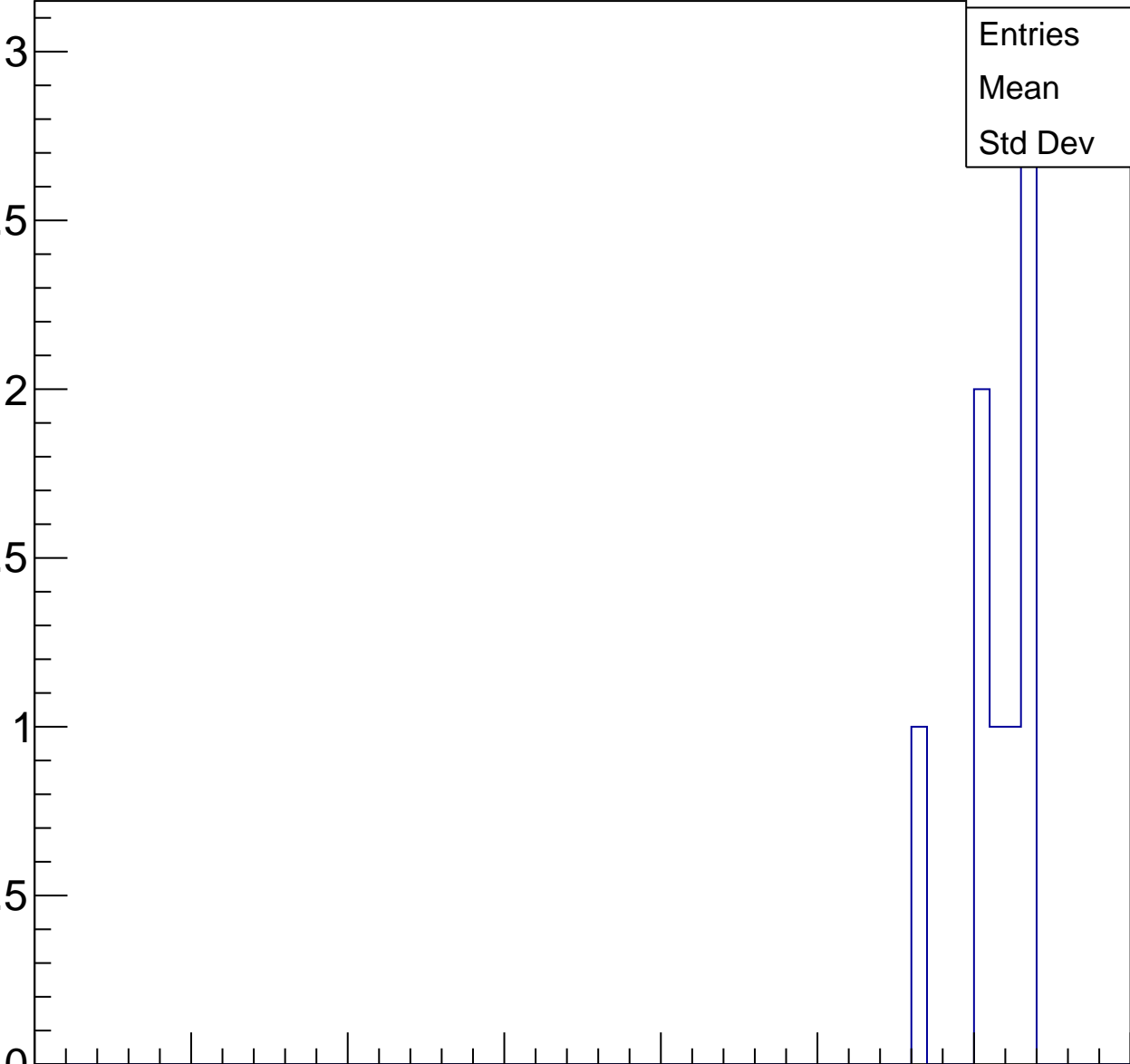
Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61
Std Dev	2.236

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch84, adc0

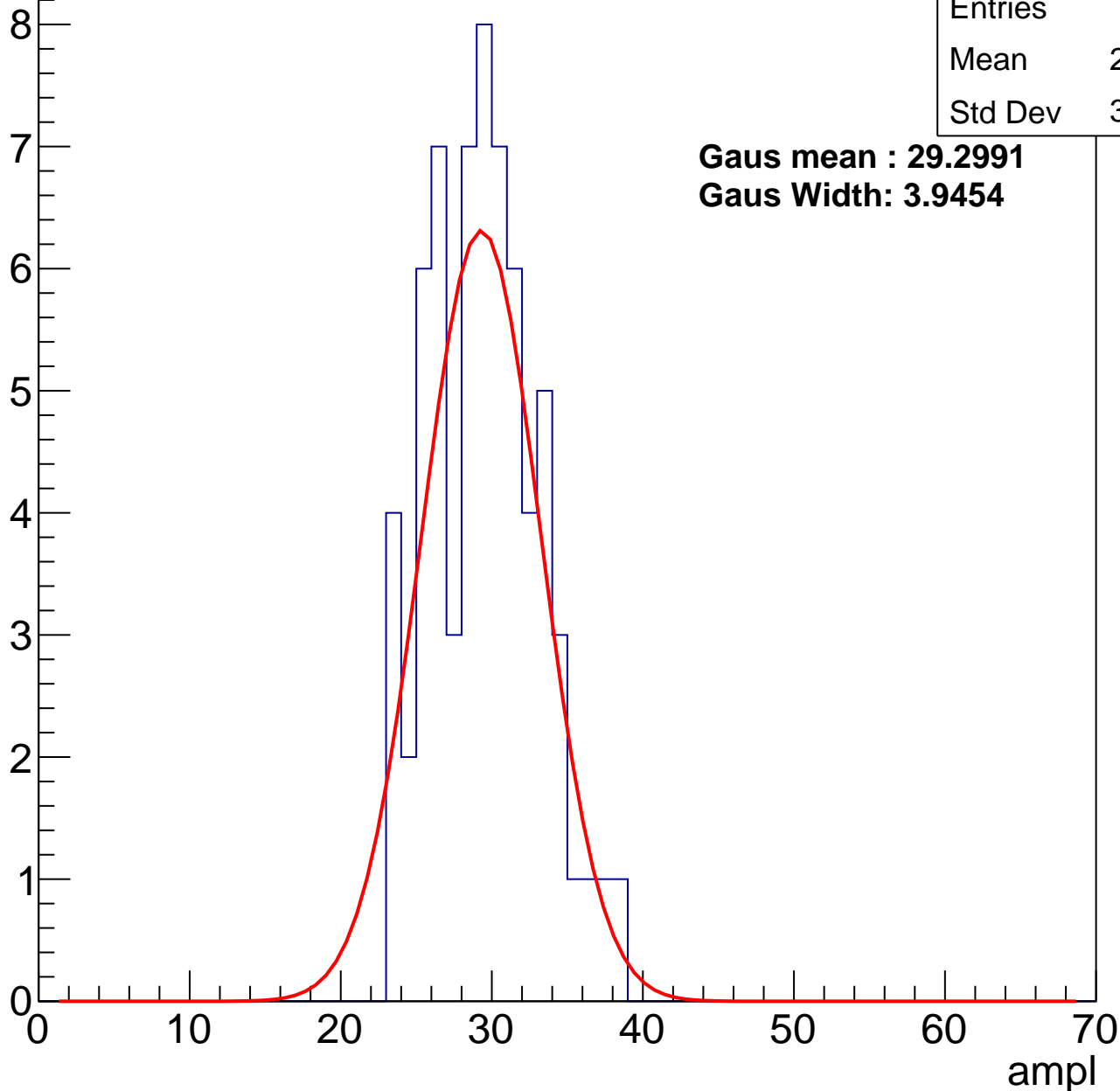
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.06
Std Dev	3.533

Gaus mean : 29.2991

Gaus Width: 3.9454



B1L103S, U20-ch84, adc1

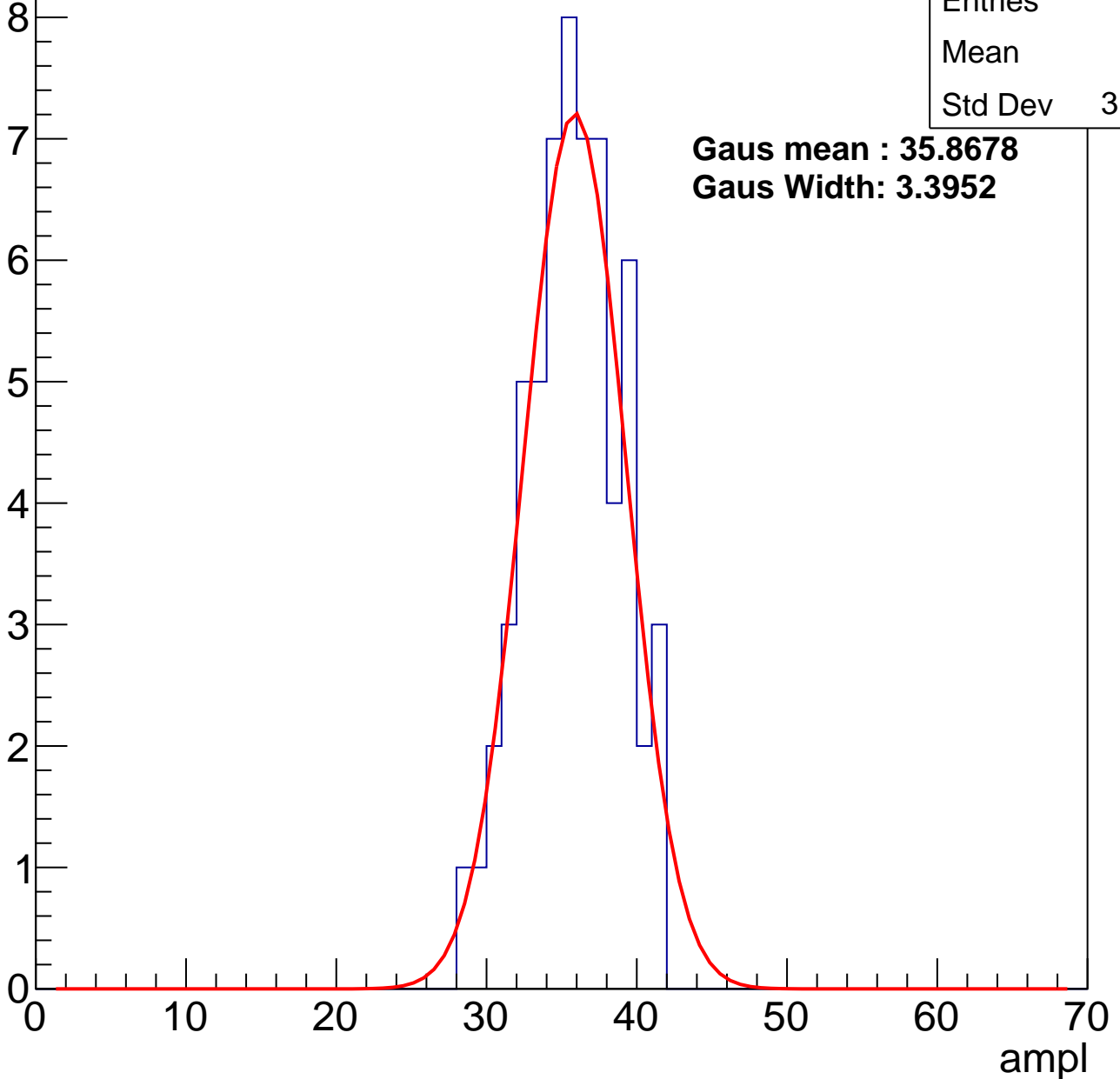
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.3
Std Dev	3.069

Gaus mean : 35.8678

Gaus Width: 3.3952



B1L103S, U20-ch84, adc2

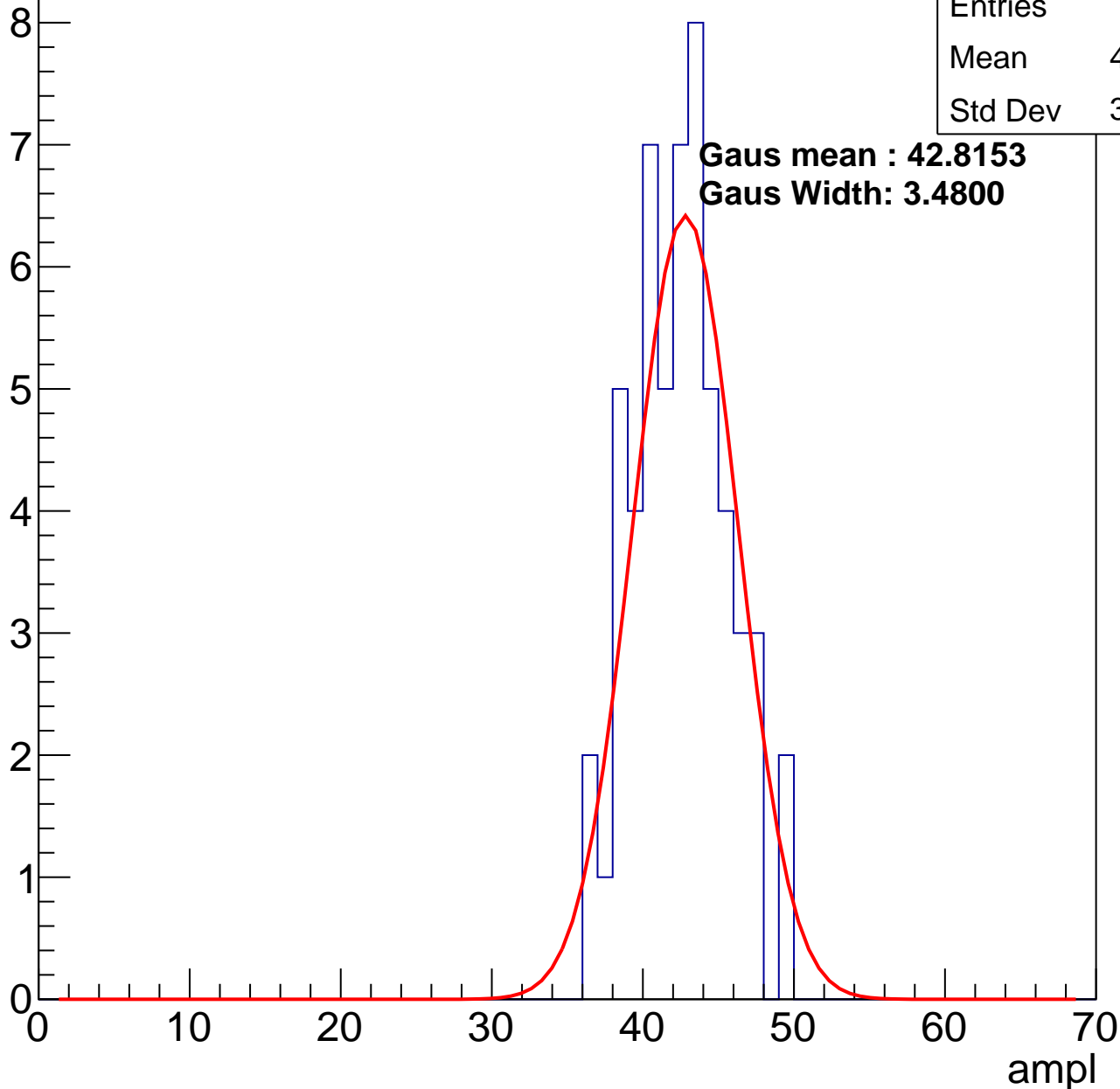
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.05
Std Dev	3.079

Gaus mean : 42.8153

Gaus Width: 3.4800

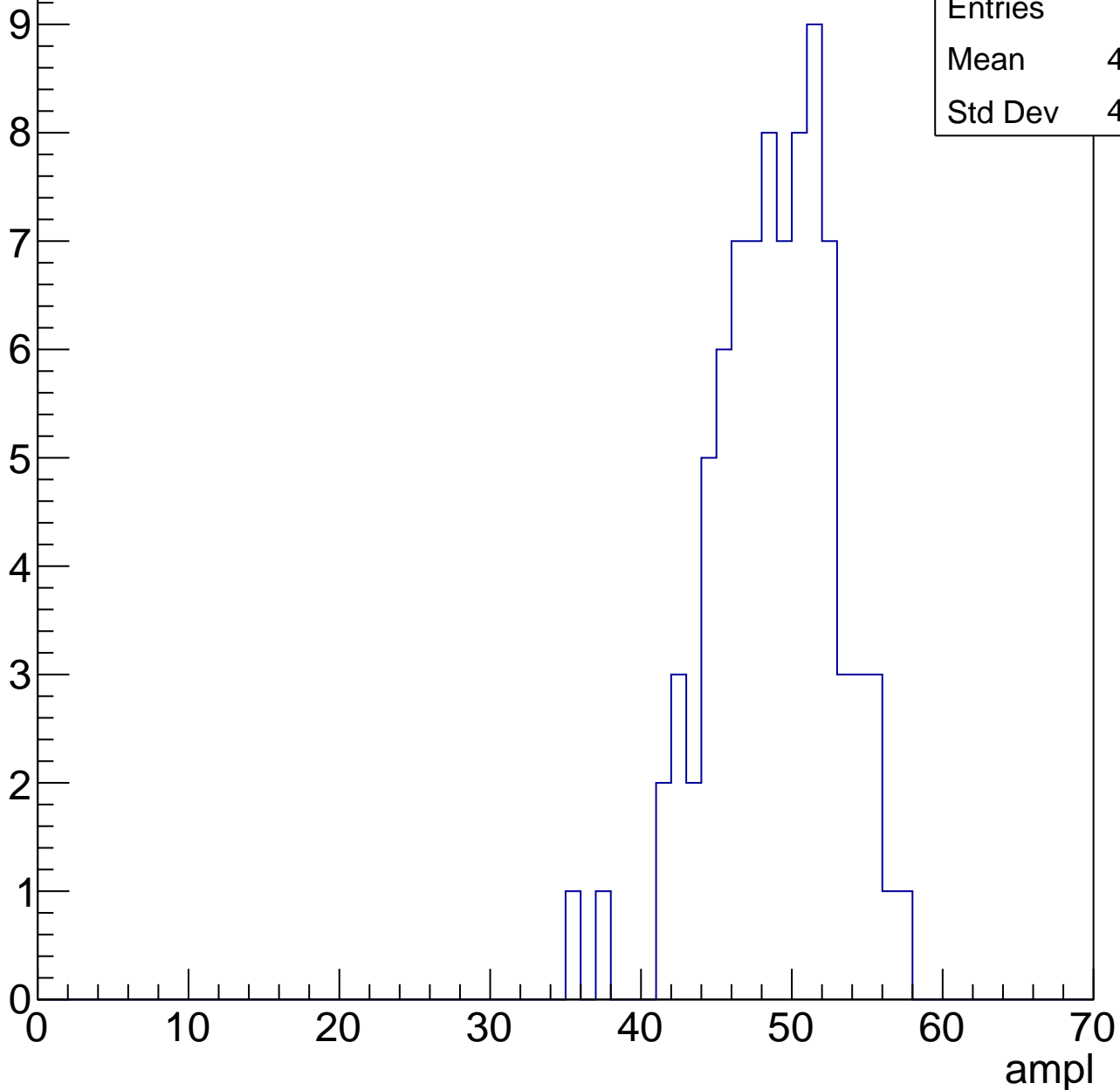


B1L103S, U20-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	48.29
Std Dev	4.105

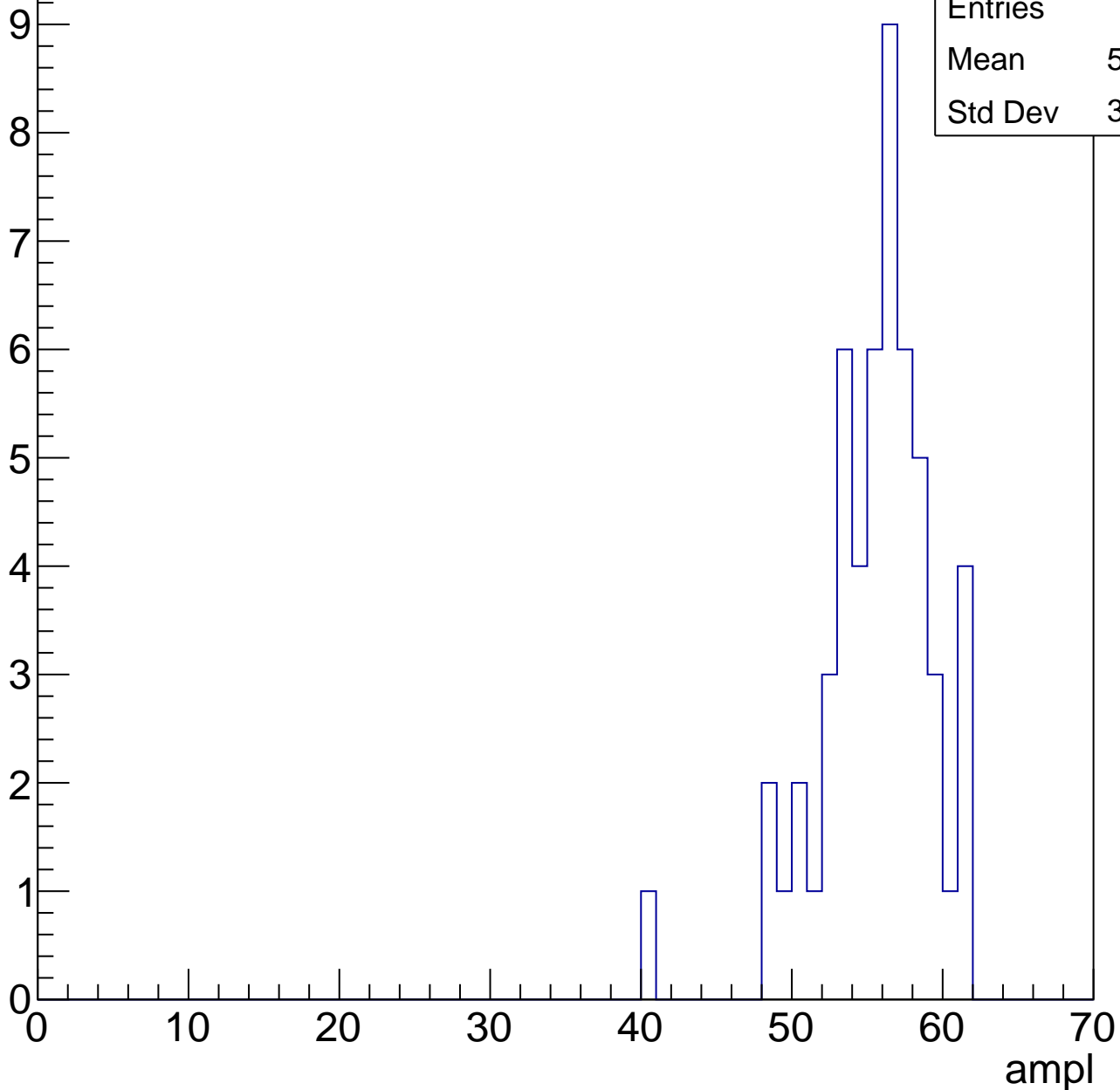


B1L103S, U20-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.06
Std Dev	3.788

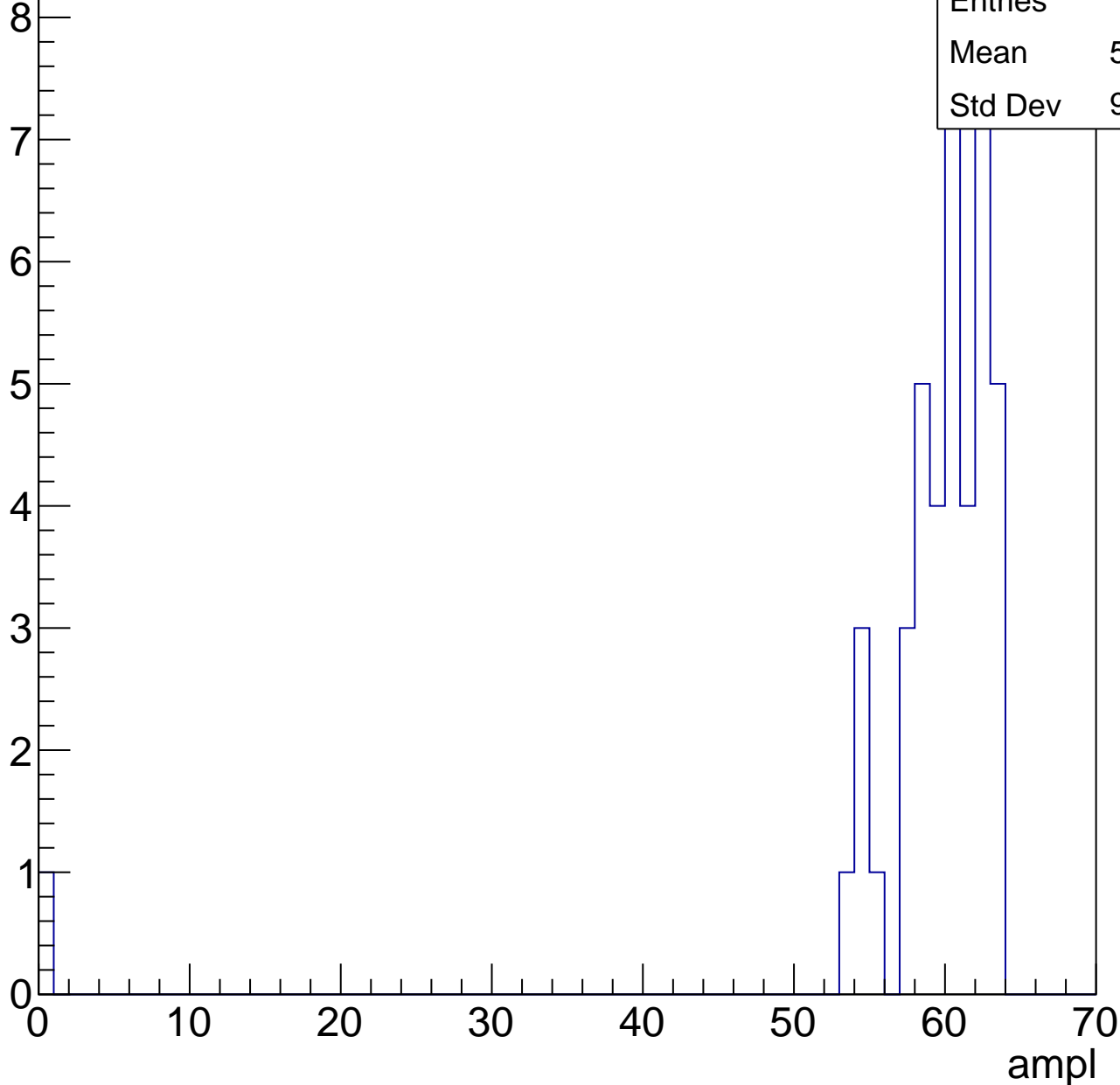


B1L103S, U20-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.19
Std Dev	9.367

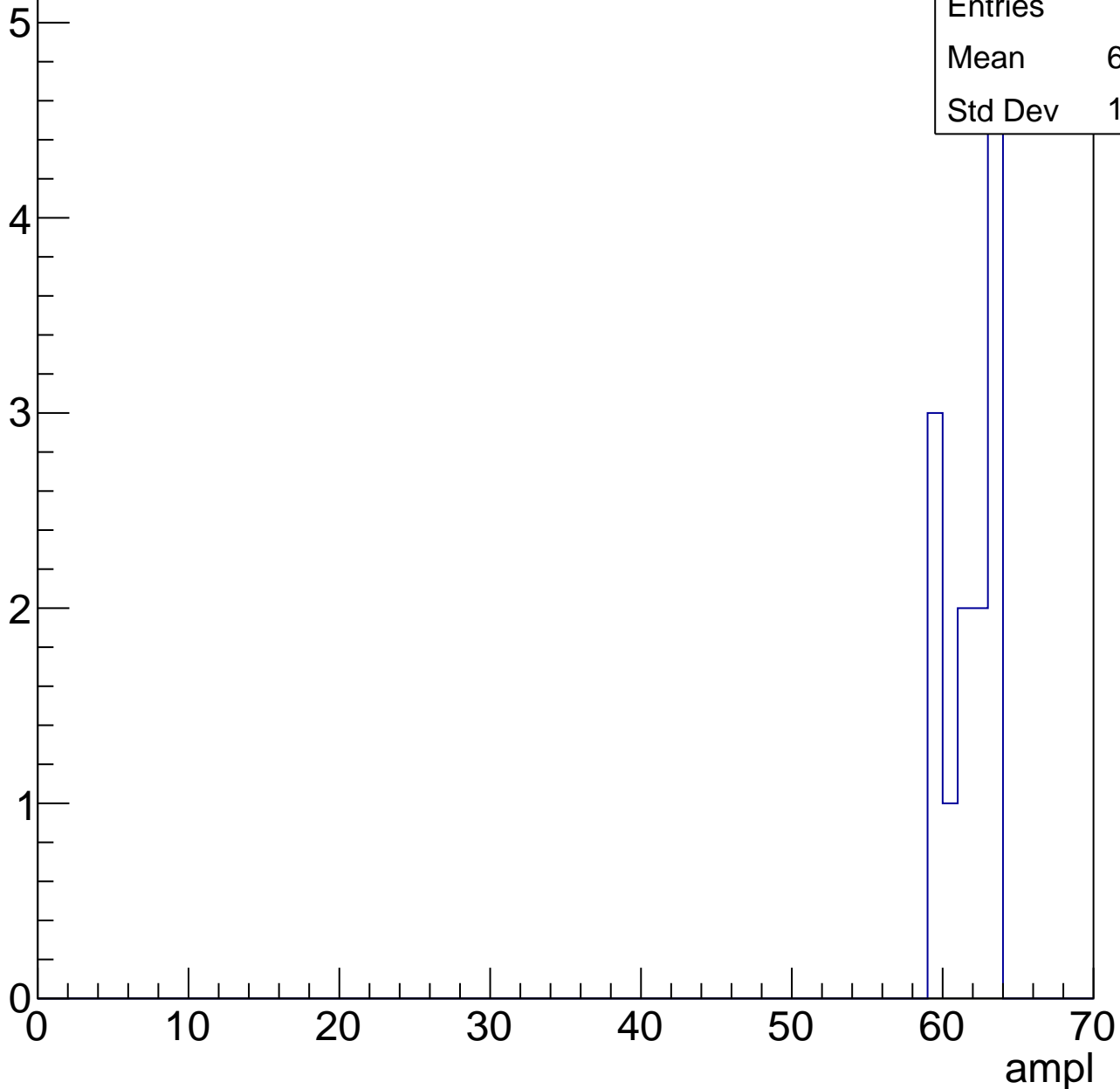


B1L103S, U20-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.38
Std Dev	1.595



B1L103S, U20-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch85, adc0

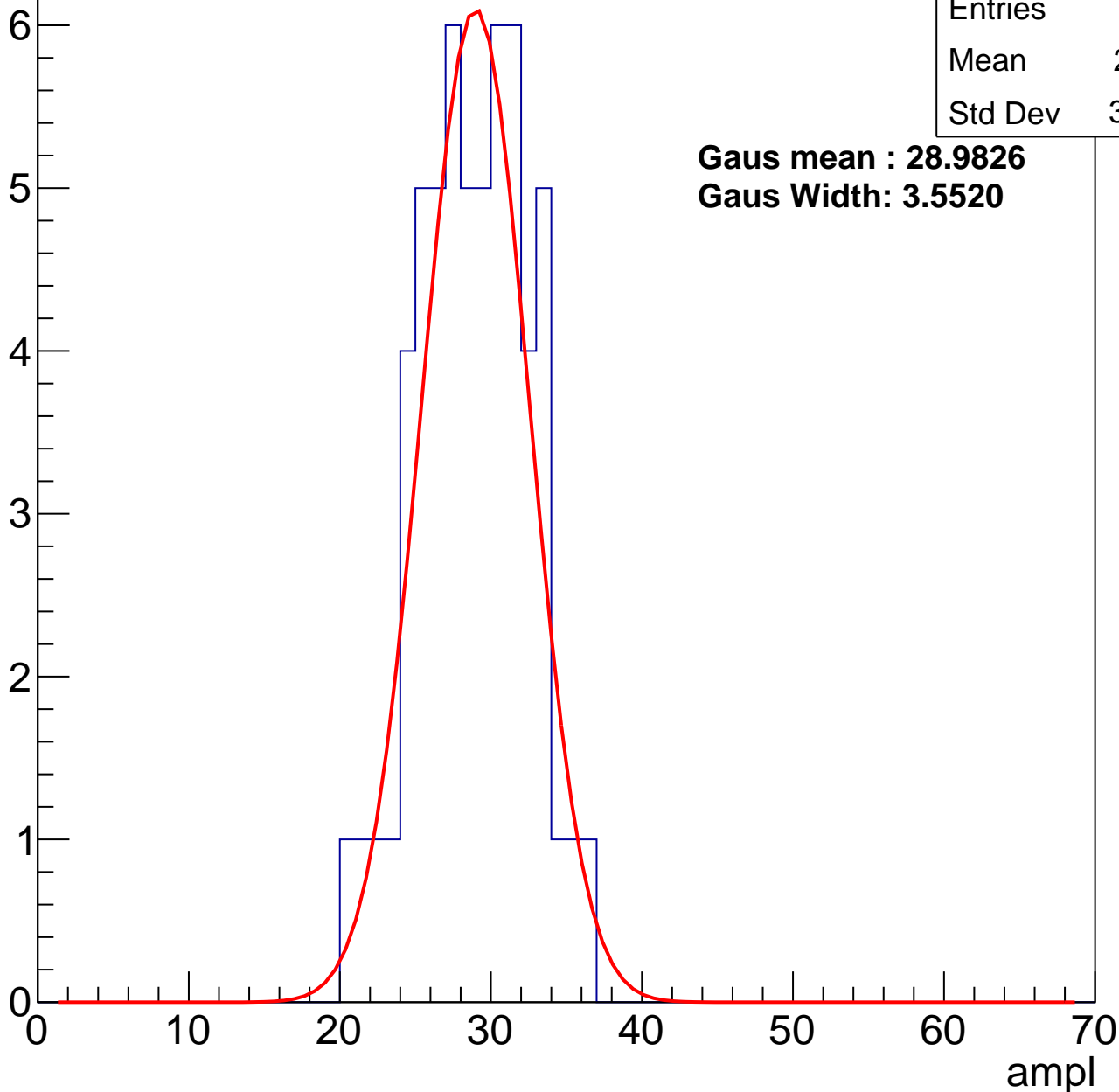
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	28.41
Std Dev	3.523

Gaus mean : 28.9826

Gaus Width: 3.5520



B1L103S, U20-ch85, adc1

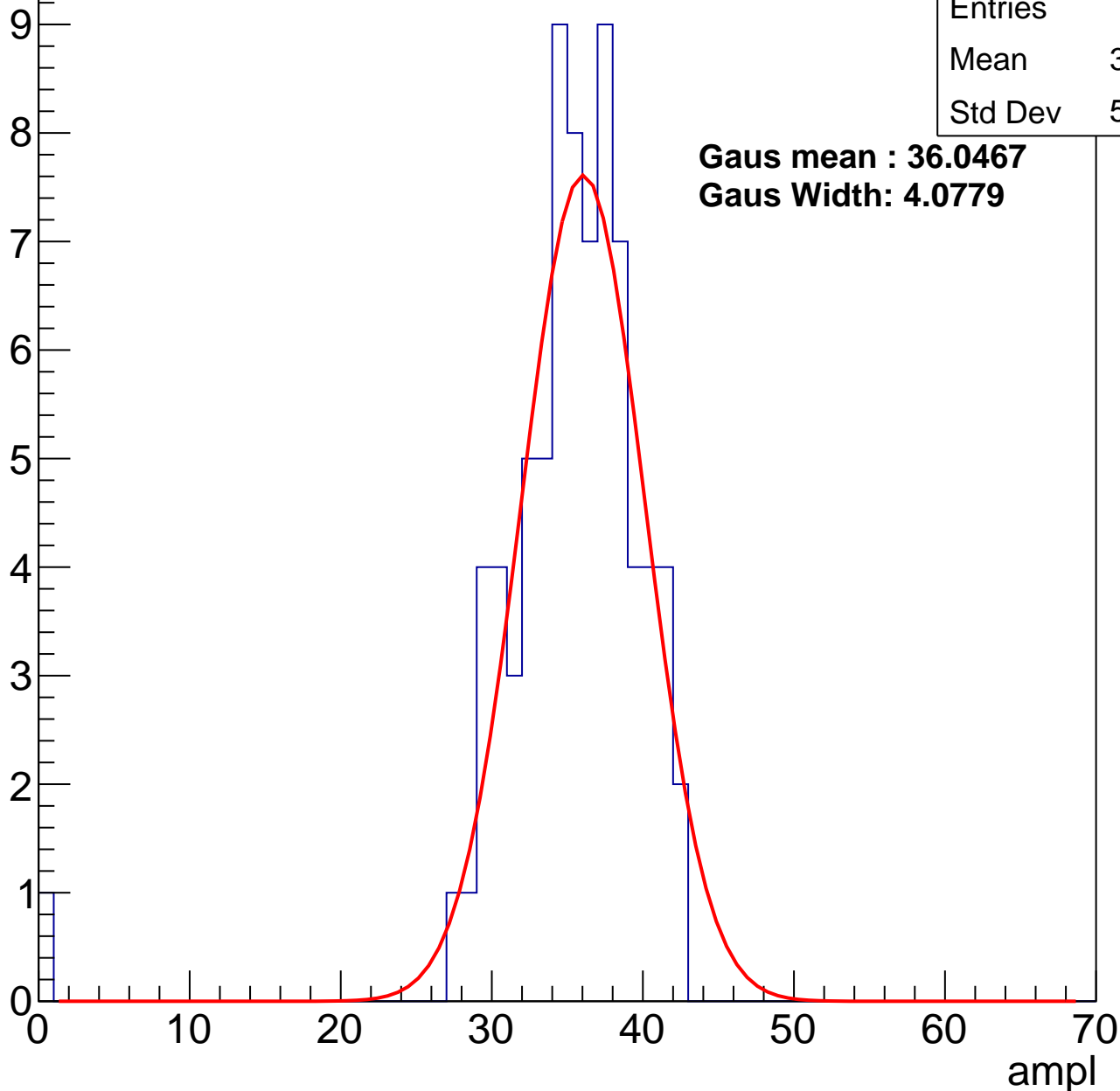
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.74
Std Dev	5.324

Gaus mean : 36.0467

Gaus Width: 4.0779



B1L103S, U20-ch85, adc2

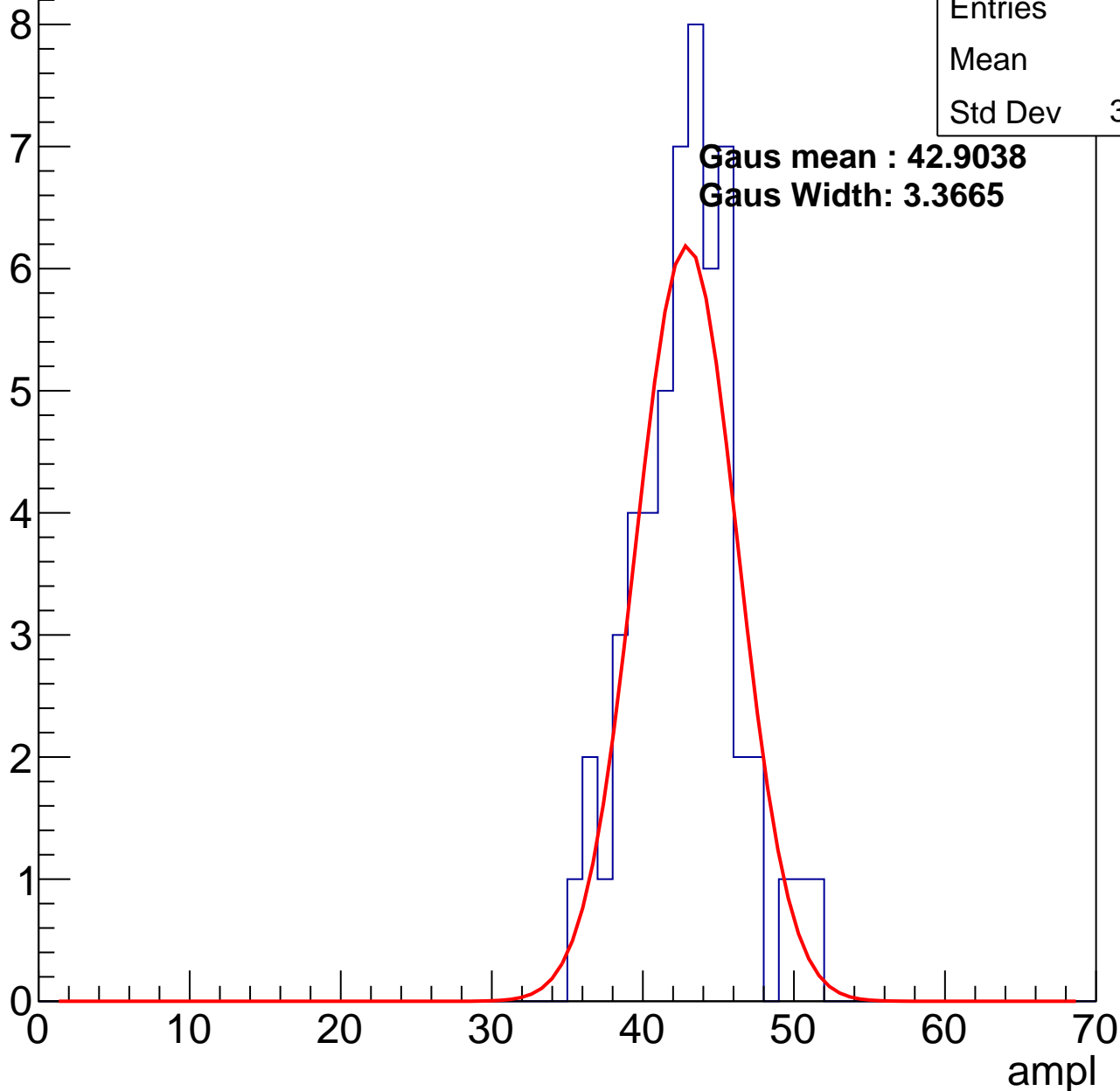
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.4
Std Dev	3.339

Gaus mean : 42.9038

Gaus Width: 3.3665

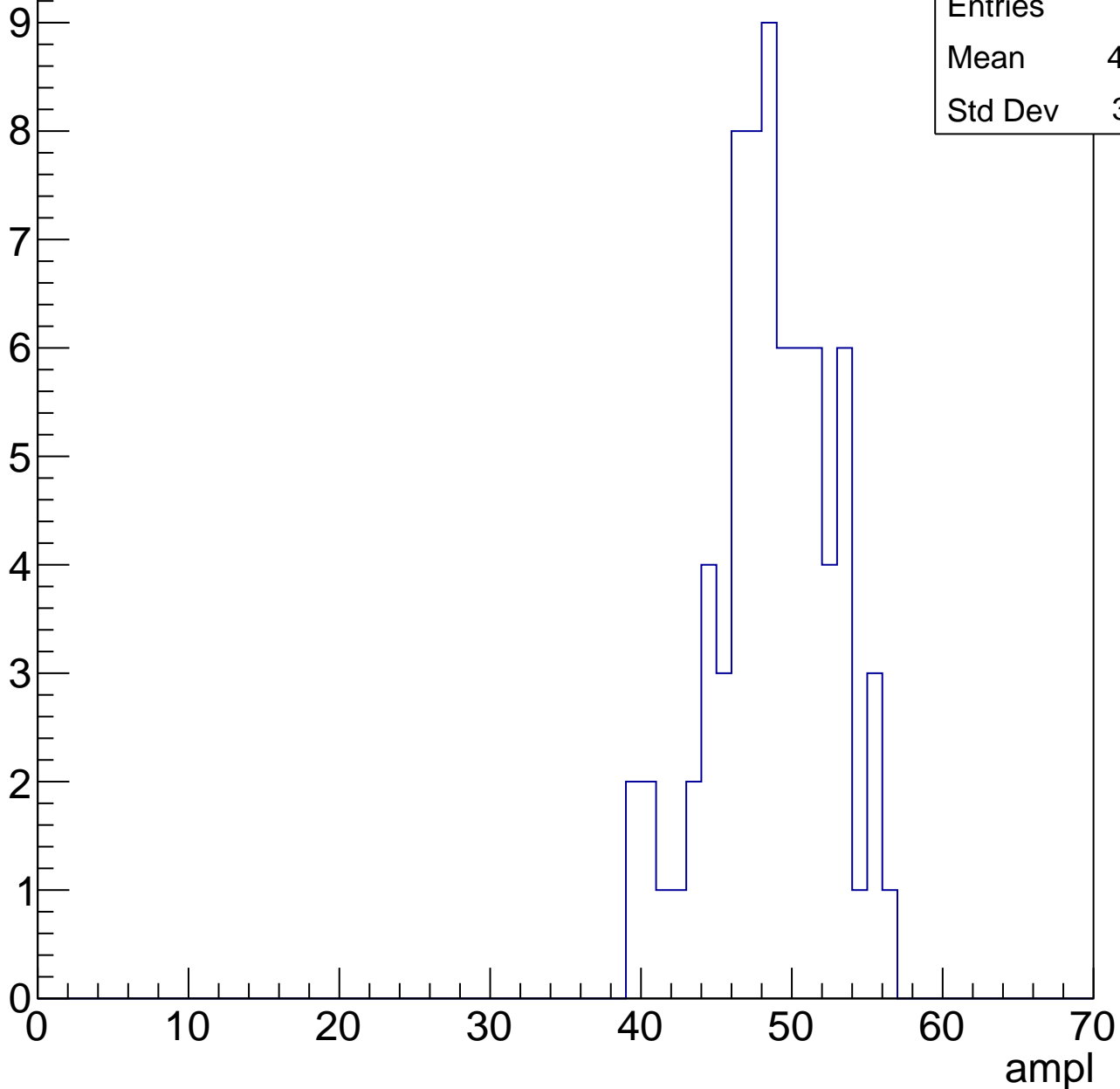


B1L103S, U20-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

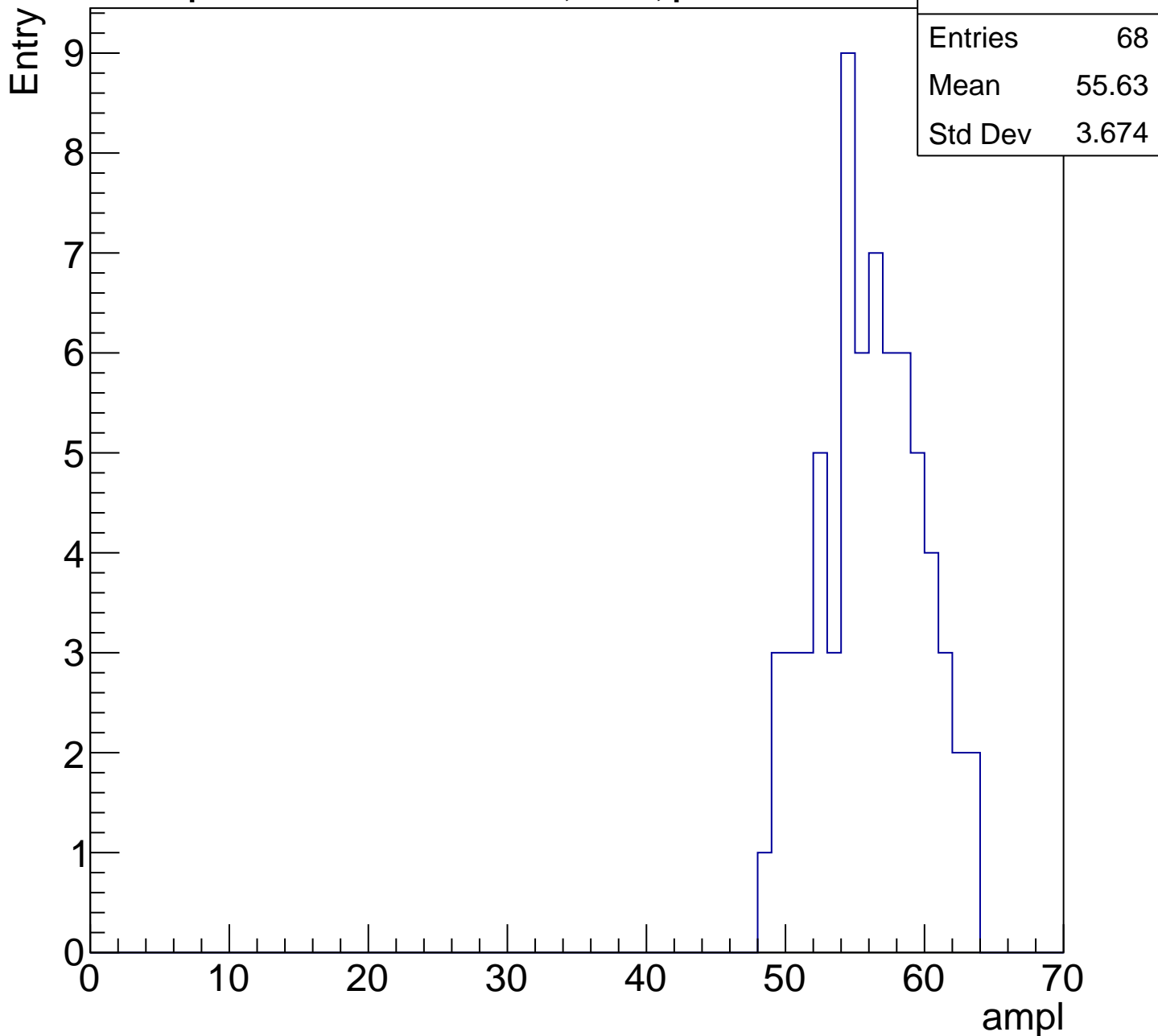
Entry

Entries	73
Mean	48.15
Std Dev	3.881



B1L103S, U20-ch85, adc4

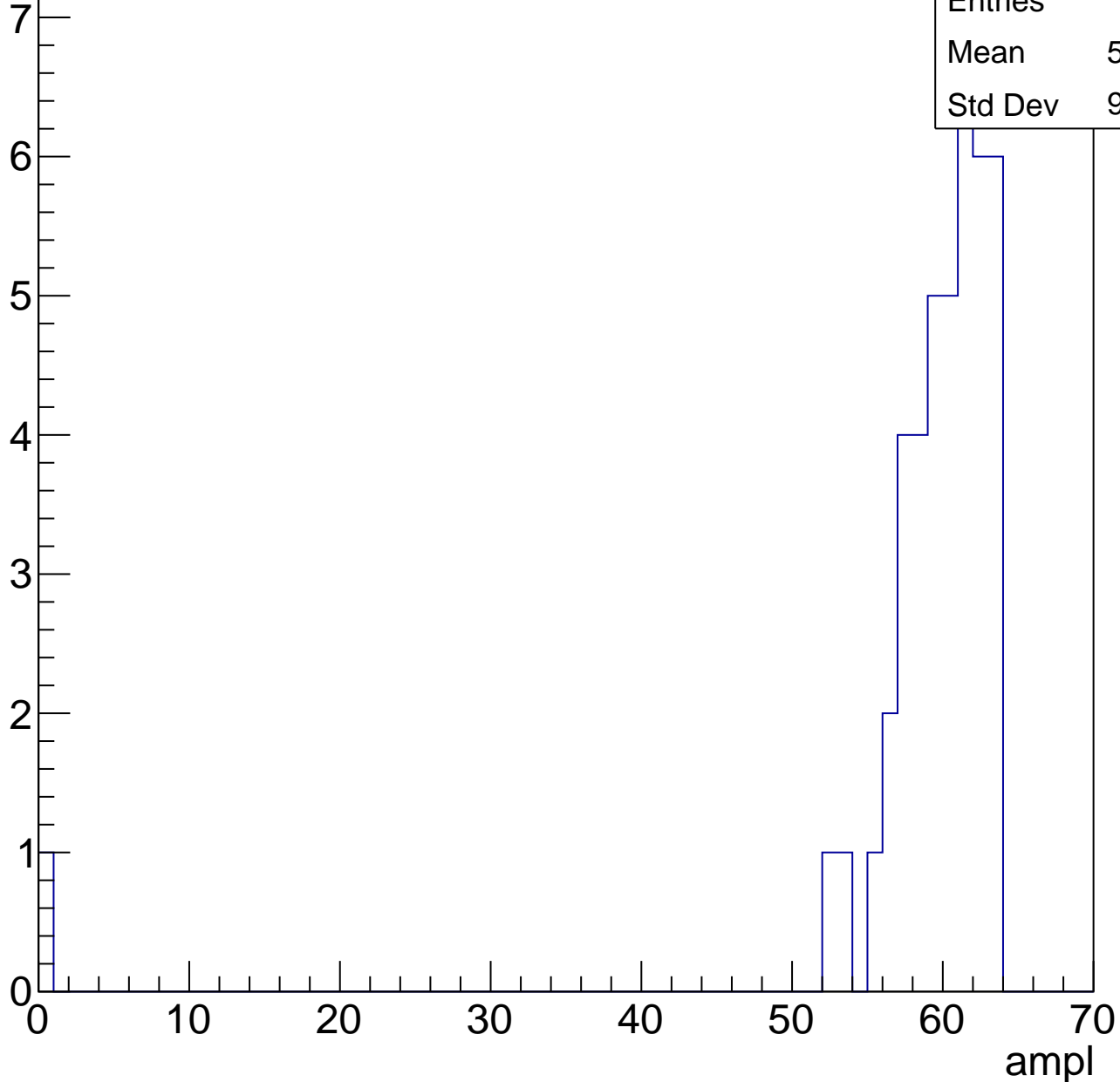
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U20-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch86, adc0

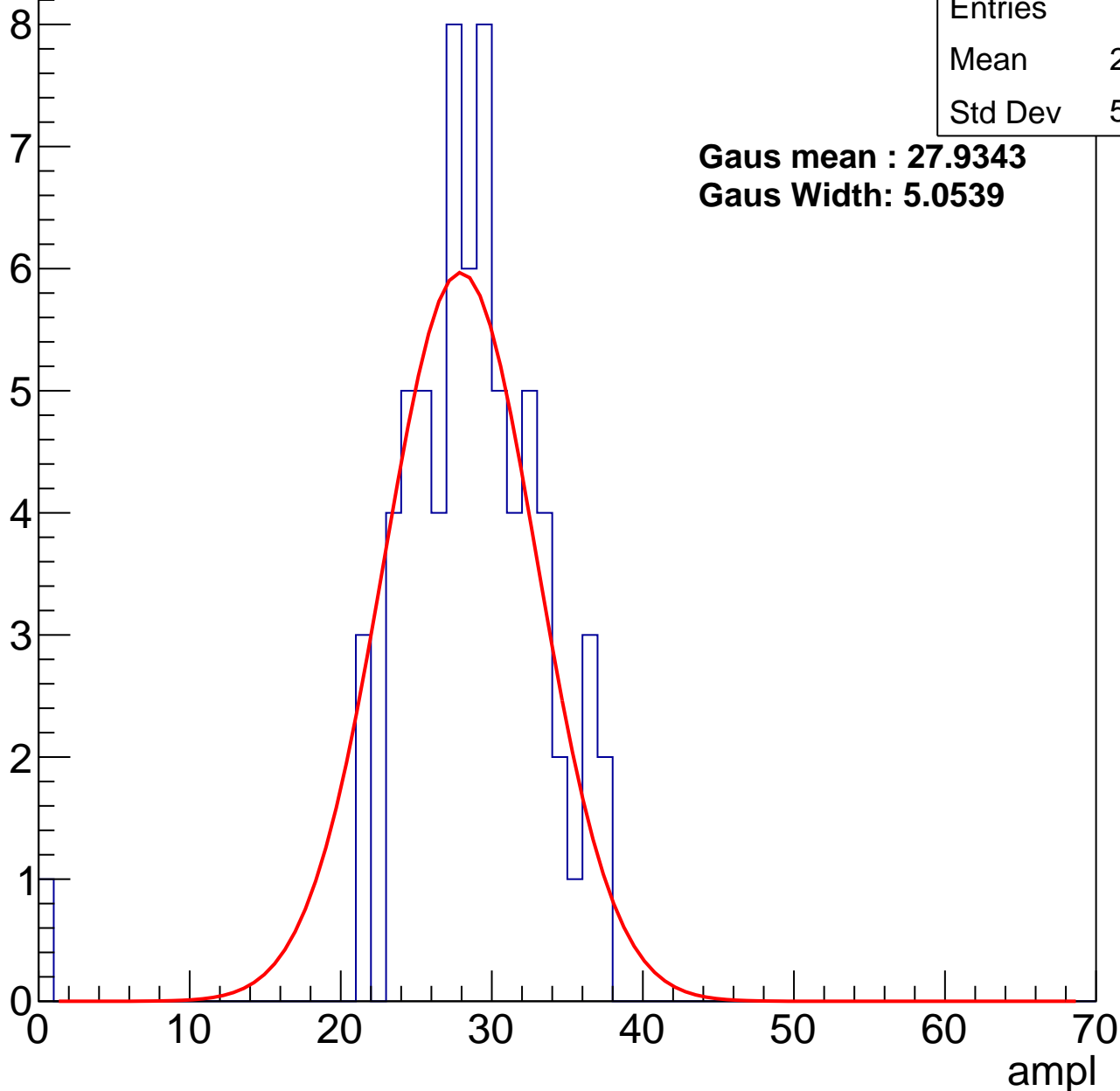
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.16
Std Dev	5.206

Gaus mean : 27.9343

Gaus Width: 5.0539



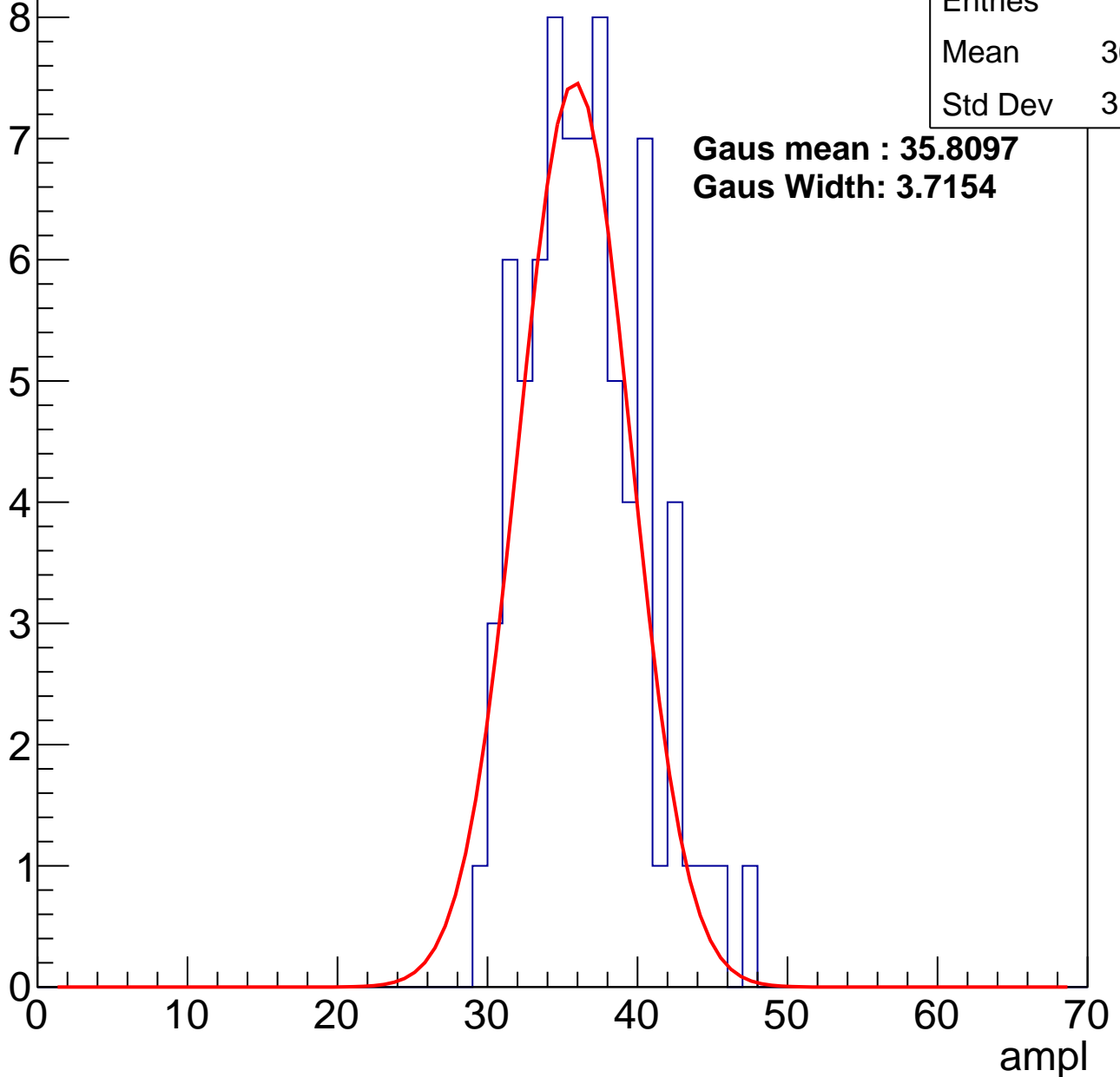
B1L103S, U20-ch86, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.08
Std Dev	3.872

Gaus mean : 35.8097
Gaus Width: 3.7154



B1L103S, U20-ch86, adc2

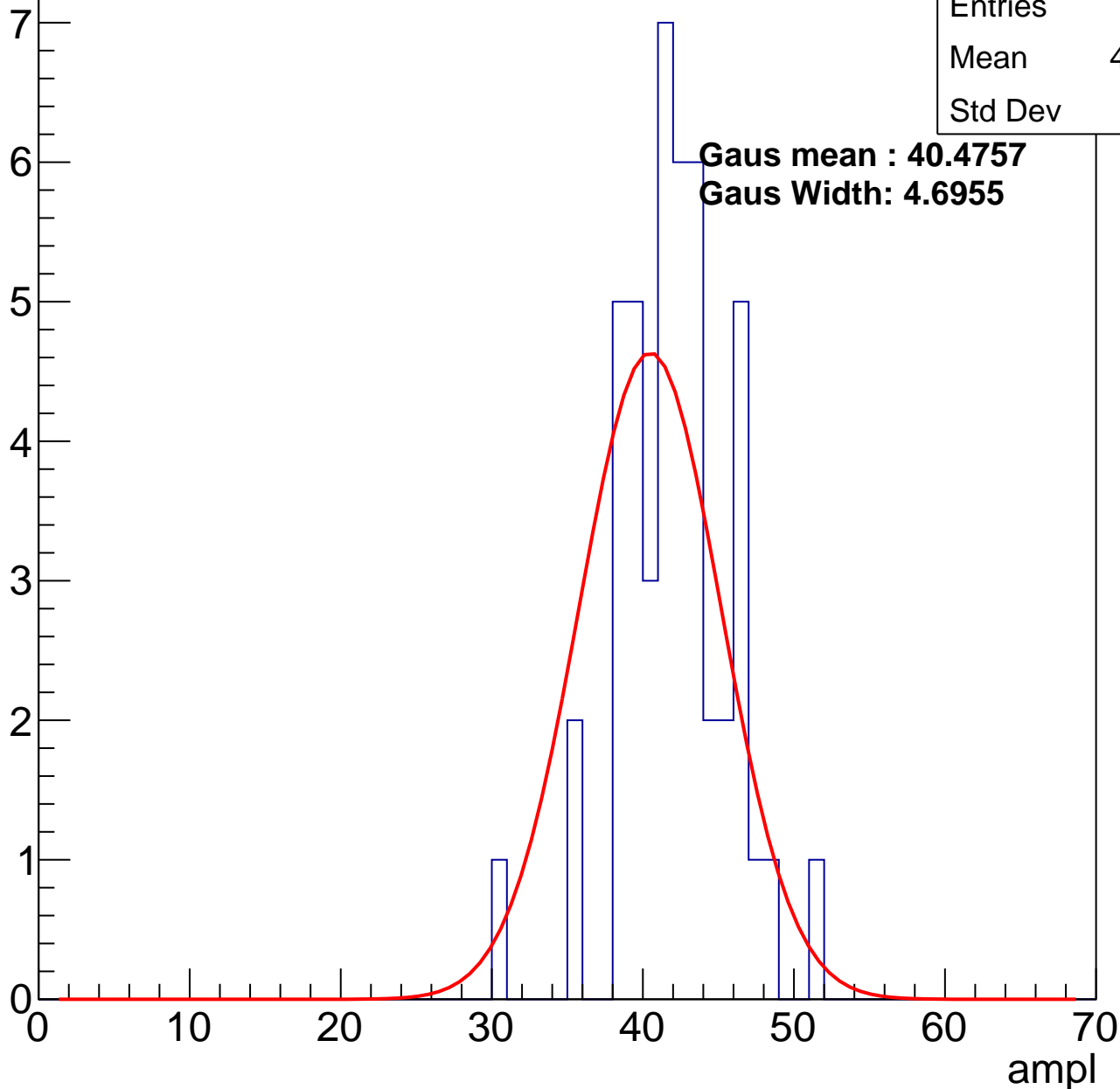
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	41.62
Std Dev	3.67

Gaus mean : 40.4757

Gaus Width: 4.6955

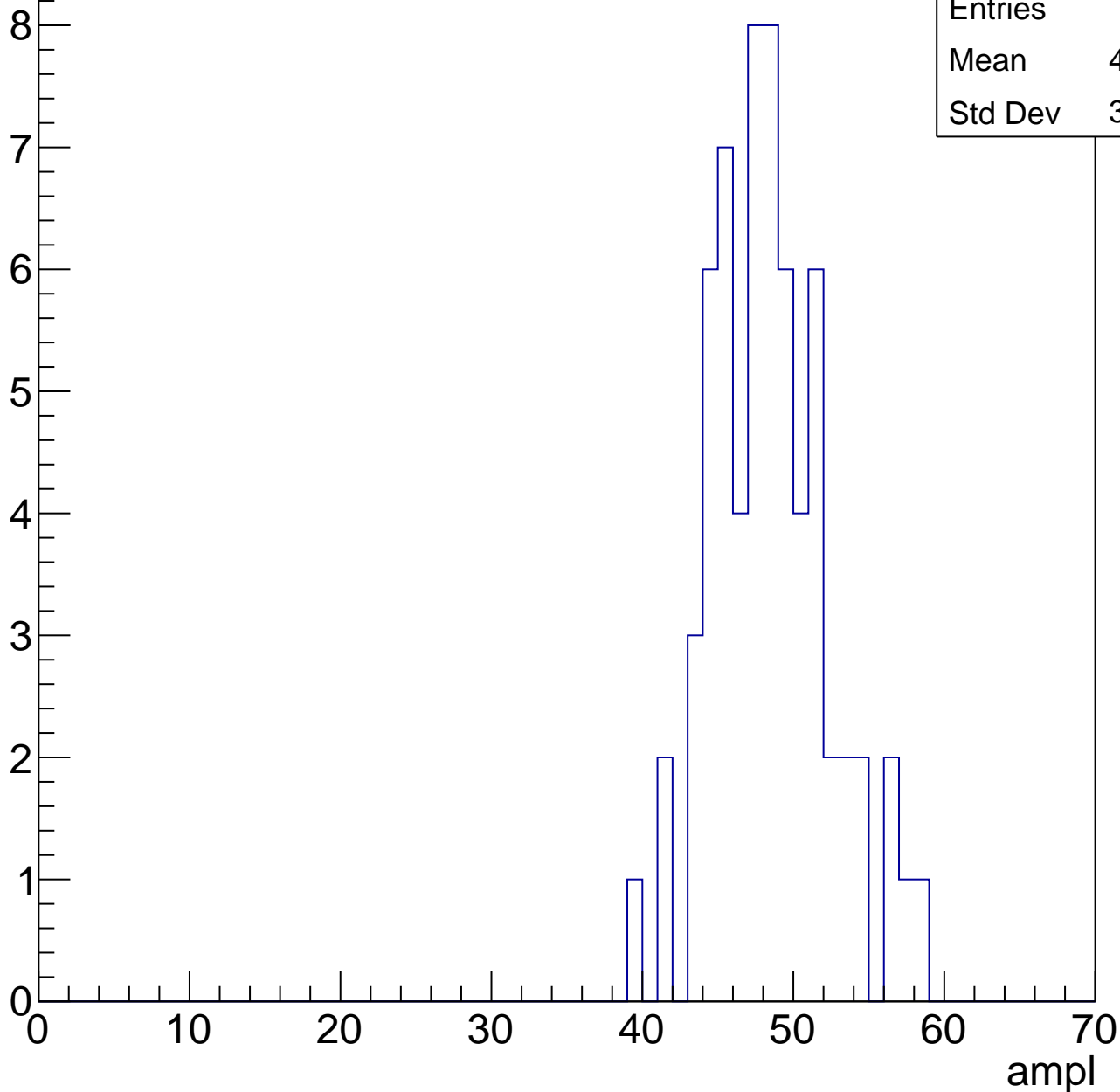


B1L103S, U20-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	47.97
Std Dev	3.875

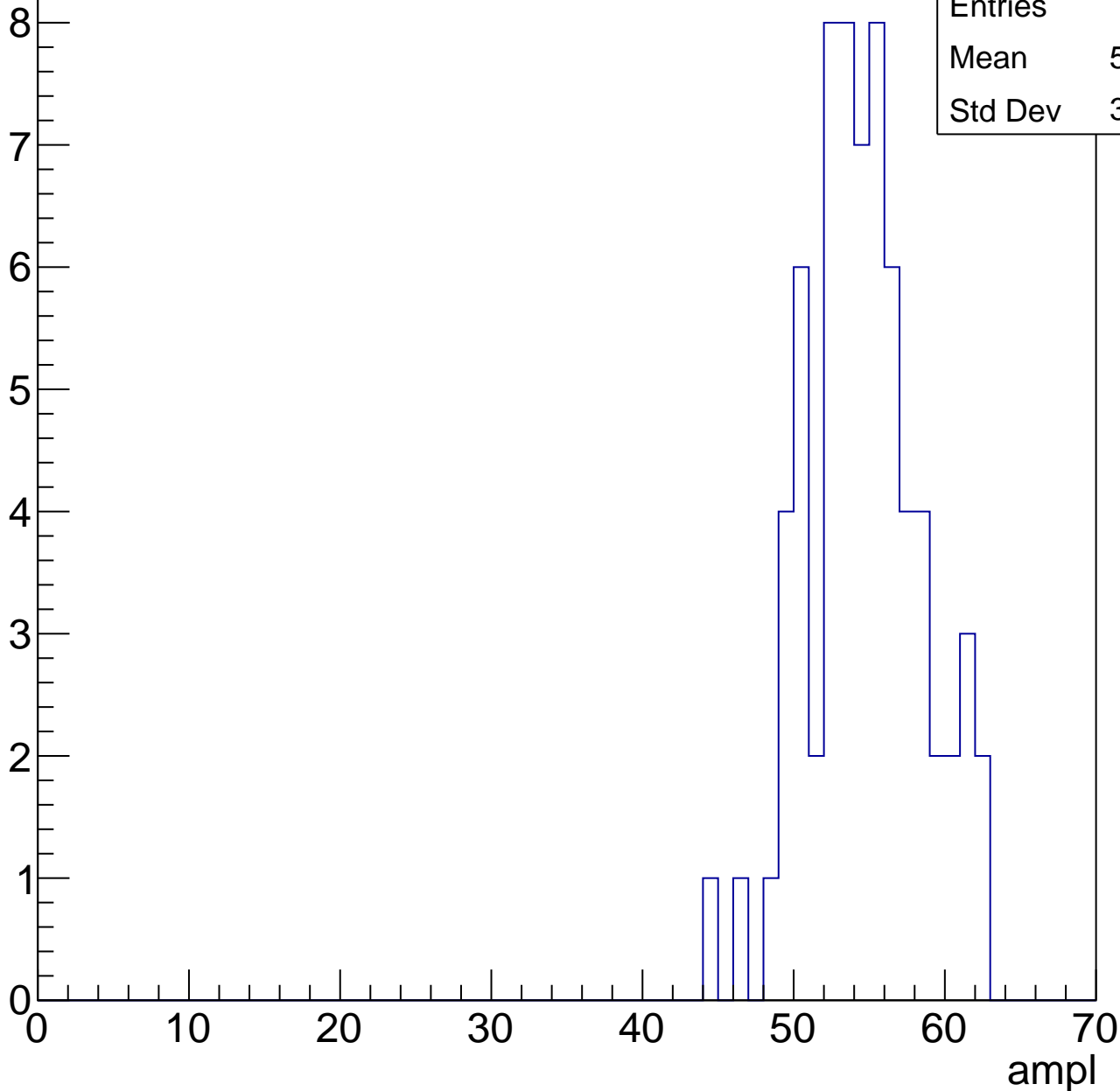


B1L103S, U20-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

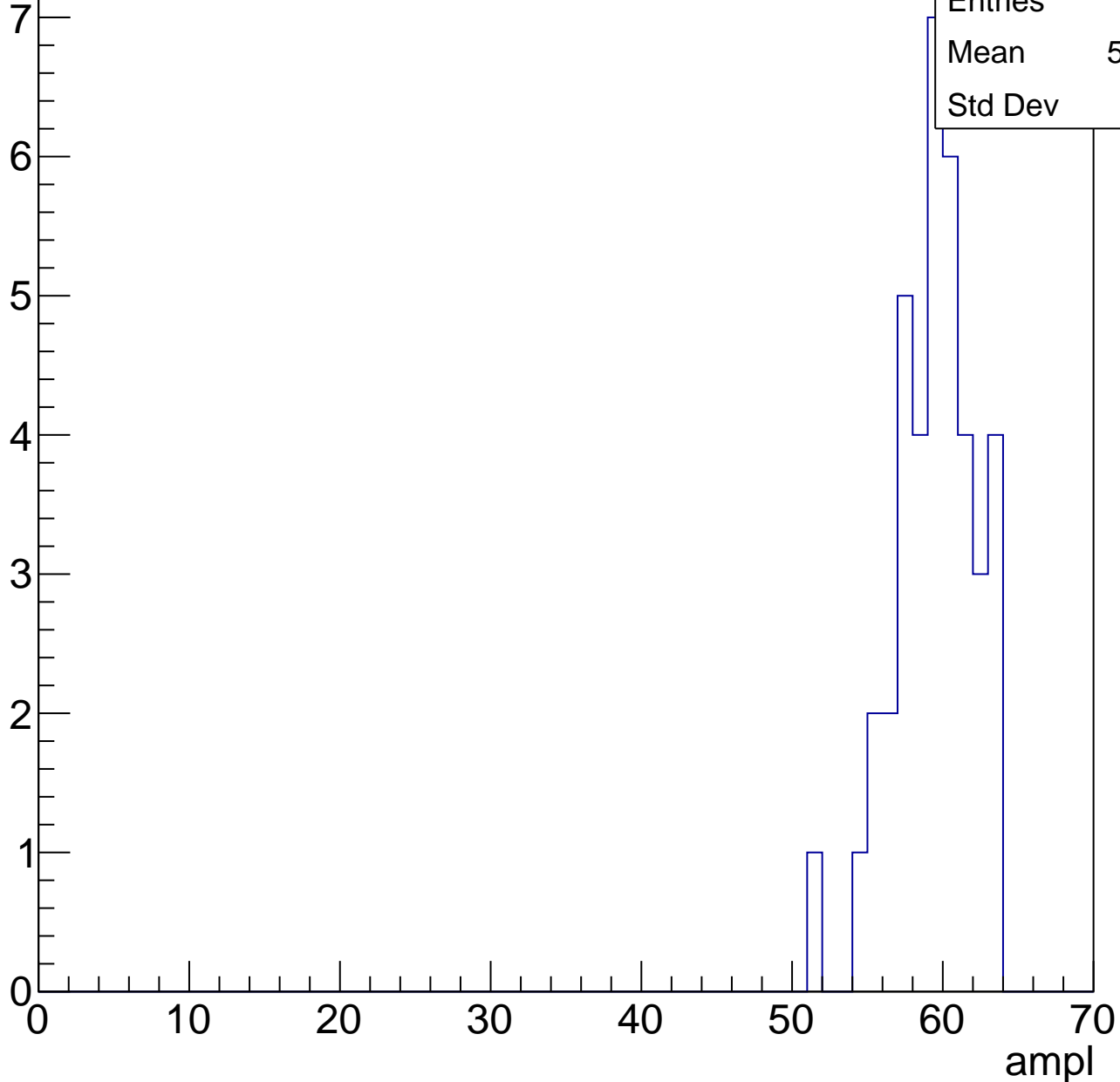
Entries	69
Mean	54.13
Std Dev	3.772



B1L103S, U20-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

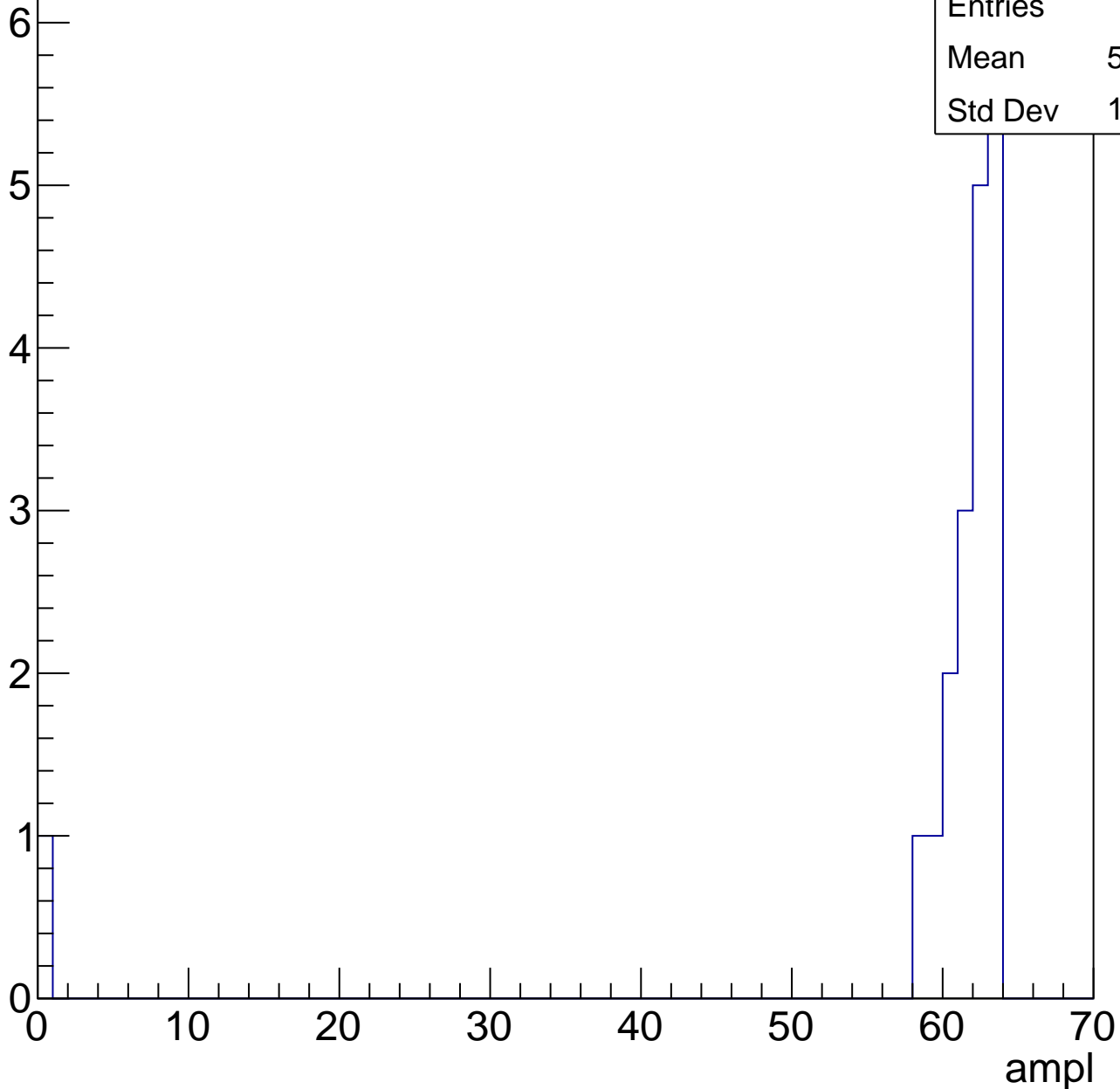


B1L103S, U20-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	58.32
Std Dev	13.82



B1L103S, U20-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch87, adc0

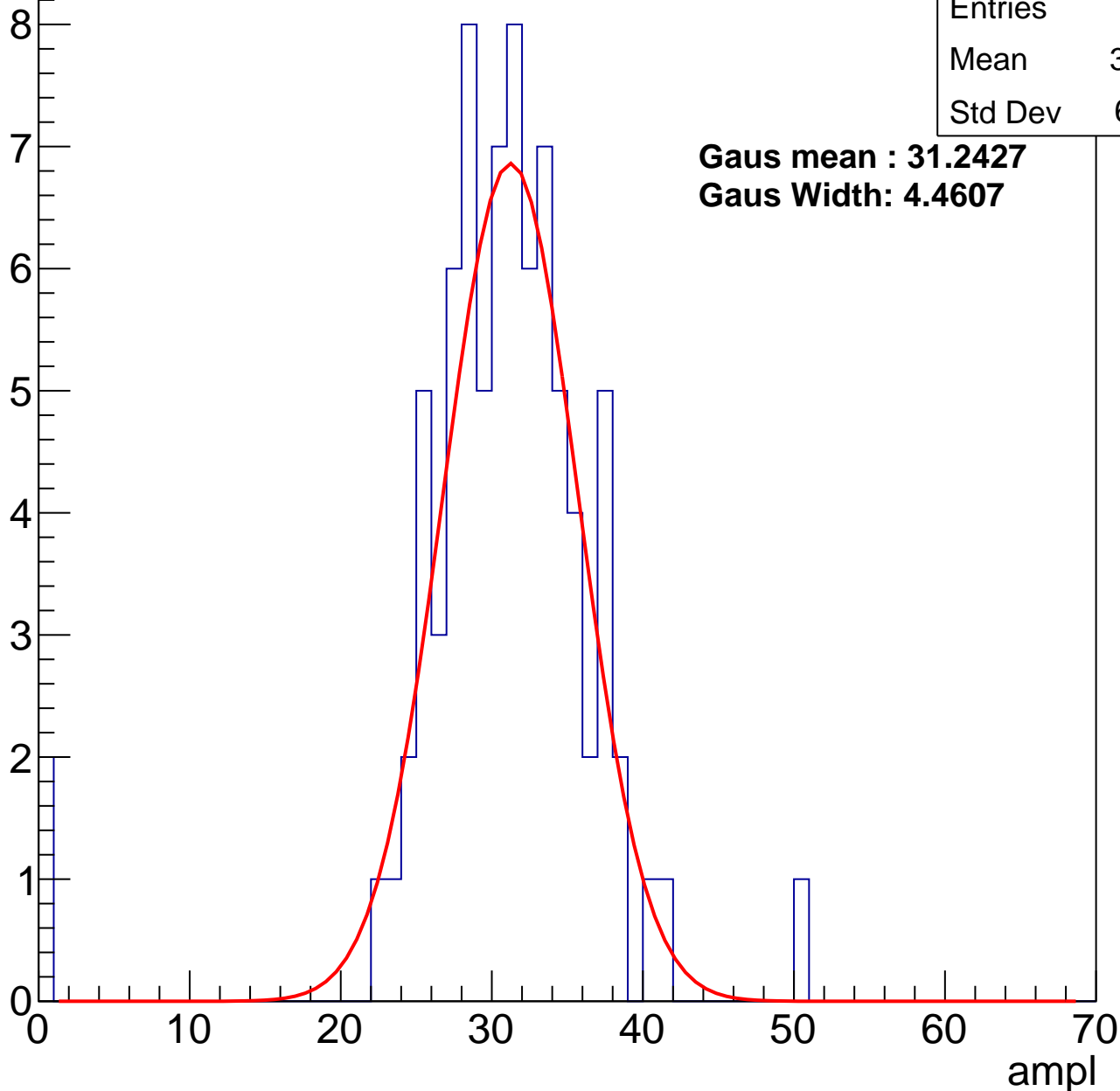
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	30.27
Std Dev	6.611

Gaus mean : 31.2427

Gaus Width: 4.4607



B1L103S, U20-ch87, adc1

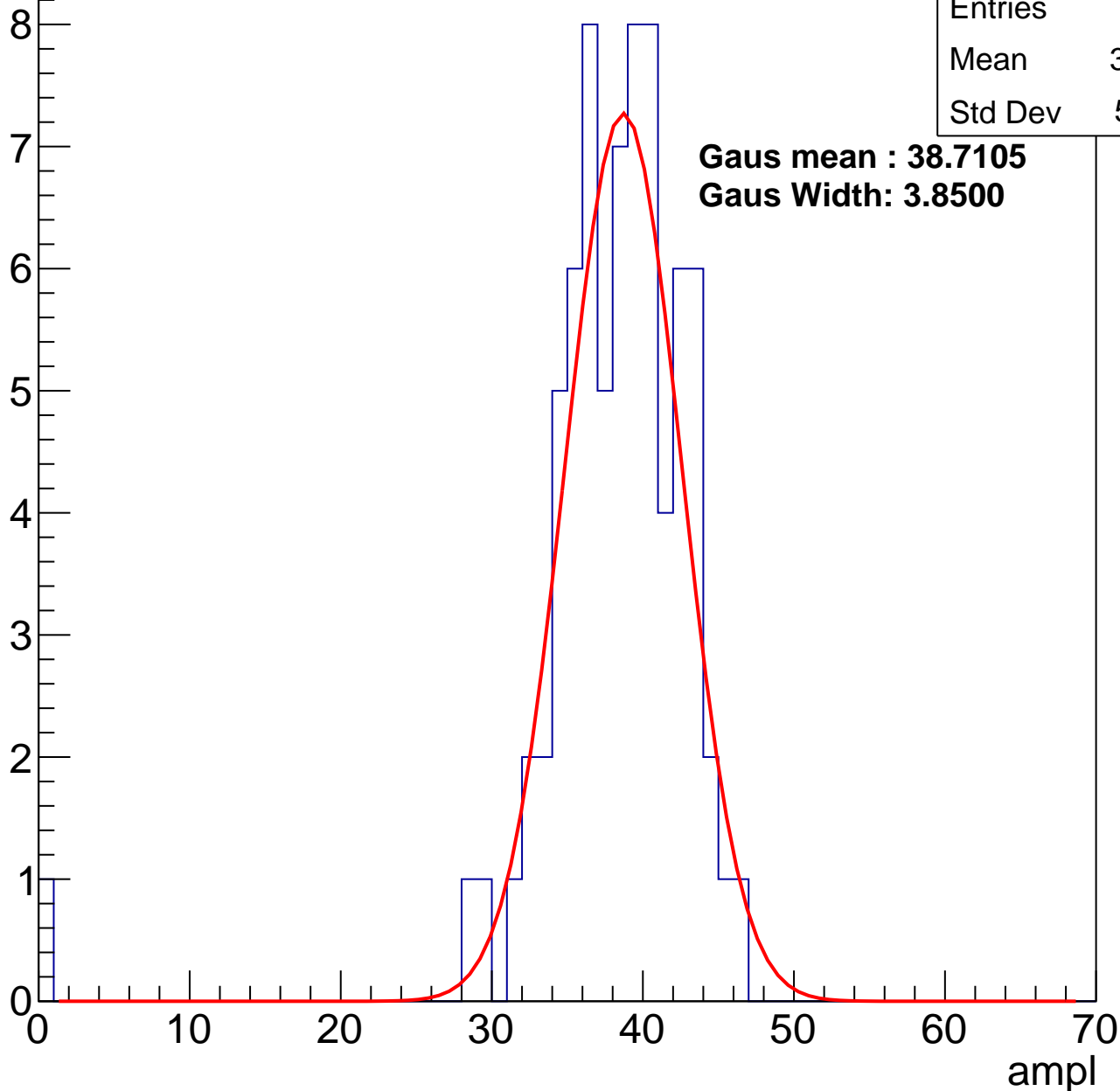
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.63
Std Dev	5.731

Gaus mean : 38.7105

Gaus Width: 3.8500



B1L103S, U20-ch87, adc2

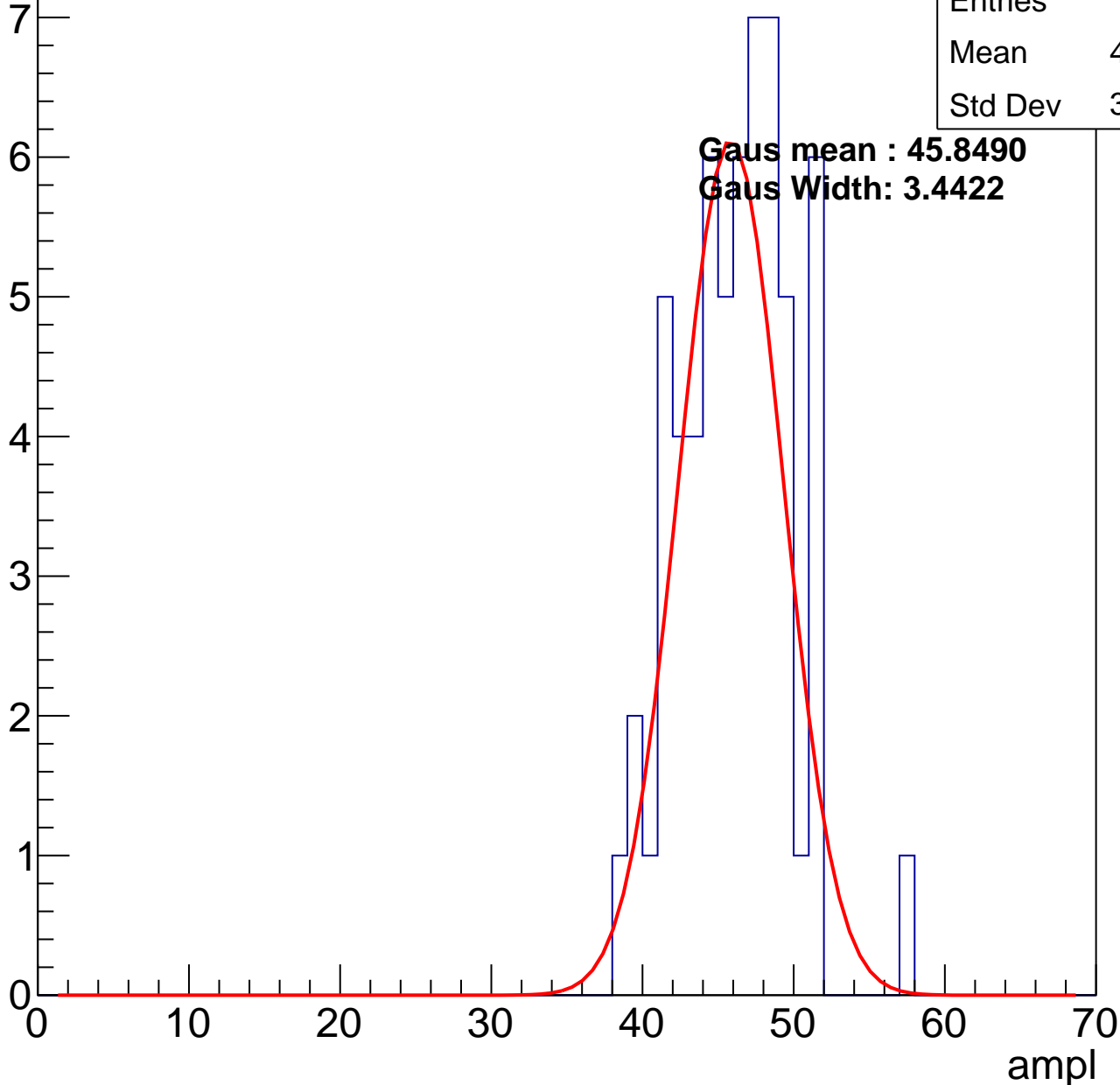
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	45.72
Std Dev	3.654

Gaus mean : 45.8490

Gaus Width: 3.4422

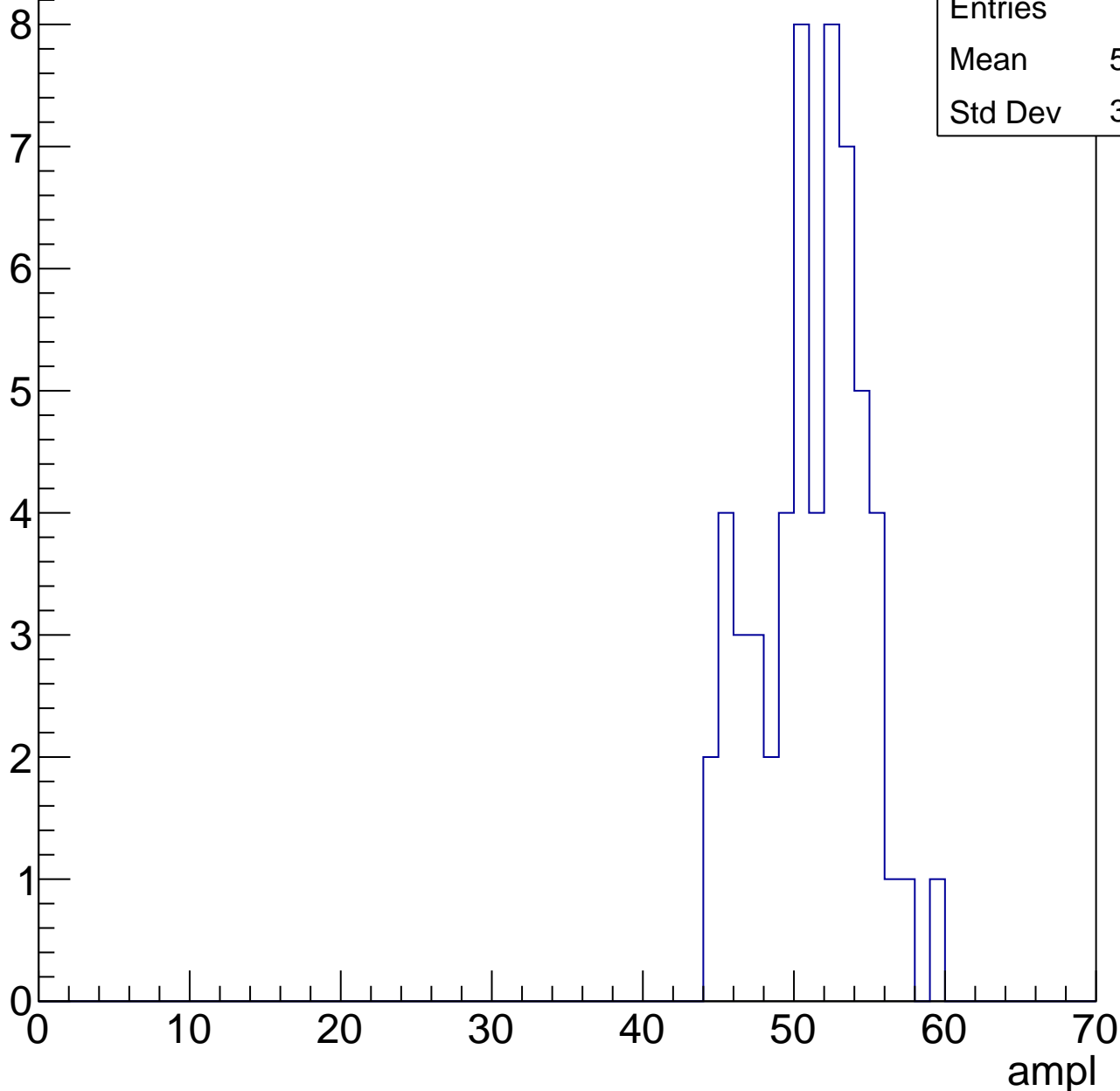


B1L103S, U20-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

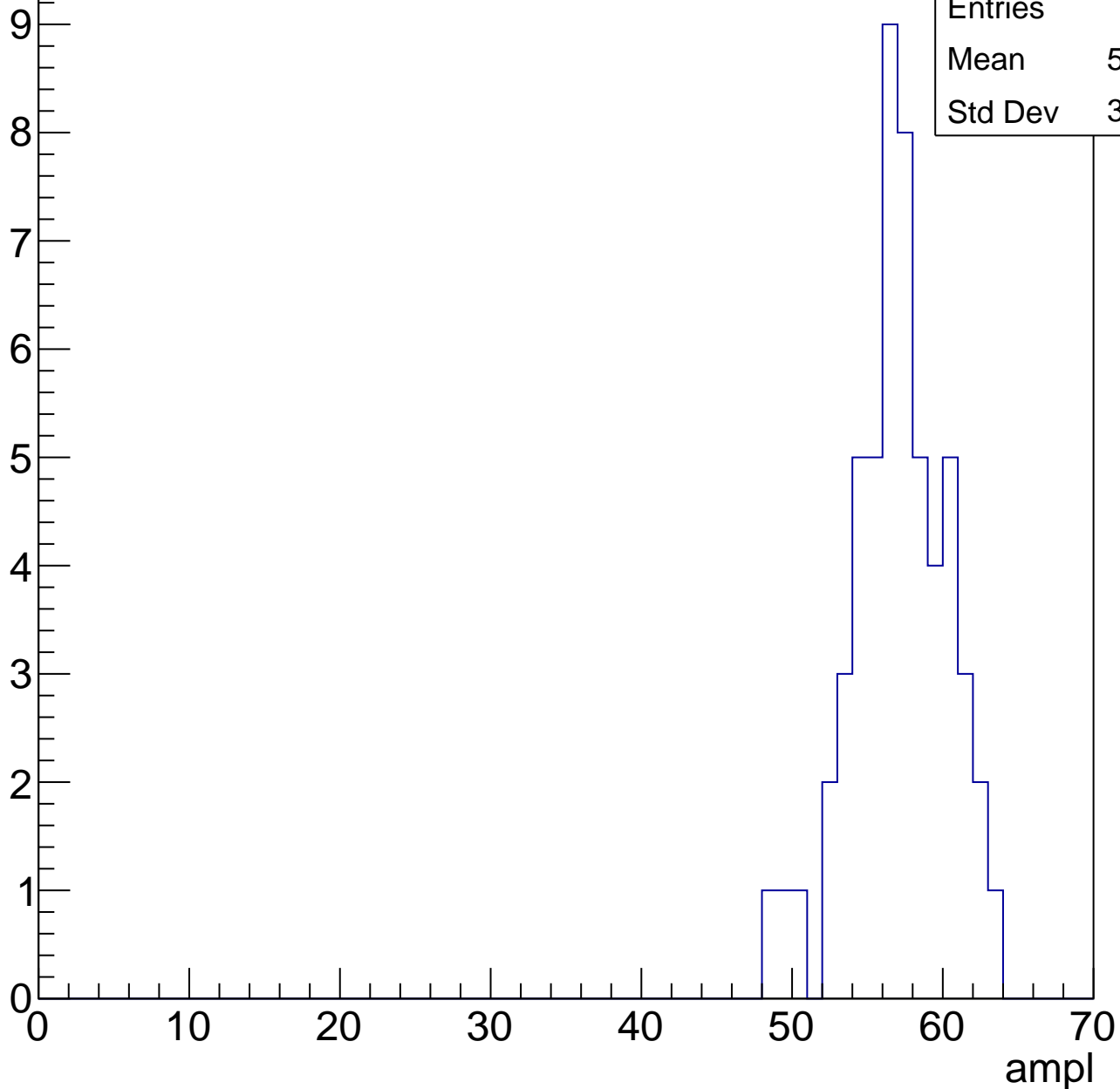
Entries	57
Mean	50.74
Std Dev	3.436



B1L103S, U20-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

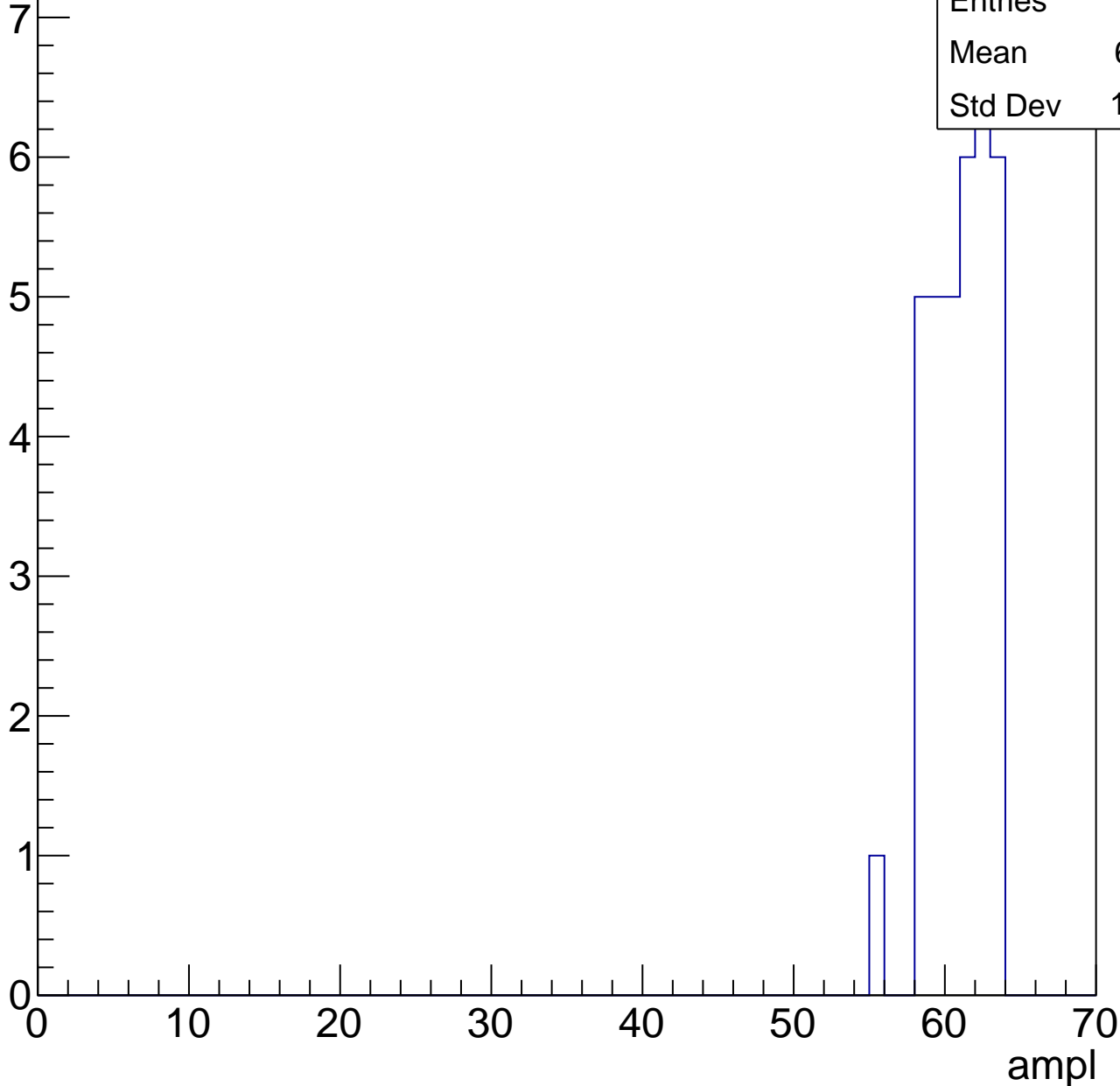


B1L103S, U20-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	60.51
Std Dev	1.918



B1L103S, U20-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	52
Std Dev	23.27

B1L103S, U20-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch88, adc0

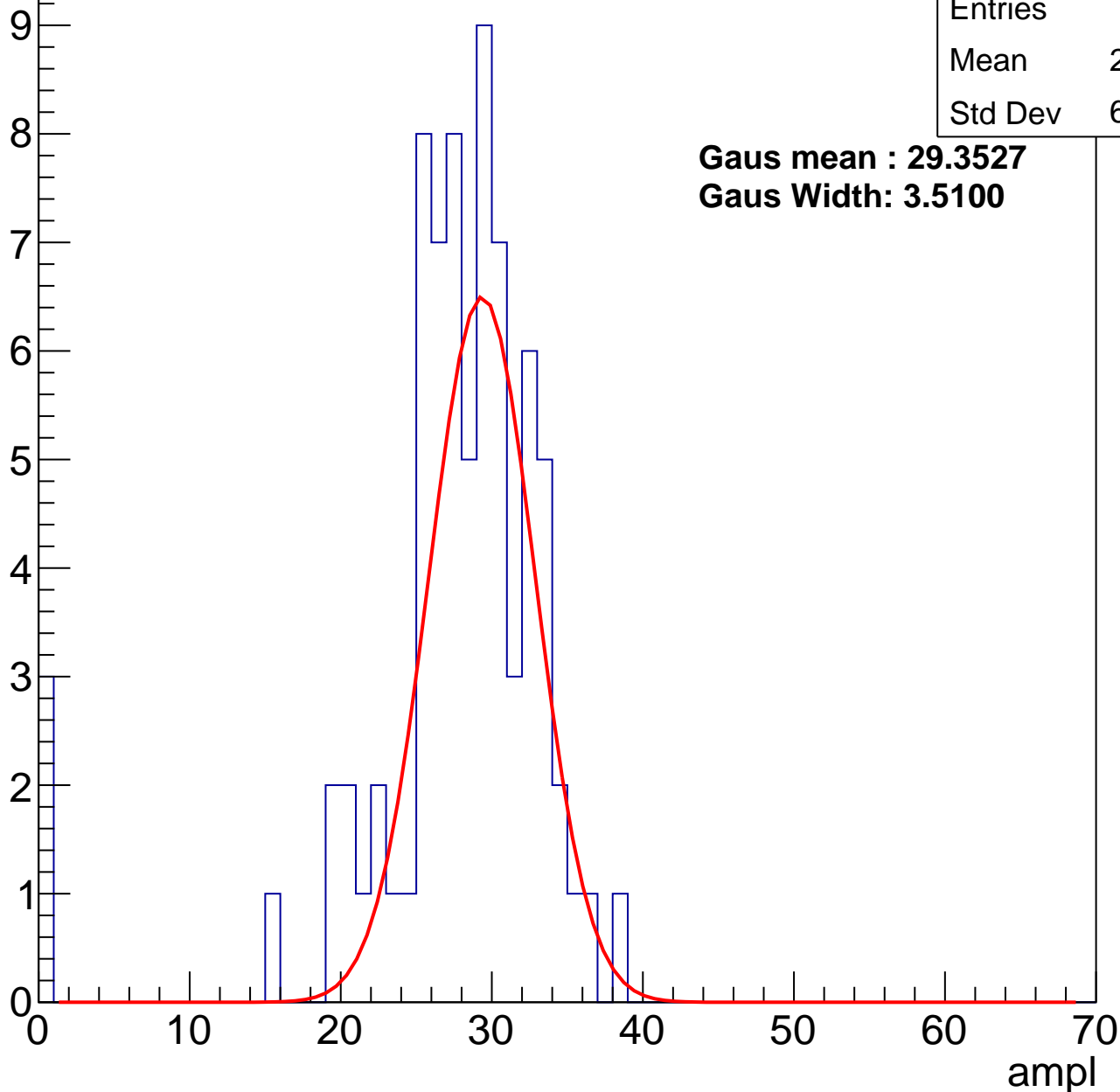
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	26.86
Std Dev	6.836

Gaus mean : 29.3527

Gaus Width: 3.5100



B1L103S, U20-ch88, adc1

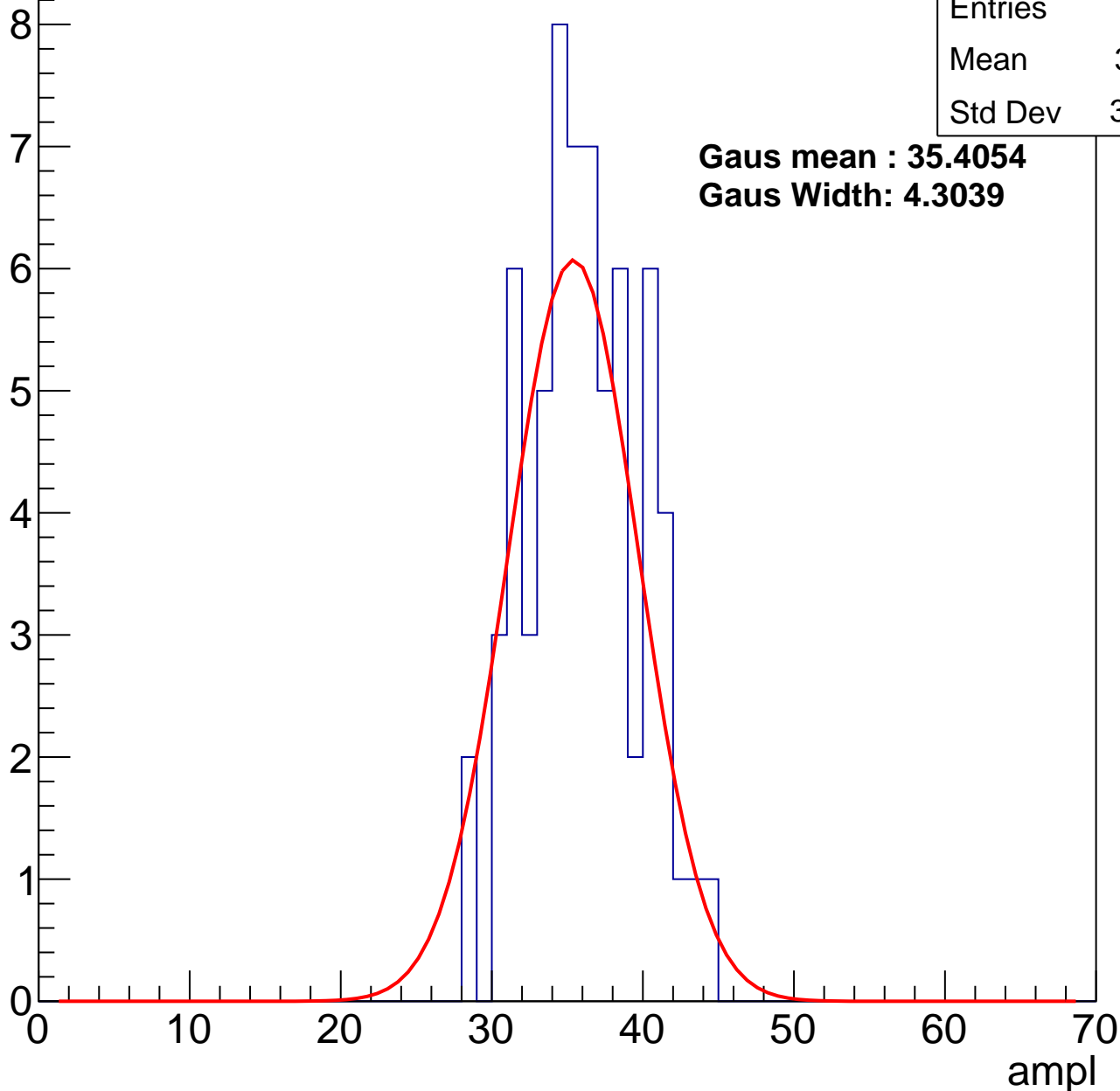
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.61
Std Dev	3.665

Gaus mean : 35.4054

Gaus Width: 4.3039



B1L103S, U20-ch88, adc2

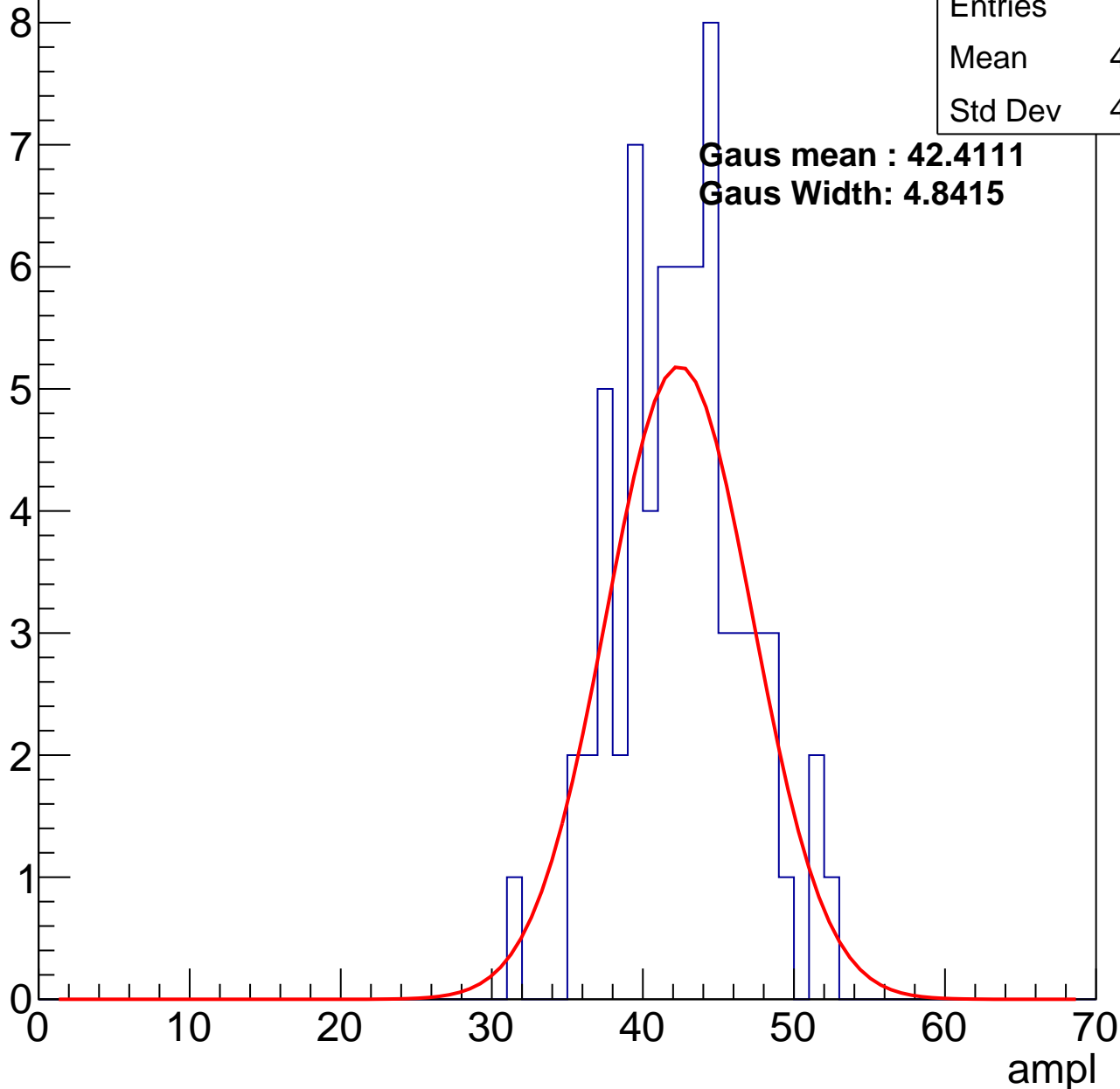
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.09
Std Dev	4.187

Gaus mean : 42.4111

Gaus Width: 4.8415

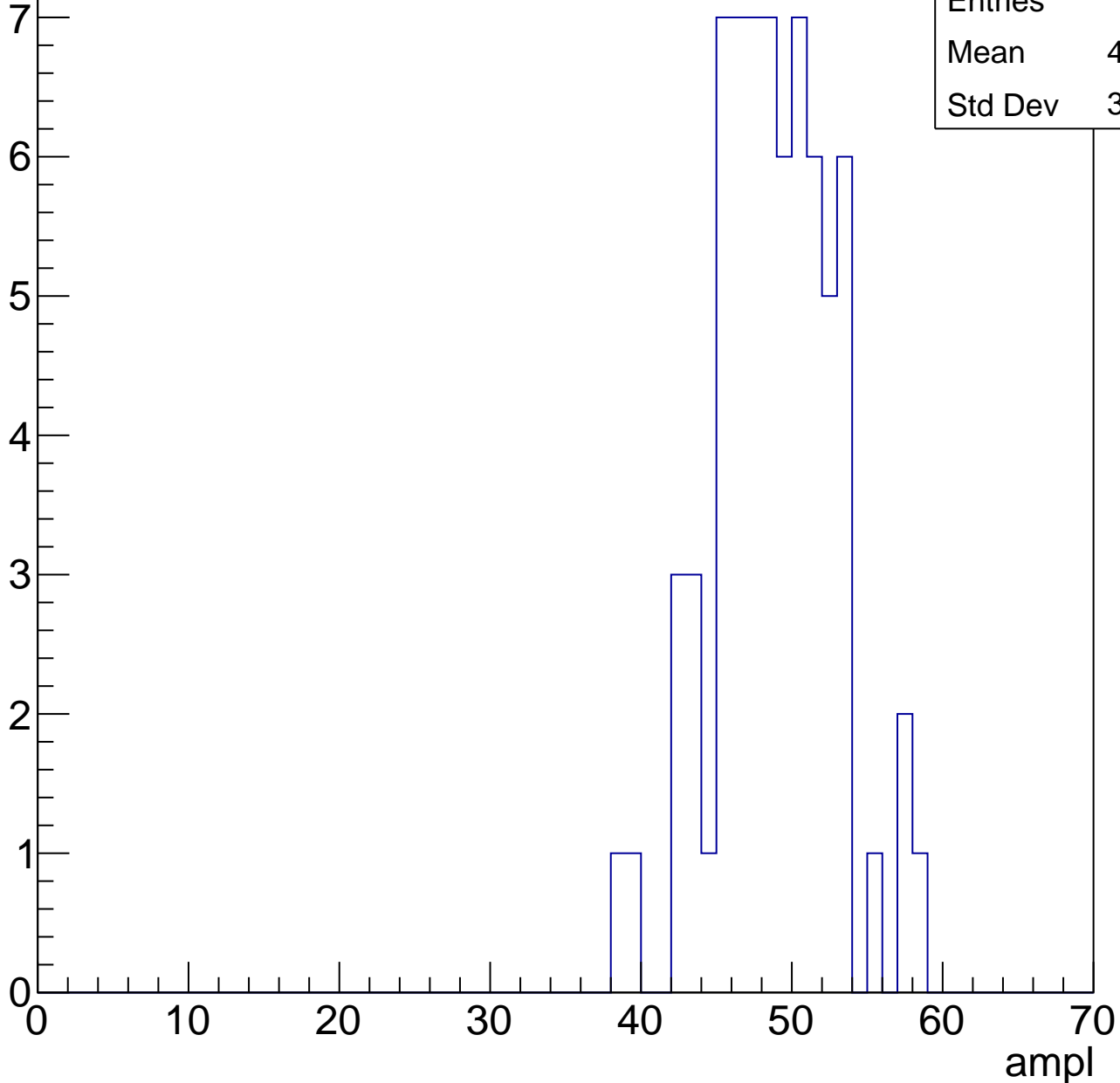


B1L103S, U20-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.35
Std Dev	3.933

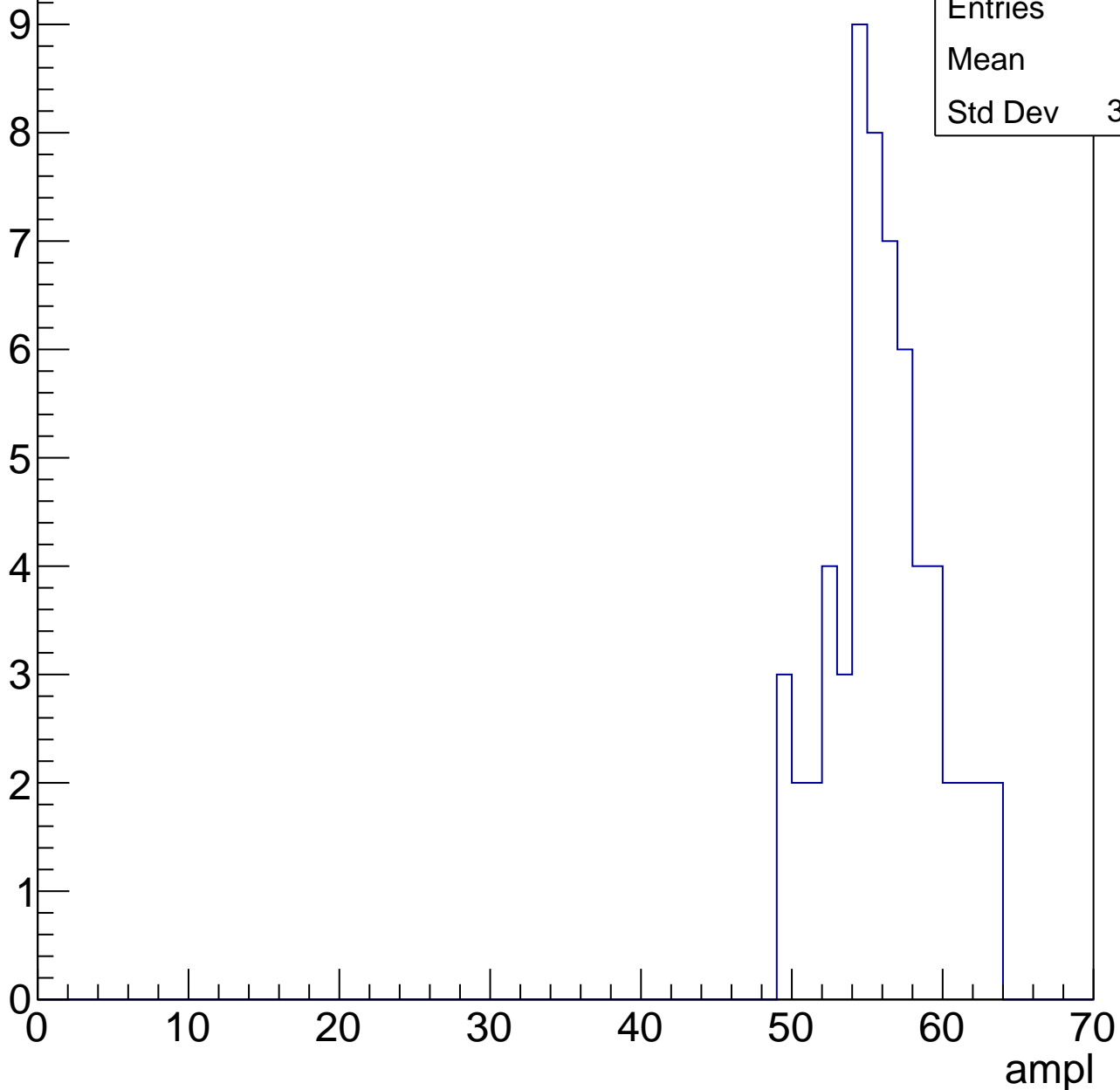


B1L103S, U20-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.6
Std Dev	3.426

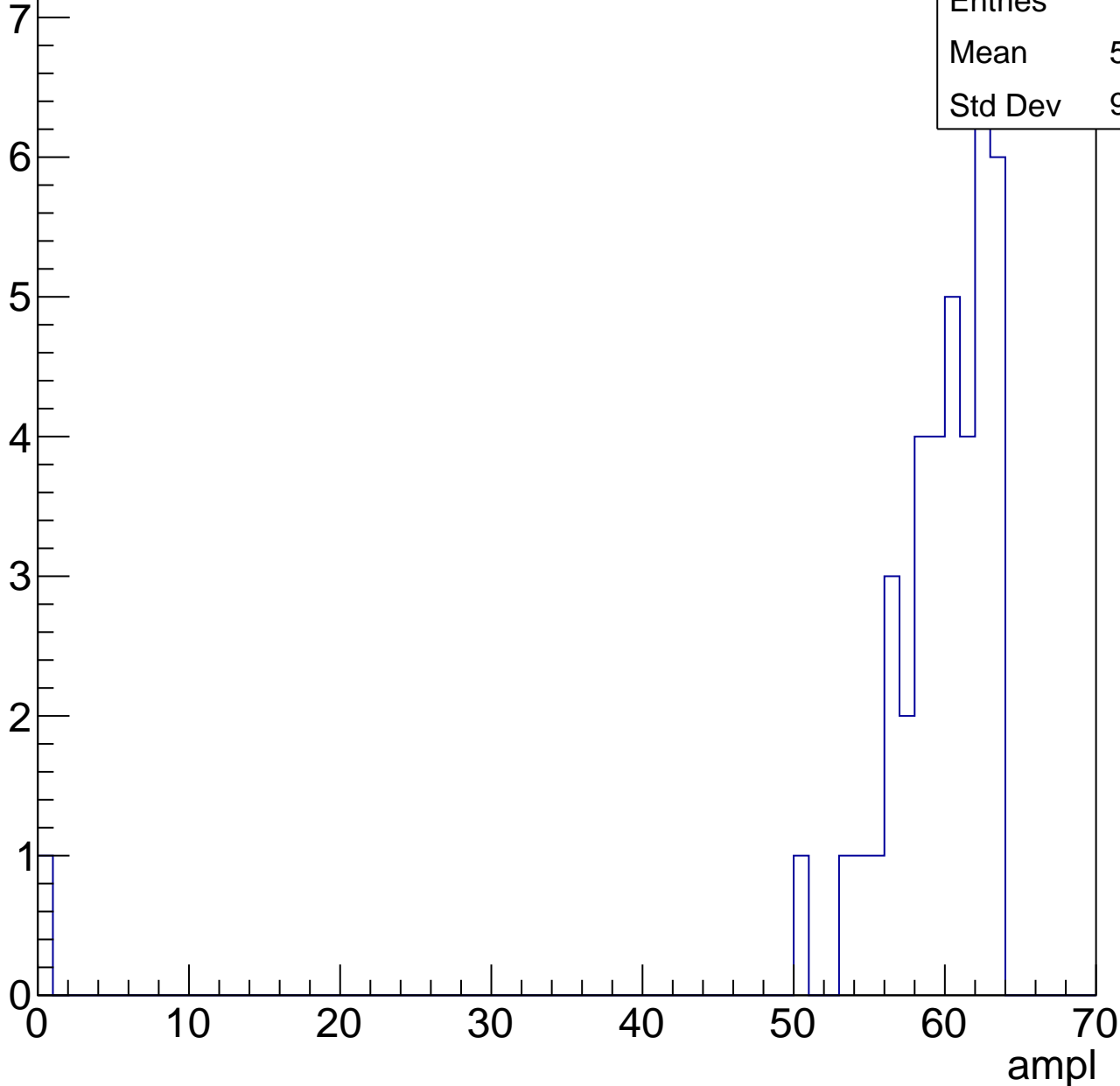


B1L103S, U20-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

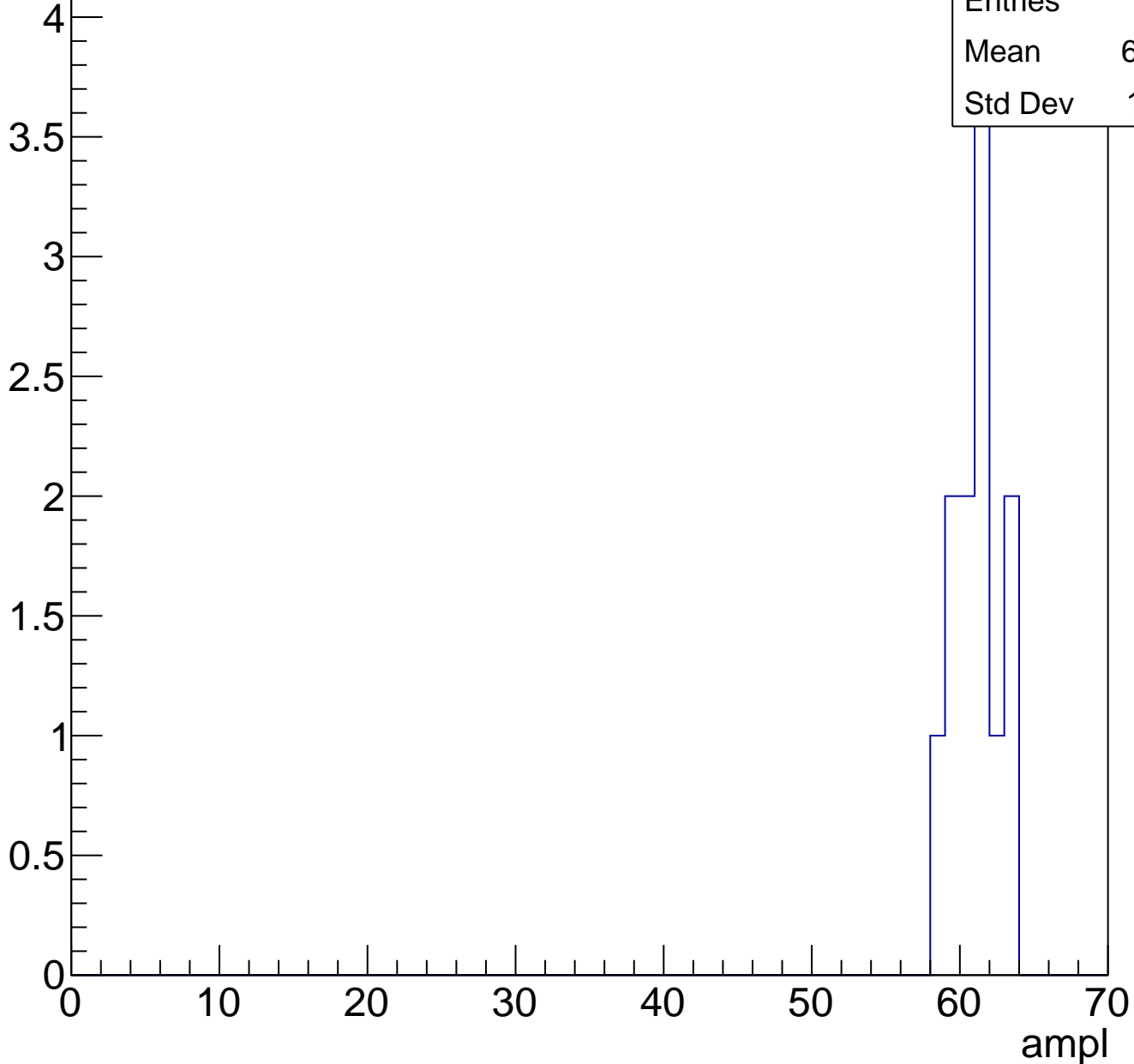
Entries	40
Mean	57.95
Std Dev	9.765



B1L103S, U20-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

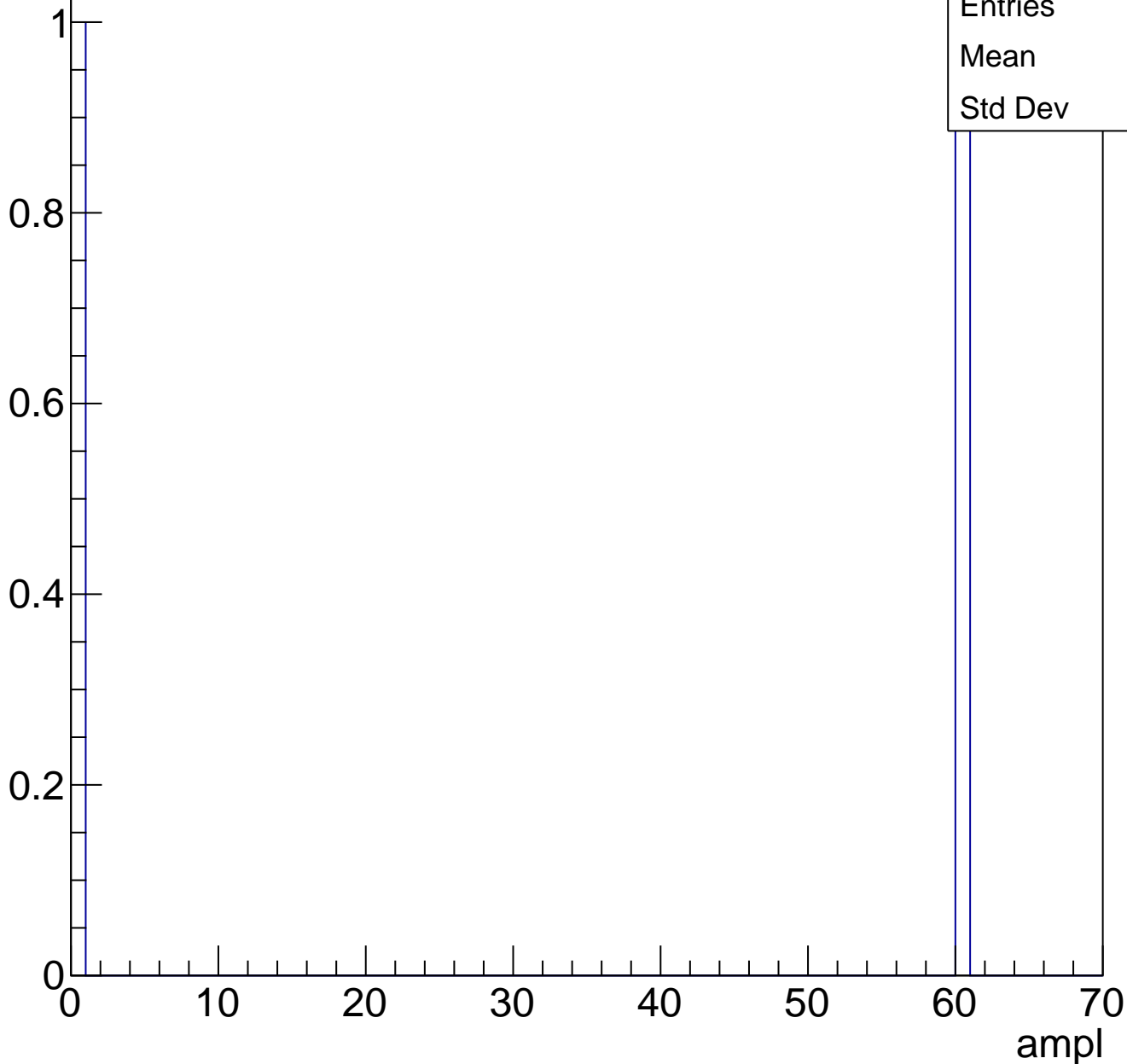
Entry



B1L103S, U20-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch89, adc0

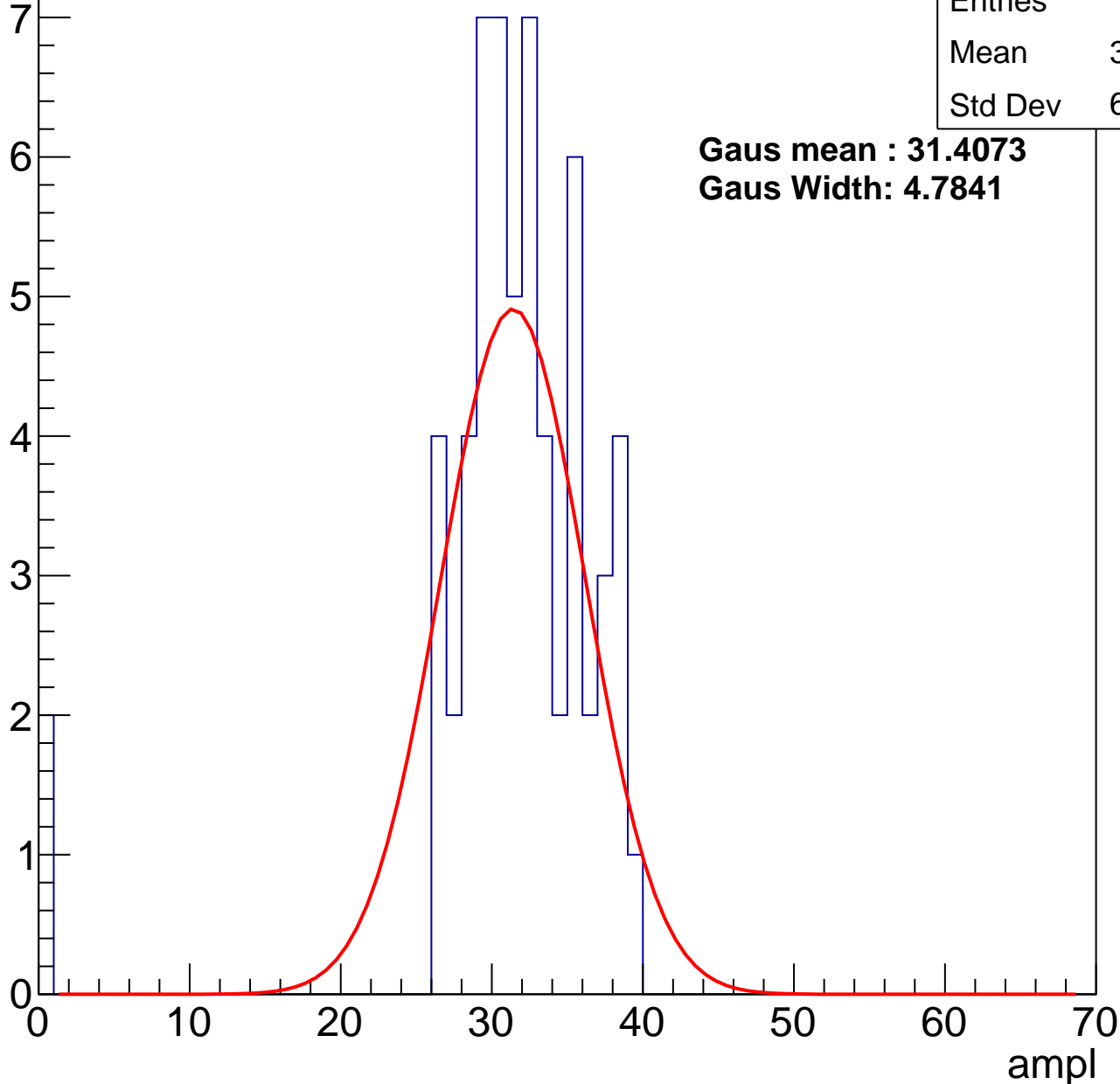
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	30.77
Std Dev	6.679

Gaus mean : 31.4073

Gaus Width: 4.7841



B1L103S, U20-ch89, adc1

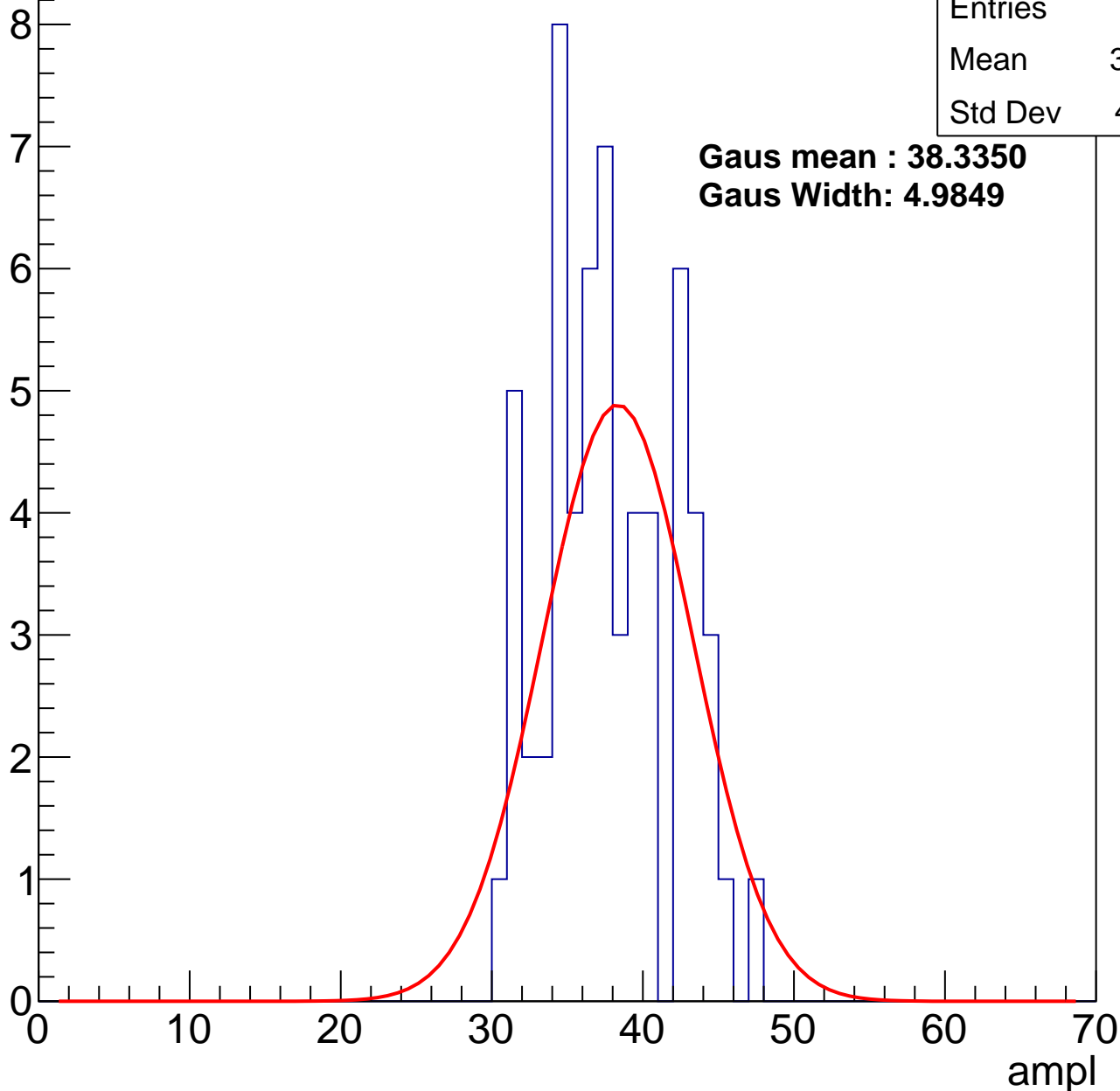
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.38
Std Dev	4.161

Gaus mean : 38.3350

Gaus Width: 4.9849



B1L103S, U20-ch89, adc2

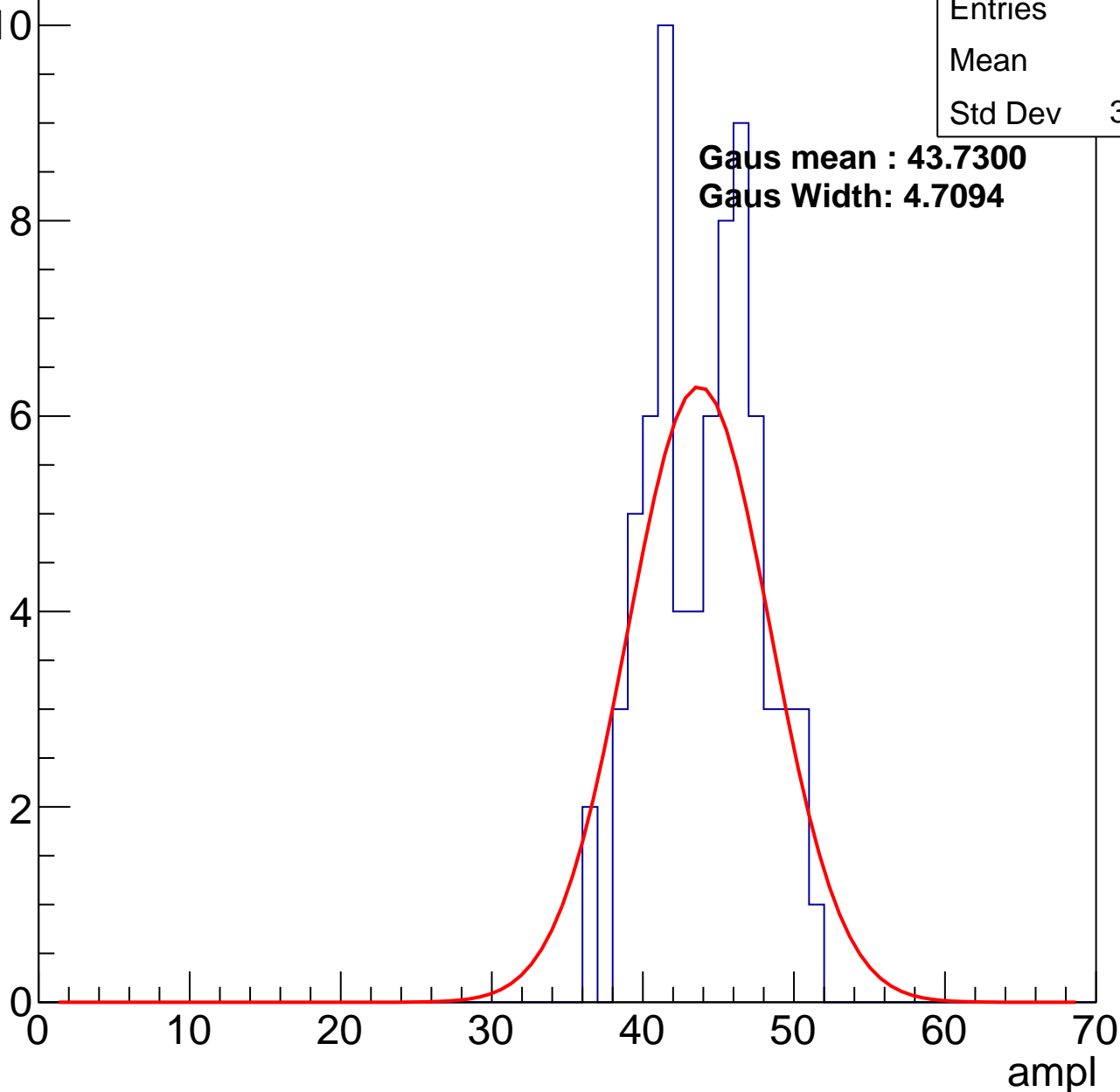
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.6
Std Dev	3.576

Gaus mean : 43.7300

Gaus Width: 4.7094

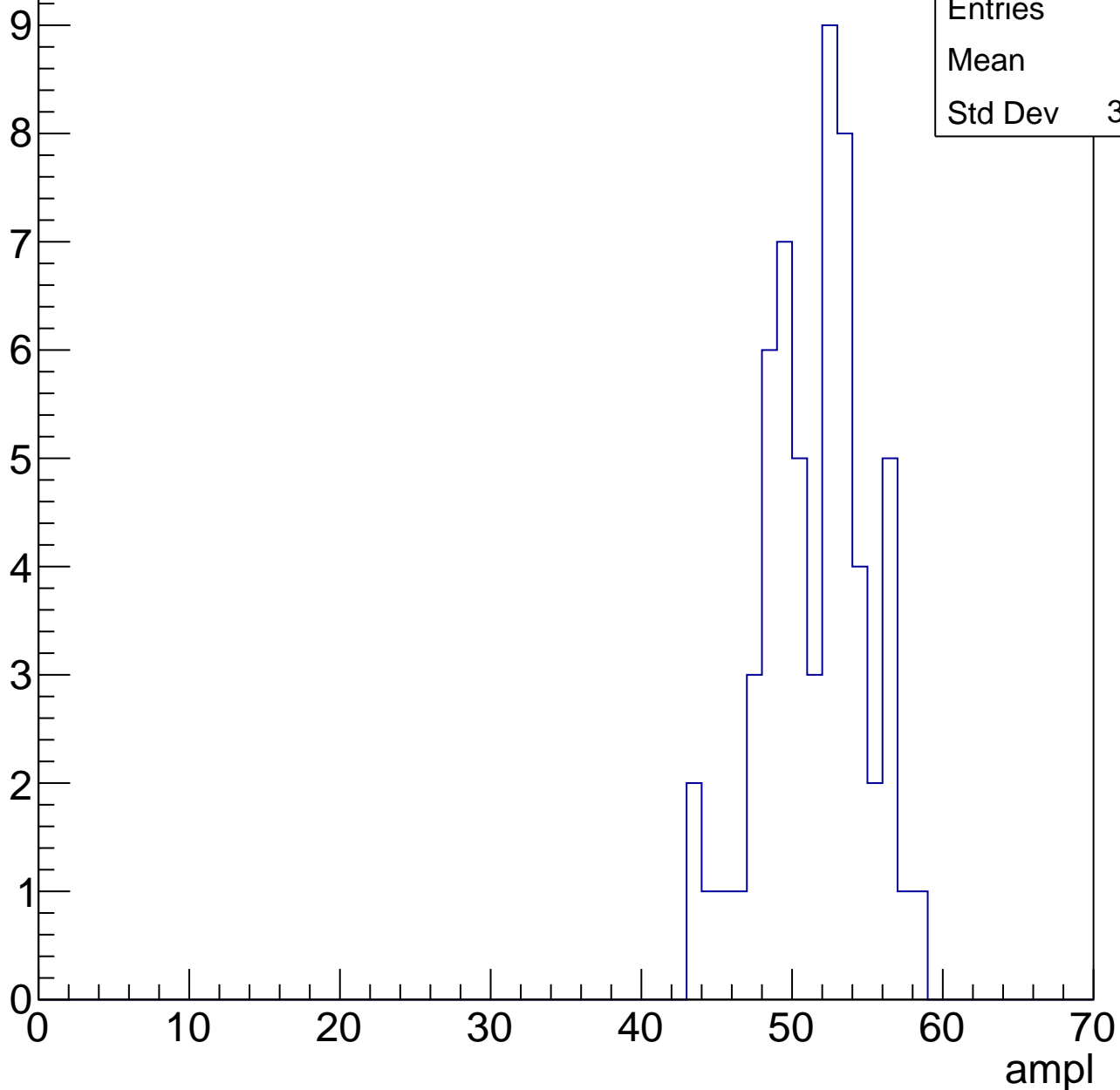


B1L103S, U20-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	51
Std Dev	3.425

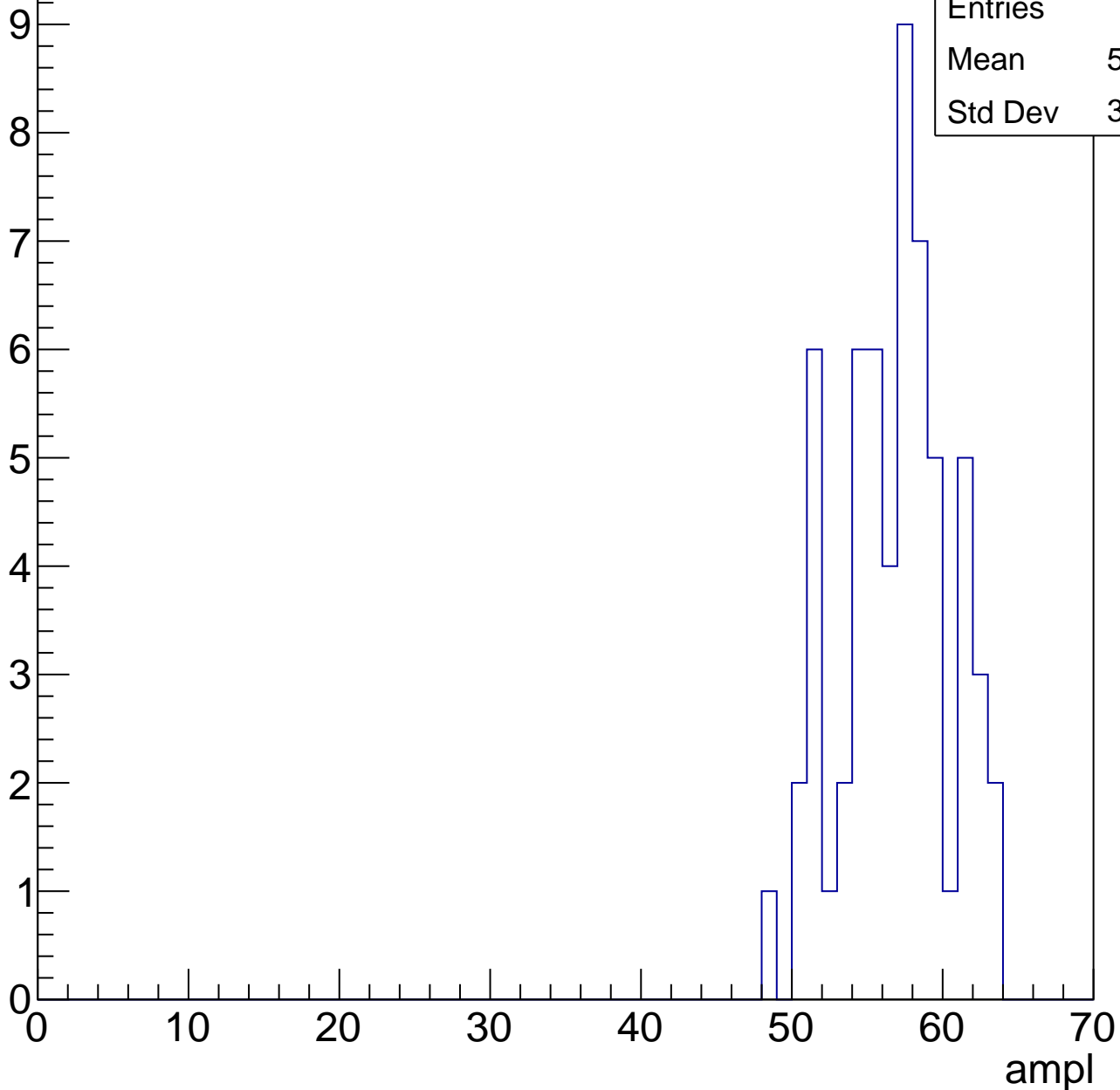


B1L103S, U20-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

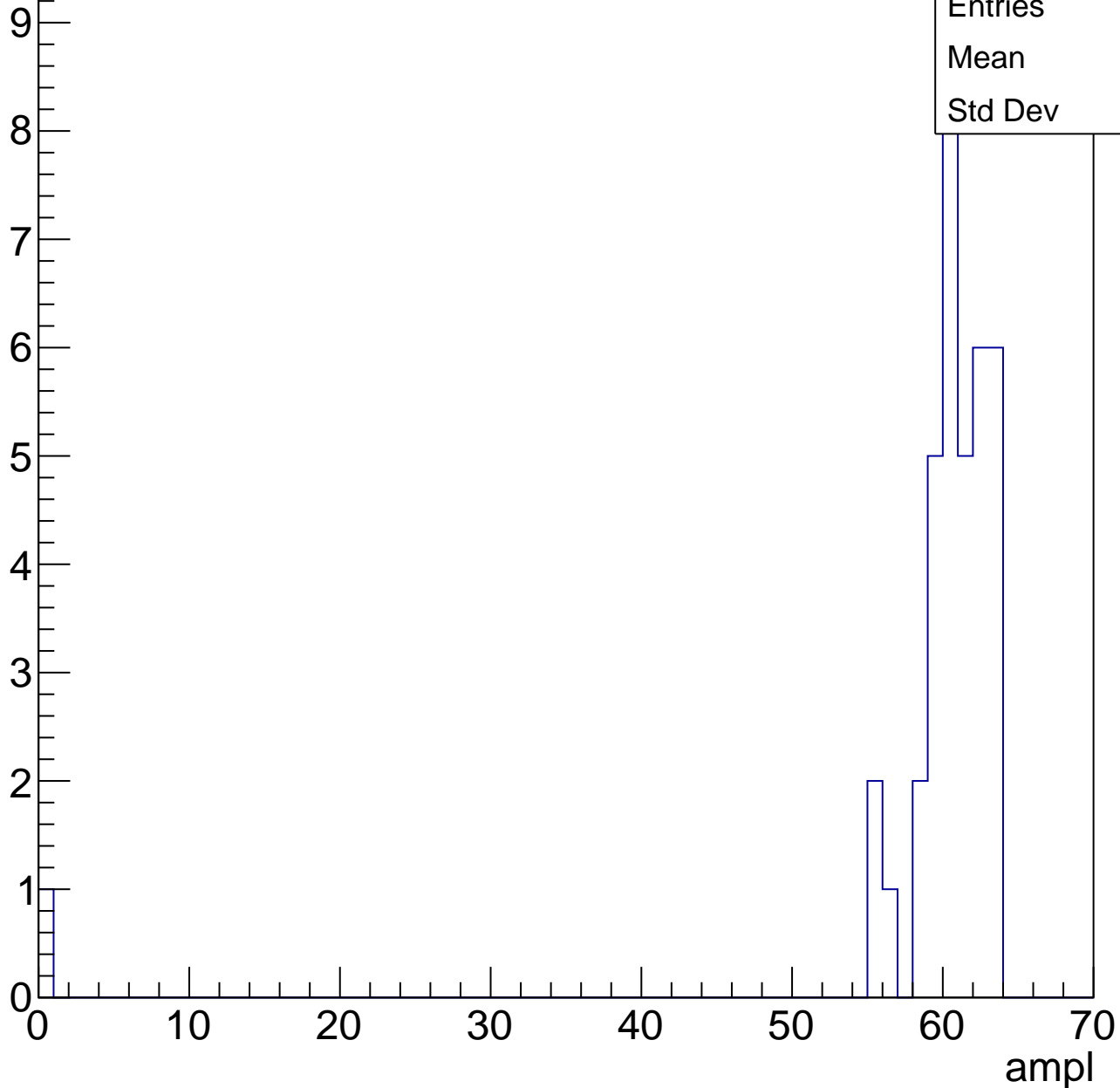
Entries	60
Mean	56.35
Std Dev	3.586



B1L103S, U20-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

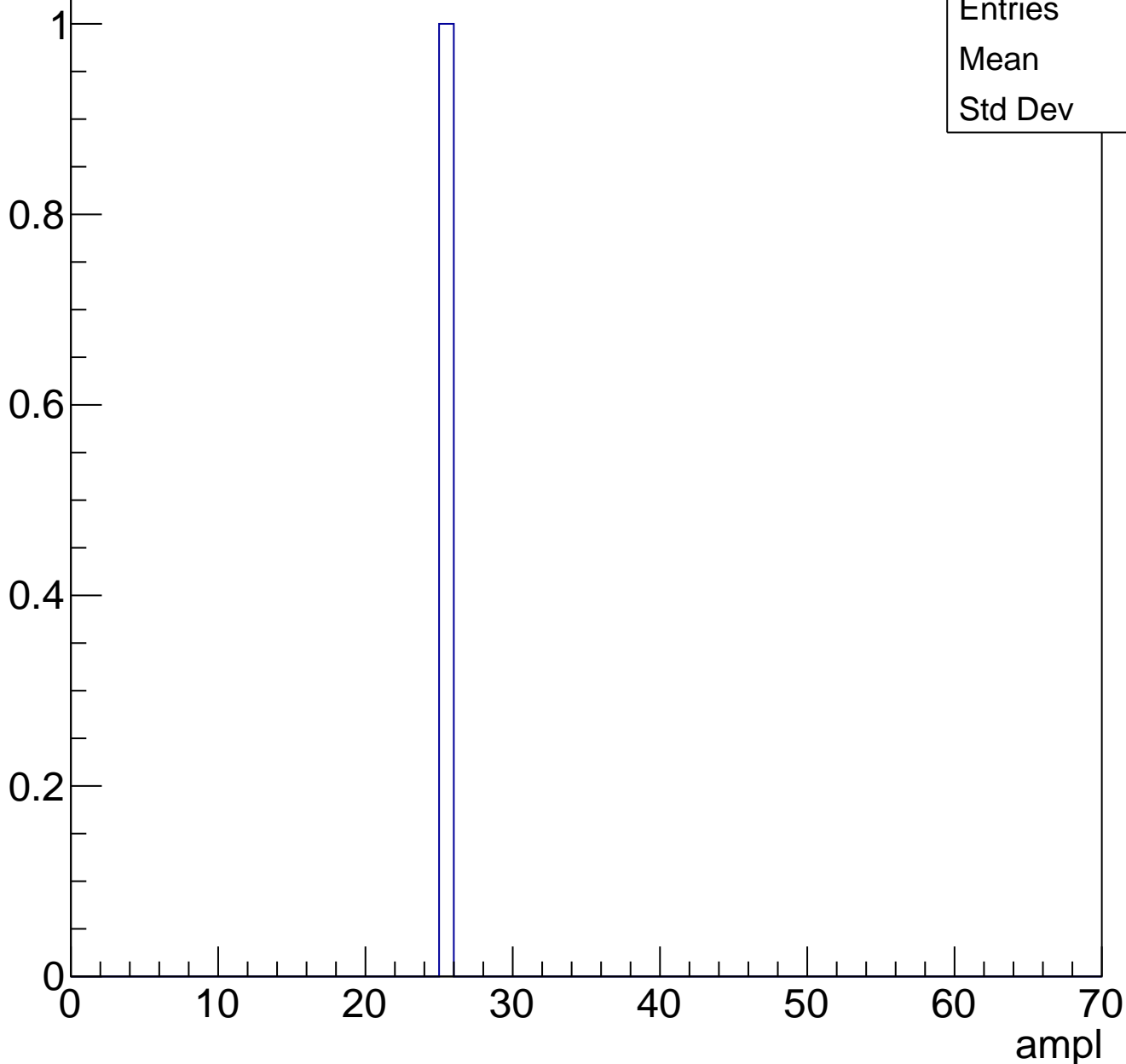
ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch90, adc0

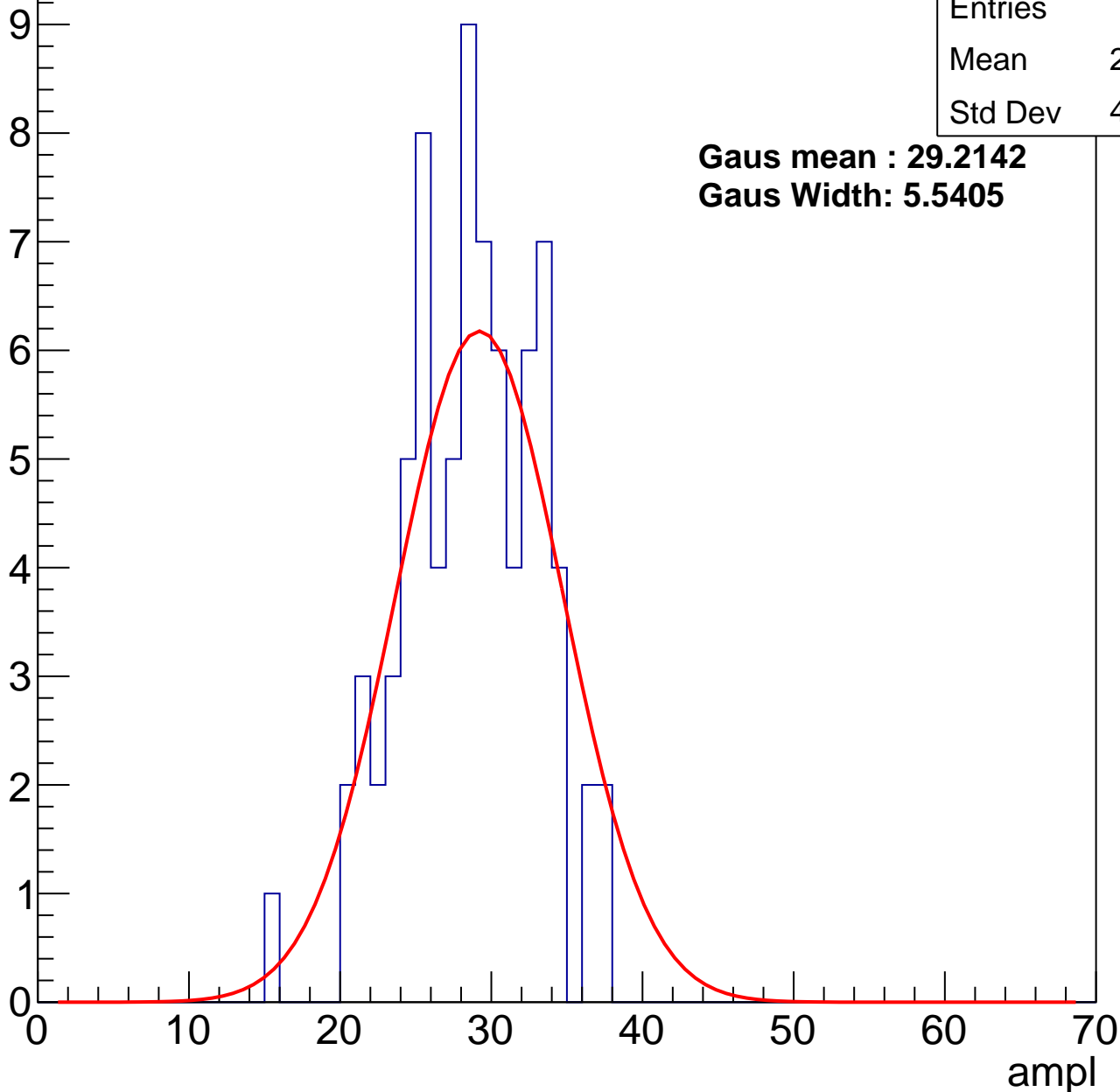
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	28.18
Std Dev	4.378

Gaus mean : 29.2142

Gaus Width: 5.5405



B1L103S, U20-ch90, adc1

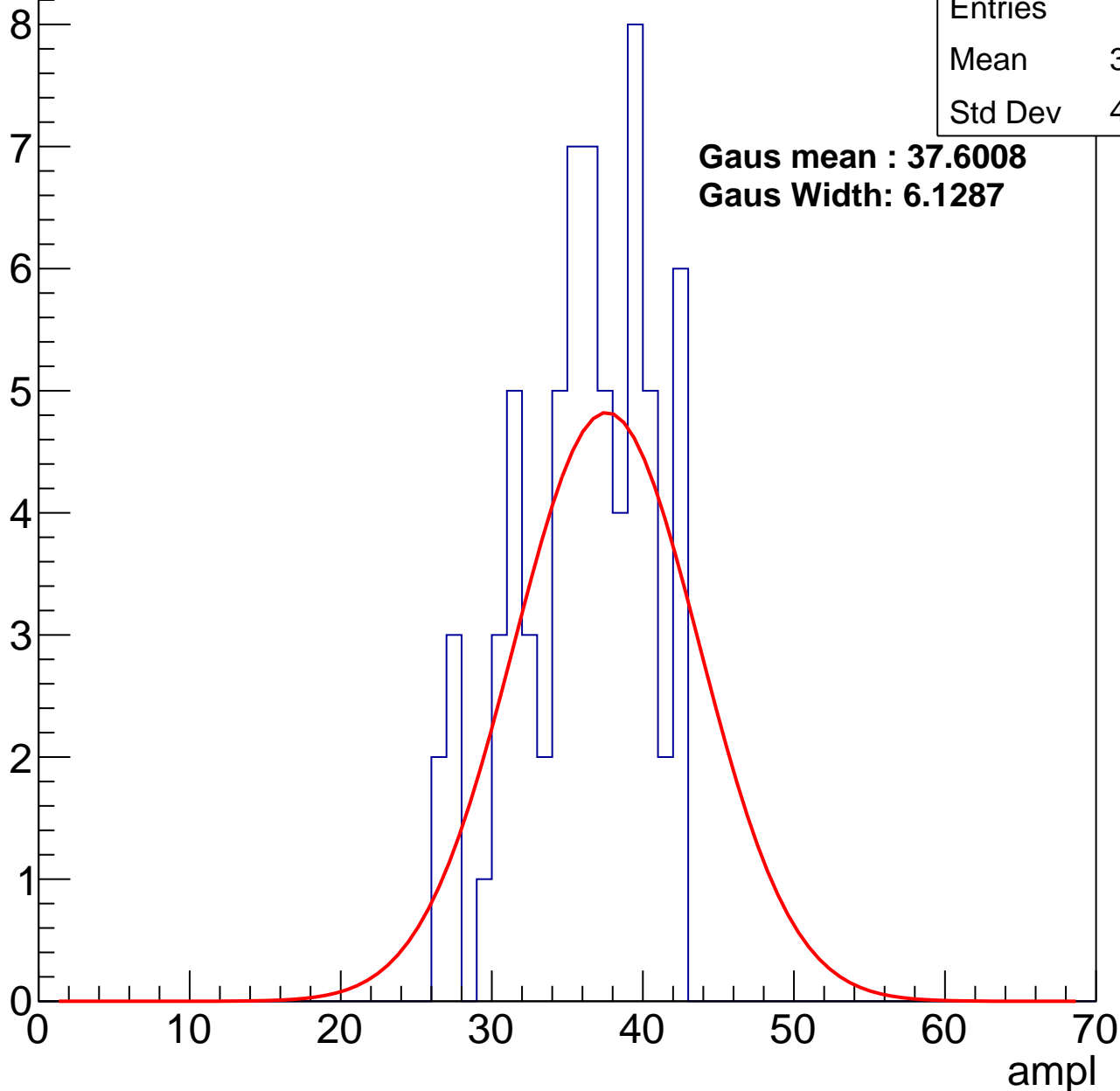
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.57
Std Dev	4.278

Gaus mean : 37.6008

Gaus Width: 6.1287



B1L103S, U20-ch90, adc2

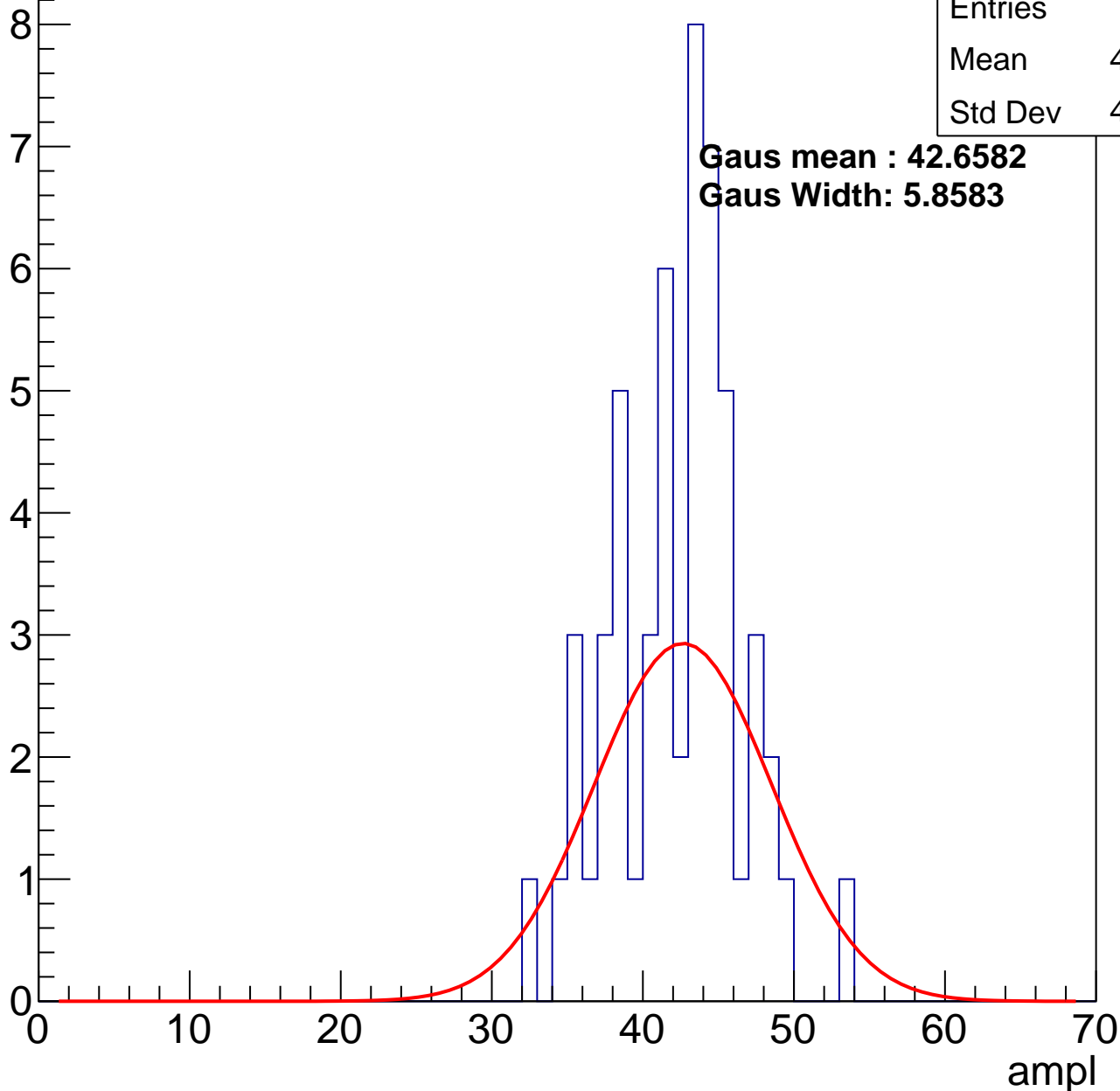
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	41.83
Std Dev	4.176

Gaus mean : 42.6582

Gaus Width: 5.8583

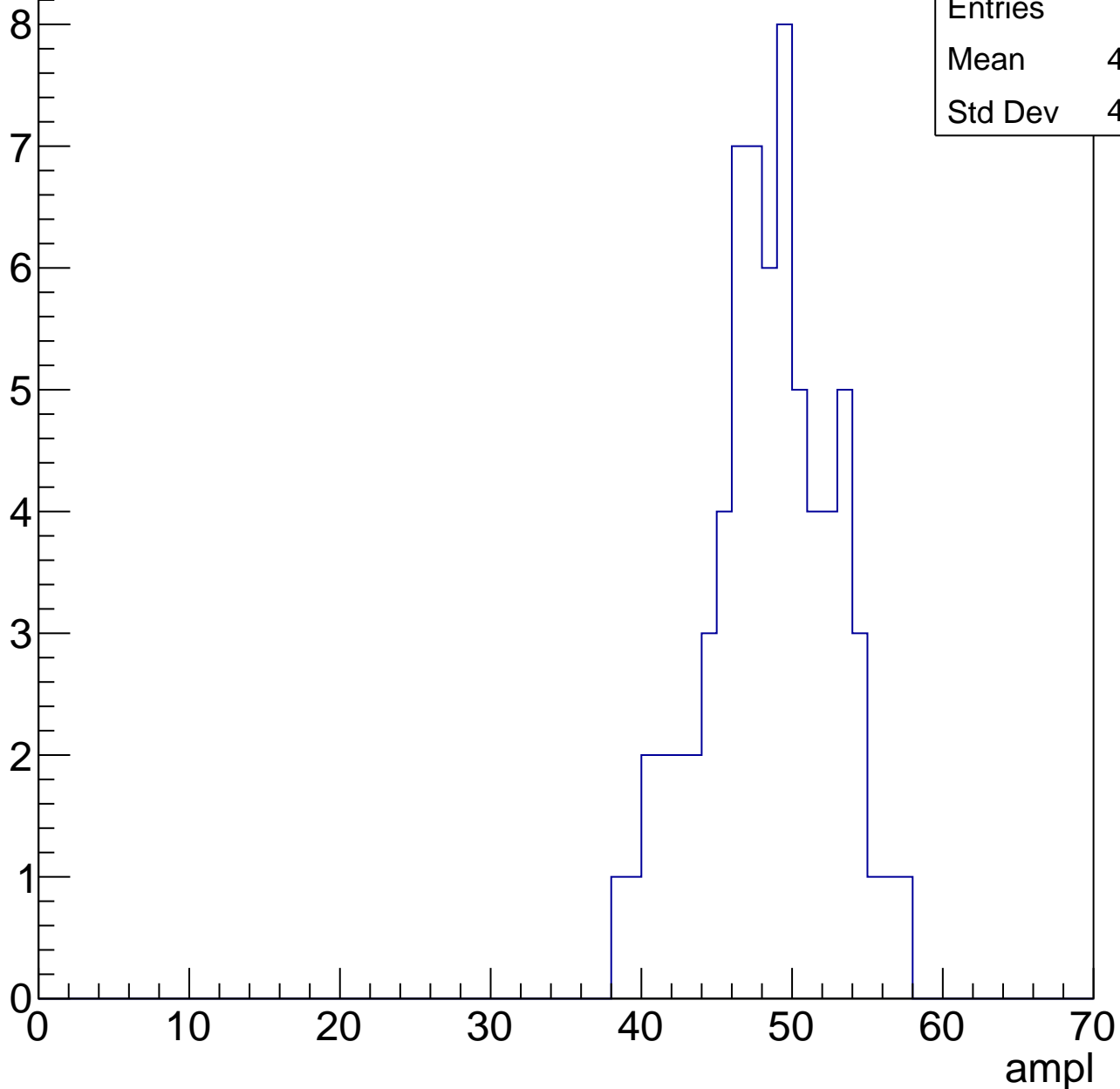


B1L103S, U20-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	47.96
Std Dev	4.175

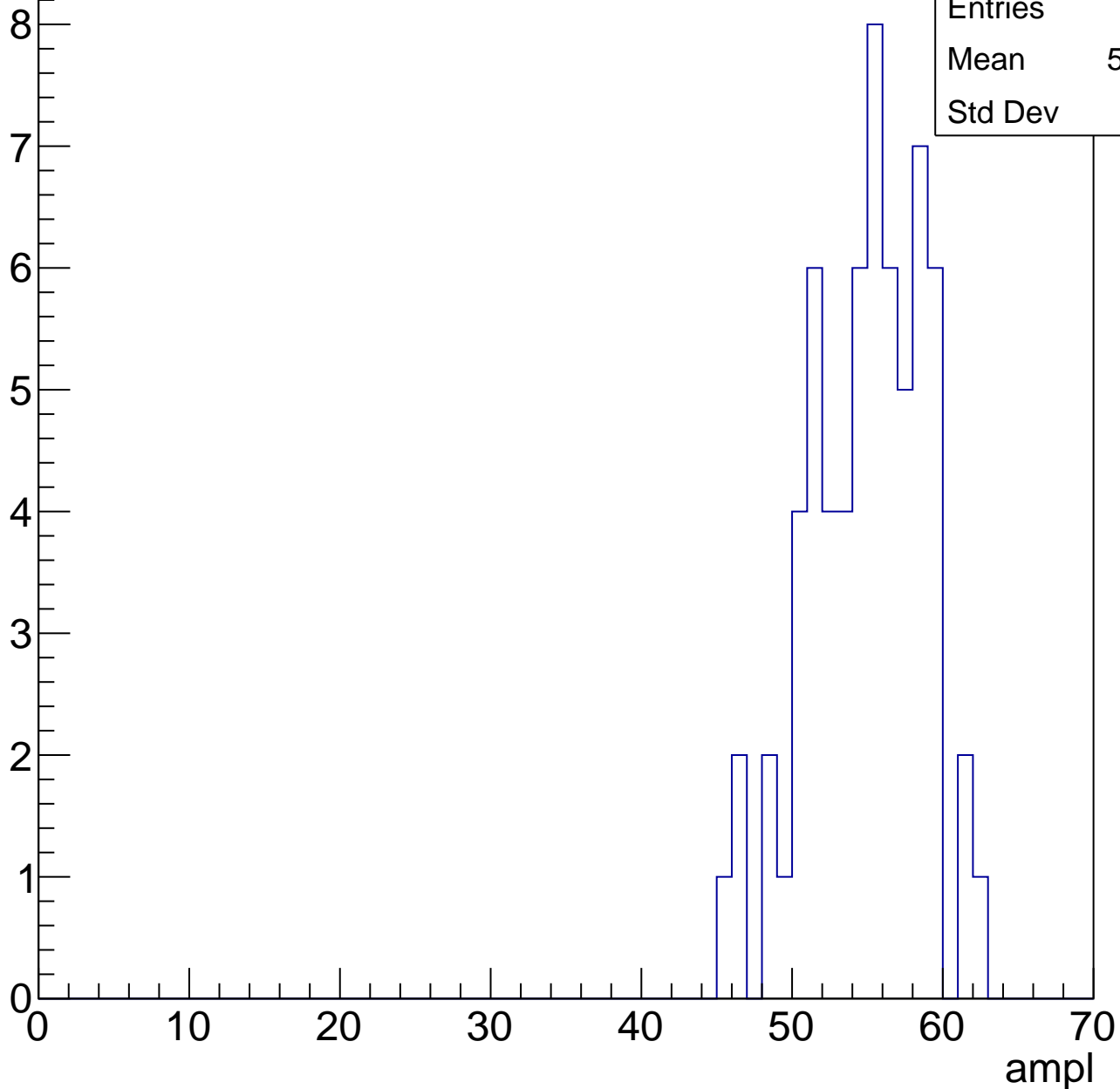


B1L103S, U20-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

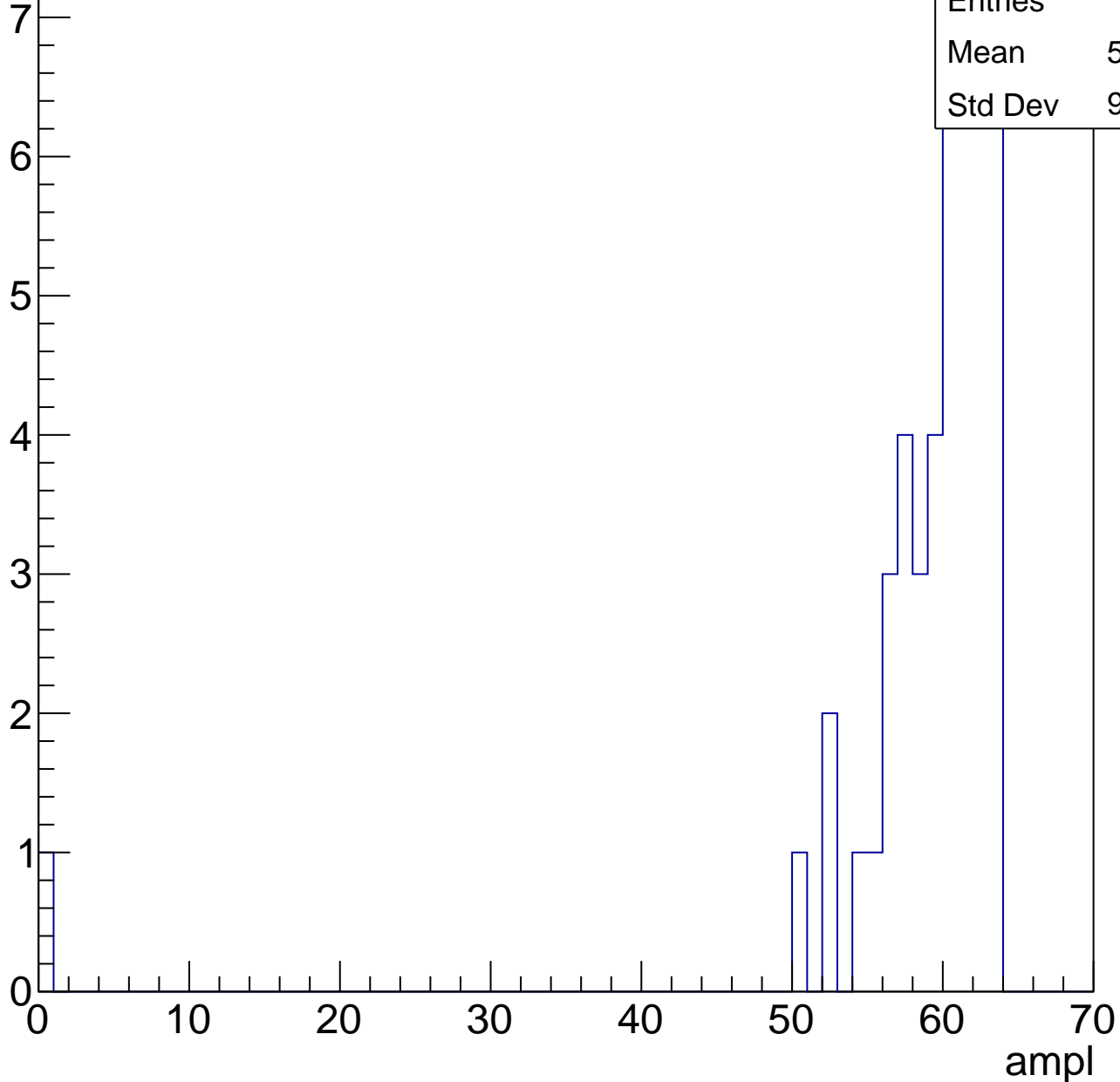
Entries	65
Mean	54.42
Std Dev	3.79



B1L103S, U20-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

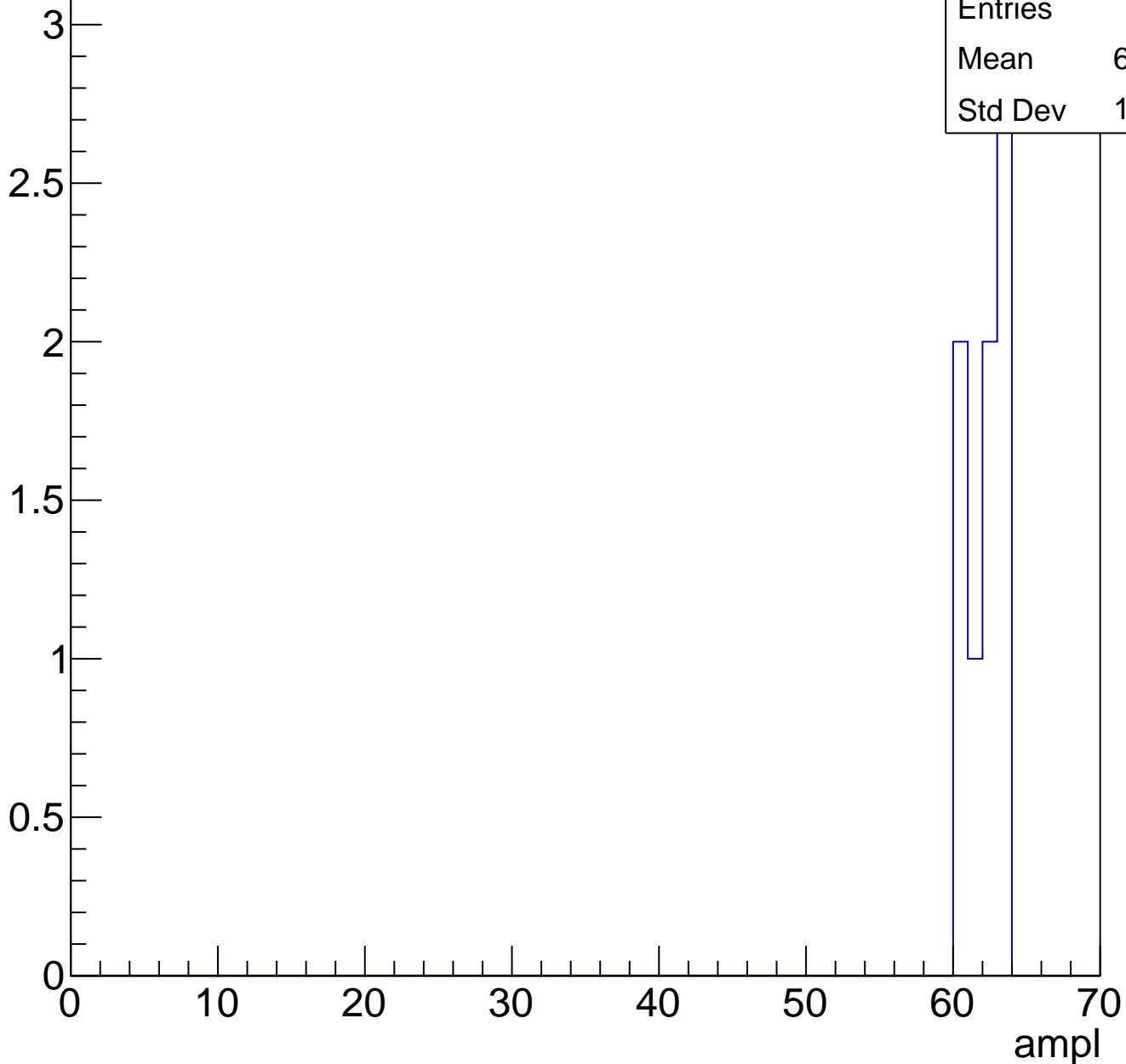
Entry



B1L103S, U20-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	60
Std Dev	0

B1L103S, U20-ch91, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	30.39
Std Dev	3.998

Gaus mean : 30.3989

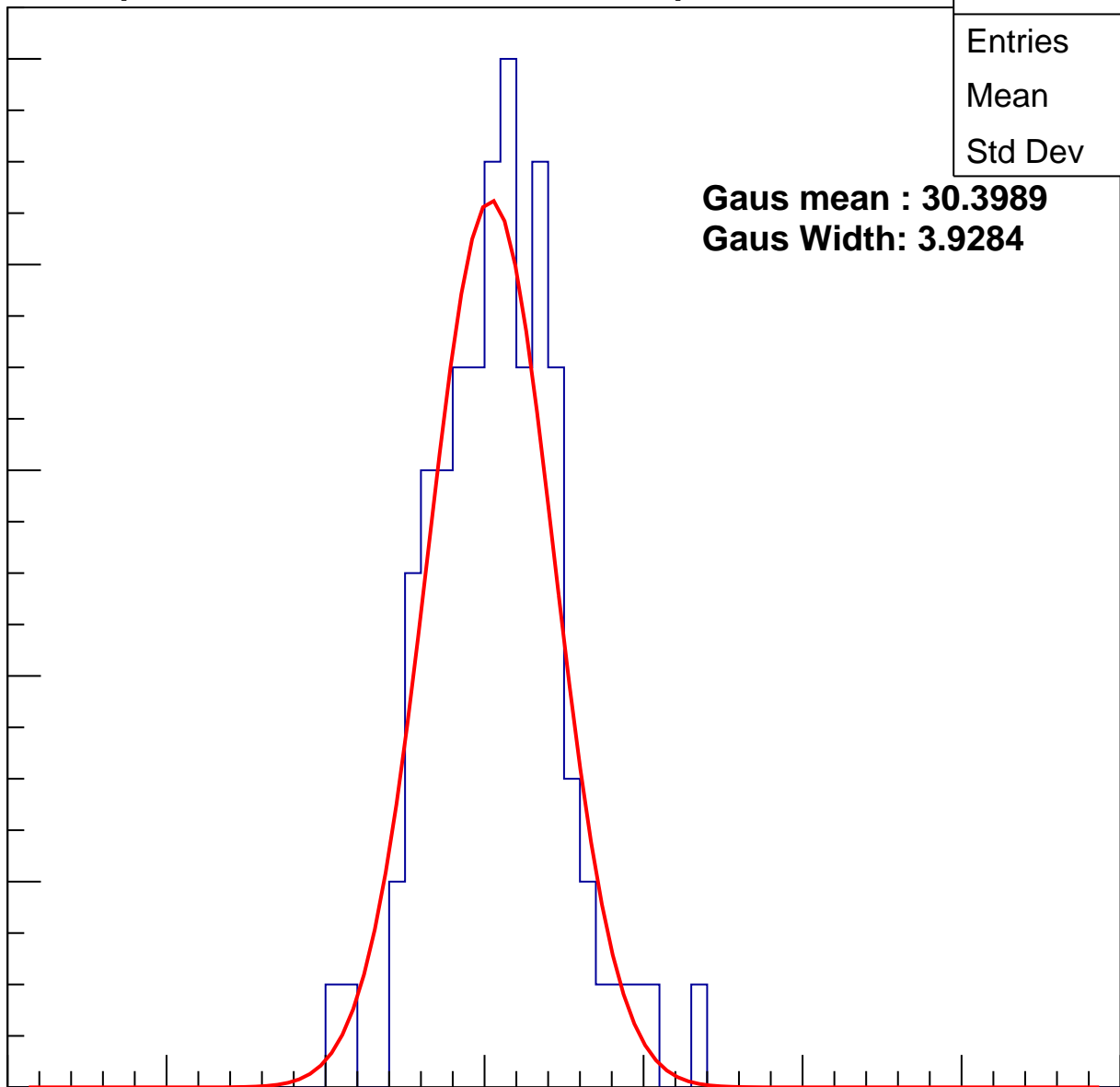
Gaus Width: 3.9284

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



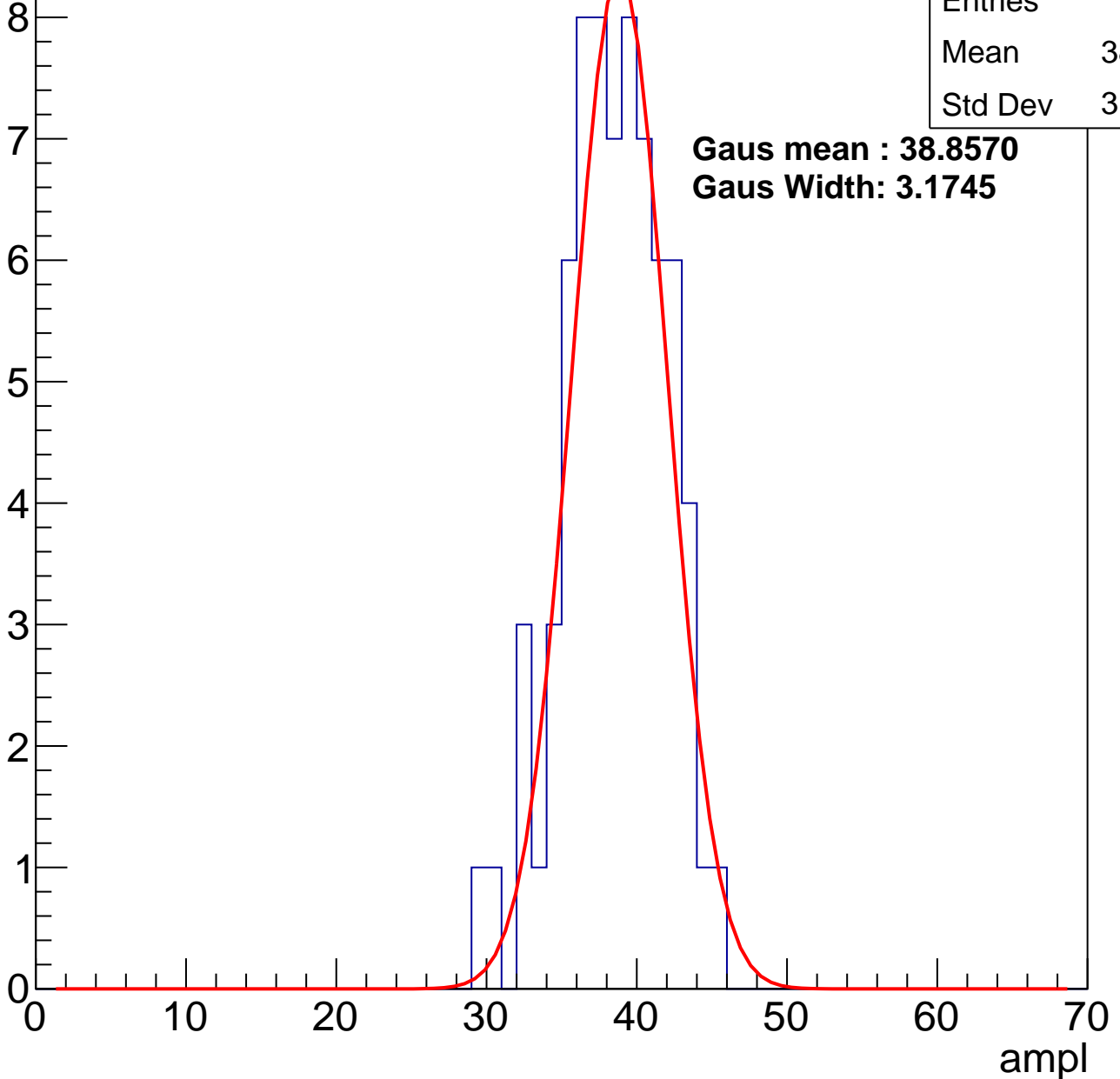
B1L103S, U20-ch91, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	38.04
Std Dev	3.338

Gaus mean : 38.8570
Gaus Width: 3.1745



B1L103S, U20-ch91, adc2

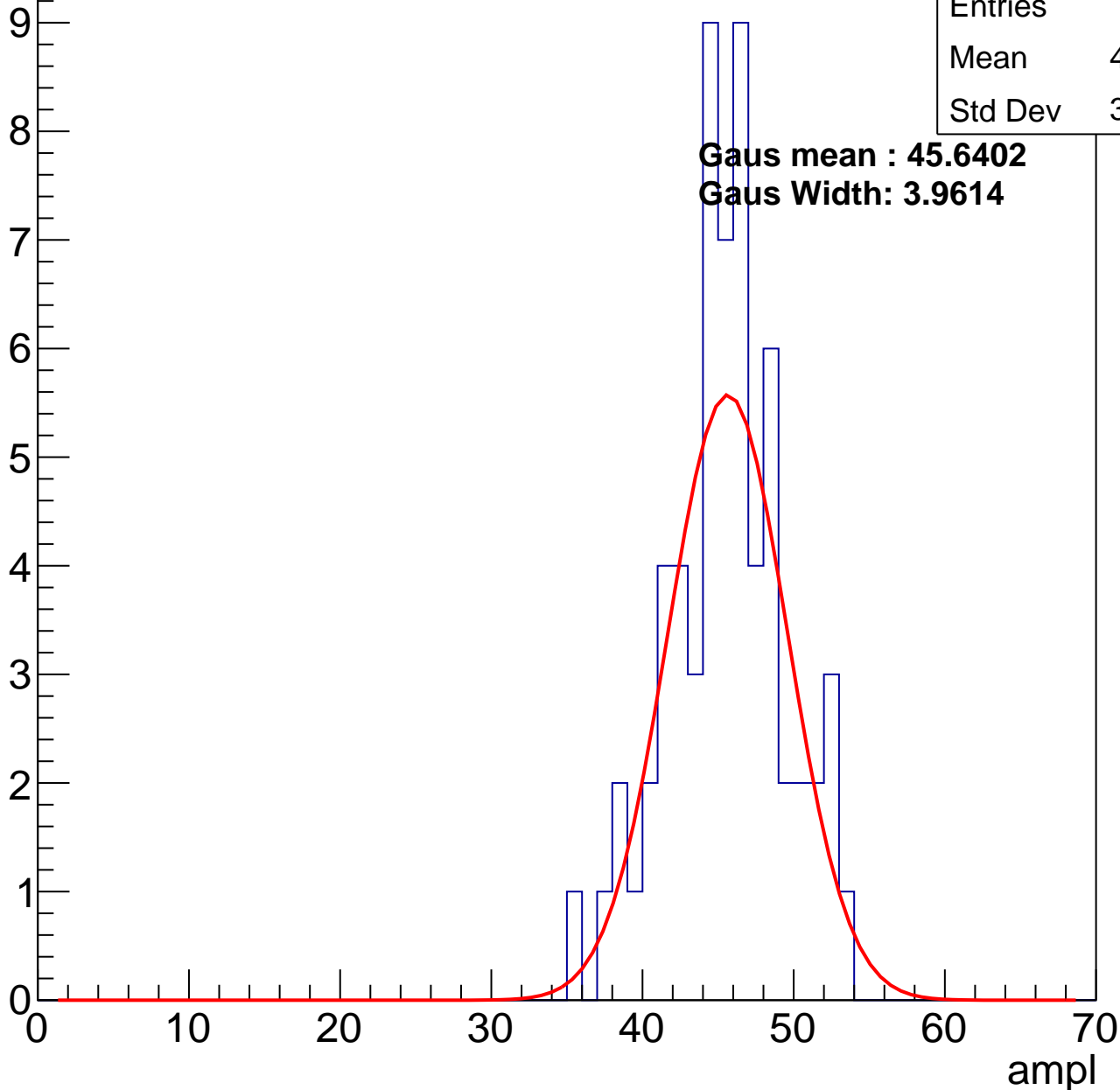
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	45.05
Std Dev	3.807

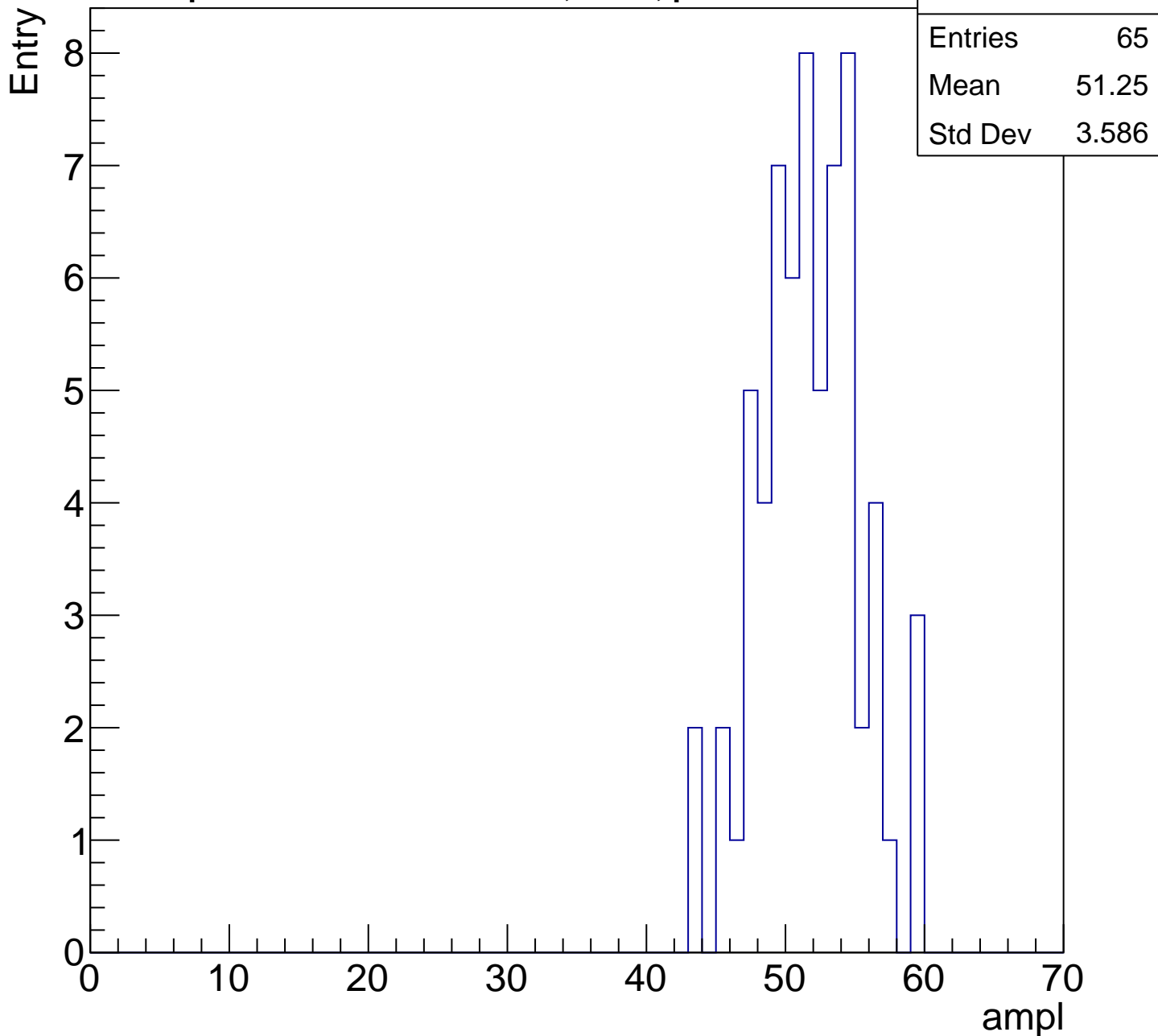
Gaus mean : 45.6402

Gaus Width: 3.9614



B1L103S, U20-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

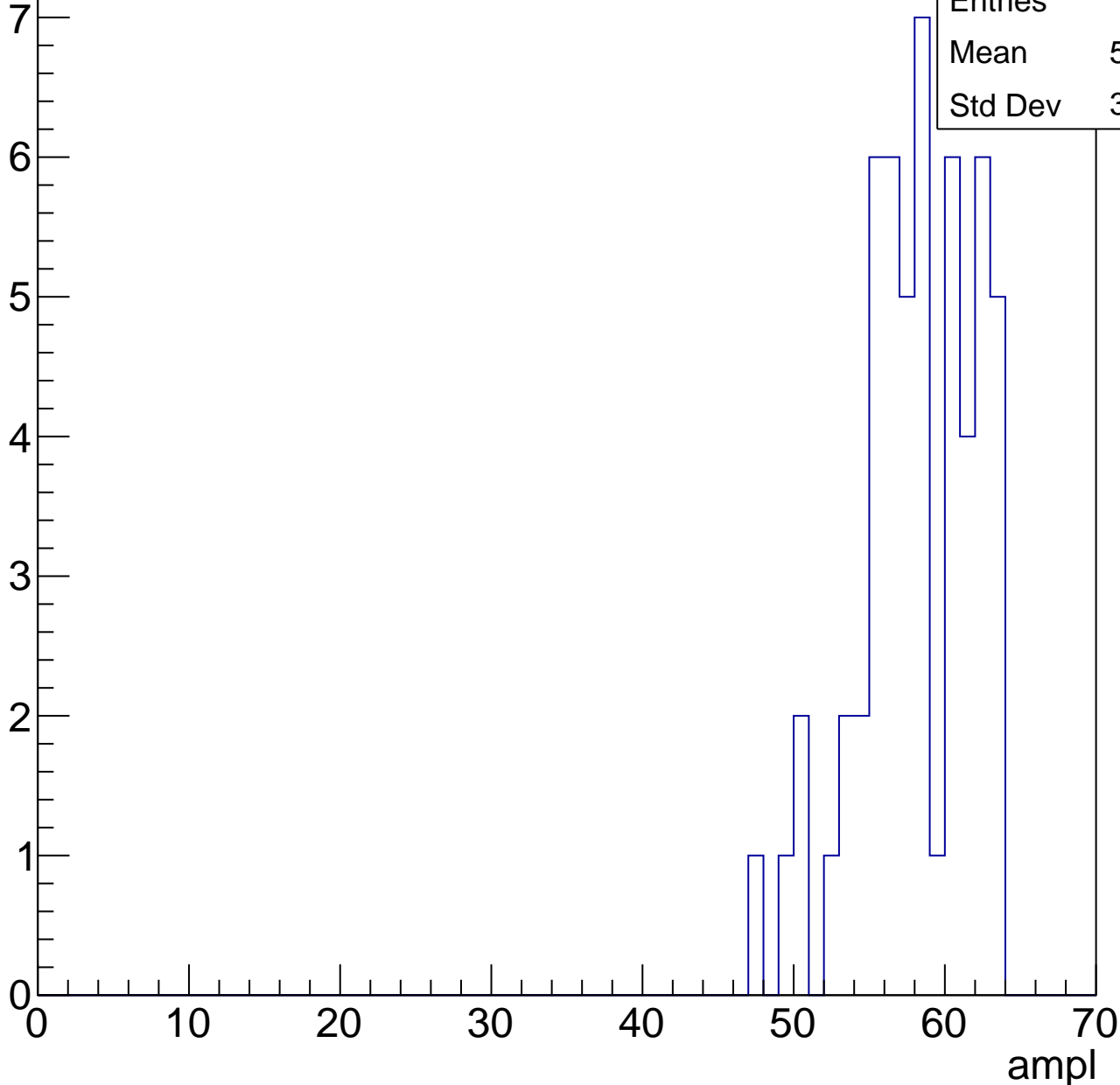


B1L103S, U20-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

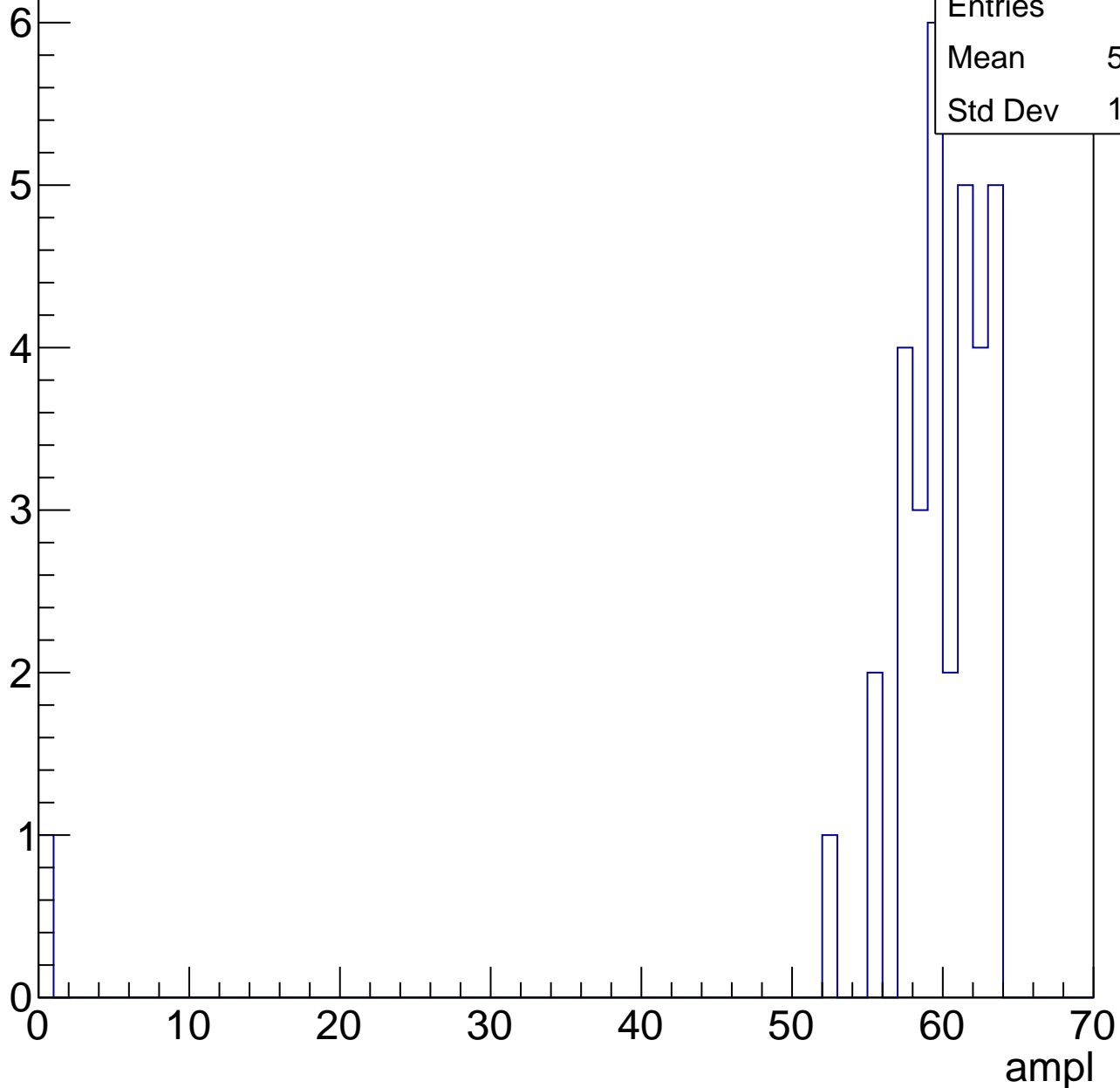
Entries	55
Mean	57.62
Std Dev	3.826



B1L103S, U20-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U20-ch92, adc0

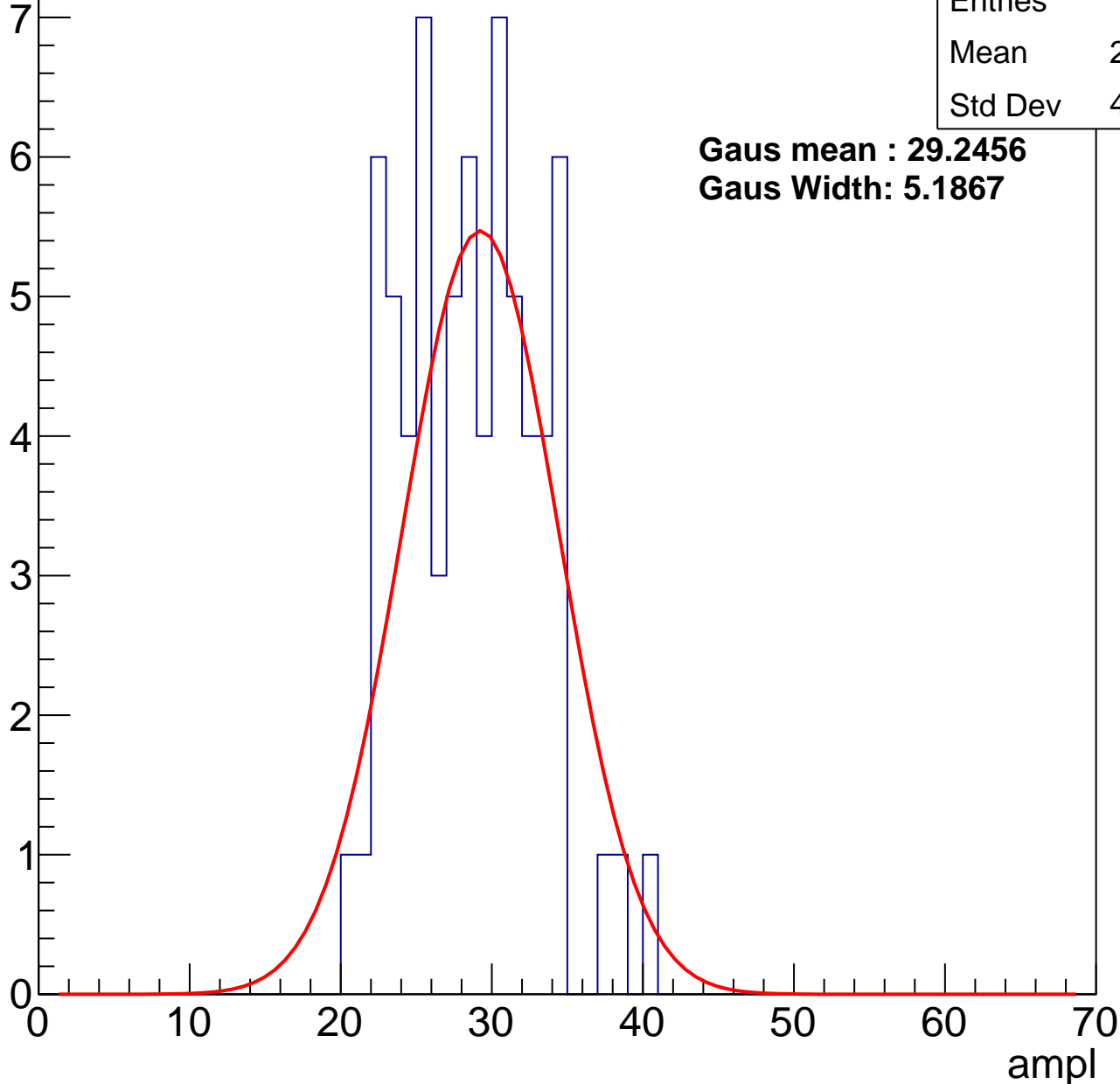
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.17
Std Dev	4.405

Gaus mean : 29.2456

Gaus Width: 5.1867



B1L103S, U20-ch92, adc1

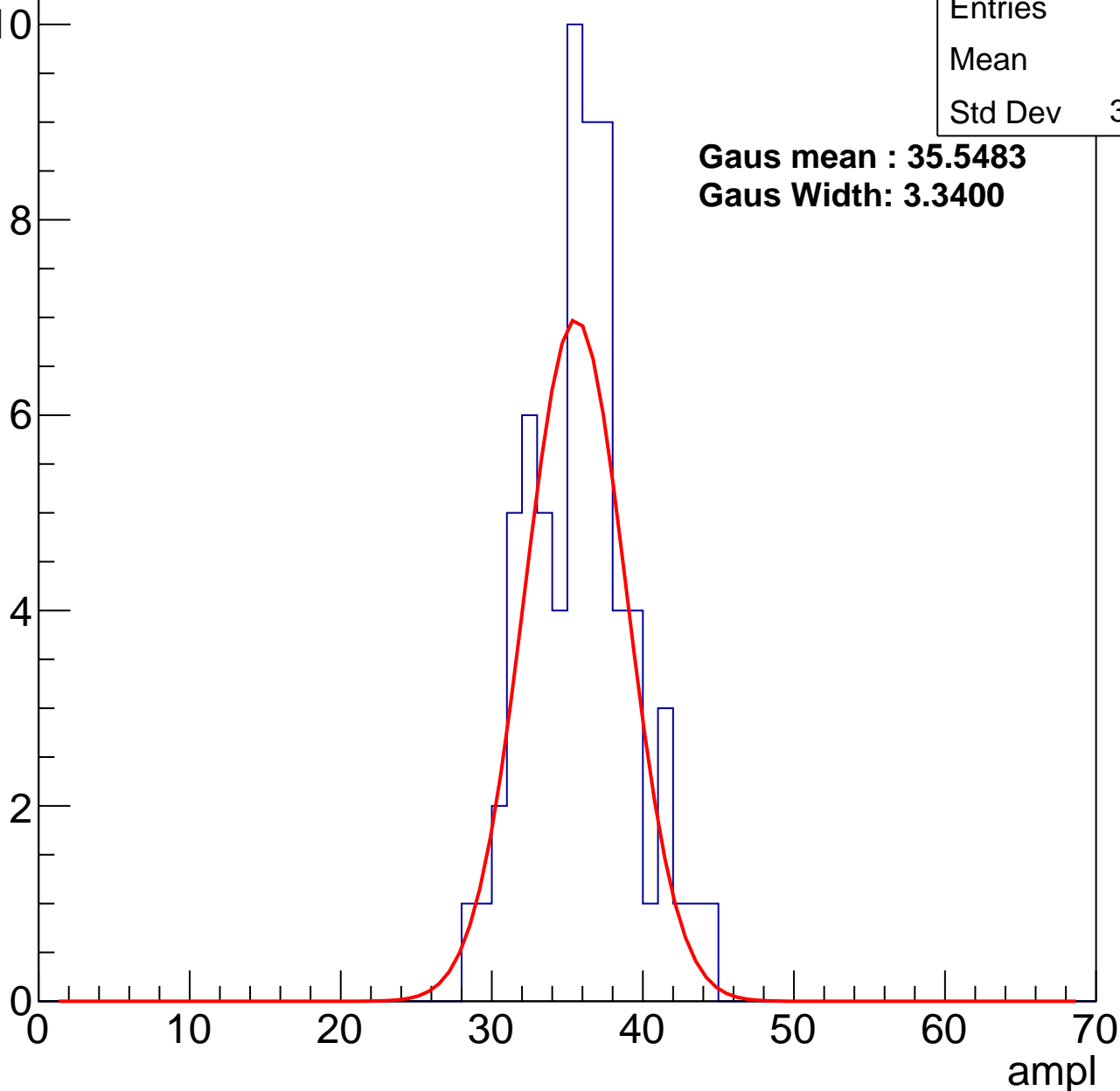
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.4
Std Dev	3.355

Gaus mean : 35.5483

Gaus Width: 3.3400



B1L103S, U20-ch92, adc2

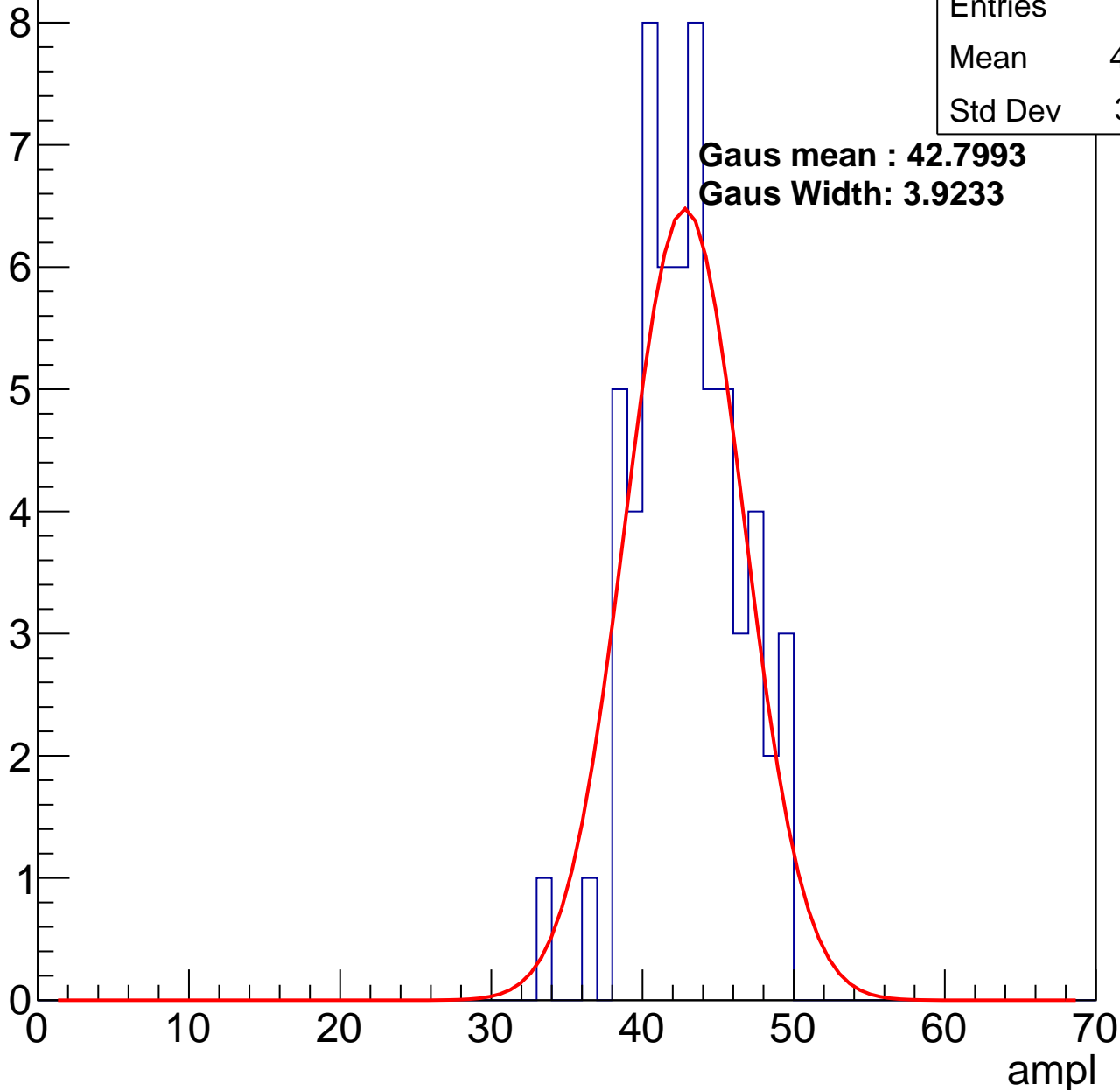
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.48
Std Dev	3.381

Gaus mean : 42.7993

Gaus Width: 3.9233

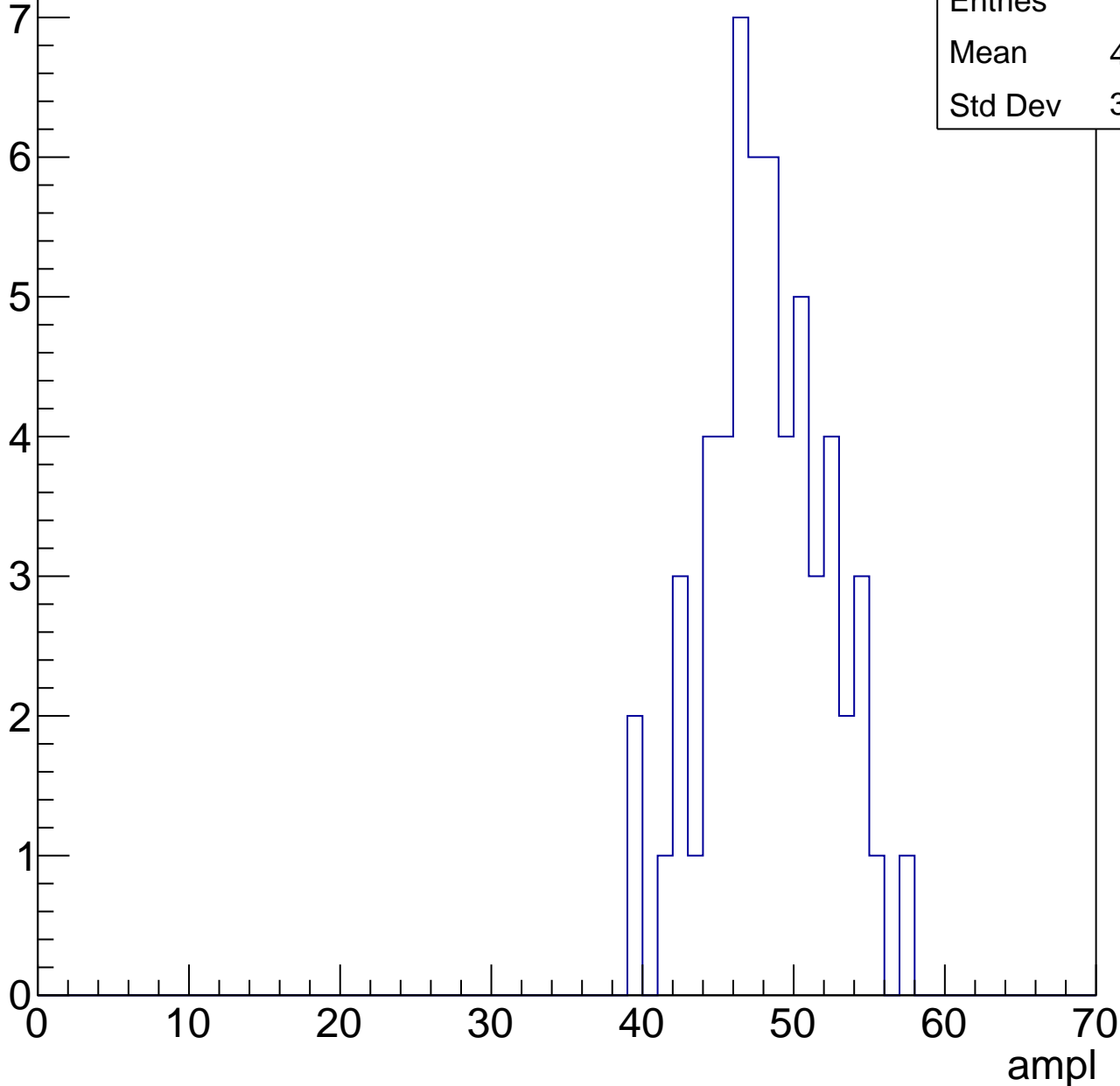


B1L103S, U20-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

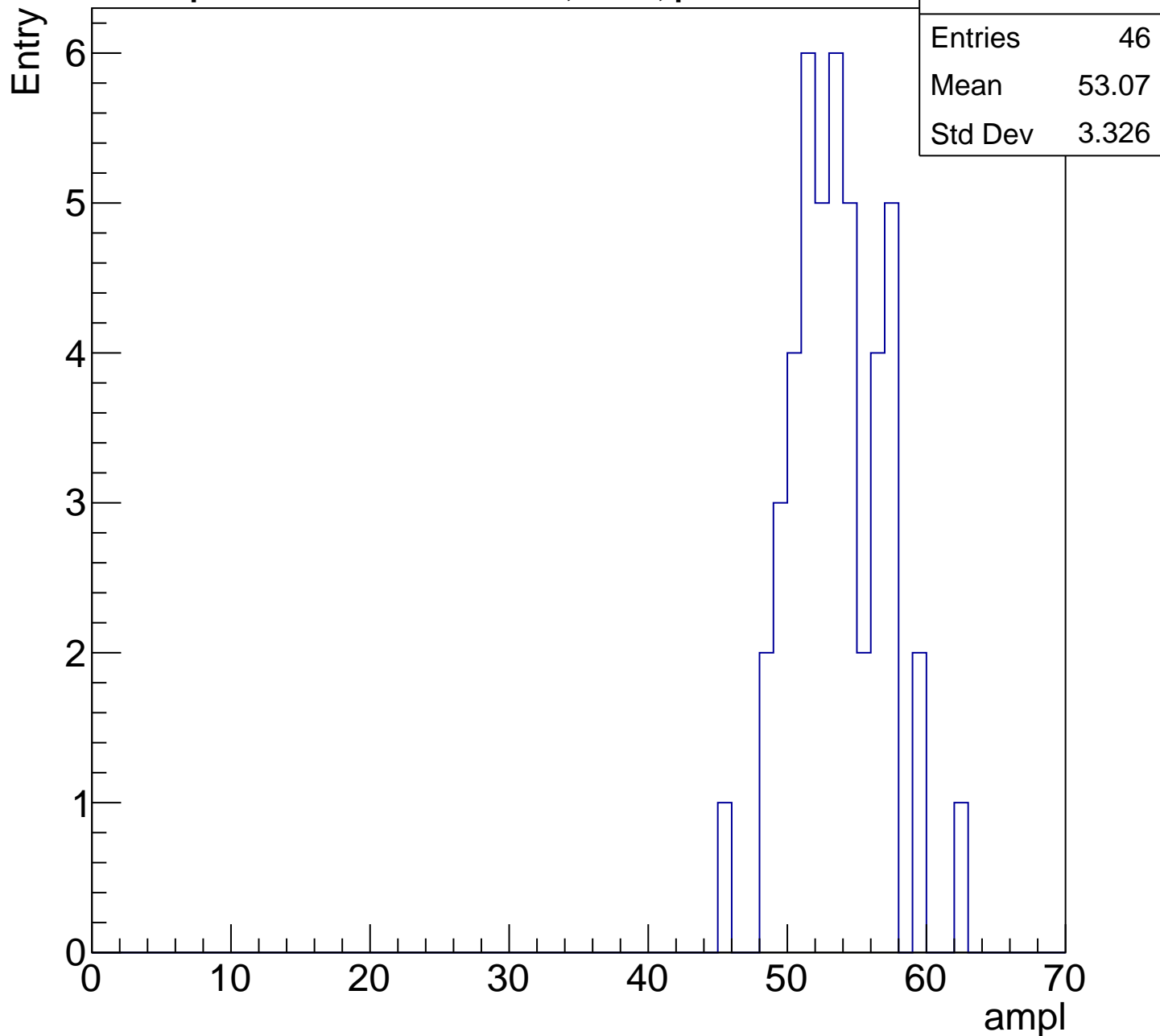
Entry

Entries	57
Mean	47.77
Std Dev	3.925



B1L103S, U20-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

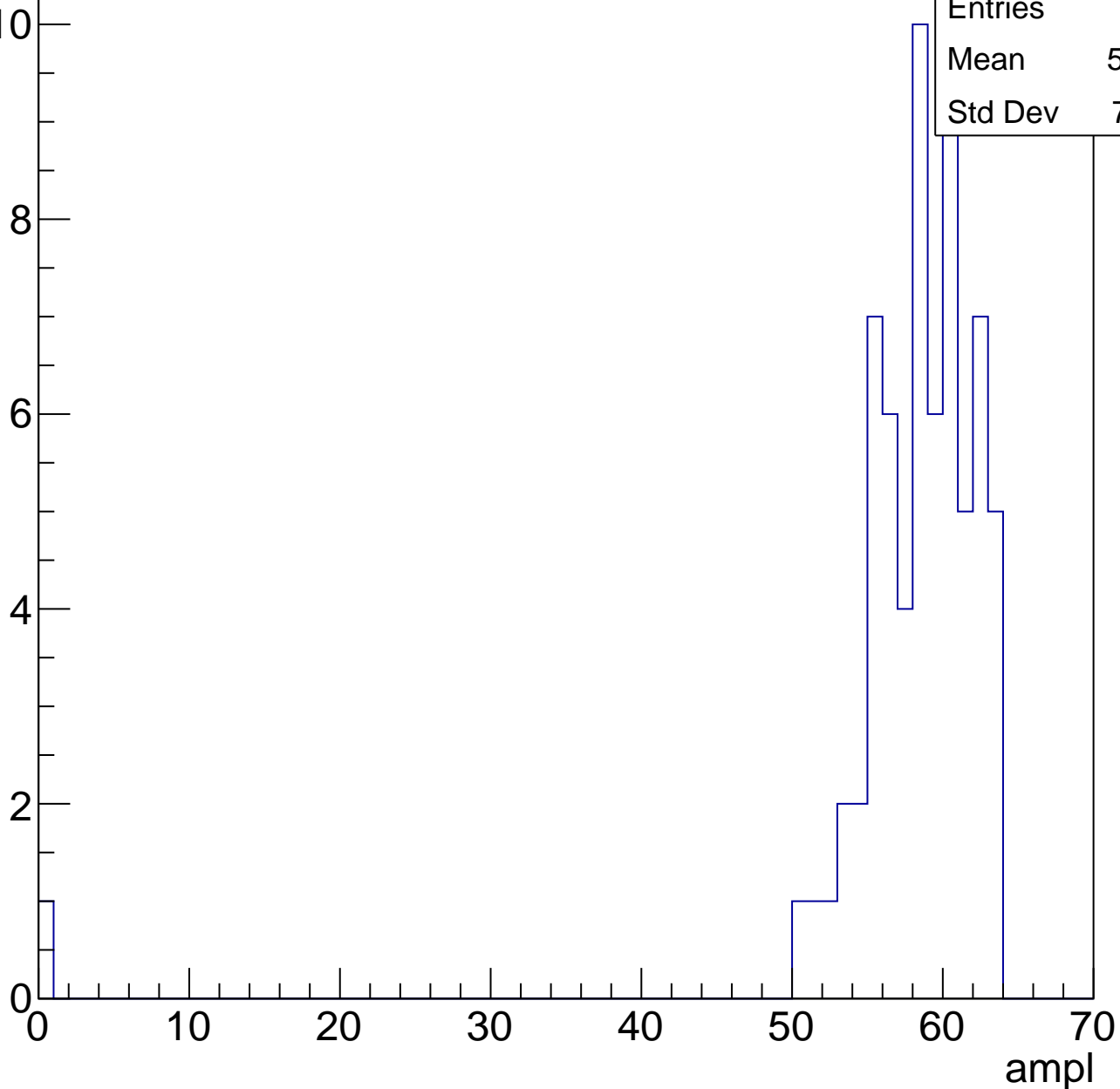


B1L103S, U20-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	57.37
Std Dev	7.711

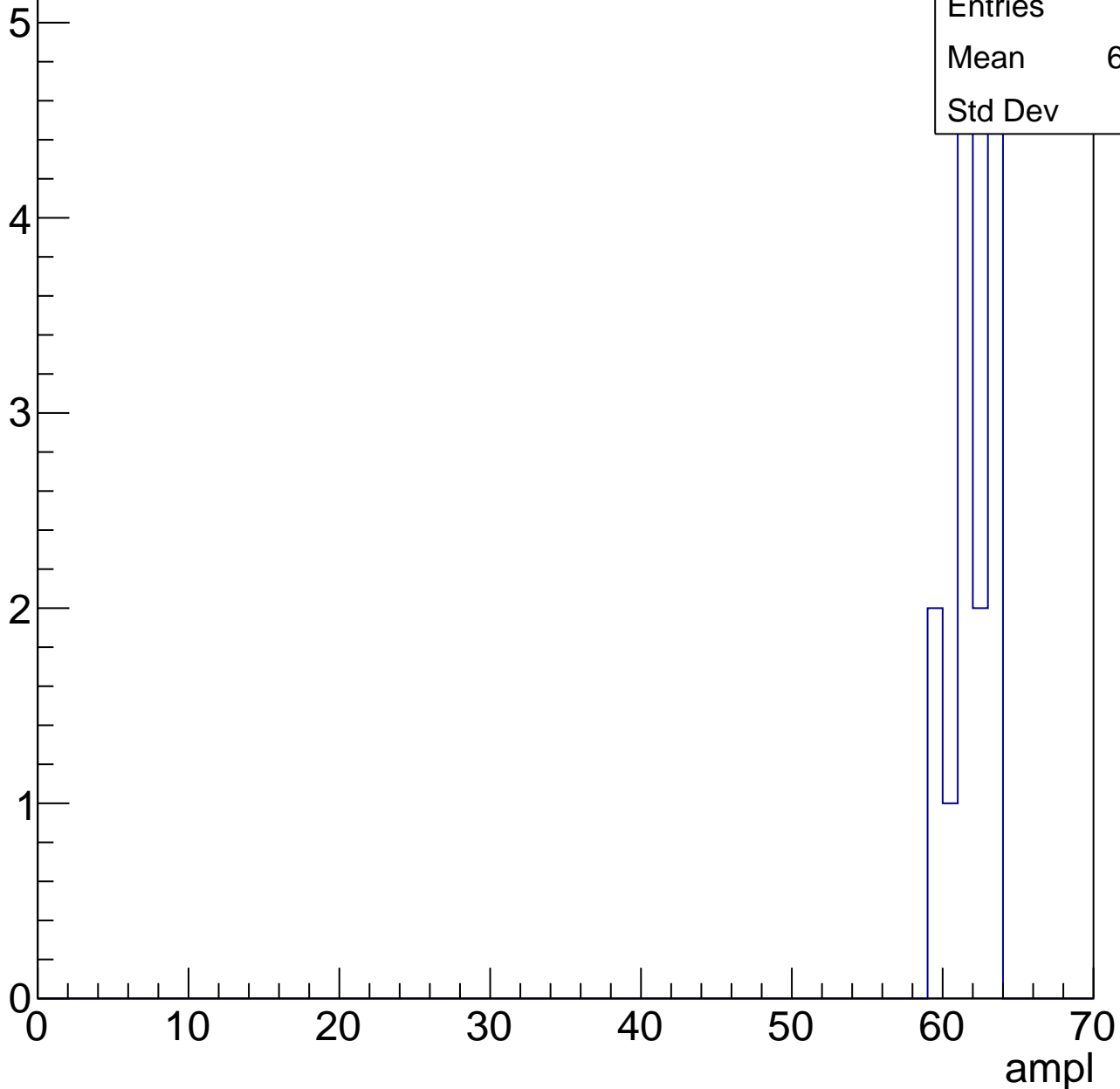


B1L103S, U20-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

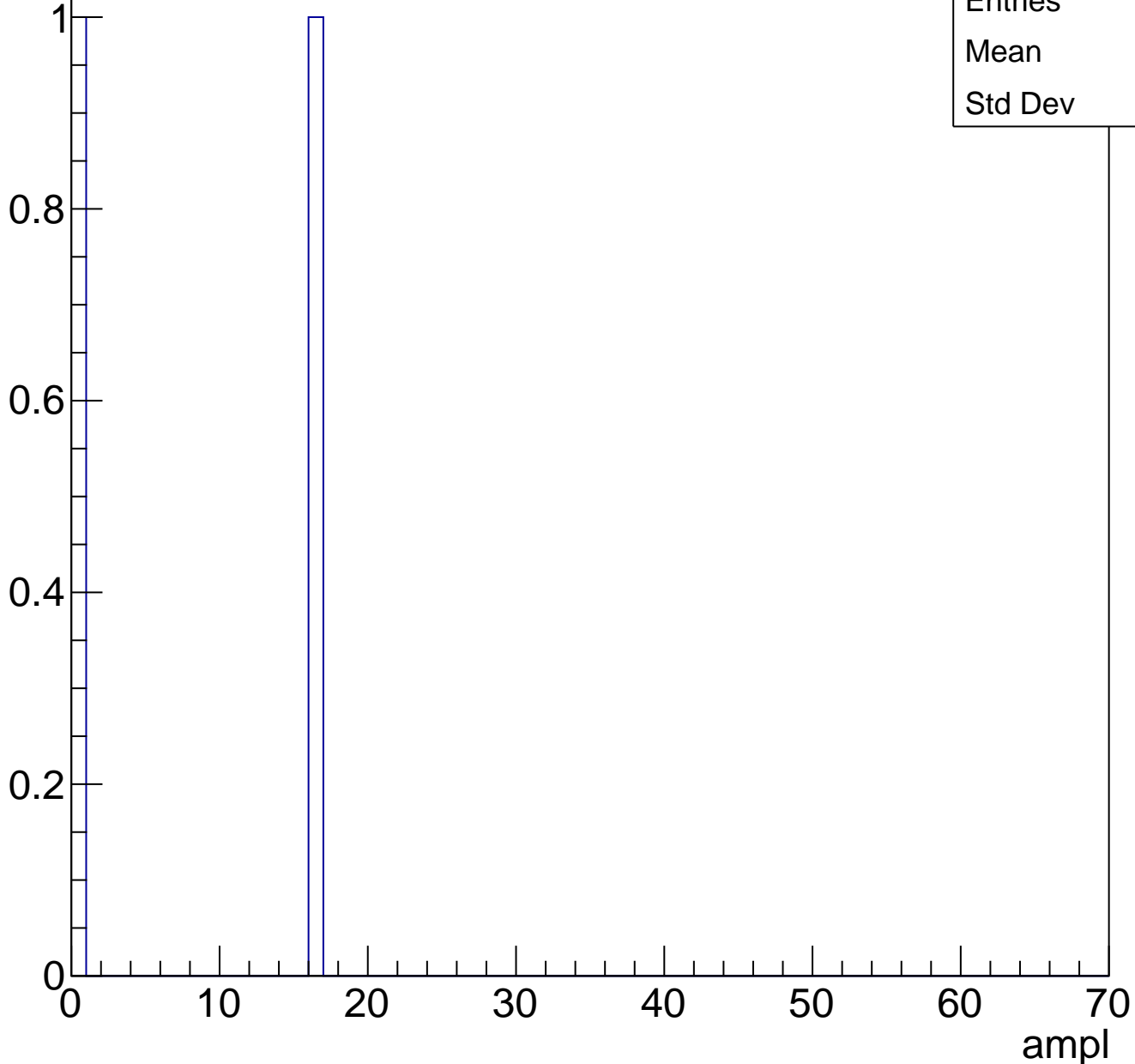
Entries	15
Mean	61.47
Std Dev	1.36



B1L103S, U20-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	8
Std Dev	8

B1L103S, U20-ch93, adc0

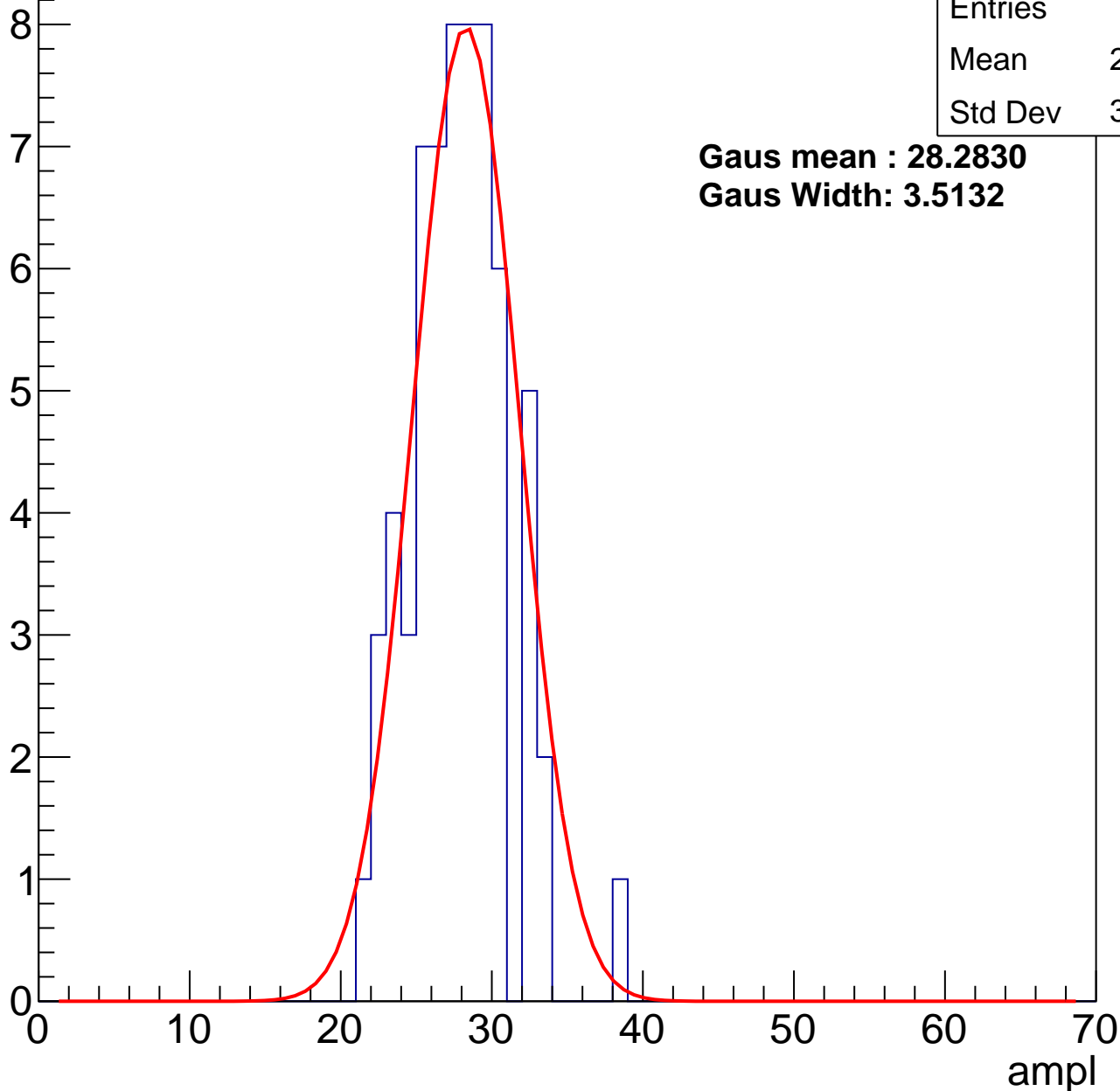
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.37
Std Dev	3.189

Gaus mean : 28.2830

Gaus Width: 3.5132



B1L103S, U20-ch93, adc1

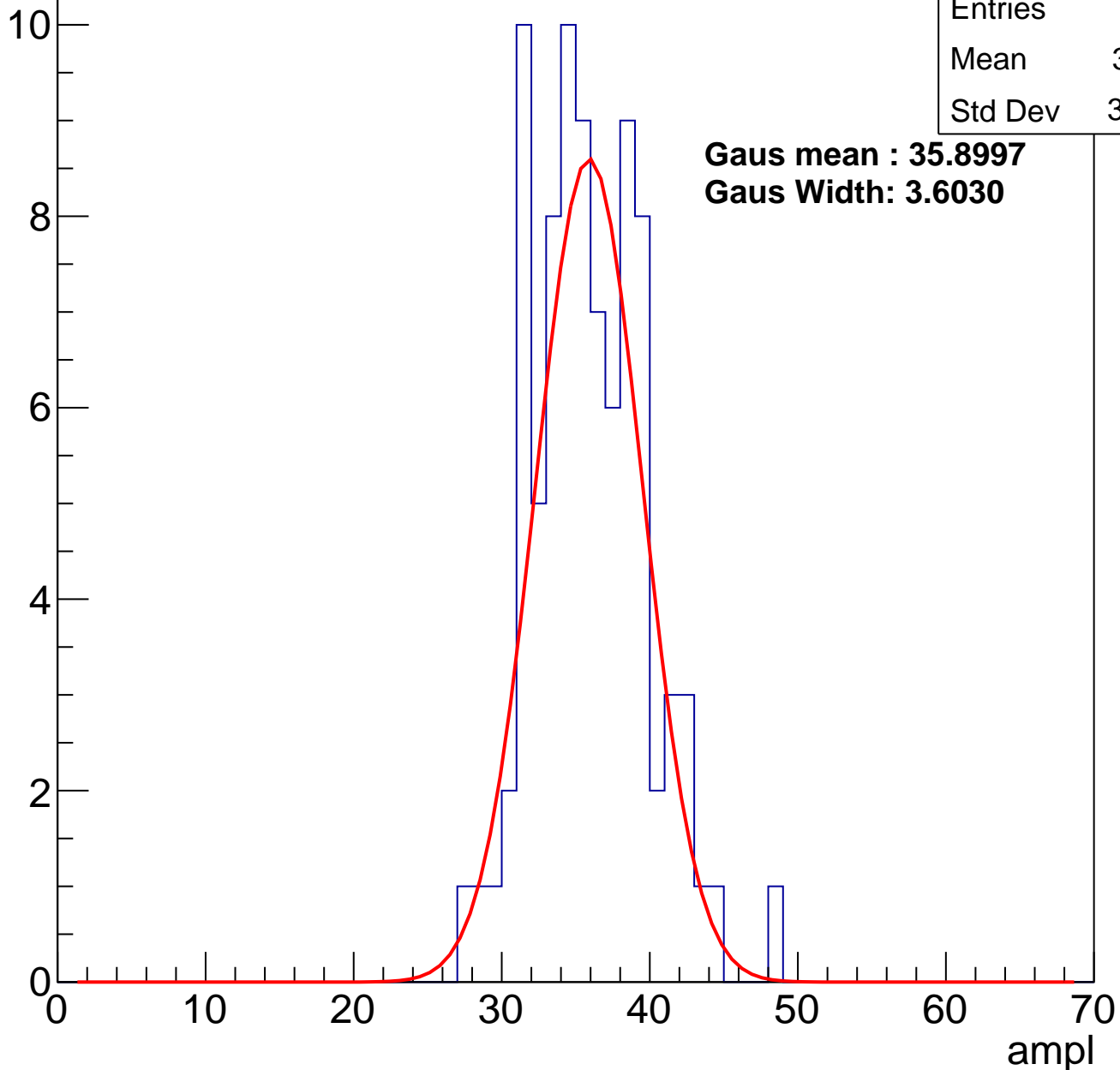
calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	35.51
Std Dev	3.817

Gaus mean : 35.8997

Gaus Width: 3.6030

Entry



B1L103S, U20-ch93, adc2

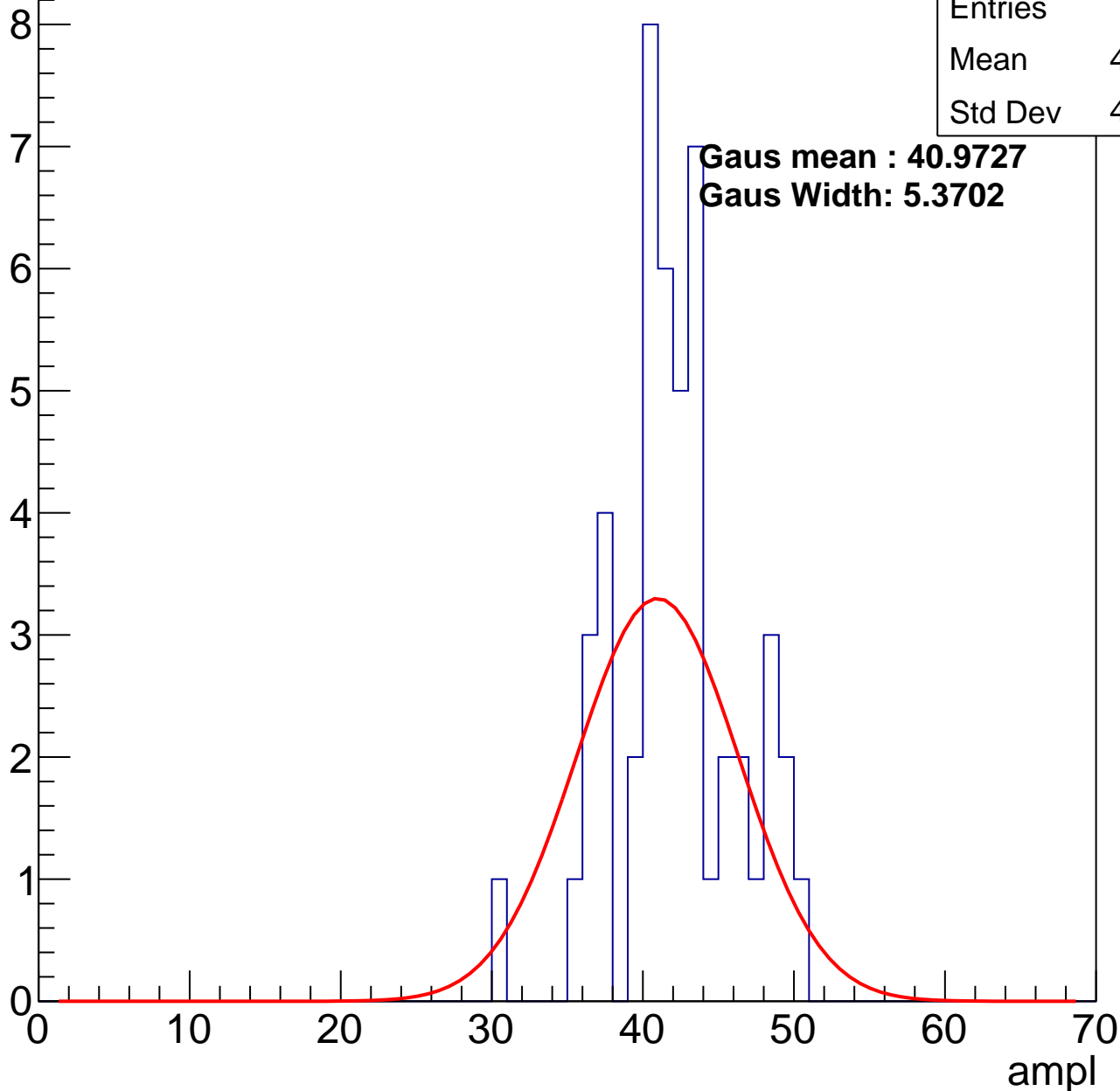
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	41.65
Std Dev	4.059

Gaus mean : 40.9727

Gaus Width: 5.3702

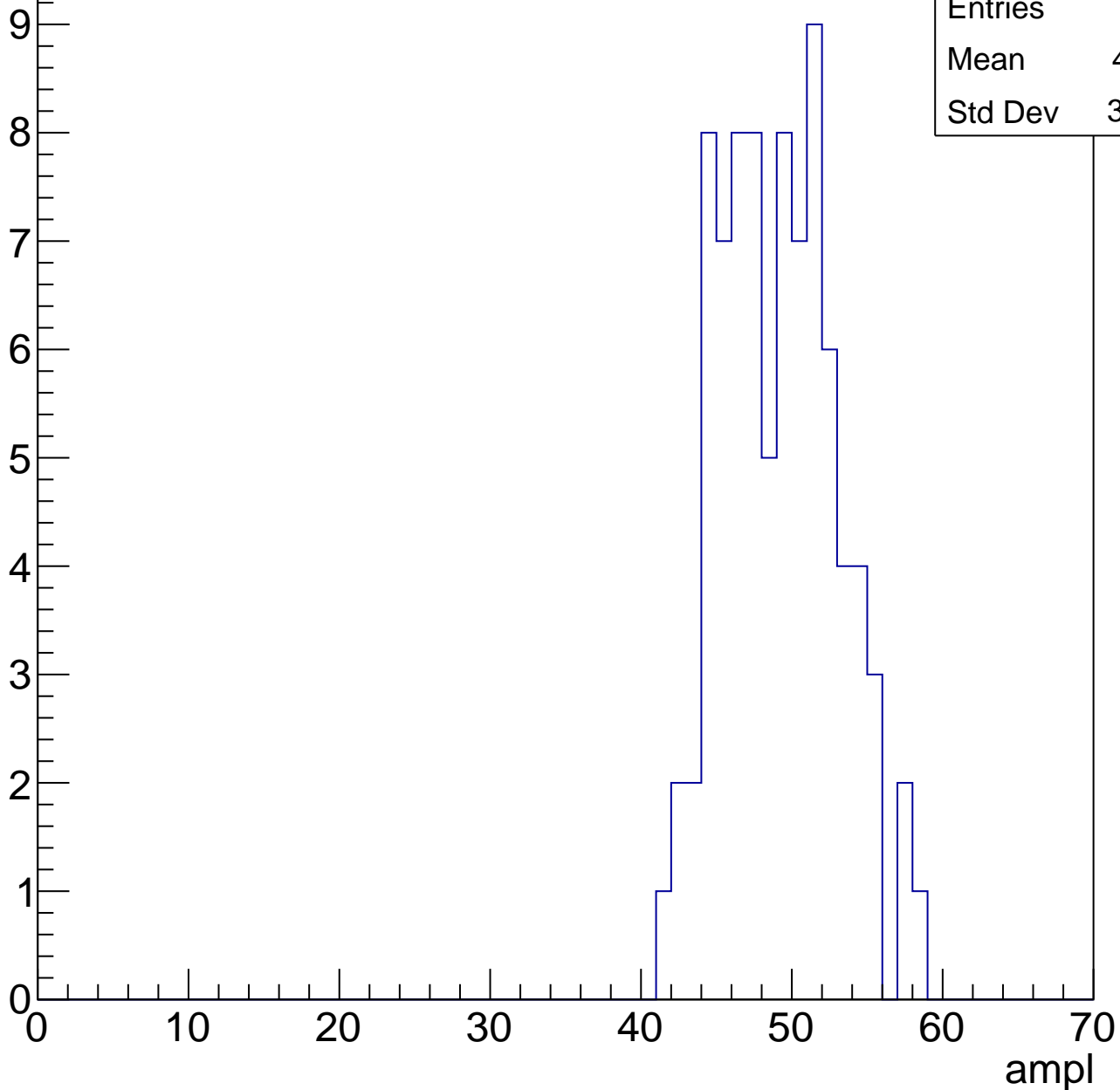


B1L103S, U20-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	48.71
Std Dev	3.797

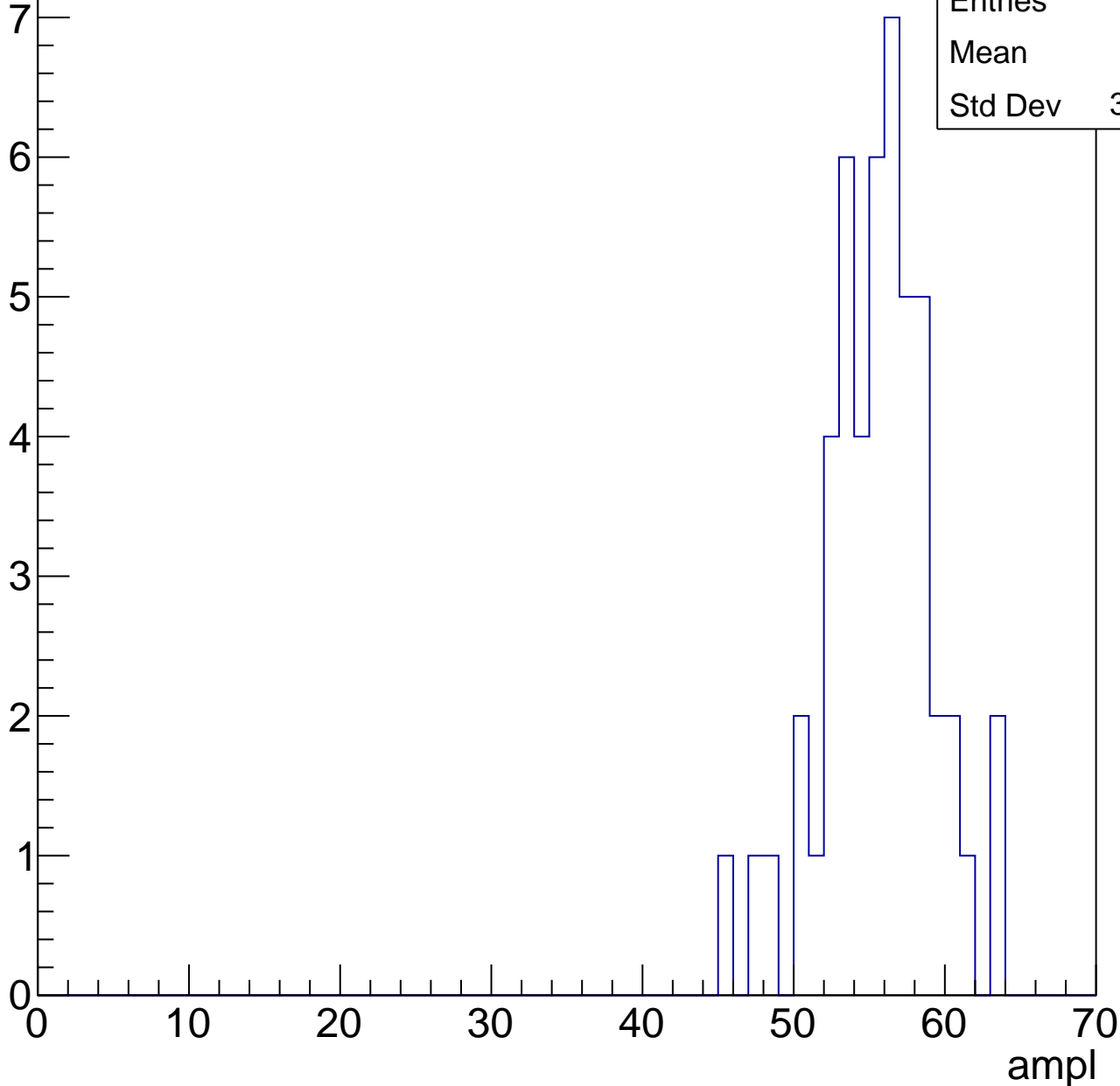


B1L103S, U20-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.1
Std Dev	3.635



B1L103S, U20-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

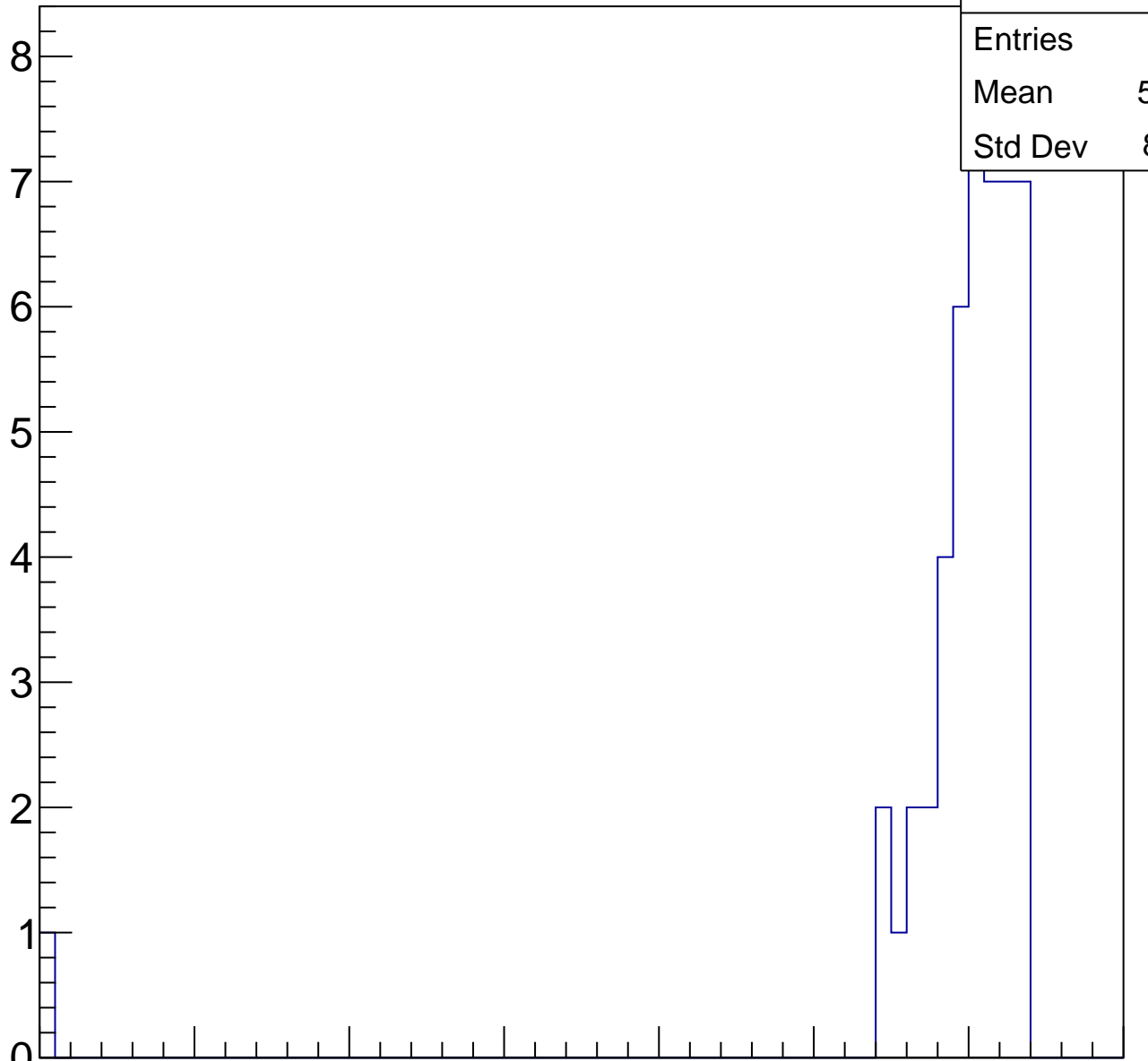
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.66
Std Dev	8.971

ampl

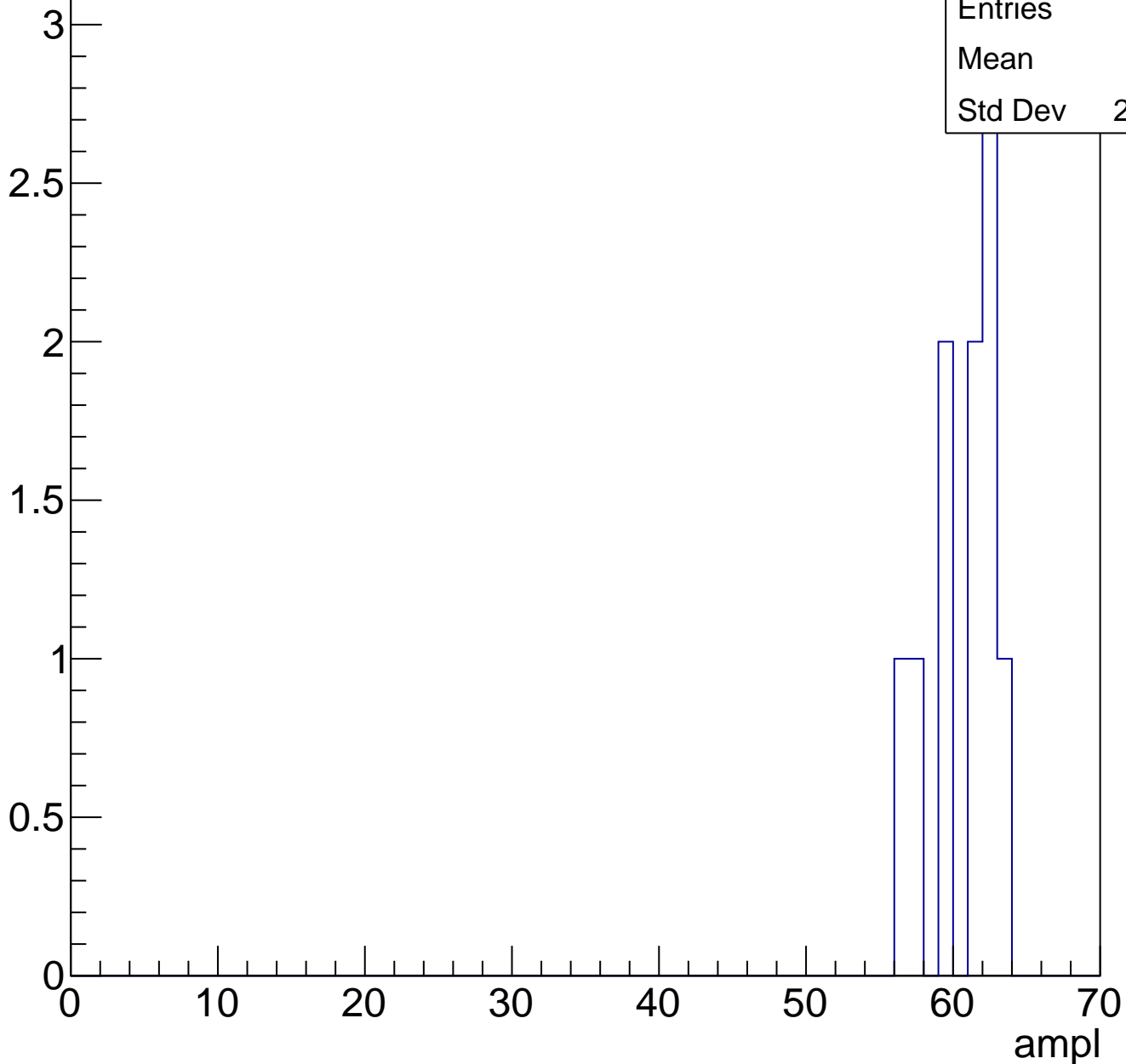
0 10 20 30 40 50 60 70



B1L103S, U20-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch94, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	27.98
Std Dev	5.282

Gaus mean : 29.6988

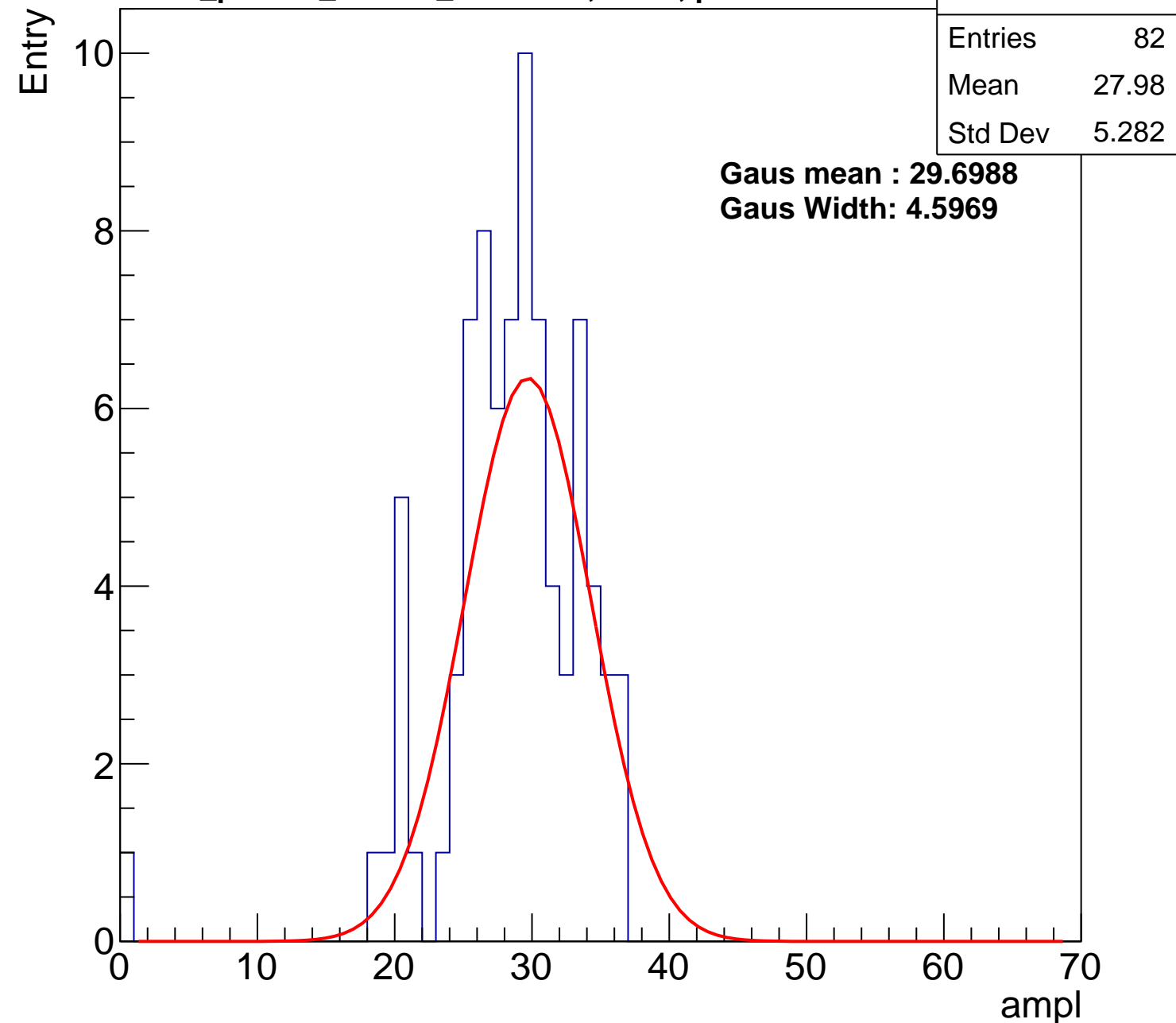
Gaus Width: 4.5969

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch94, adc1

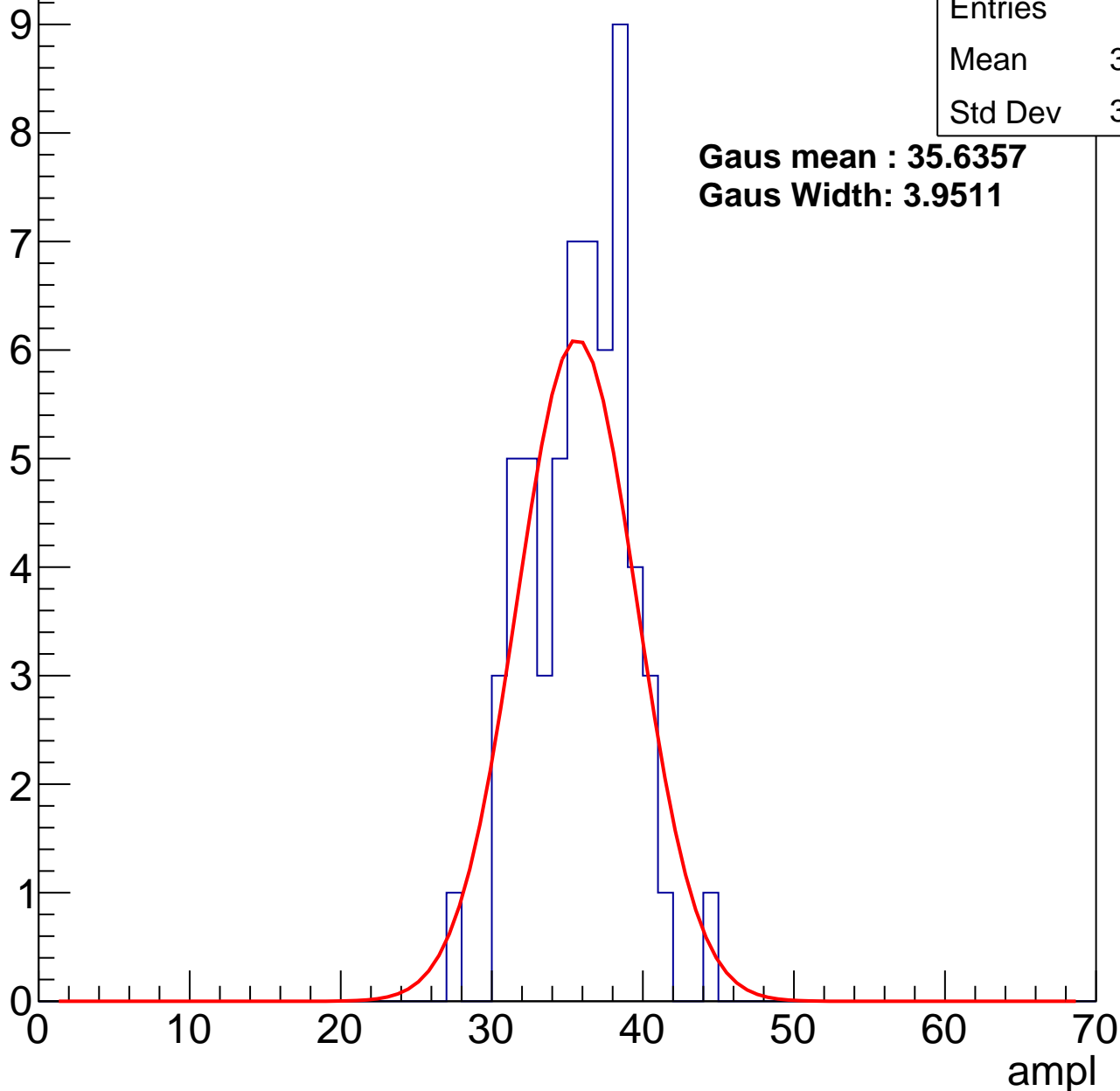
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.38
Std Dev	3.266

Gaus mean : 35.6357

Gaus Width: 3.9511



B1L103S, U20-ch94, adc2

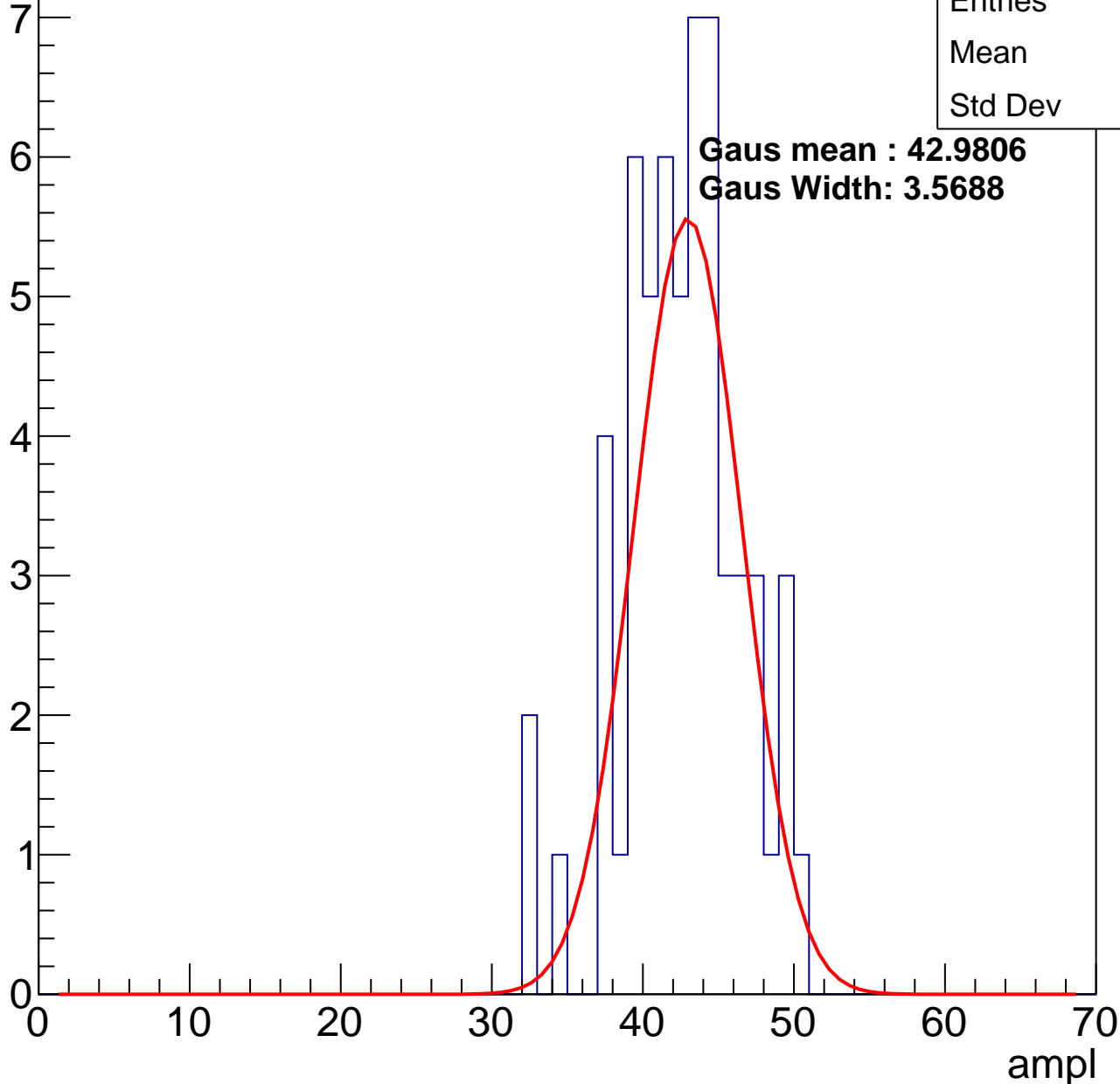
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.1
Std Dev	3.92

Gaus mean : 42.9806

Gaus Width: 3.5688

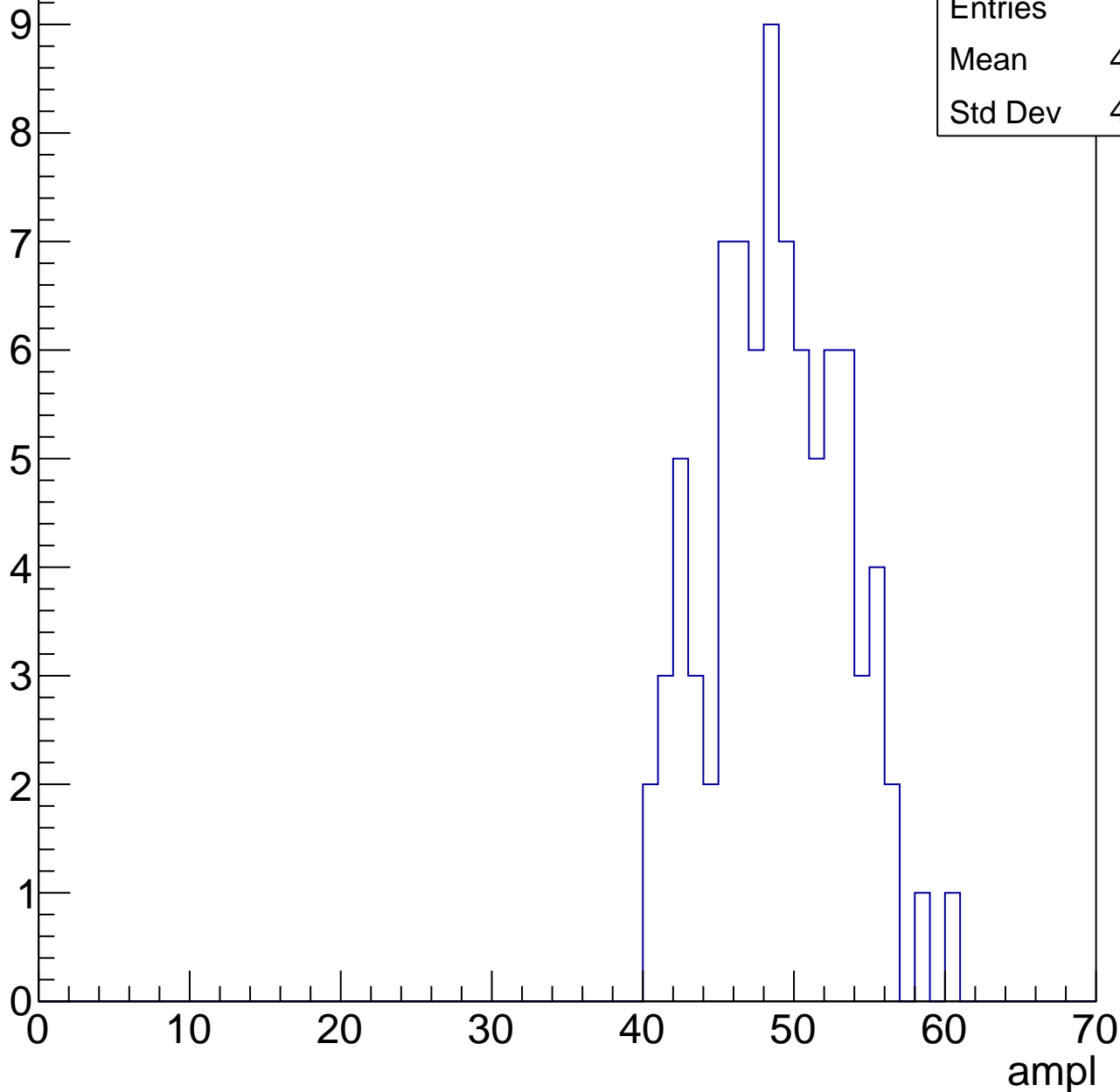


B1L103S, U20-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	48.48
Std Dev	4.389

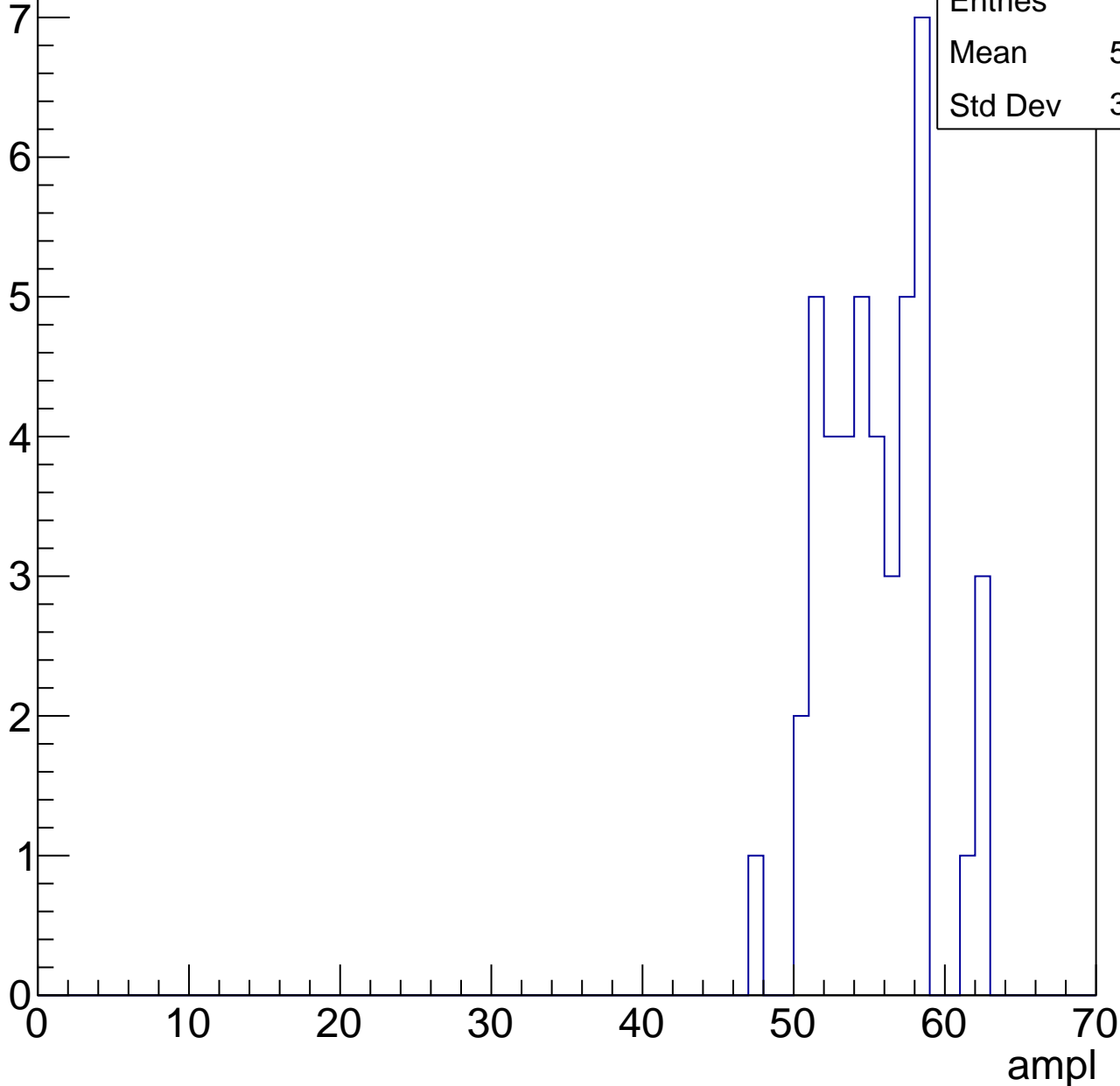


B1L103S, U20-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	54.95
Std Dev	3.437



B1L103S, U20-ch94, adc5

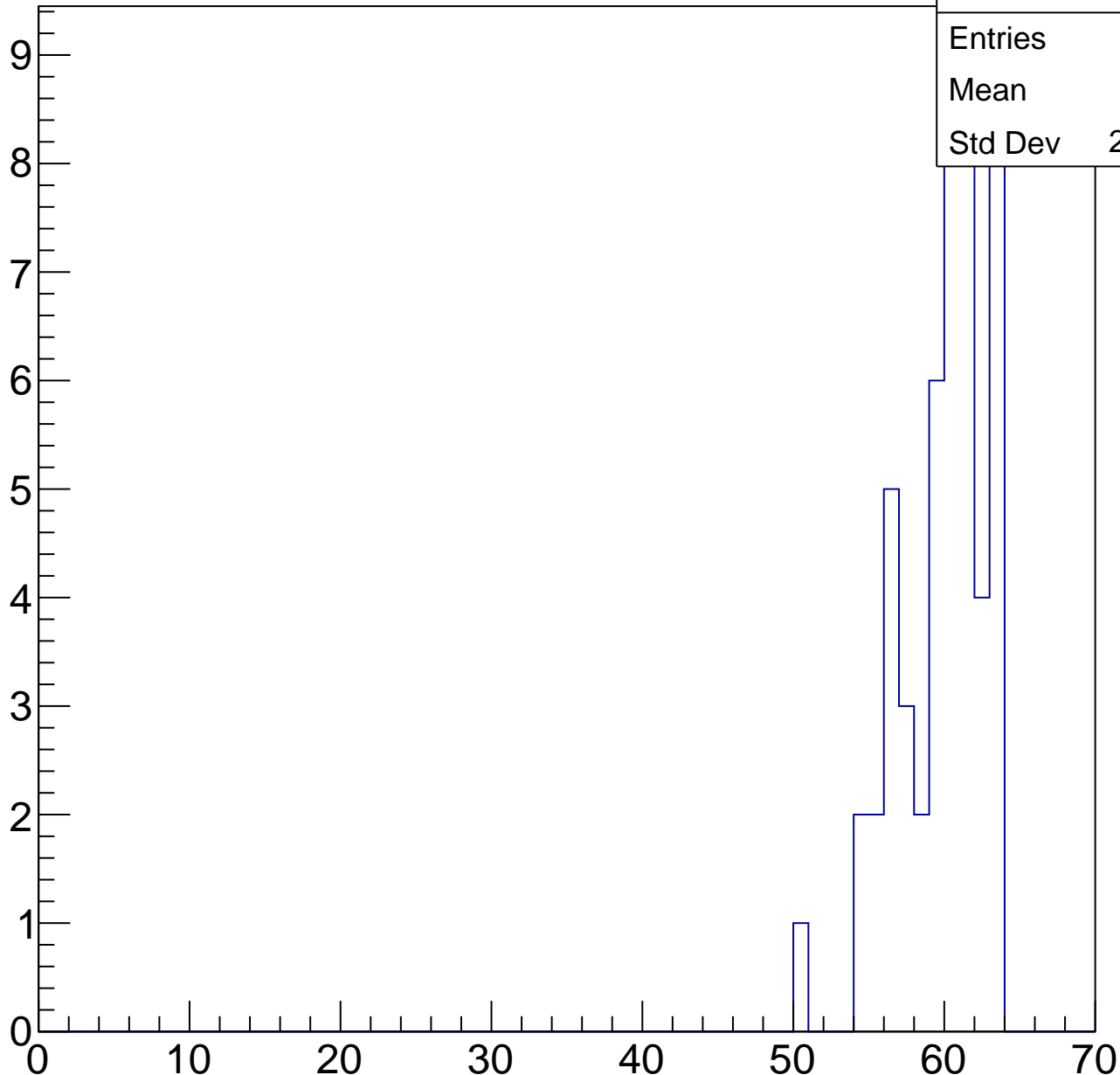
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.4
Std Dev	2.905

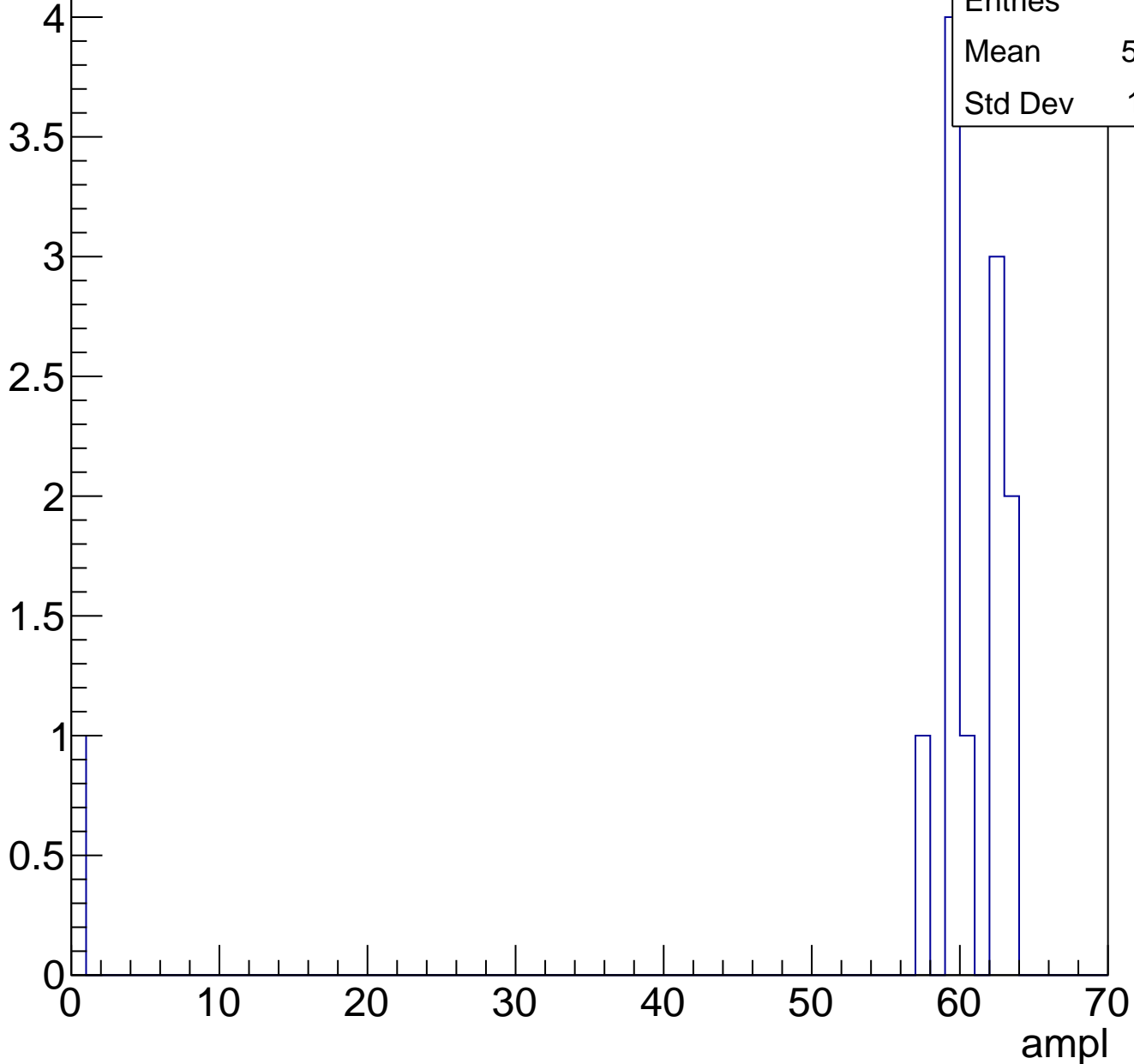
ampl



B1L103S, U20-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch95, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	28.82
Std Dev	3.312

Gaus mean : 29.3205

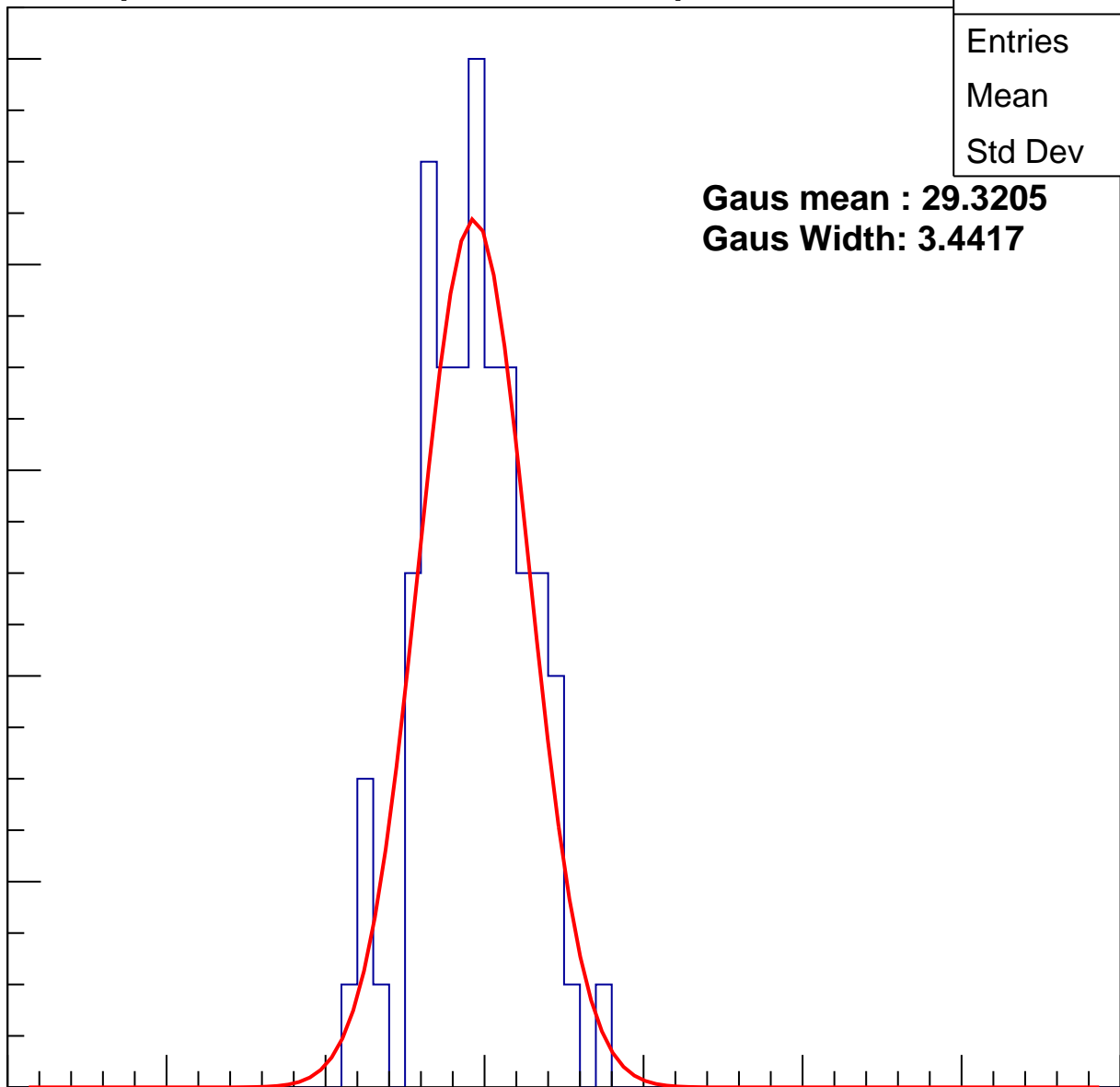
Gaus Width: 3.4417

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch95, adc1

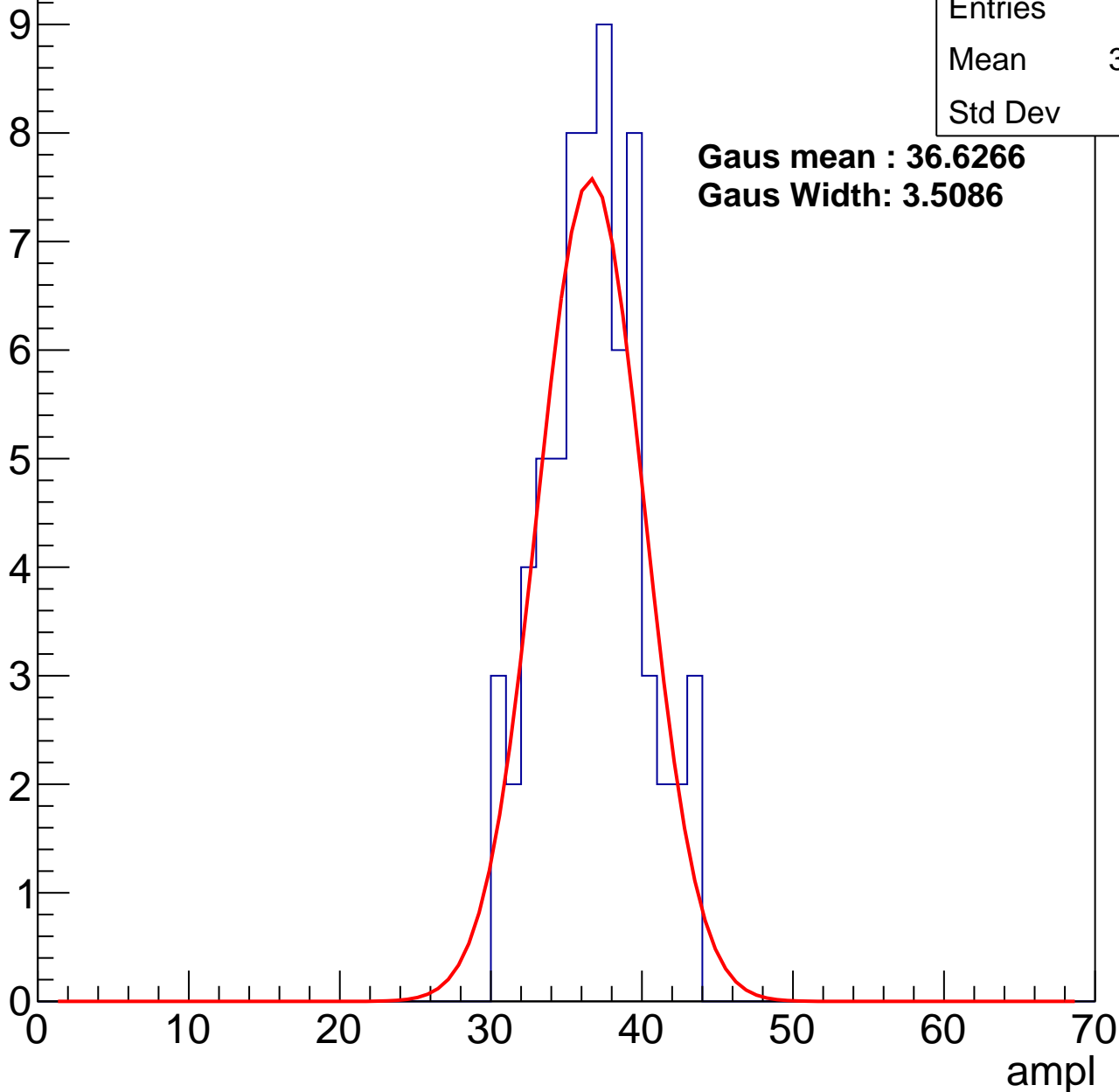
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.34
Std Dev	3.23

Gaus mean : 36.6266

Gaus Width: 3.5086



B1L103S, U20-ch95, adc2

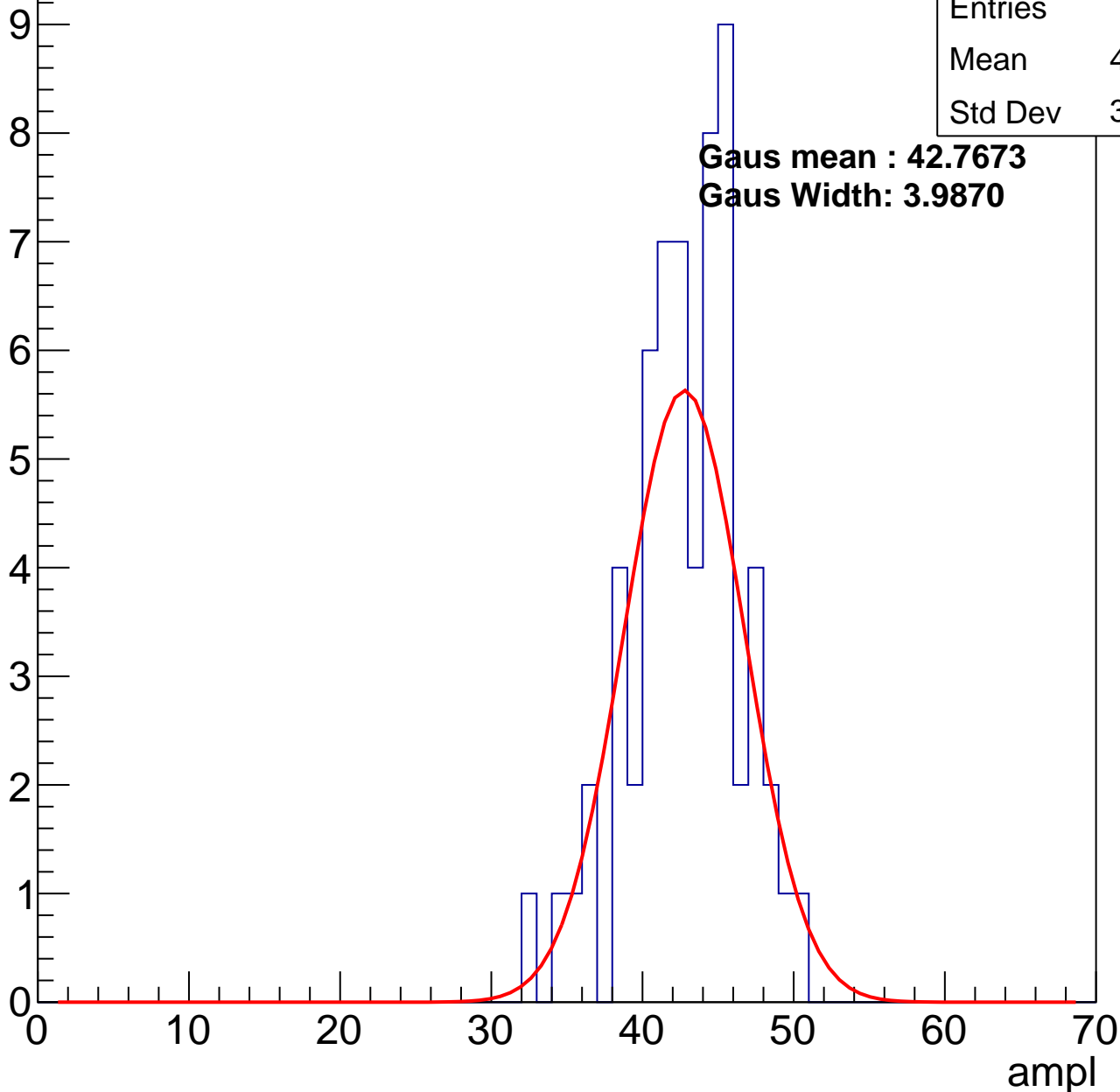
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.39
Std Dev	3.643

Gaus mean : 42.7673

Gaus Width: 3.9870

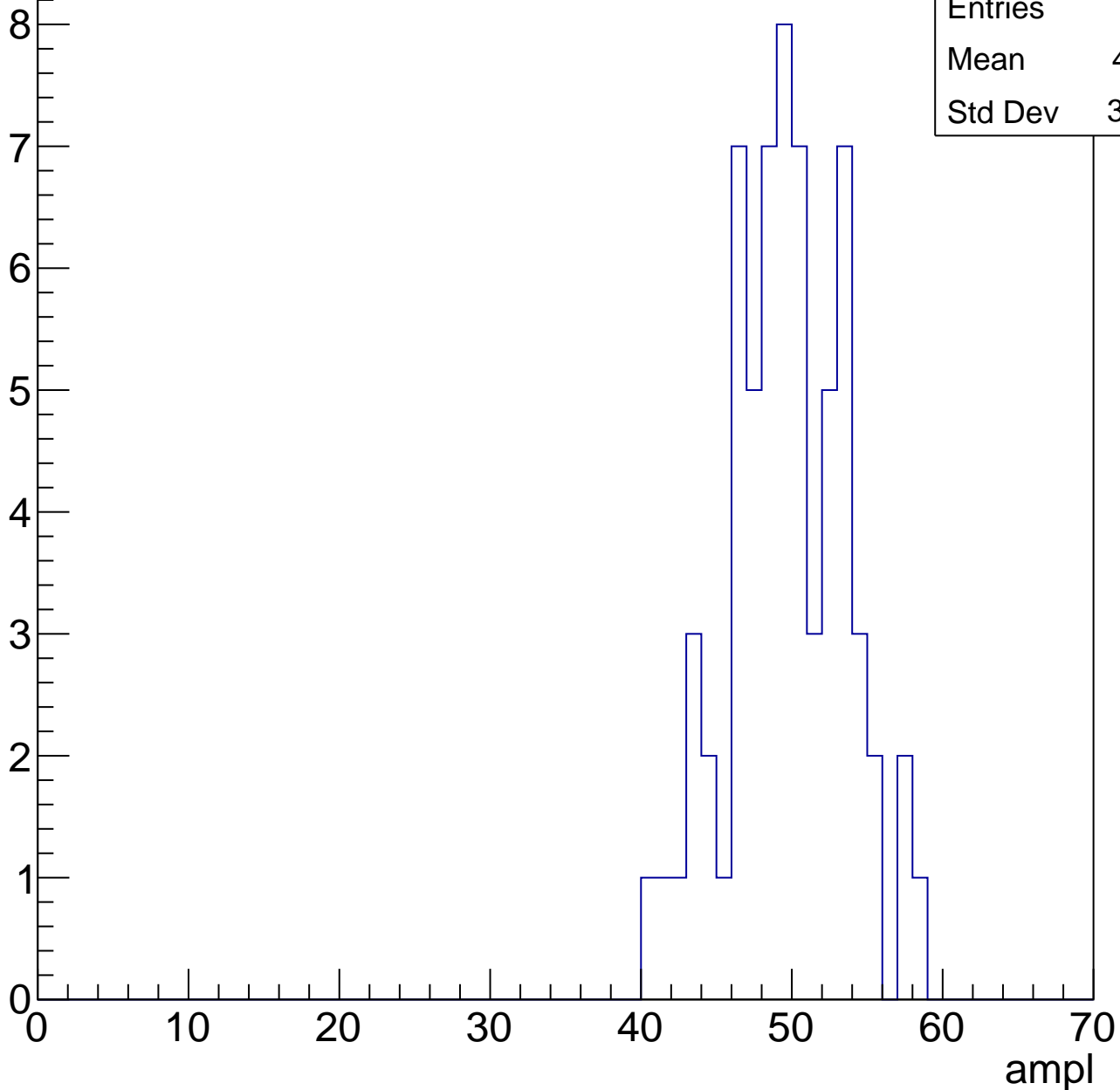


B1L103S, U20-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

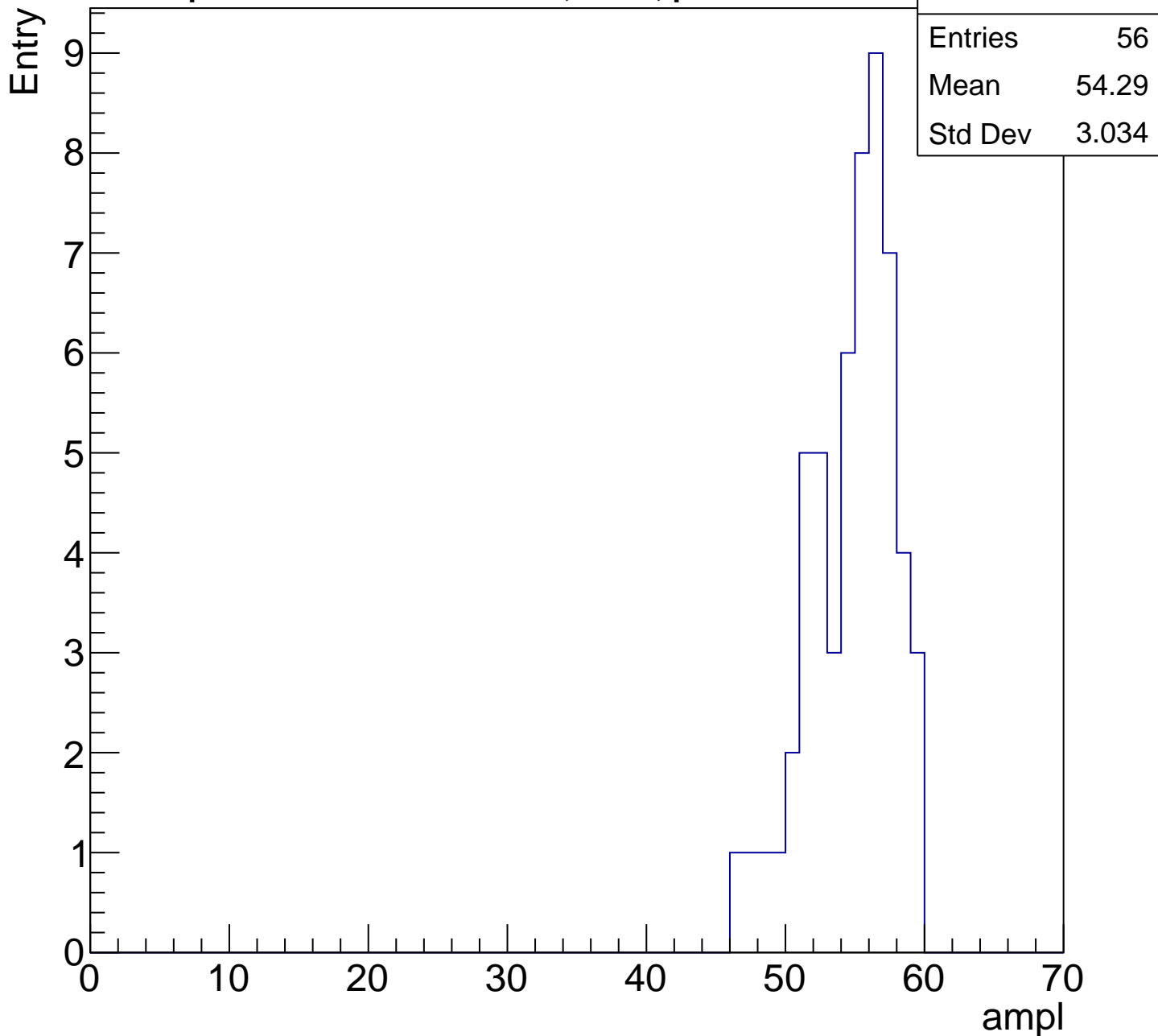
Entry

Entries	66
Mean	49.21
Std Dev	3.867



B1L103S, U20-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

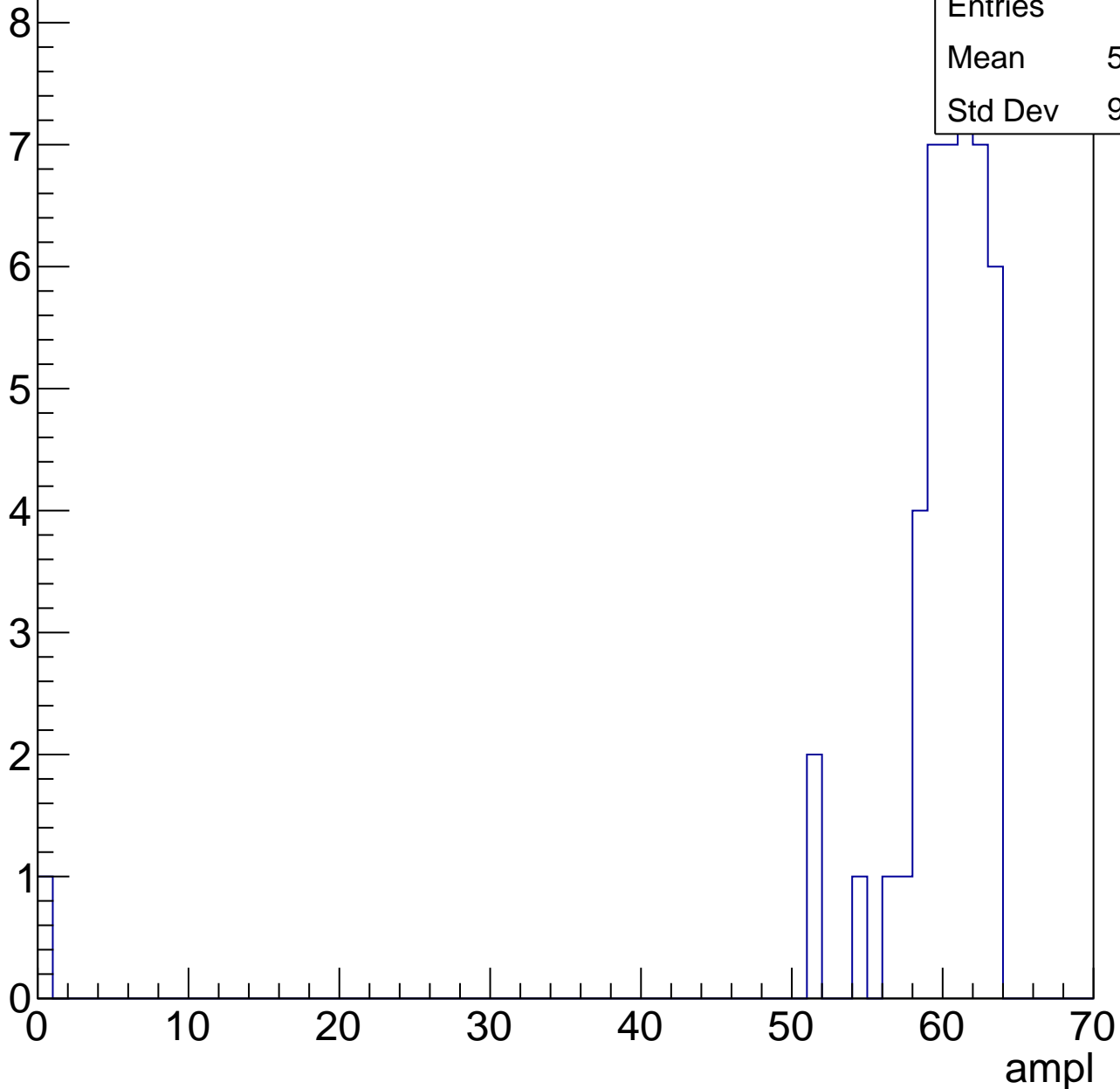


B1L103S, U20-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

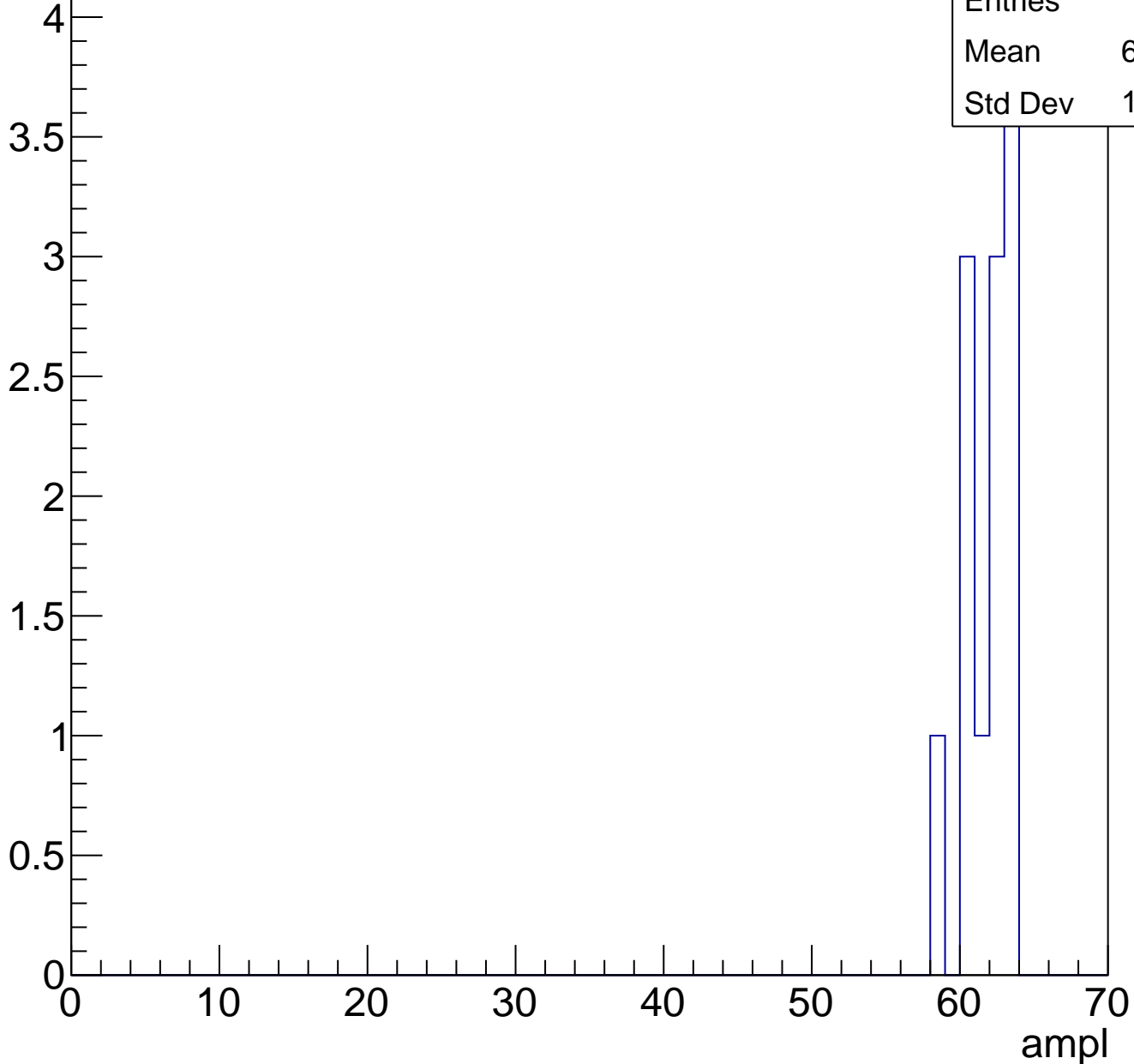
Entries	45
Mean	58.53
Std Dev	9.237



B1L103S, U20-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	61
Std Dev	0

B1L103S, U20-ch96, adc0

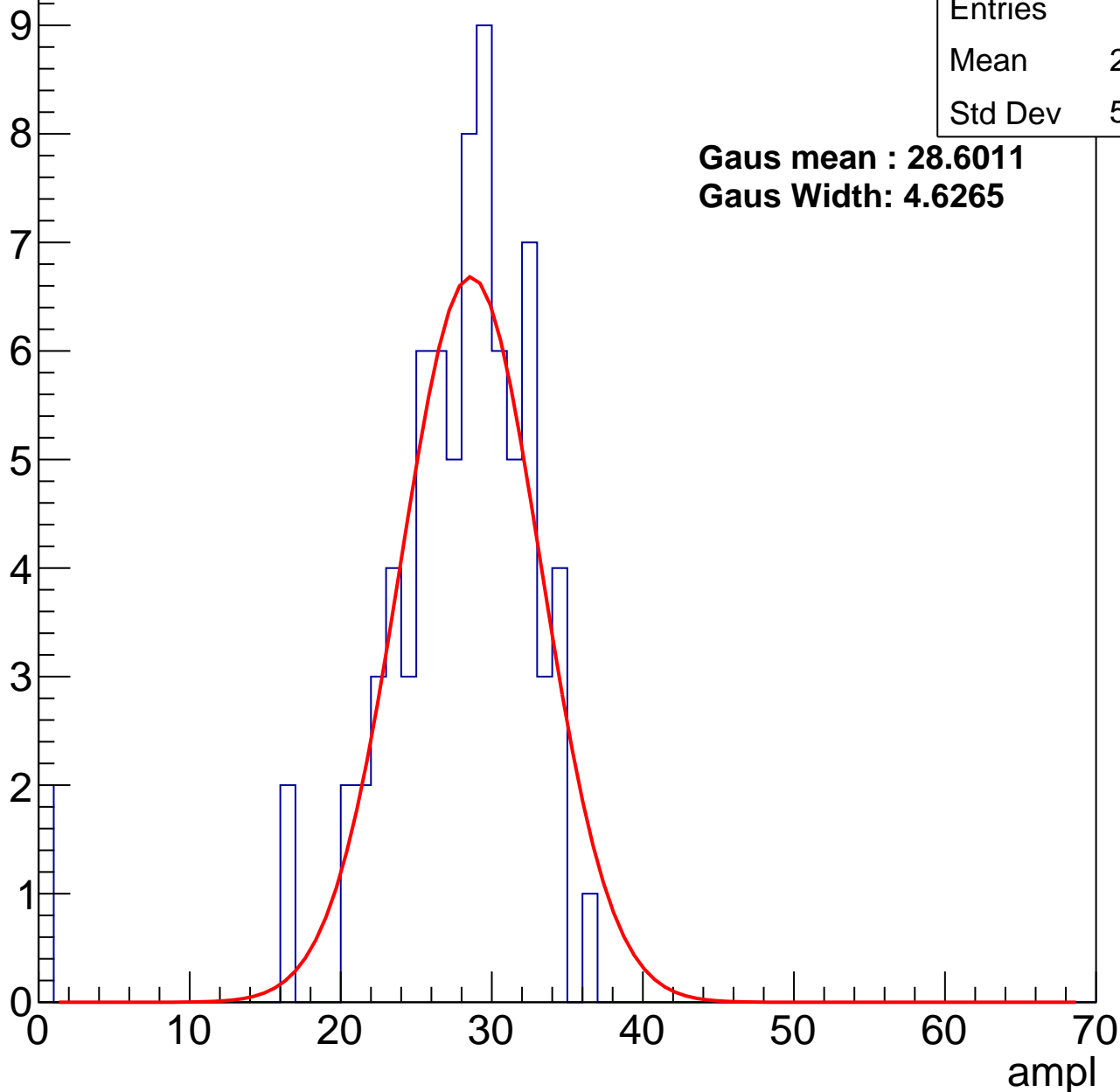
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	26.92
Std Dev	5.995

Gaus mean : 28.6011

Gaus Width: 4.6265



B1L103S, U20-ch96, adc1

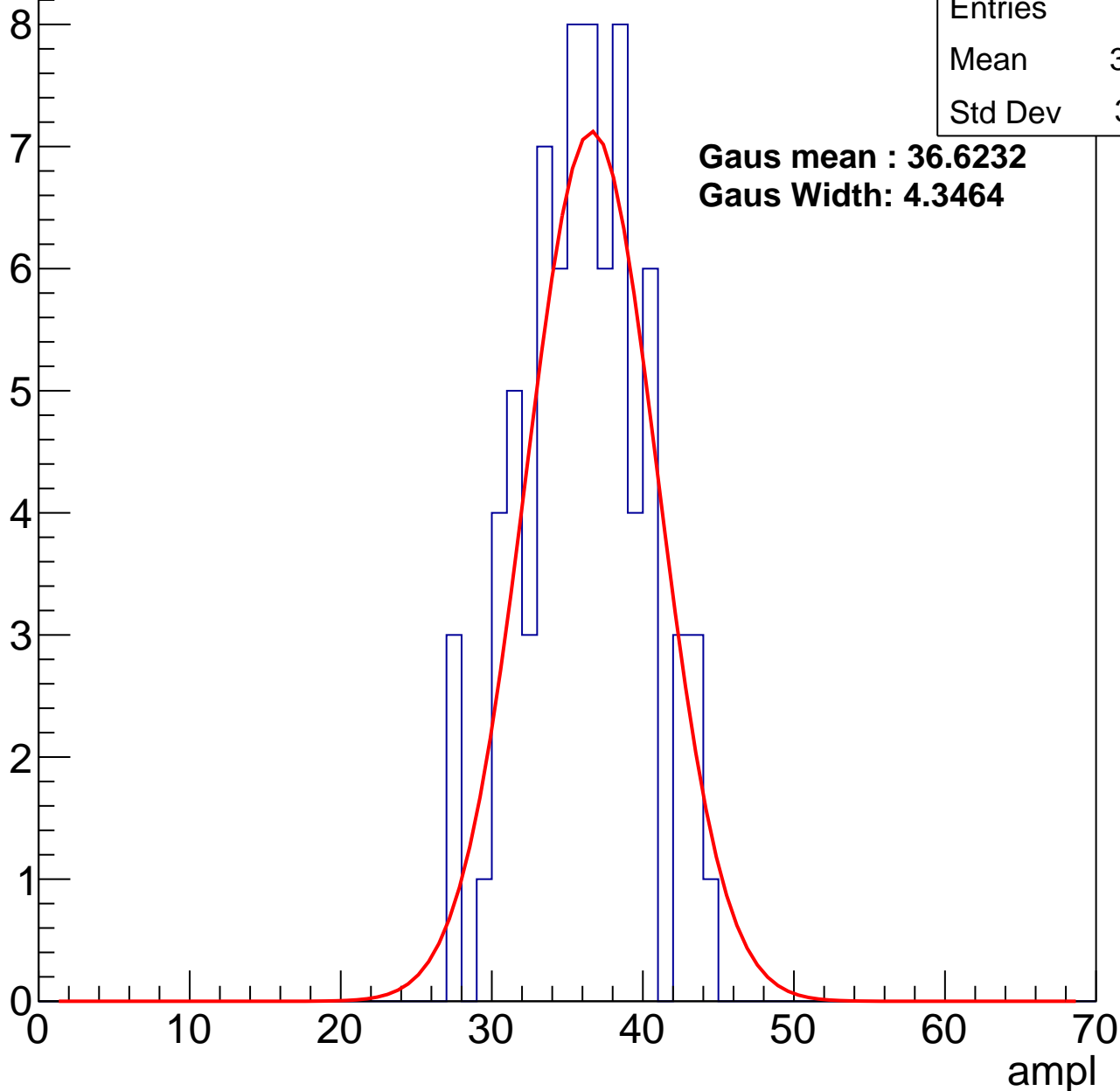
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.59
Std Dev	3.941

Gaus mean : 36.6232

Gaus Width: 4.3464



B1L103S, U20-ch96, adc2

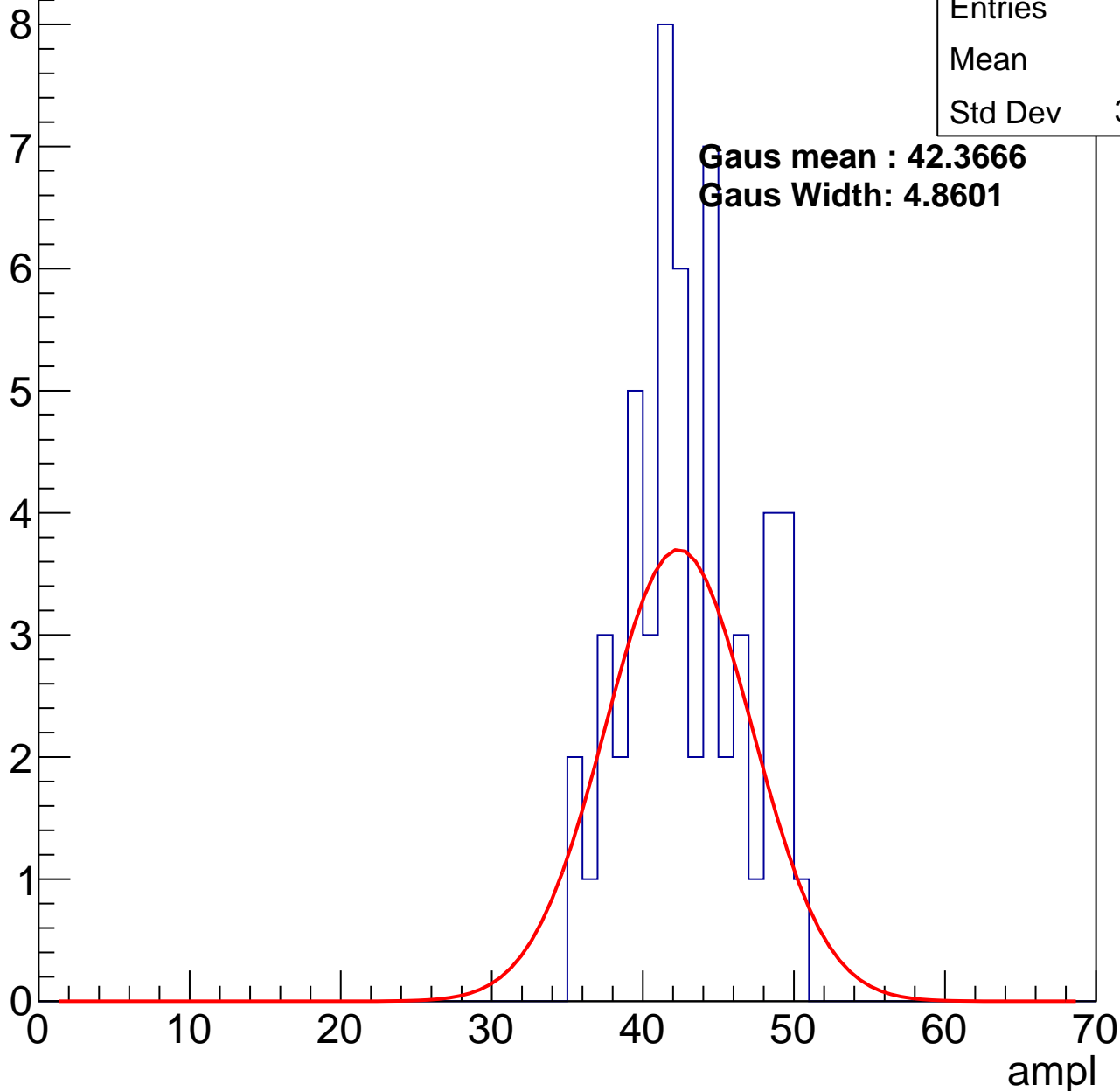
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	42.5
Std Dev	3.891

Gaus mean : 42.3666

Gaus Width: 4.8601

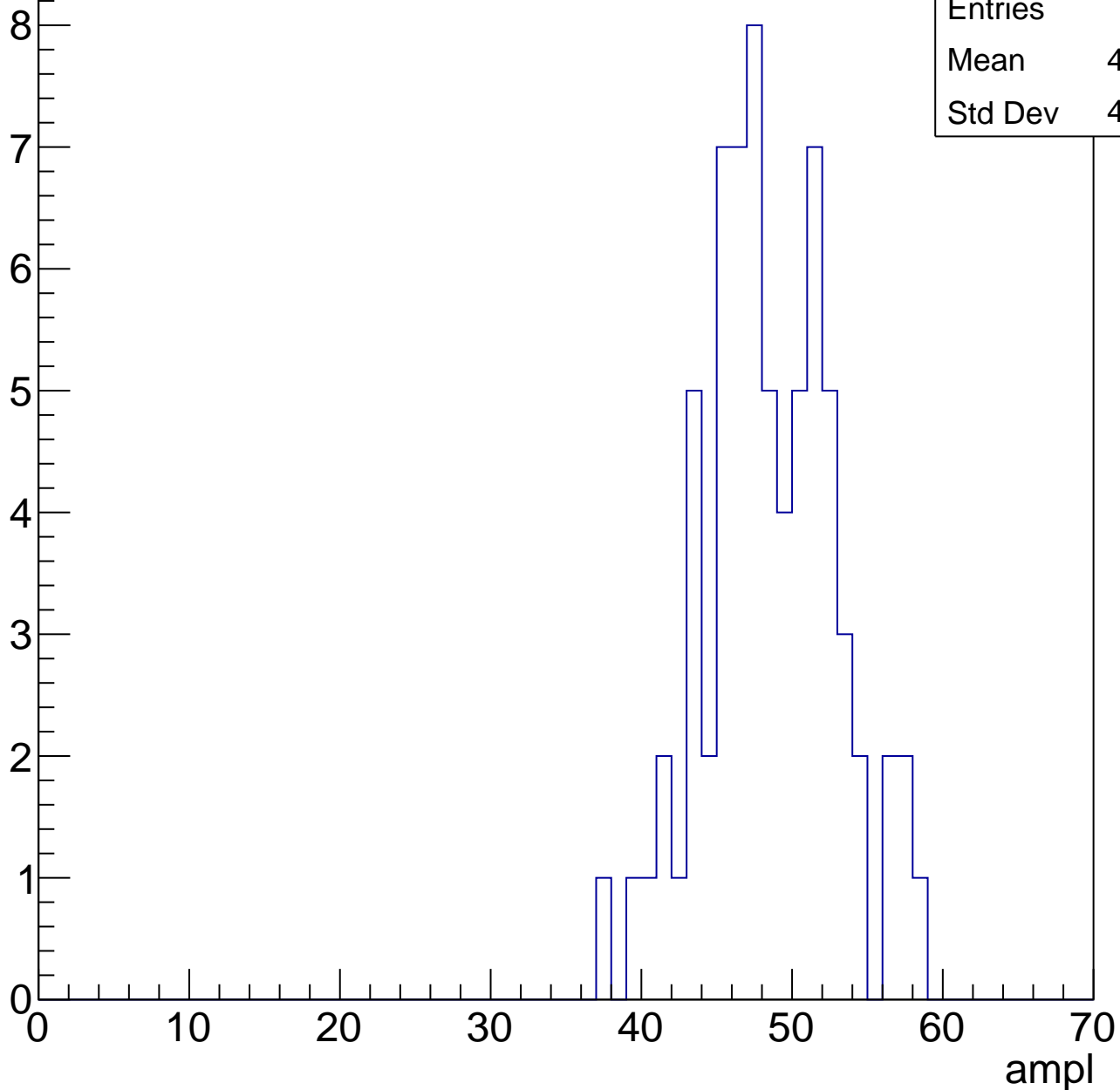


B1L103S, U20-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.03
Std Dev	4.377

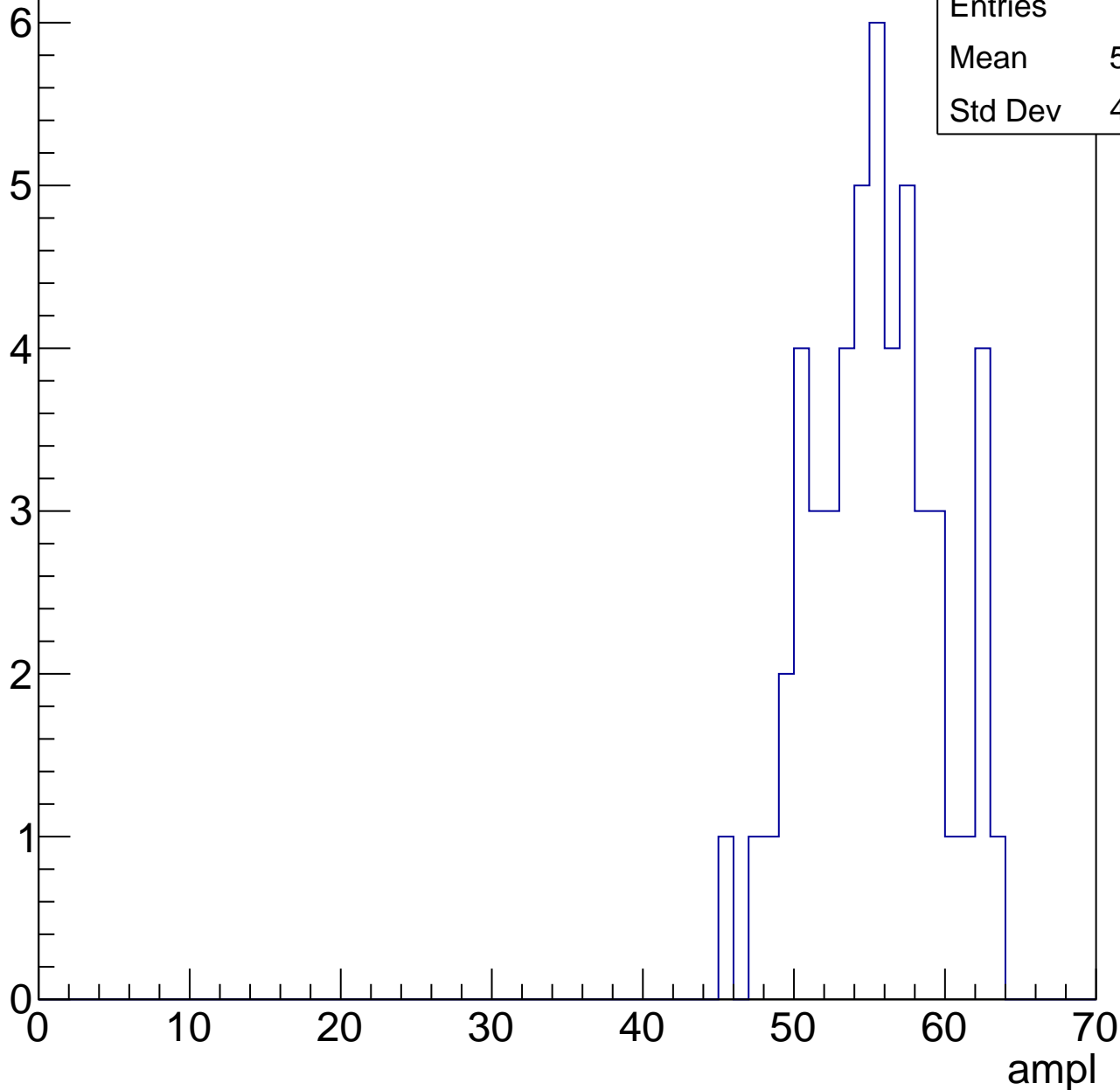


B1L103S, U20-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

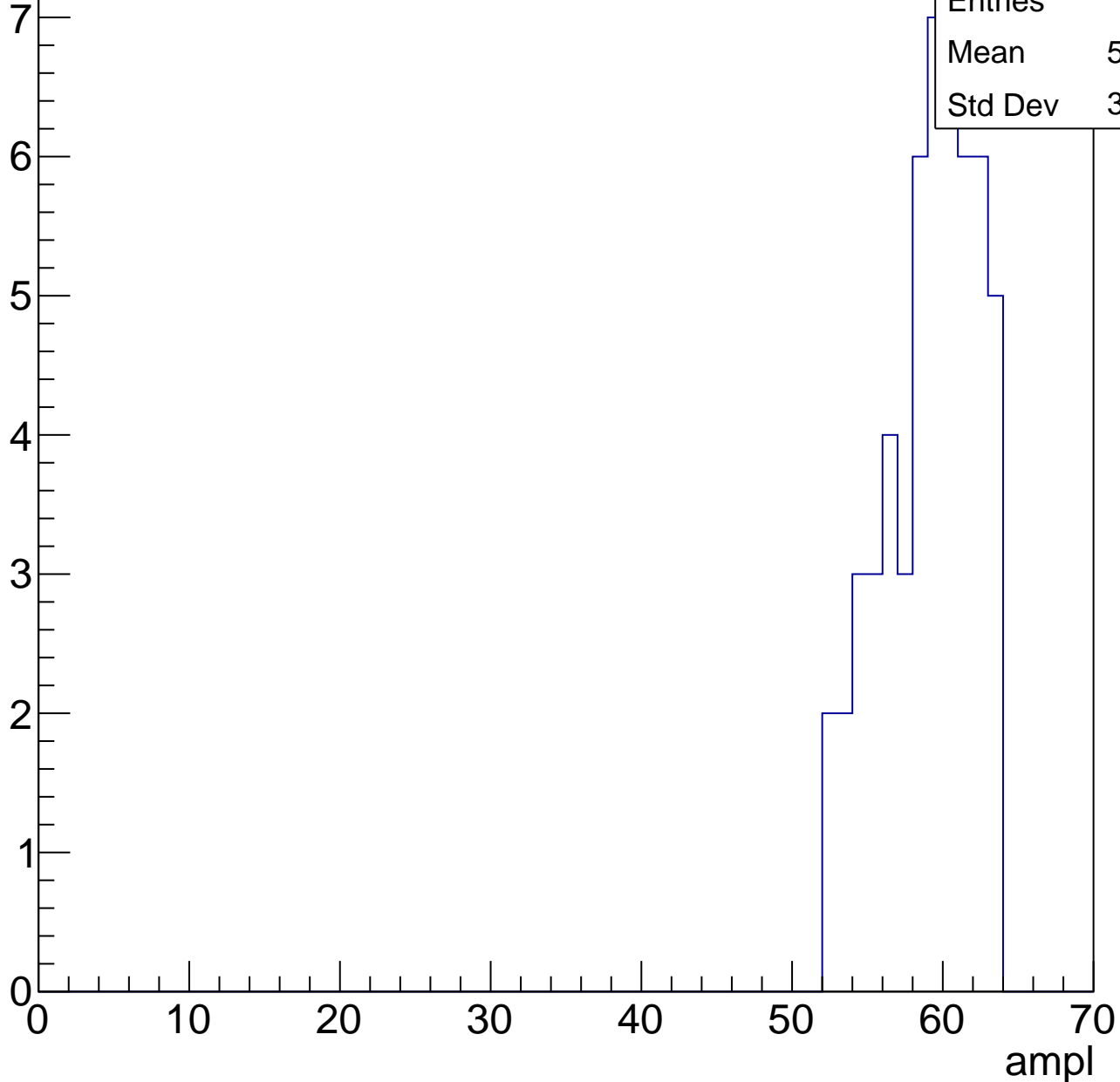
Entries	52
Mean	54.83
Std Dev	4.159



B1L103S, U20-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

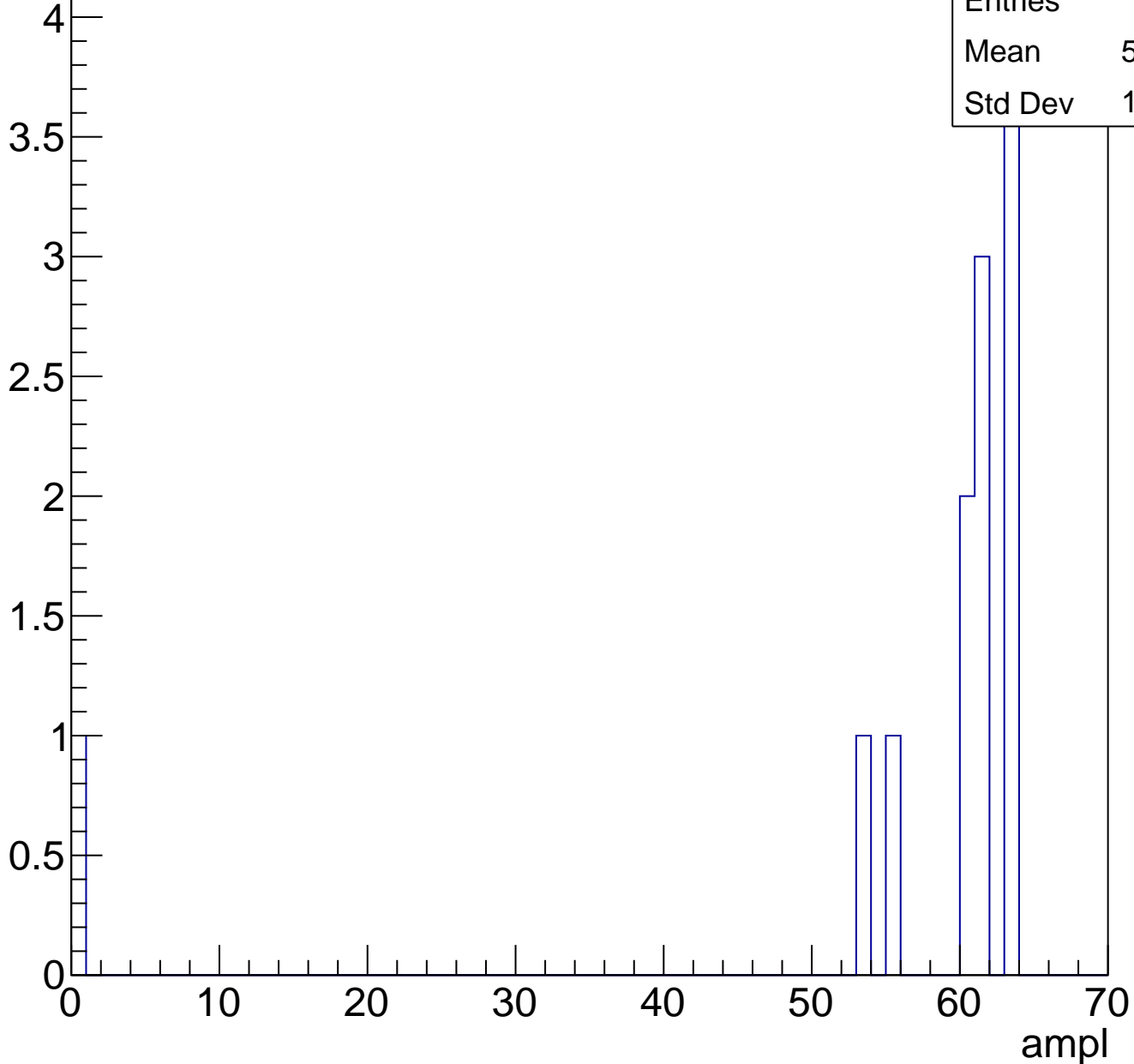


Entries	54
Mean	58.63
Std Dev	3.057

B1L103S, U20-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	55.25
Std Dev	16.94

B1L103S, U20-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch97, adc0

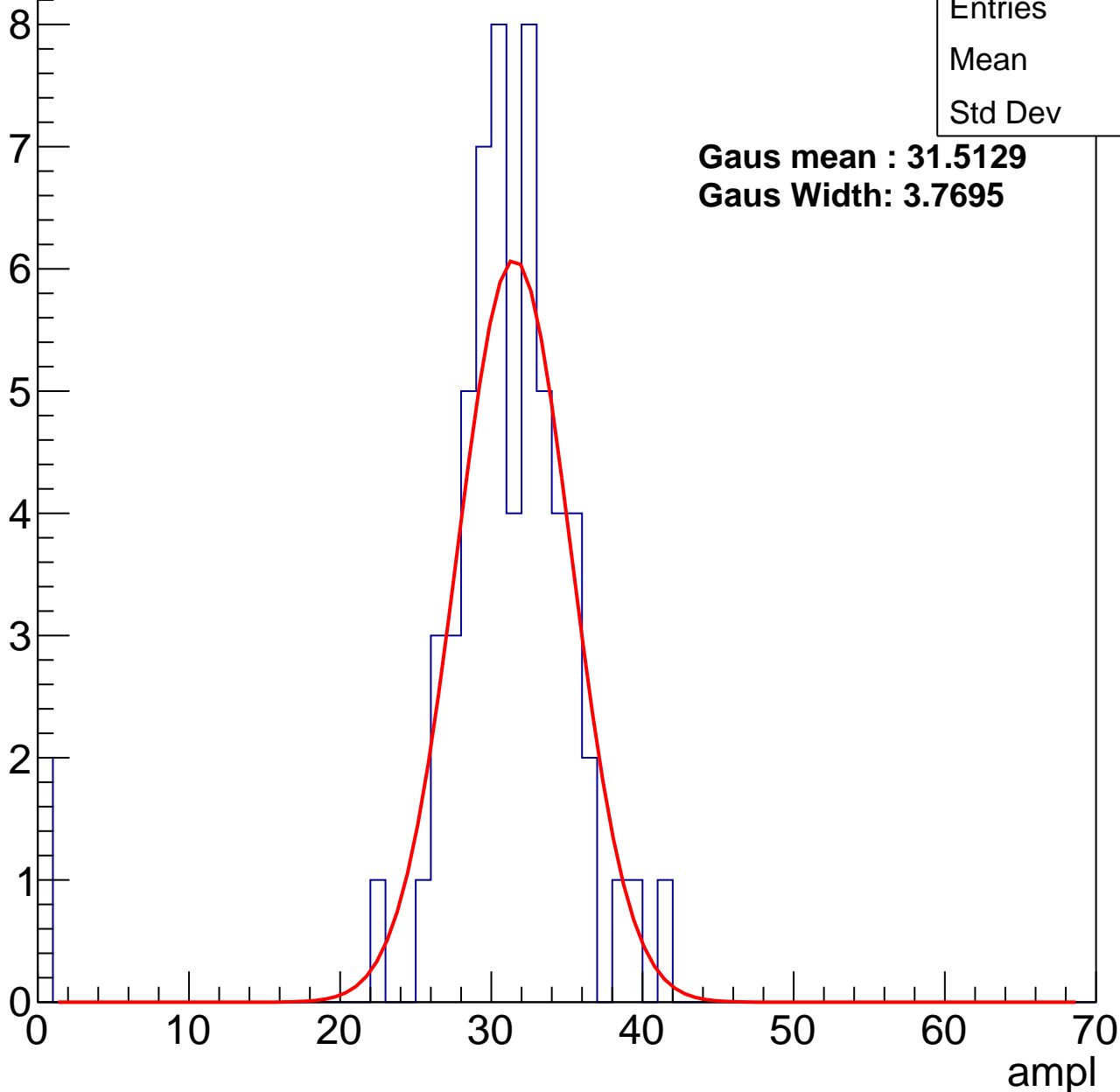
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	30
Std Dev	6.55

Gaus mean : 31.5129

Gaus Width: 3.7695



B1L103S, U20-ch97, adc1

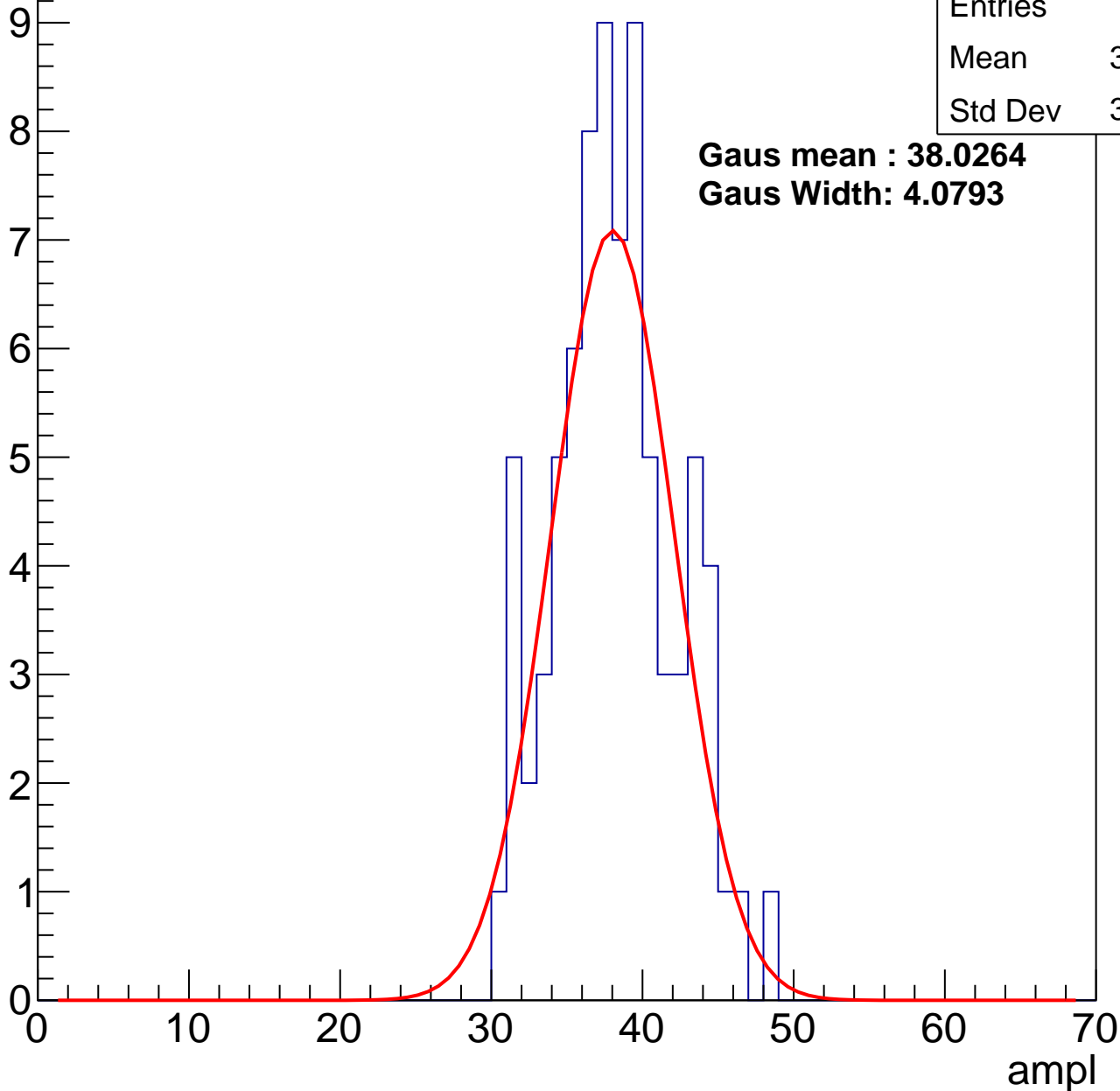
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	37.76
Std Dev	3.942

Gaus mean : 38.0264

Gaus Width: 4.0793



B1L103S, U20-ch97, adc2

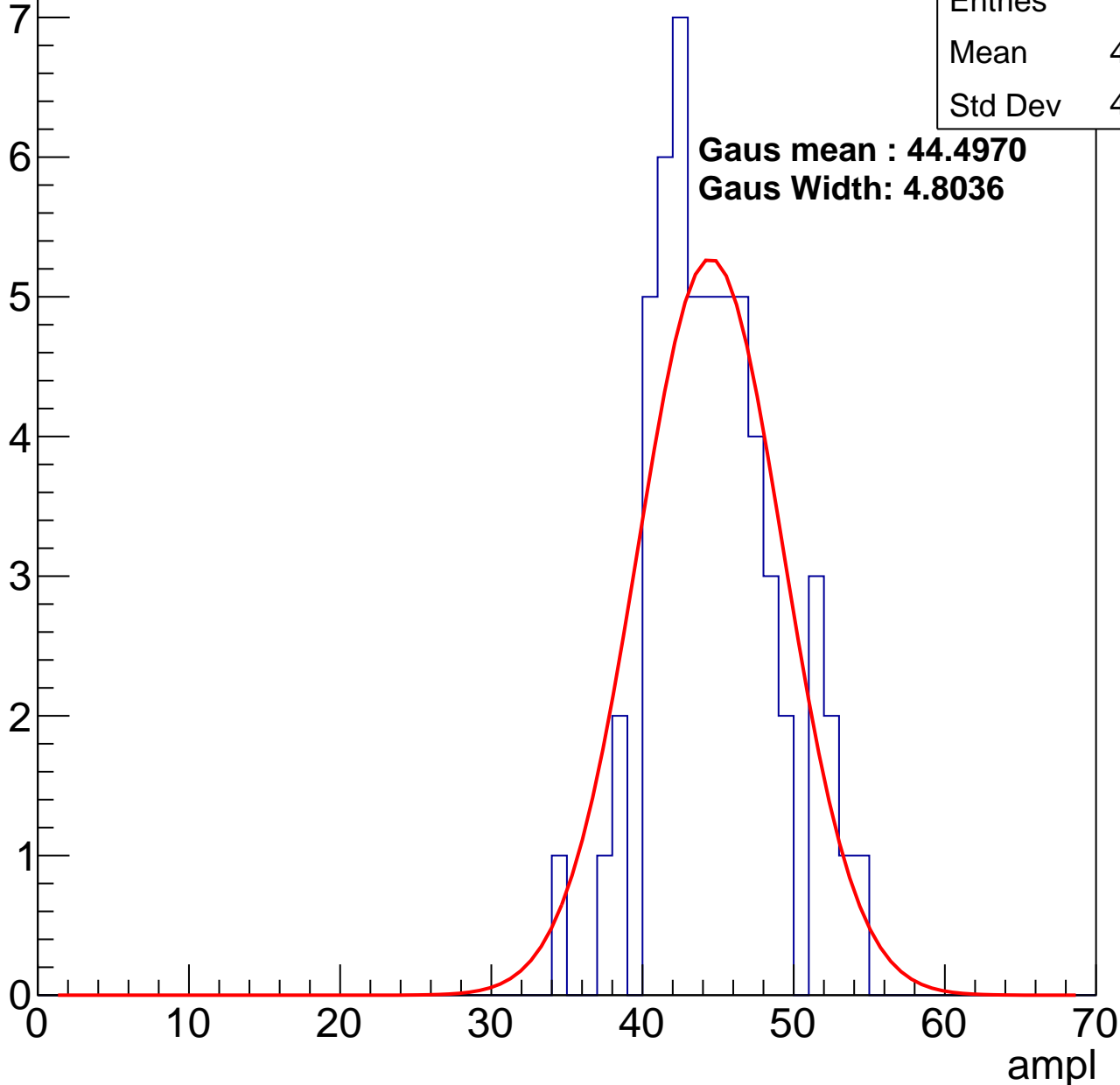
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.33
Std Dev	4.146

Gaus mean : 44.4970

Gaus Width: 4.8036

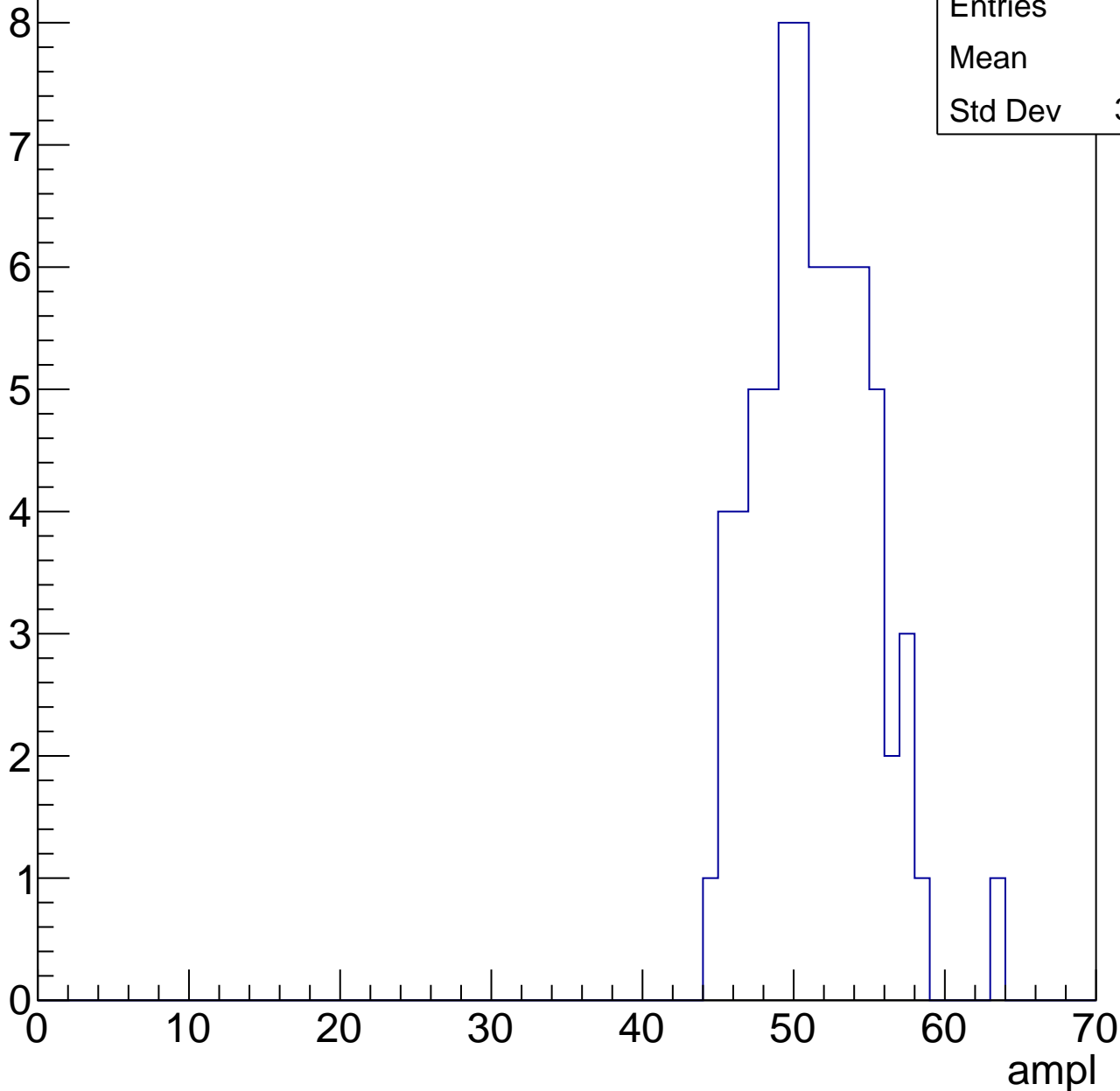


B1L103S, U20-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.9
Std Dev	3.701

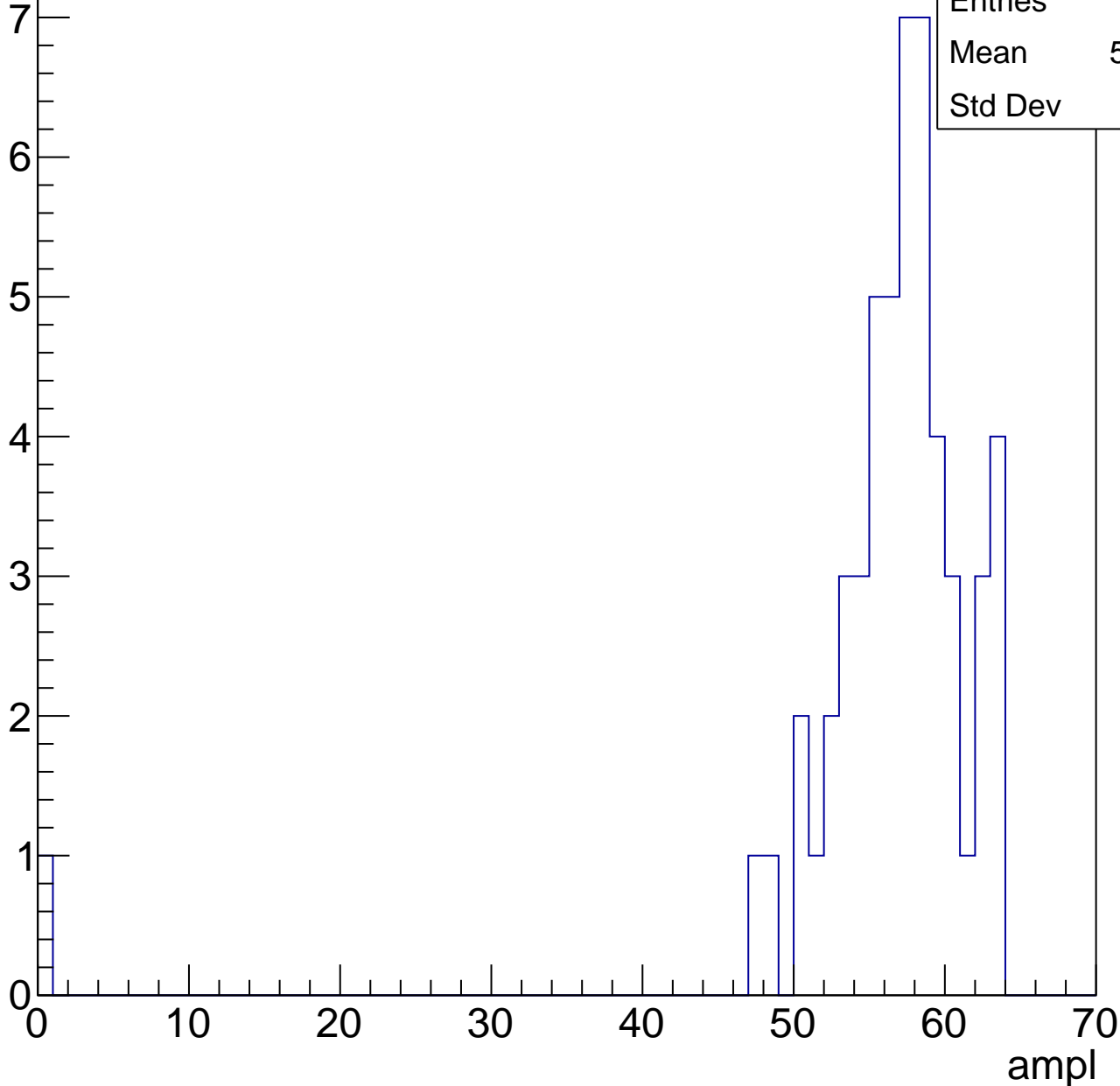


B1L103S, U20-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

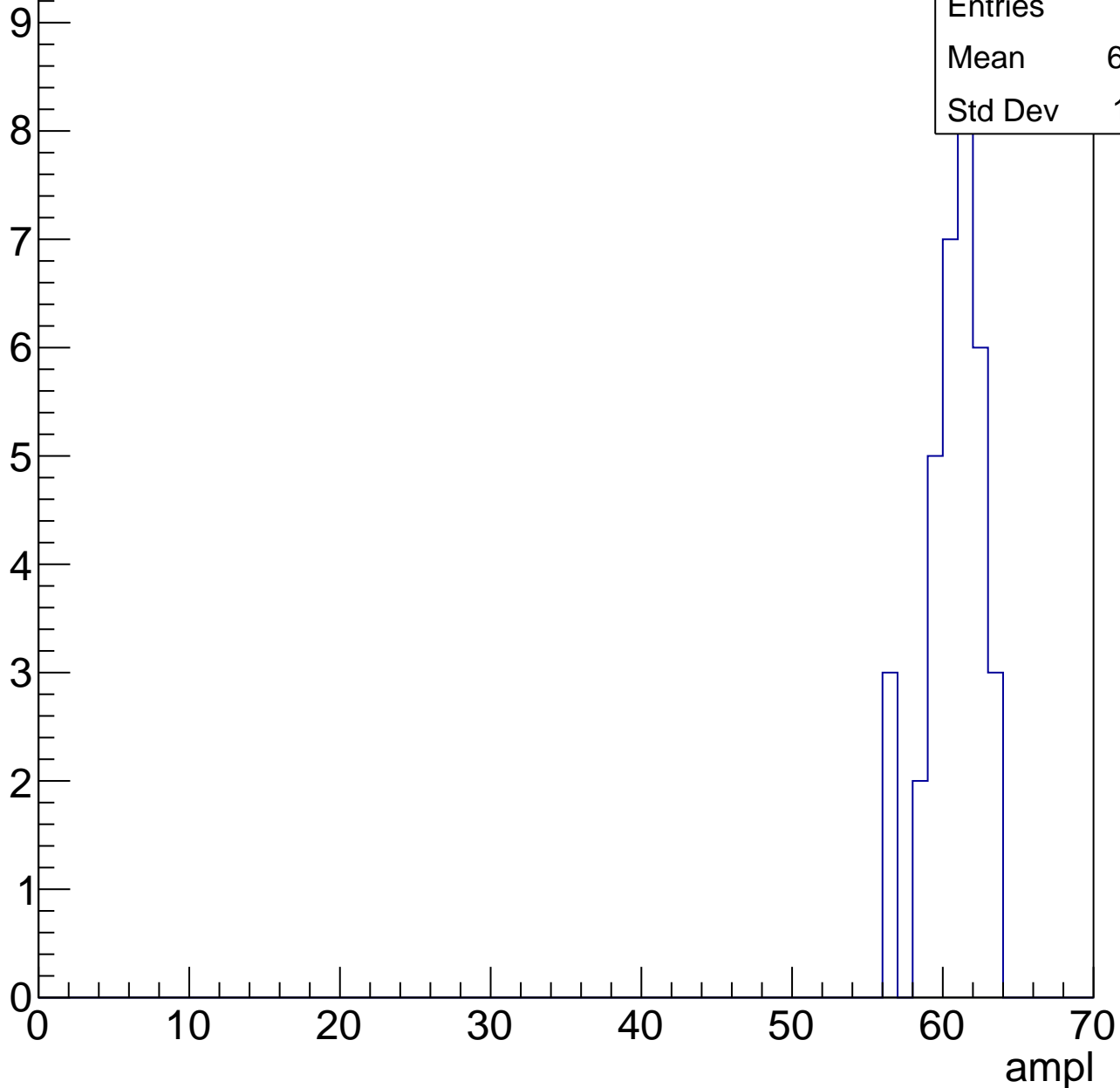
Entries	53
Mean	55.58
Std Dev	8.57



B1L103S, U20-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch98, adc0

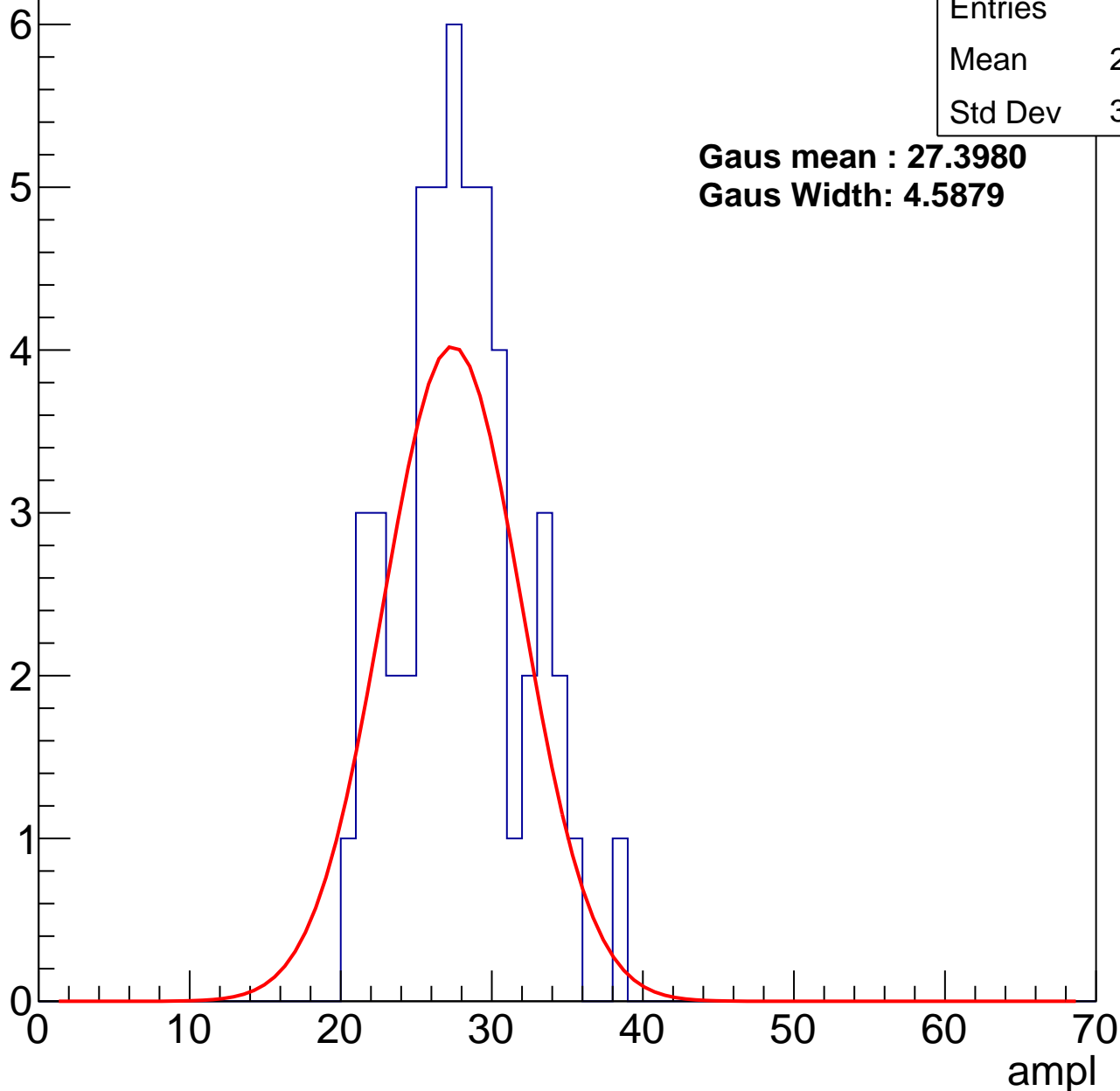
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	27.45
Std Dev	3.997

Gaus mean : 27.3980

Gaus Width: 4.5879



B1L103S, U20-ch98, adc1

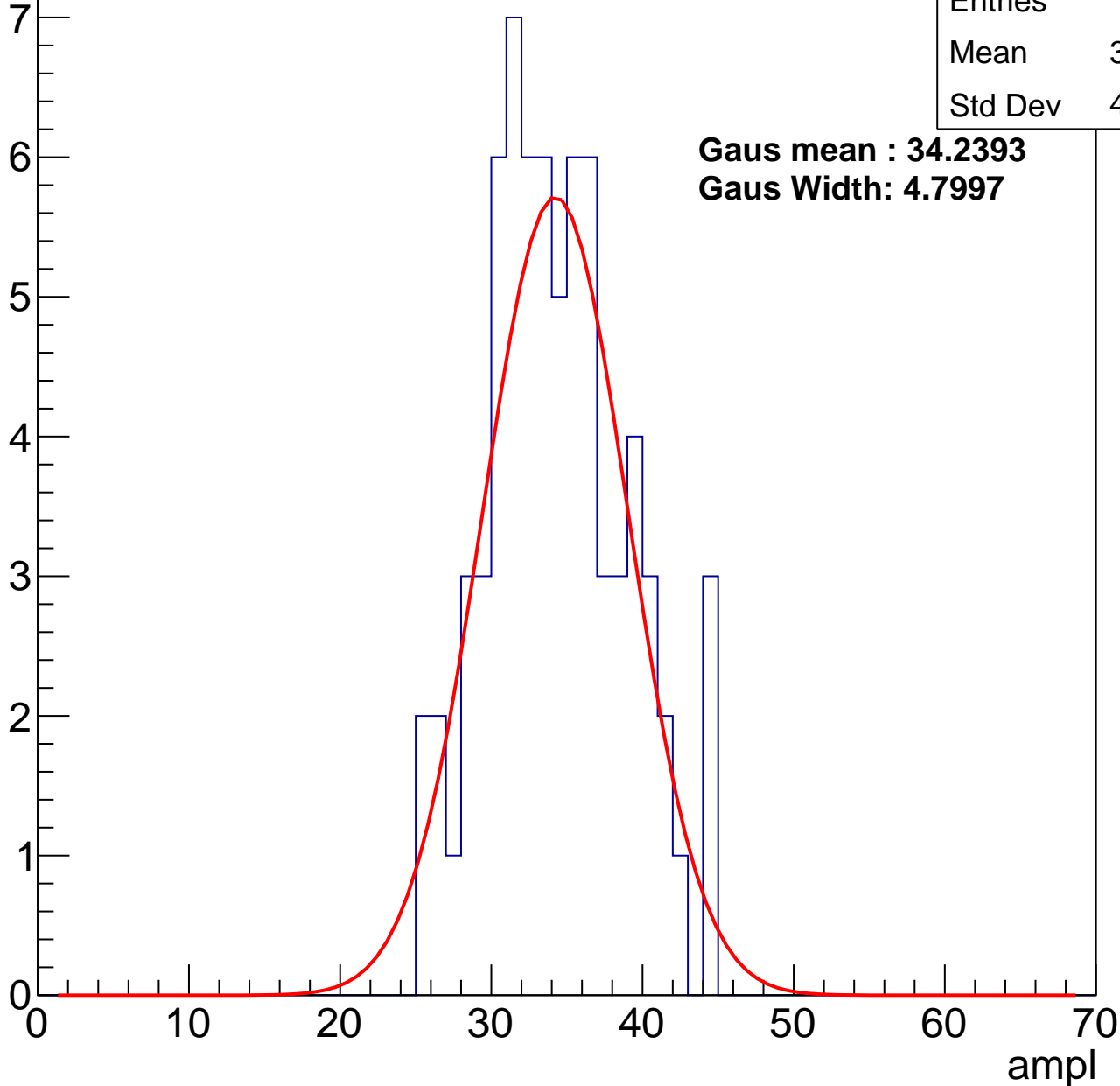
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	33.89
Std Dev	4.542

Gaus mean : 34.2393

Gaus Width: 4.7997



B1L103S, U20-ch98, adc2

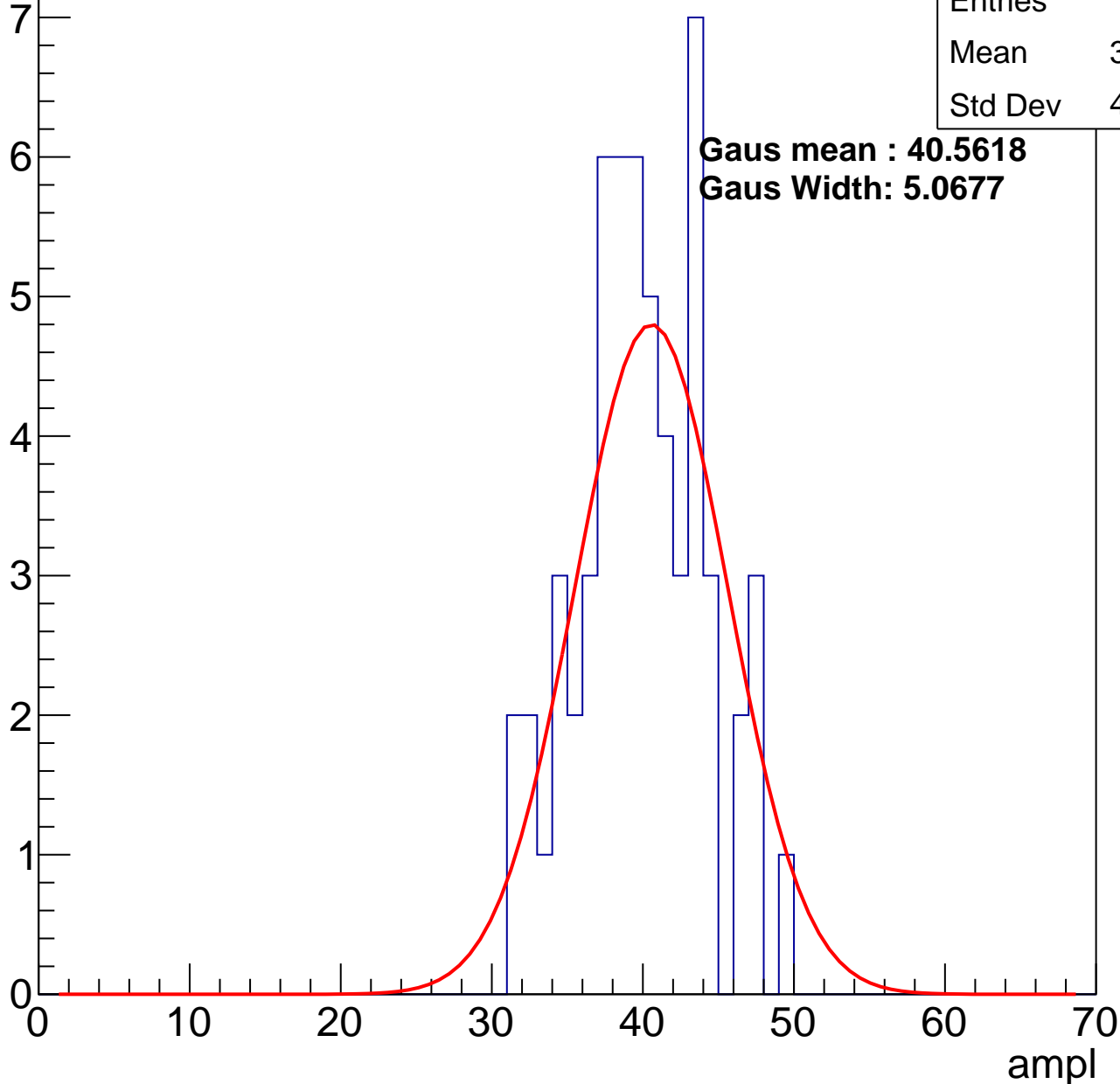
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	39.46
Std Dev	4.196

Gaus mean : 40.5618

Gaus Width: 5.0677

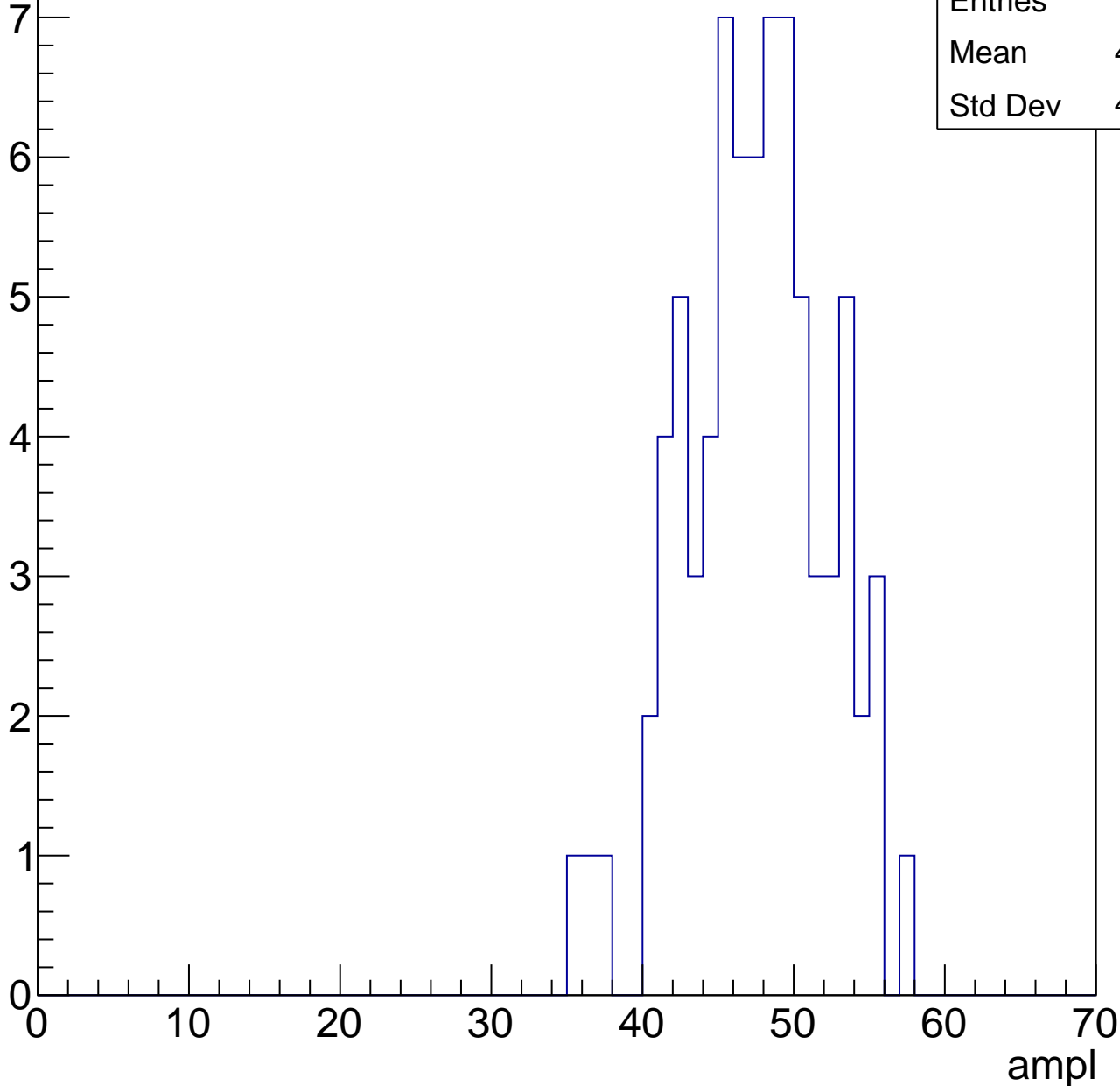


B1L103S, U20-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	47.01
Std Dev	4.621

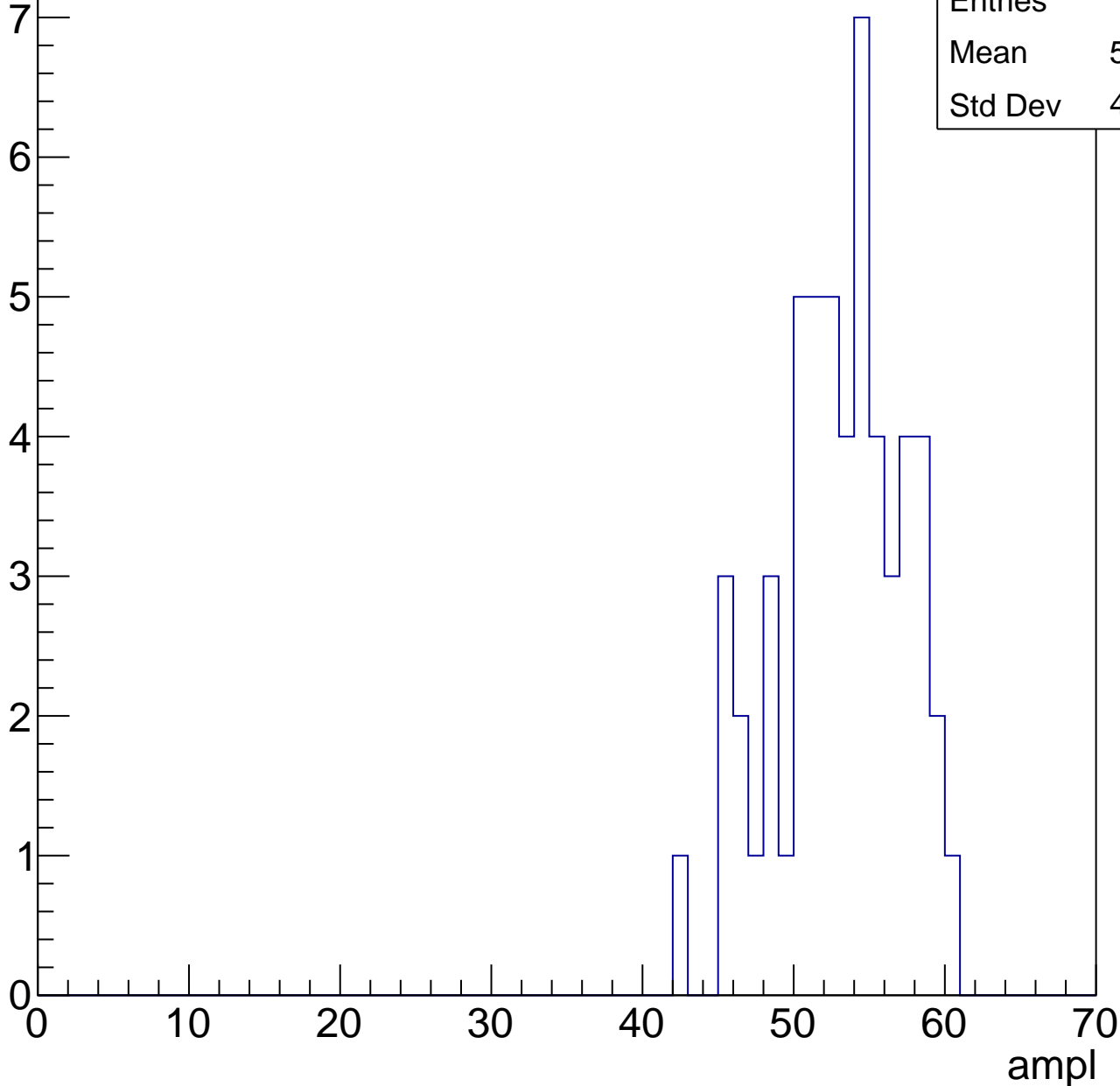


B1L103S, U20-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

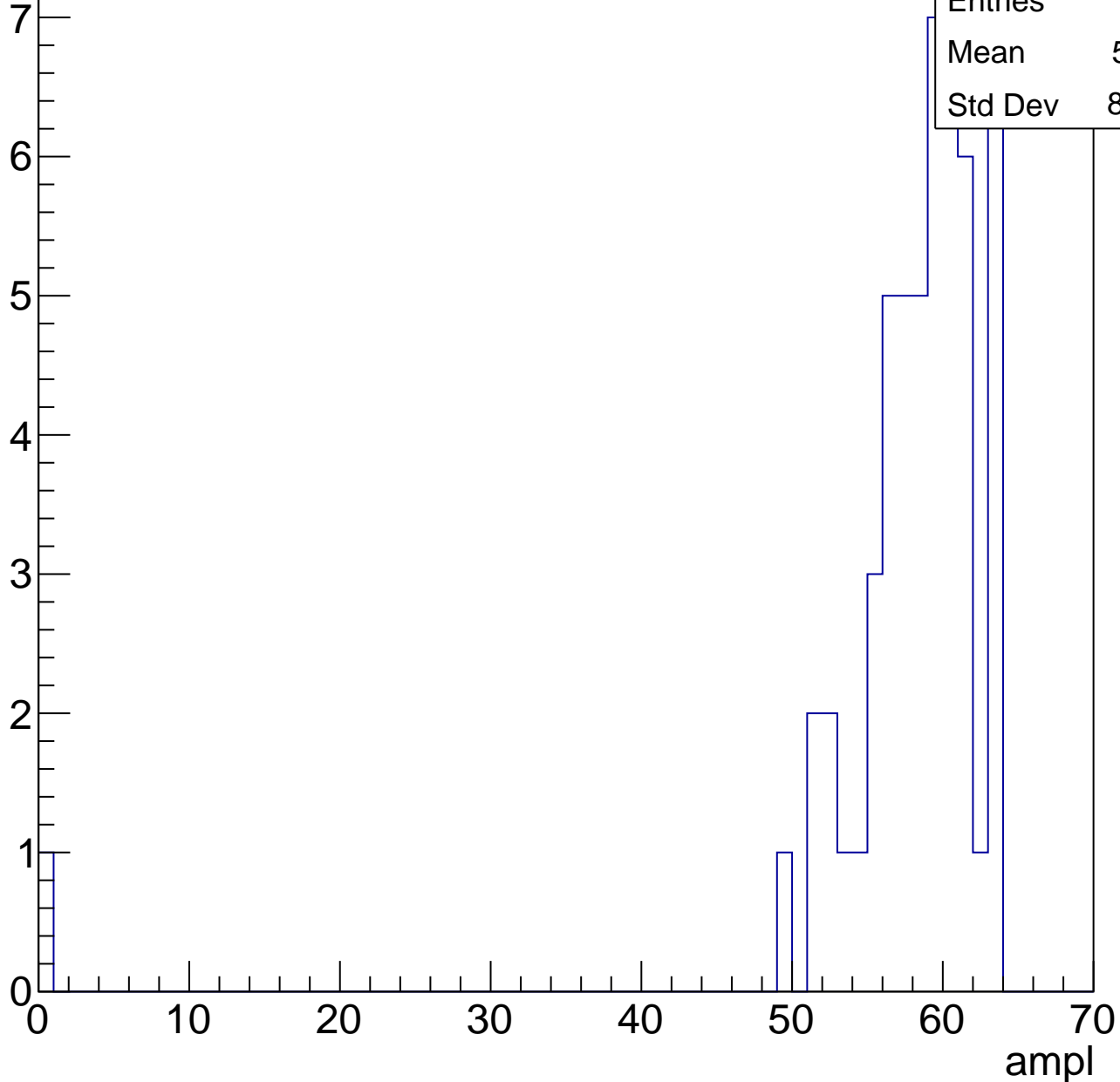
Entries	55
Mean	52.55
Std Dev	4.116



B1L103S, U20-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

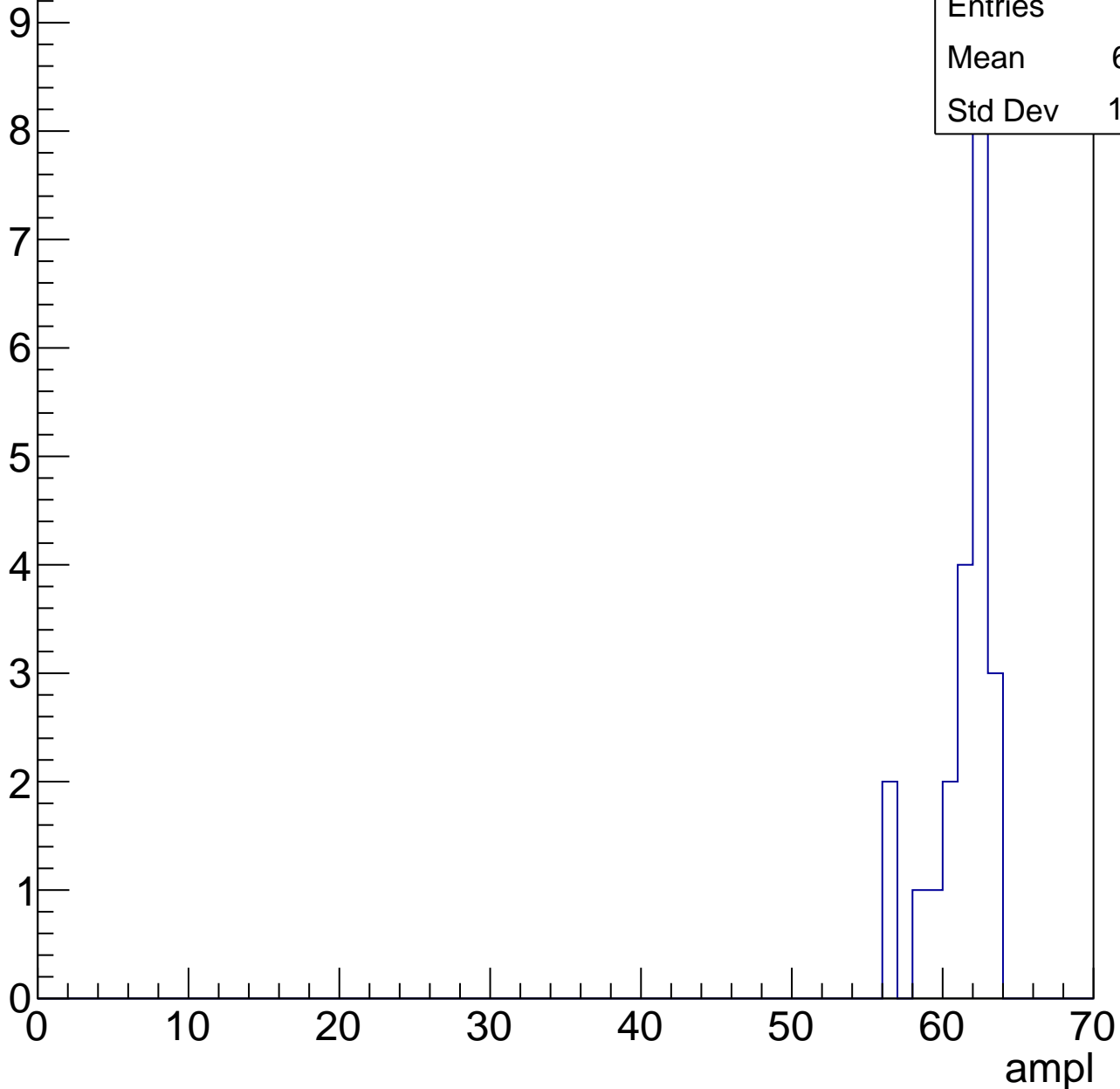


B1L103S, U20-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	22
Mean	60.91
Std Dev	1.975



B1L103S, U20-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch99, adc0

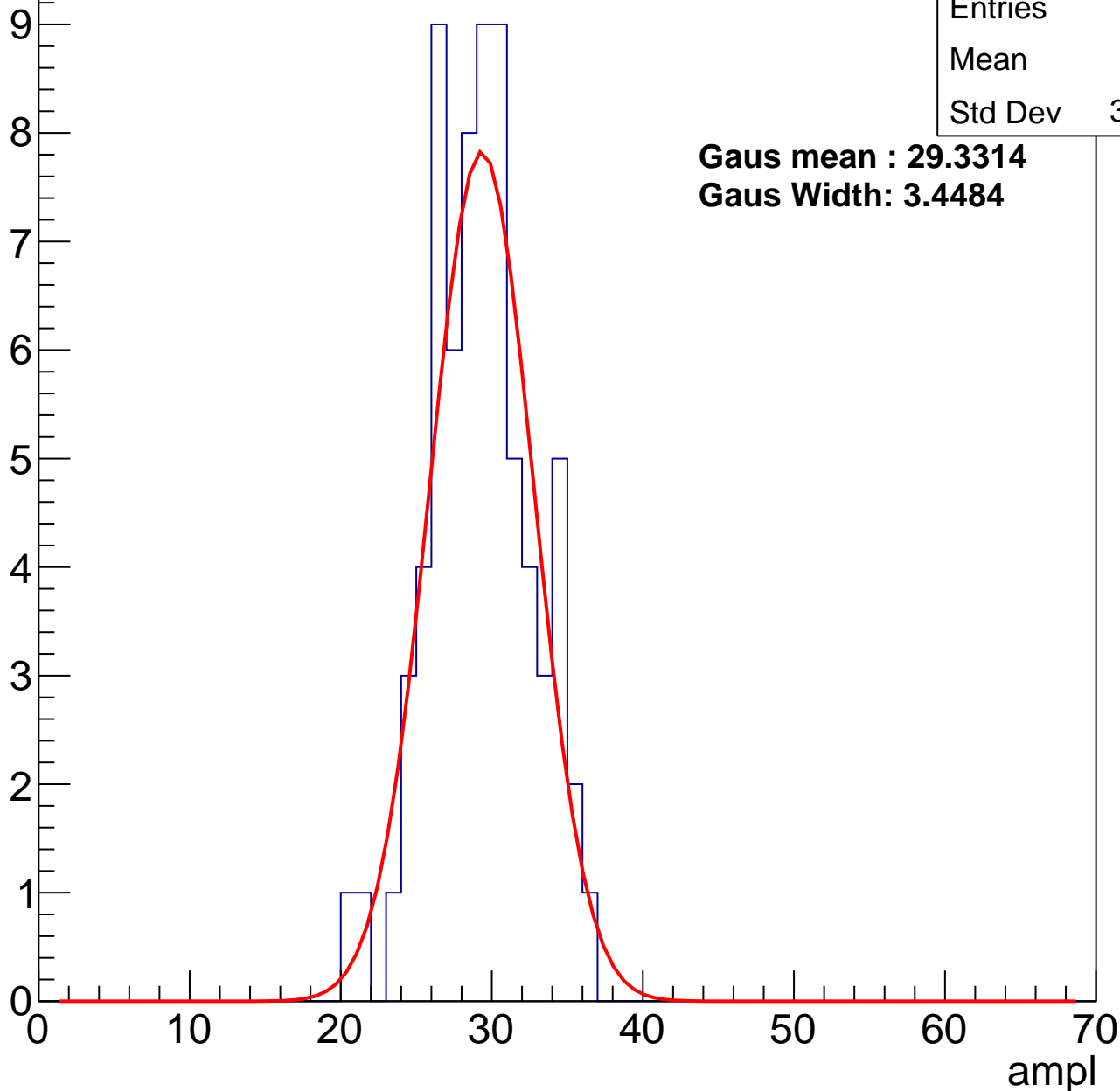
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.8
Std Dev	3.334

Gaus mean : 29.3314

Gaus Width: 3.4484



B1L103S, U20-ch99, adc1

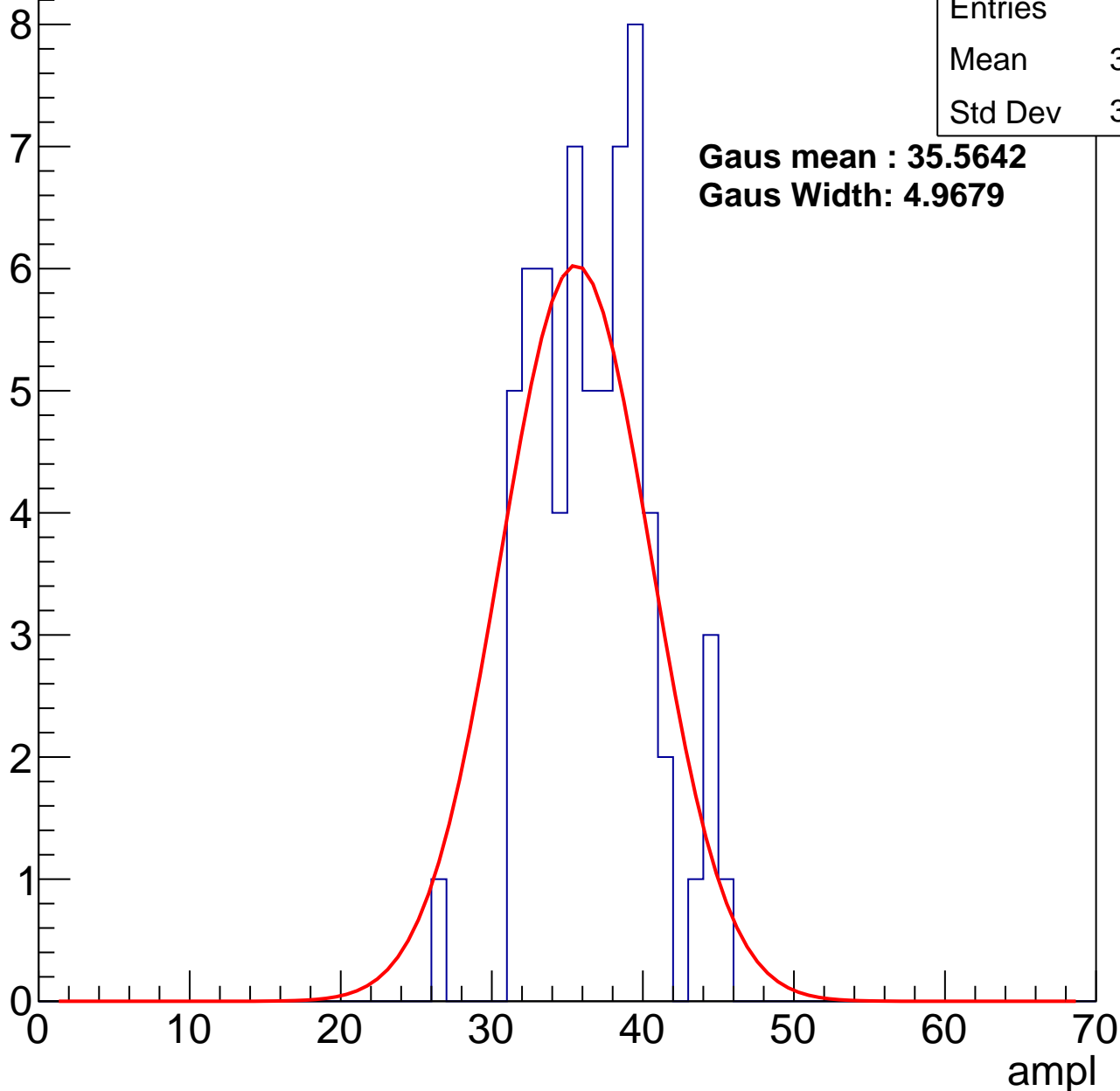
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.26
Std Dev	3.792

Gaus mean : 35.5642

Gaus Width: 4.9679



B1L103S, U20-ch99, adc2

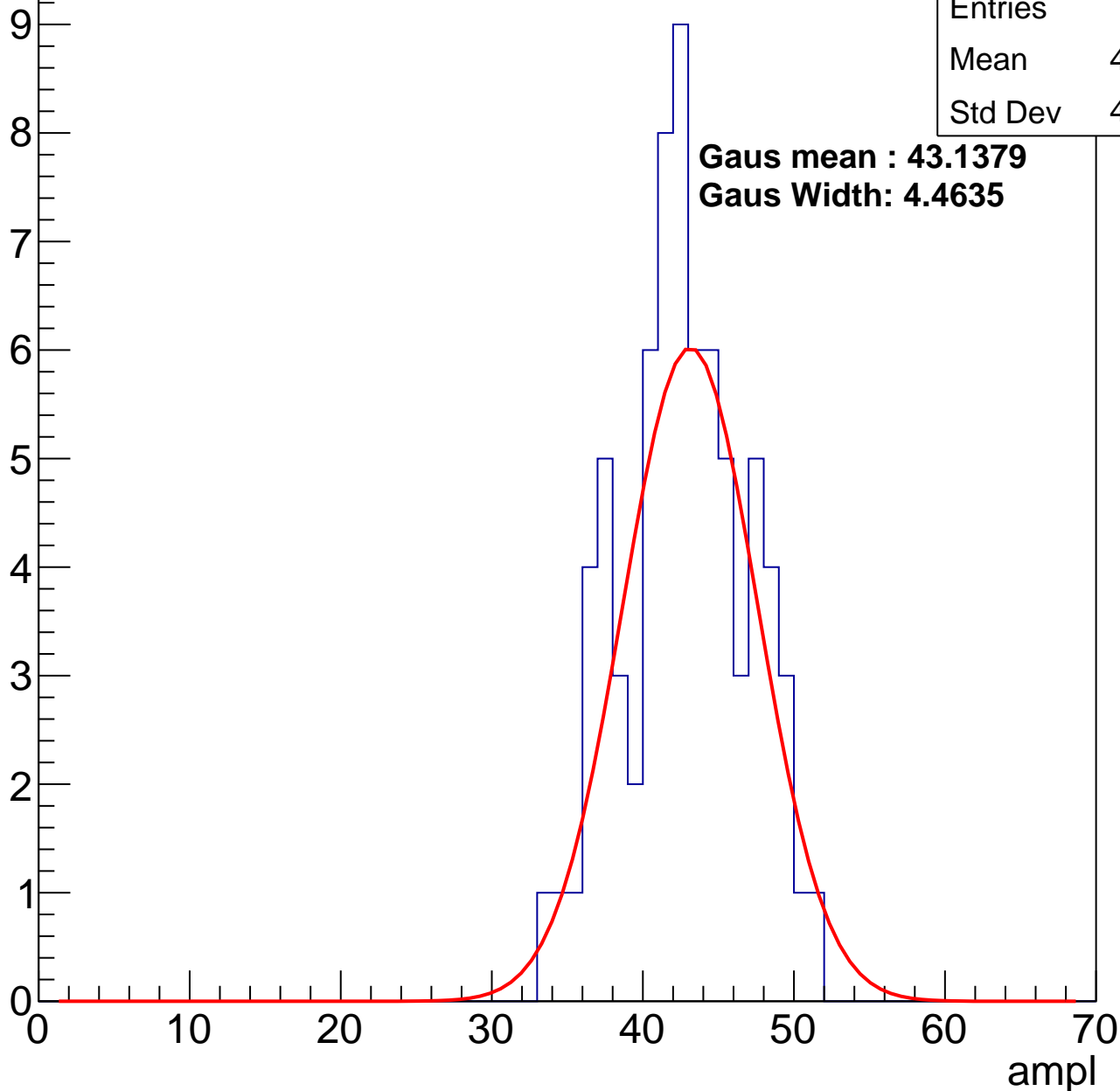
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.28
Std Dev	4.092

Gaus mean : 43.1379

Gaus Width: 4.4635

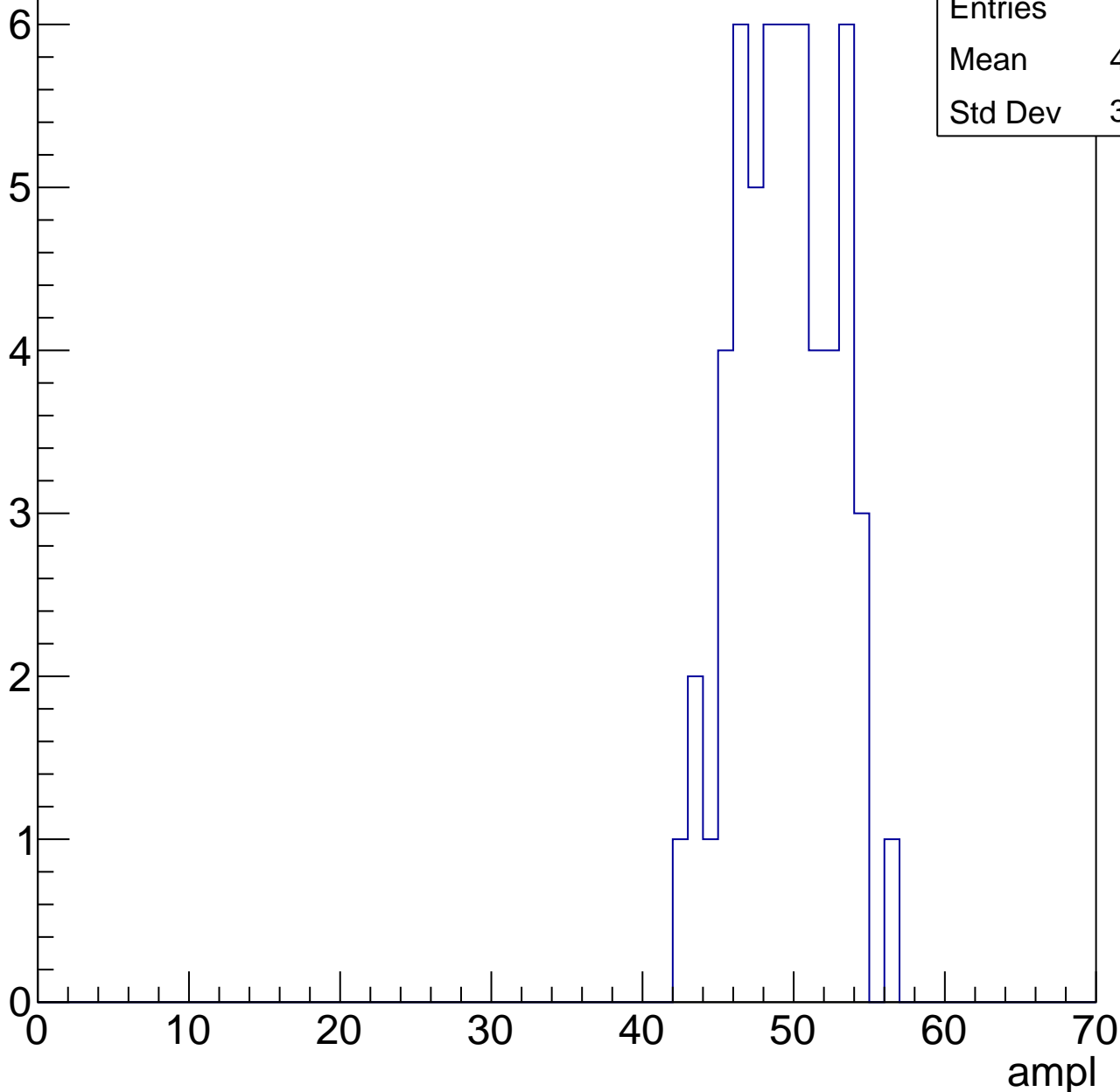


B1L103S, U20-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

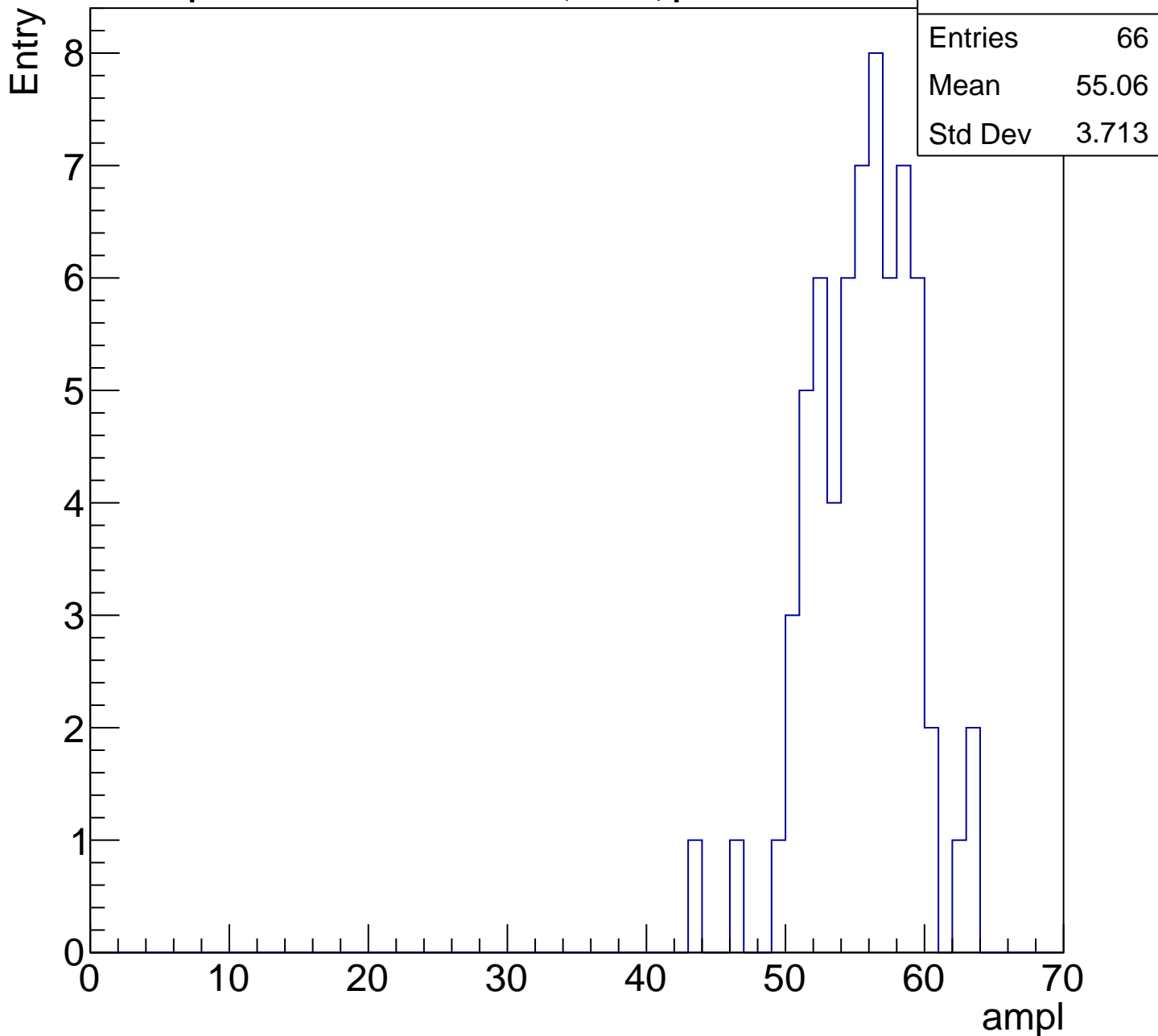
Entry

Entries	55
Mean	48.96
Std Dev	3.213



B1L103S, U20-ch99, adc4

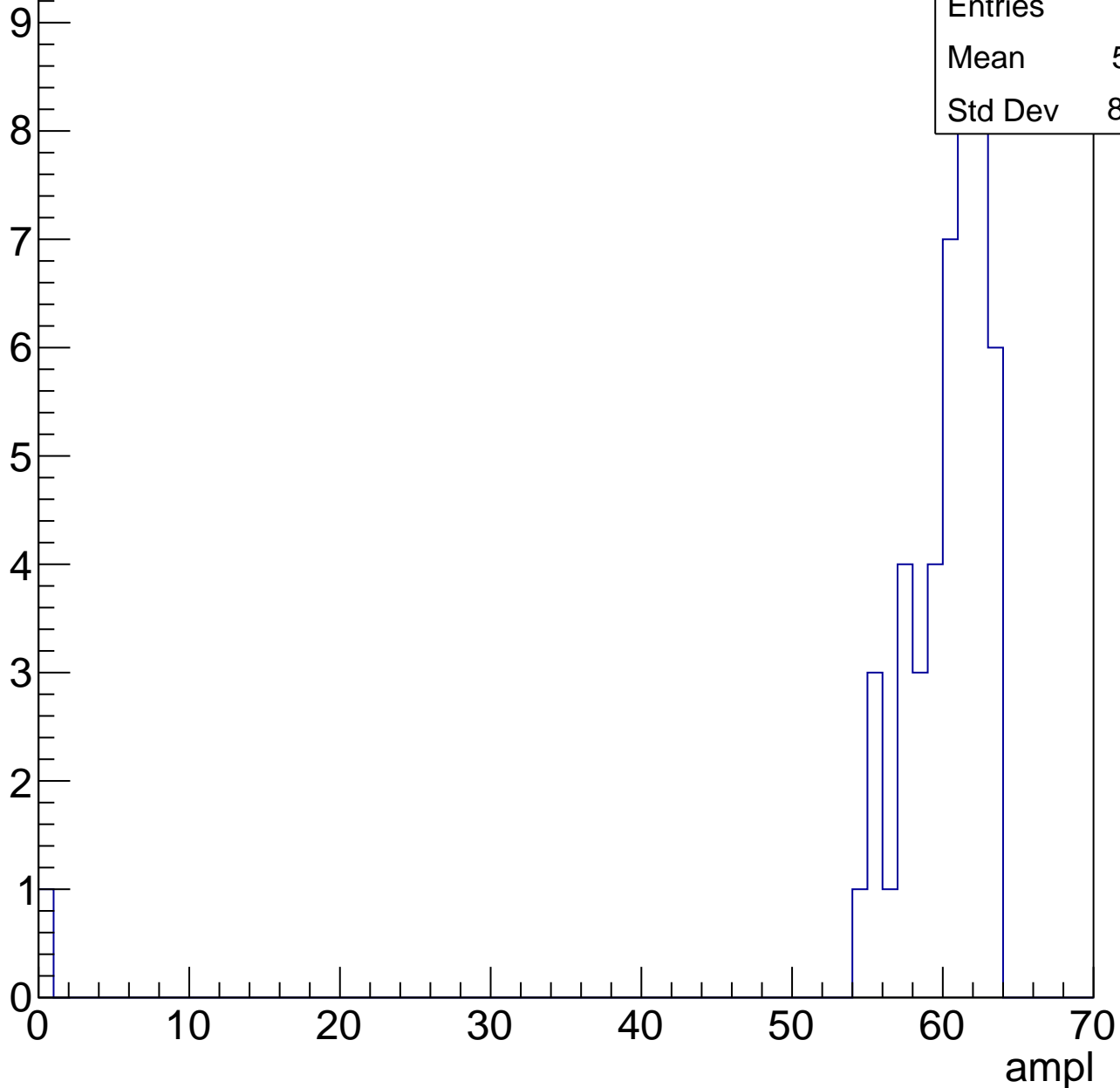
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U20-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch100, adc0

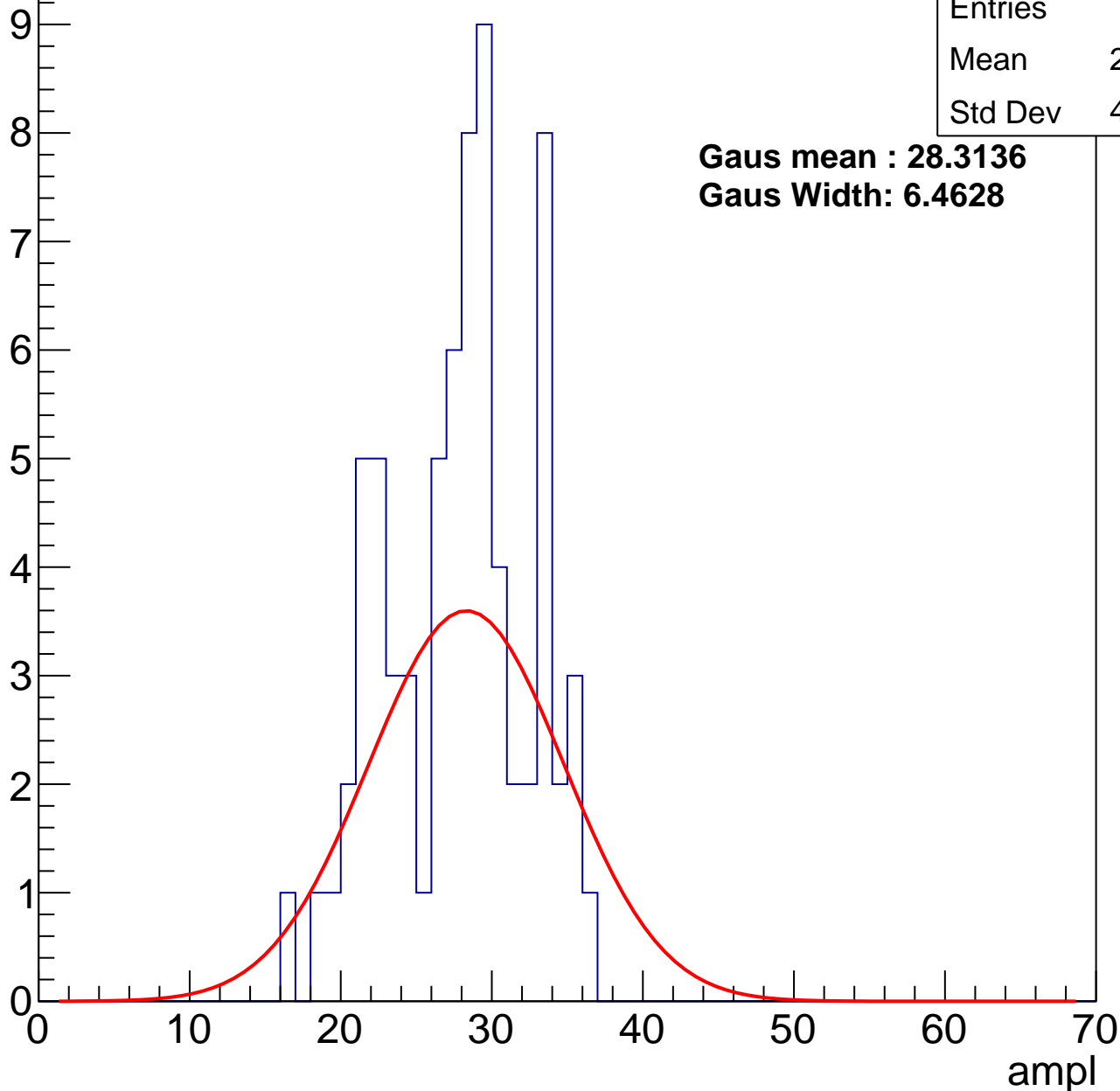
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.36
Std Dev	4.662

Gaus mean : 28.3136

Gaus Width: 6.4628



B1L103S, U20-ch100, adc1

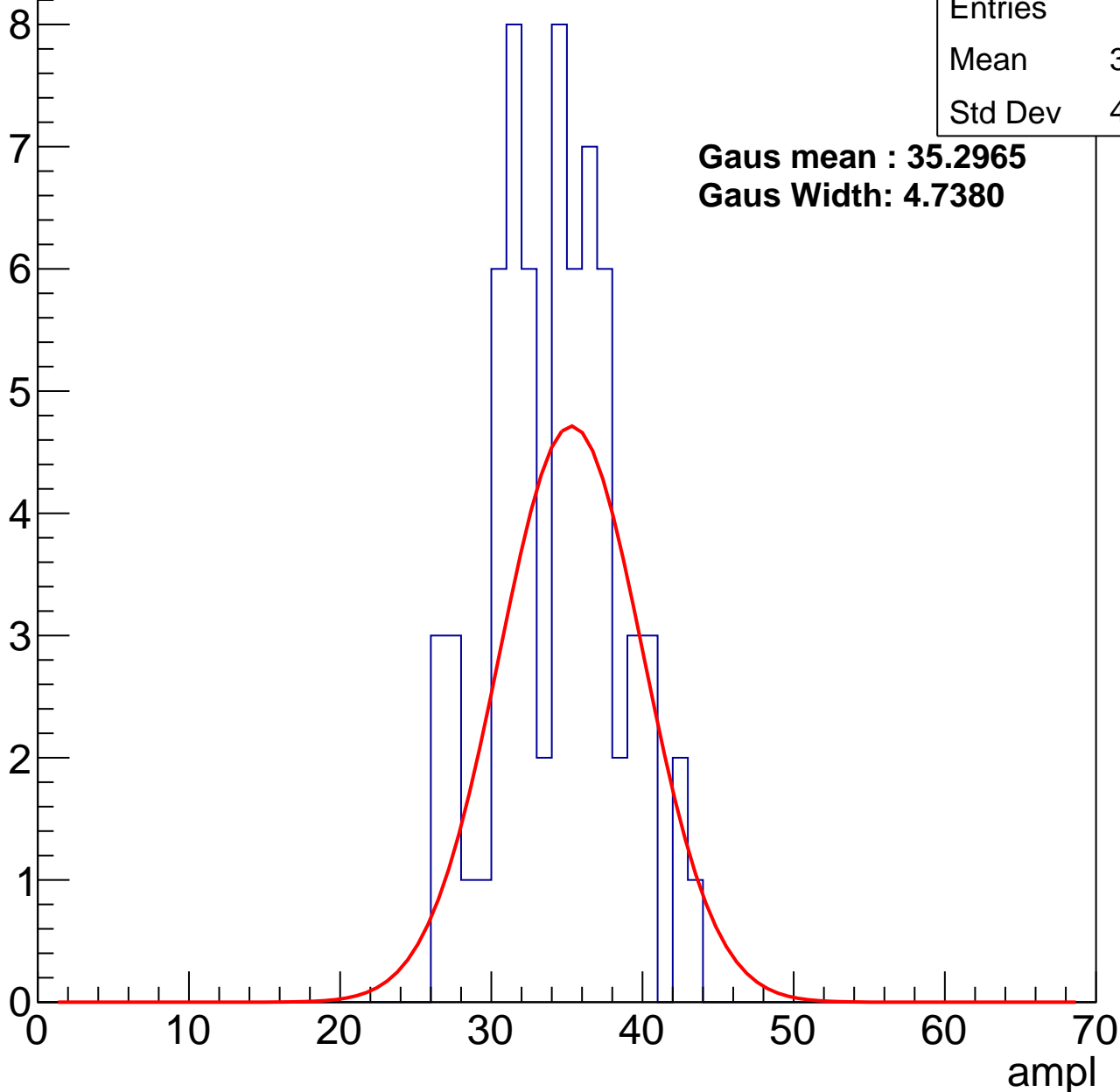
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	33.79
Std Dev	4.028

Gaus mean : 35.2965

Gaus Width: 4.7380



B1L103S, U20-ch100, adc2

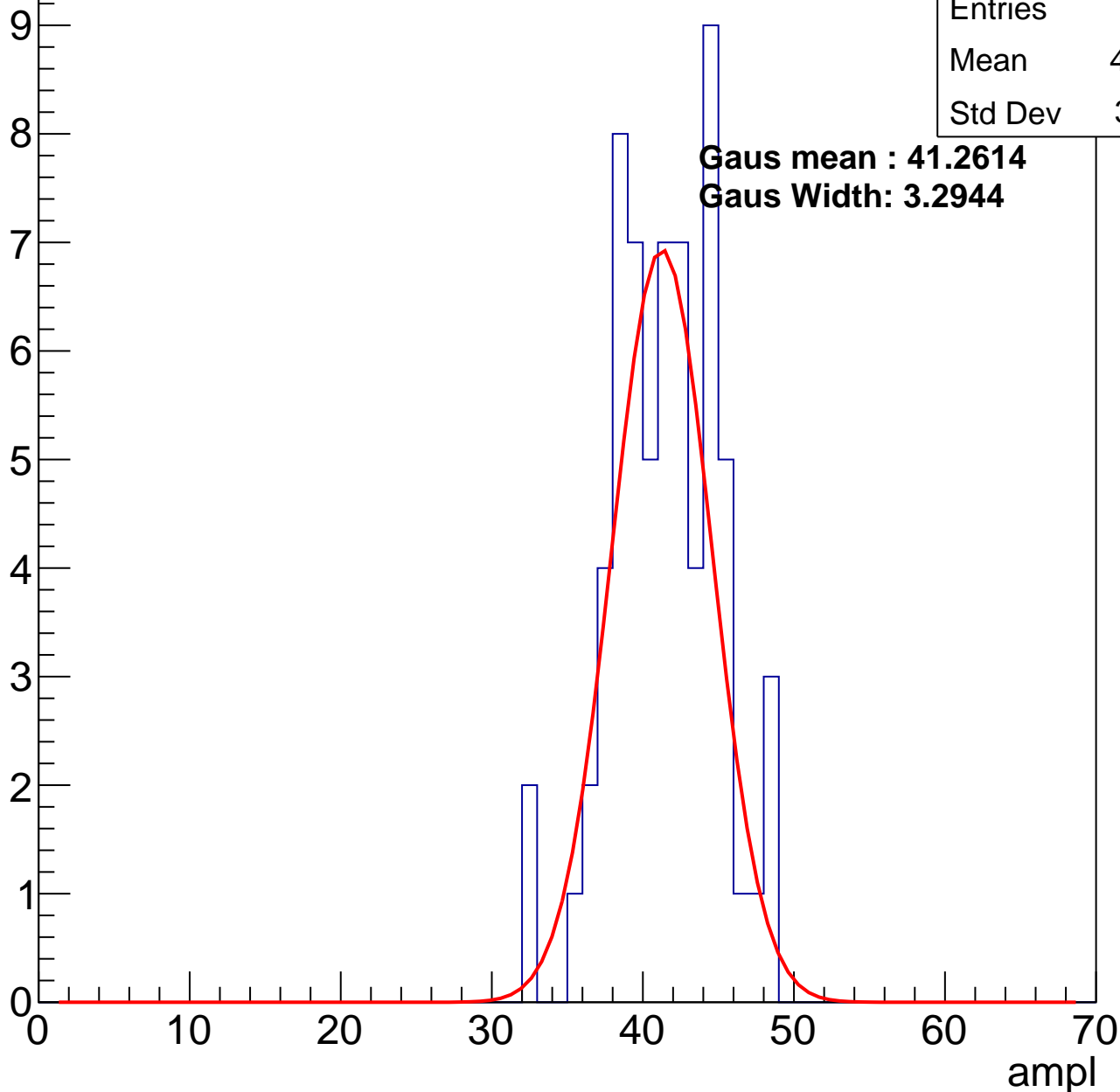
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.02
Std Dev	3.501

Gaus mean : 41.2614

Gaus Width: 3.2944

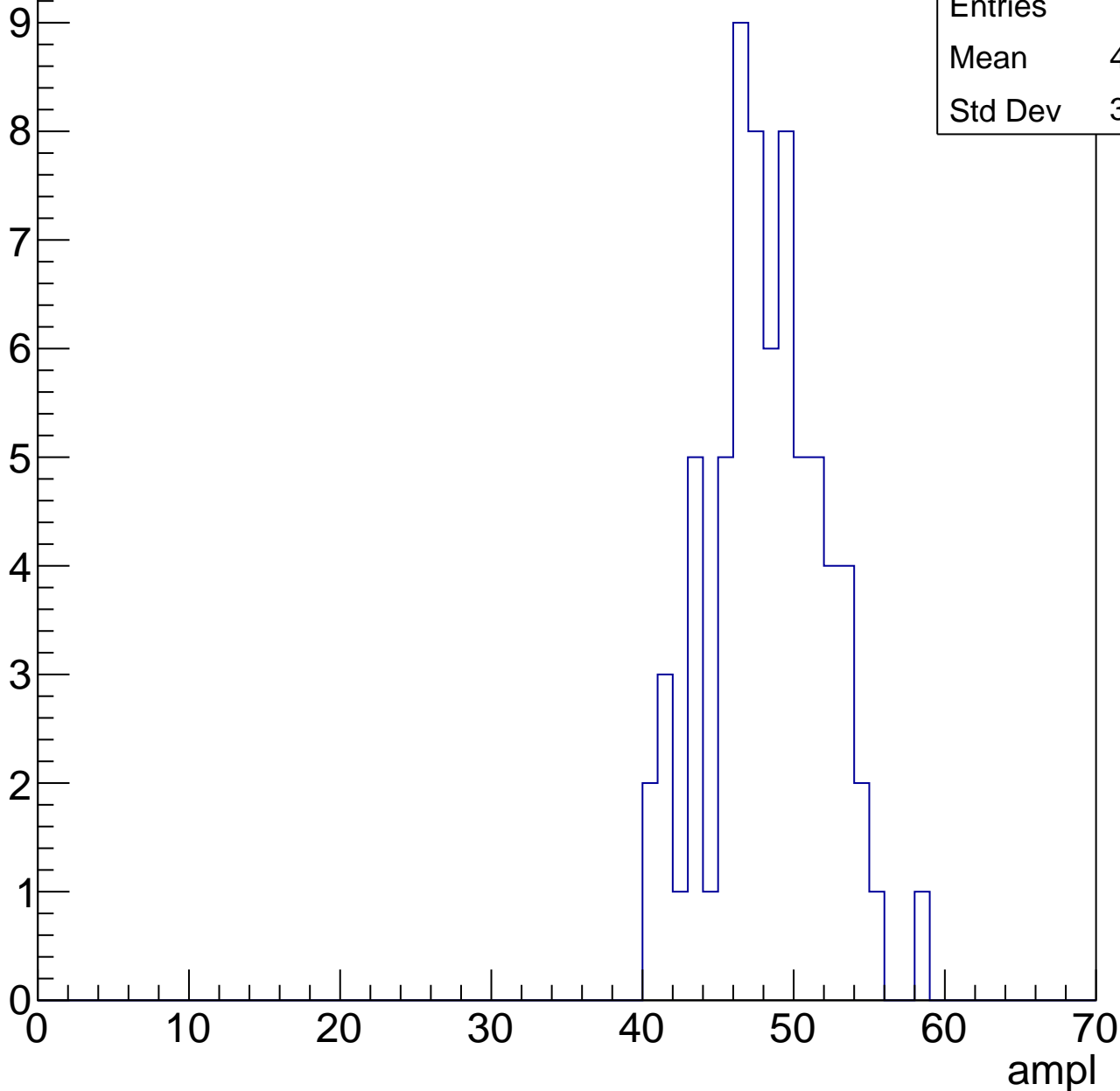


B1L103S, U20-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	47.79
Std Dev	3.772



B1L103S, U20-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

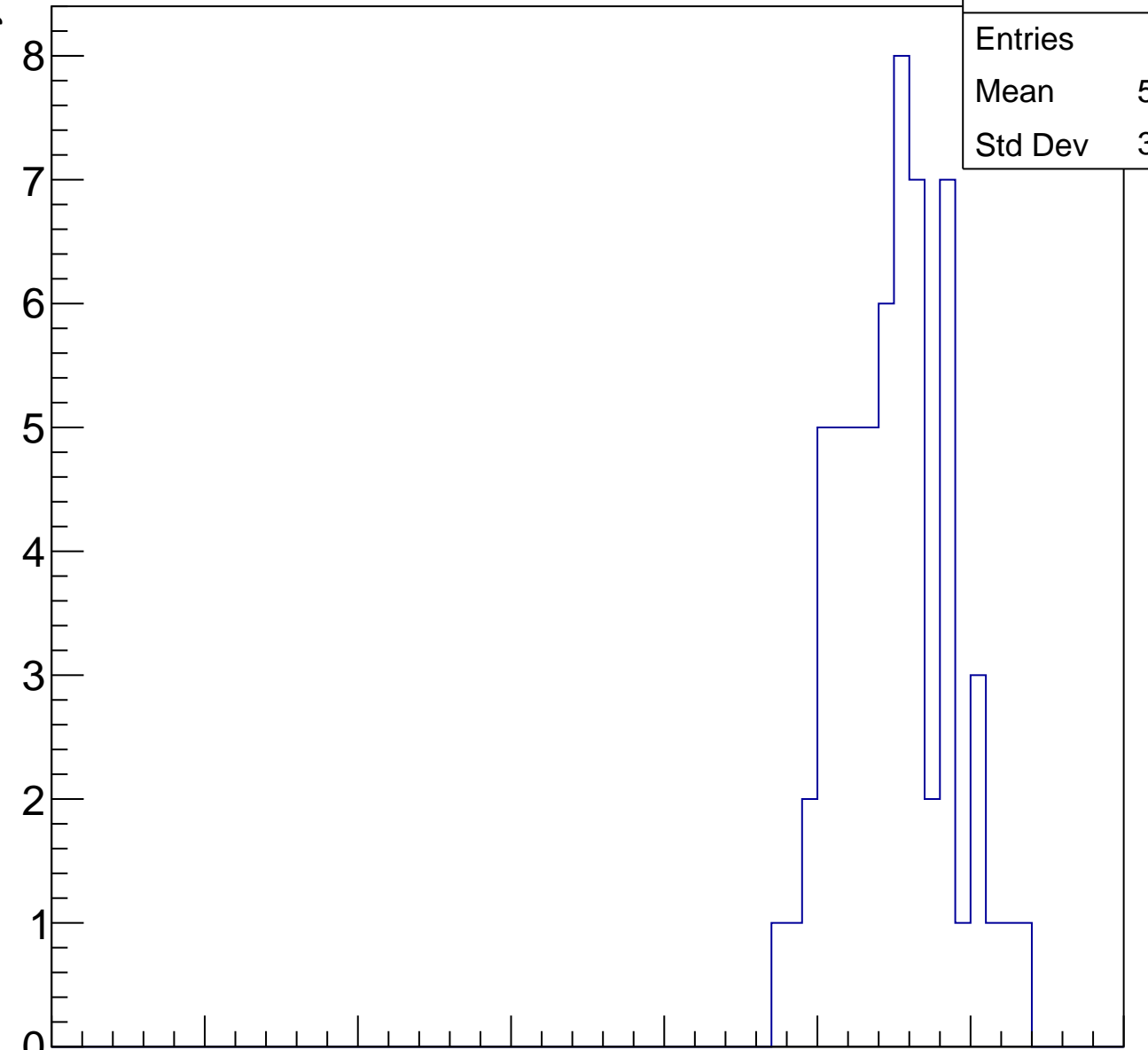
Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	54.49
Std Dev	3.537

ampl

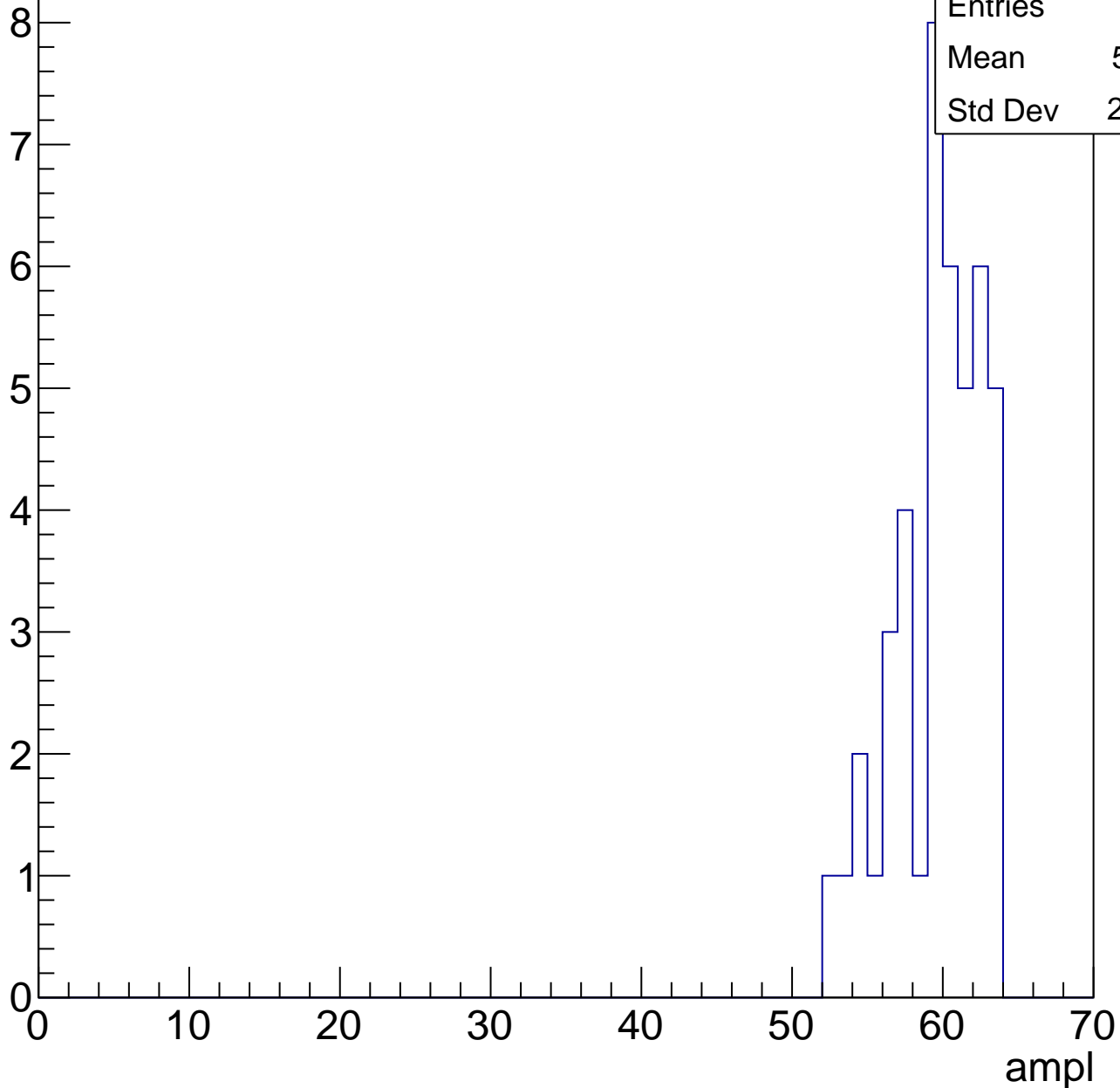
0 10 20 30 40 50 60 70



B1L103S, U20-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

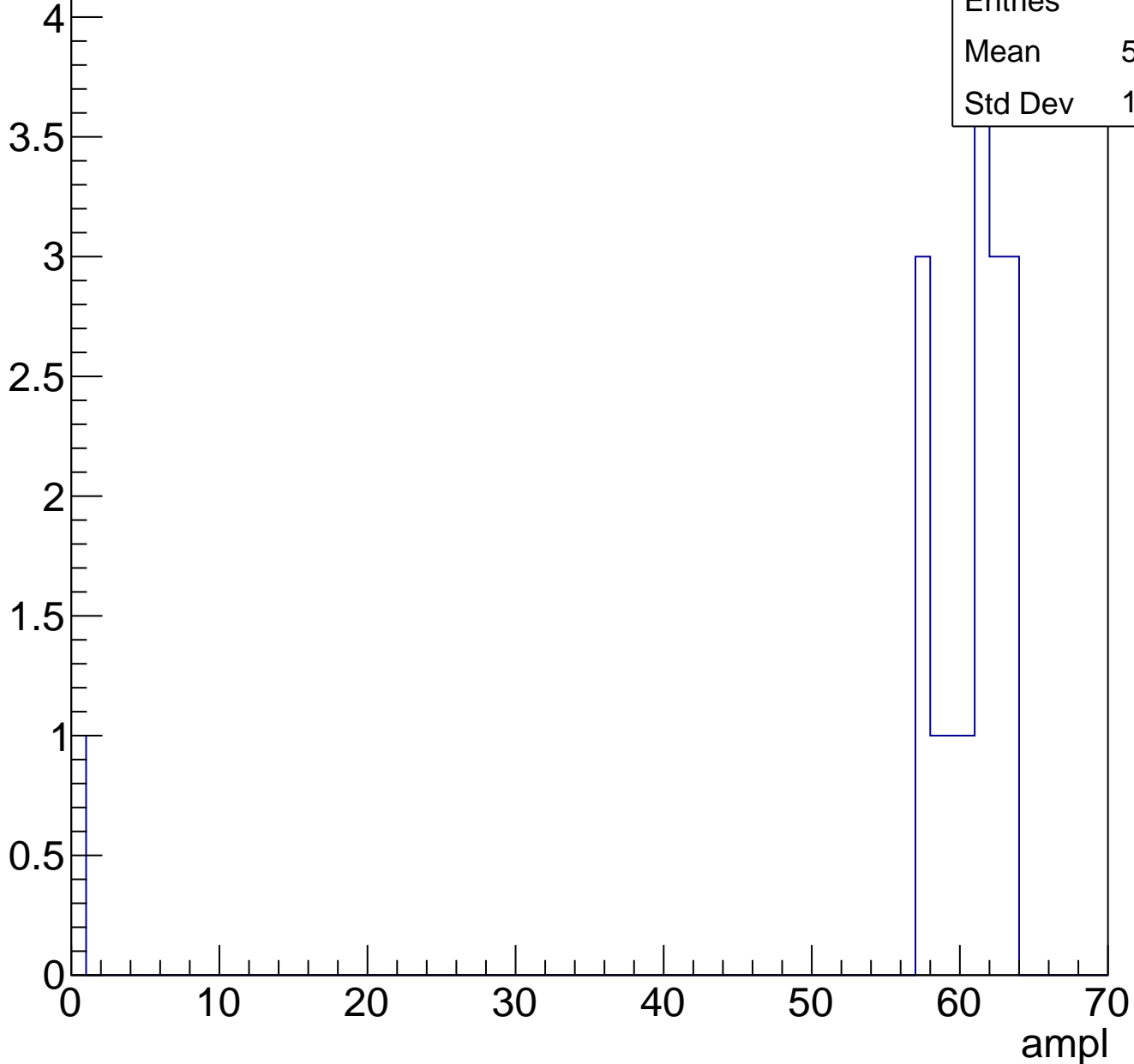
Entry



B1L103S, U20-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

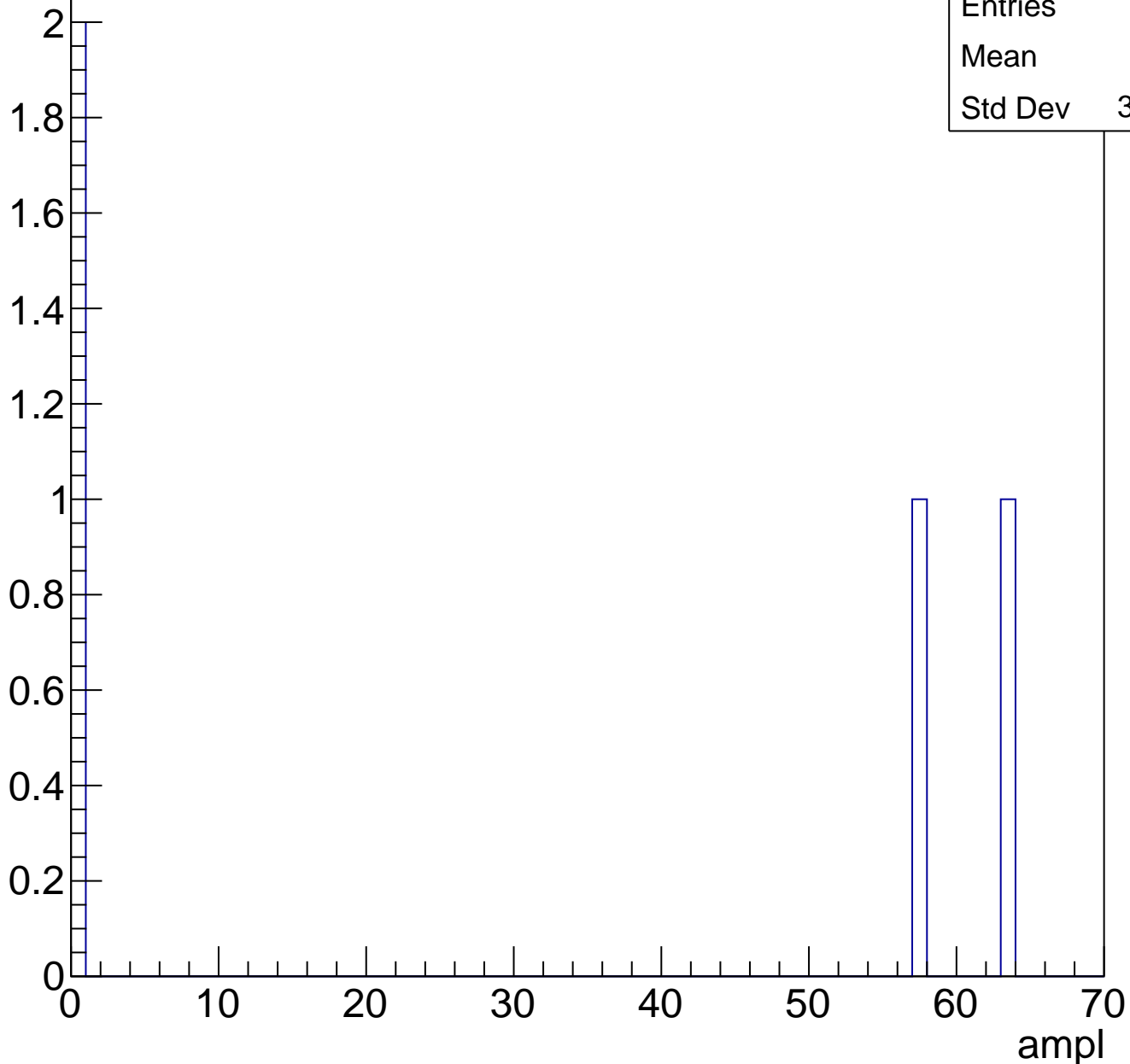


Entries	17
Mean	56.88
Std Dev	14.37

B1L103S, U20-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch101, adc0

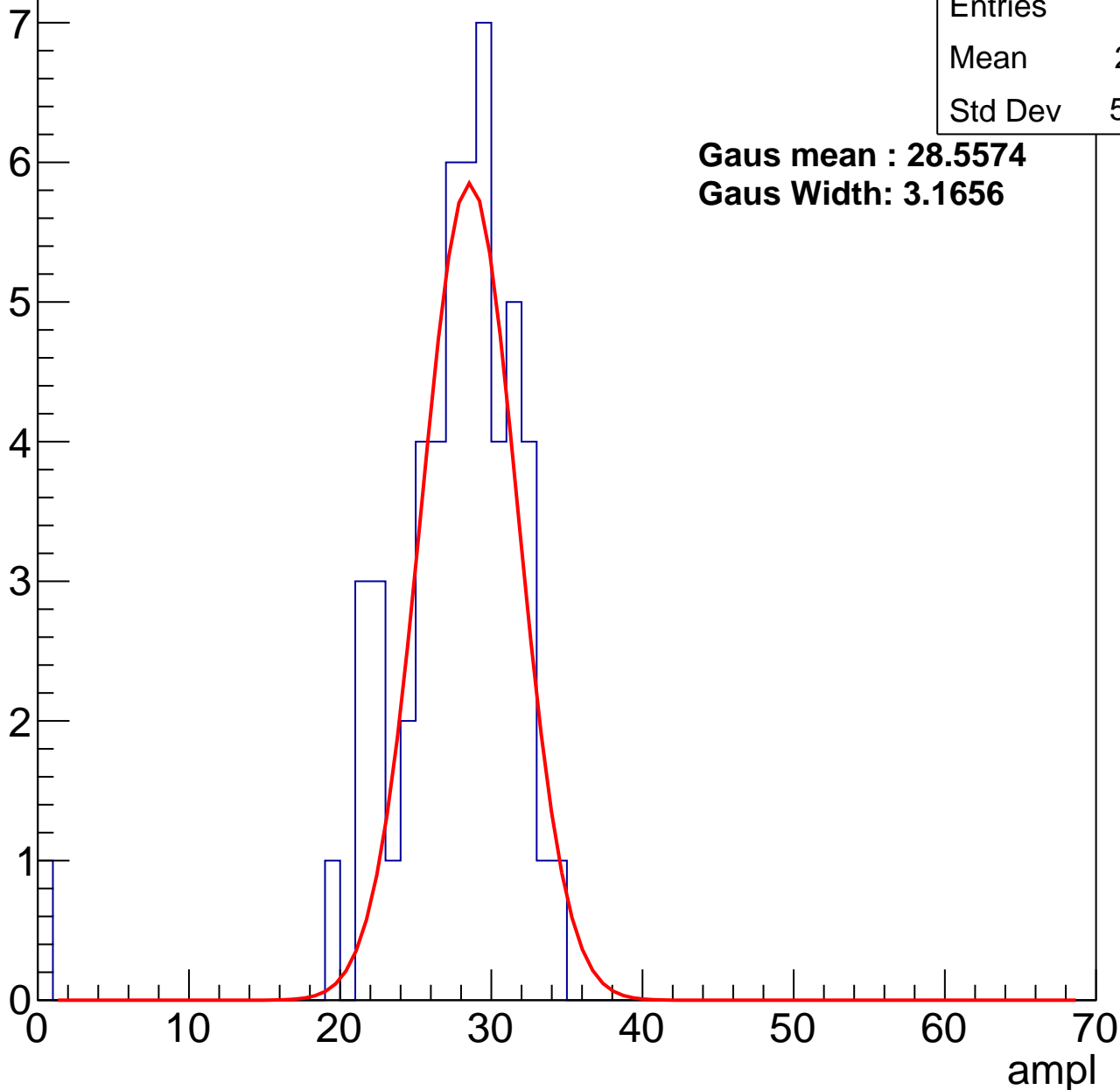
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	26.91
Std Dev	5.074

Gaus mean : 28.5574

Gaus Width: 3.1656



B1L103S, U20-ch101, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	35.13
Std Dev	4.358

Gaus mean : 35.6093

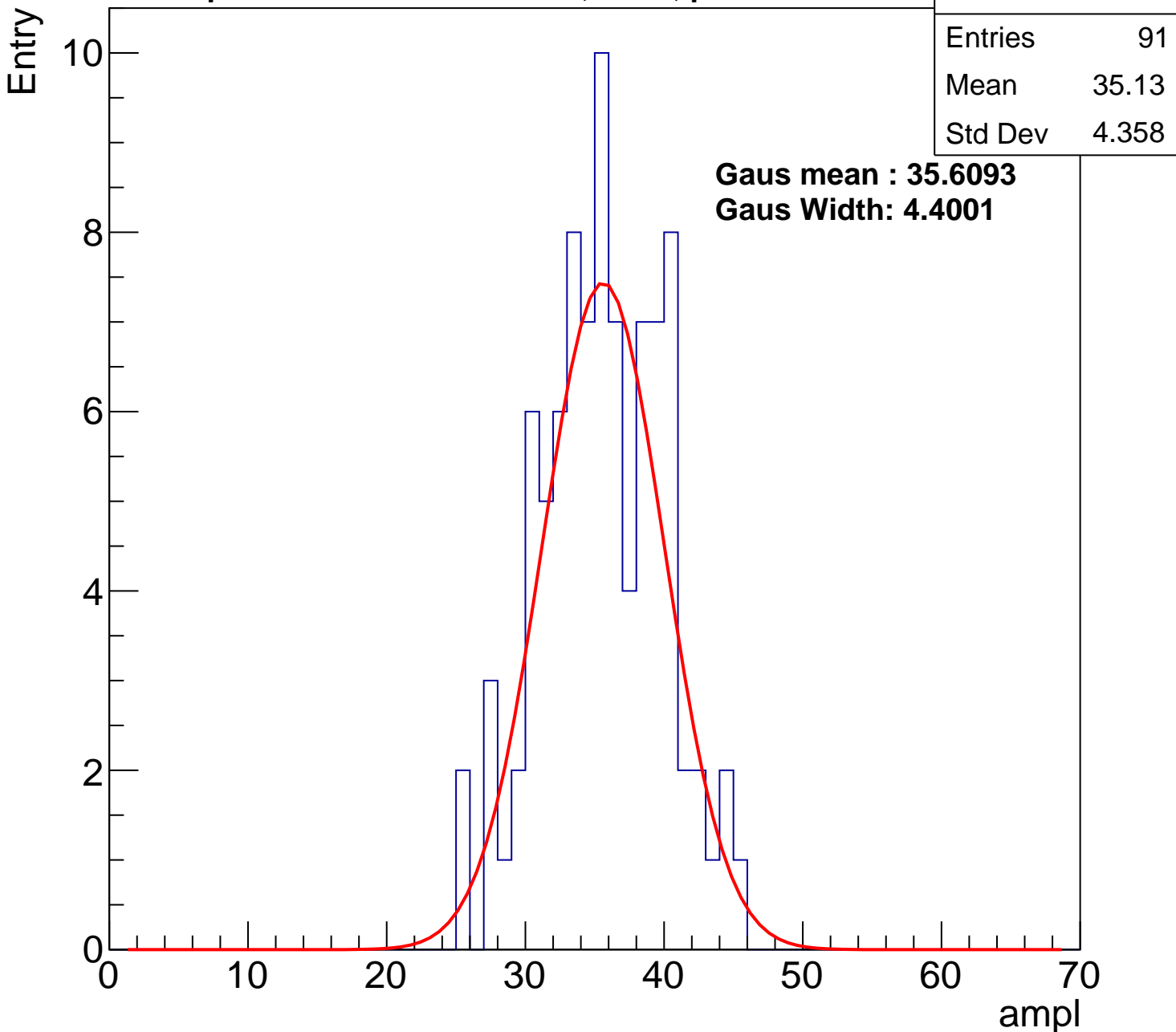
Gaus Width: 4.4001

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch101, adc2

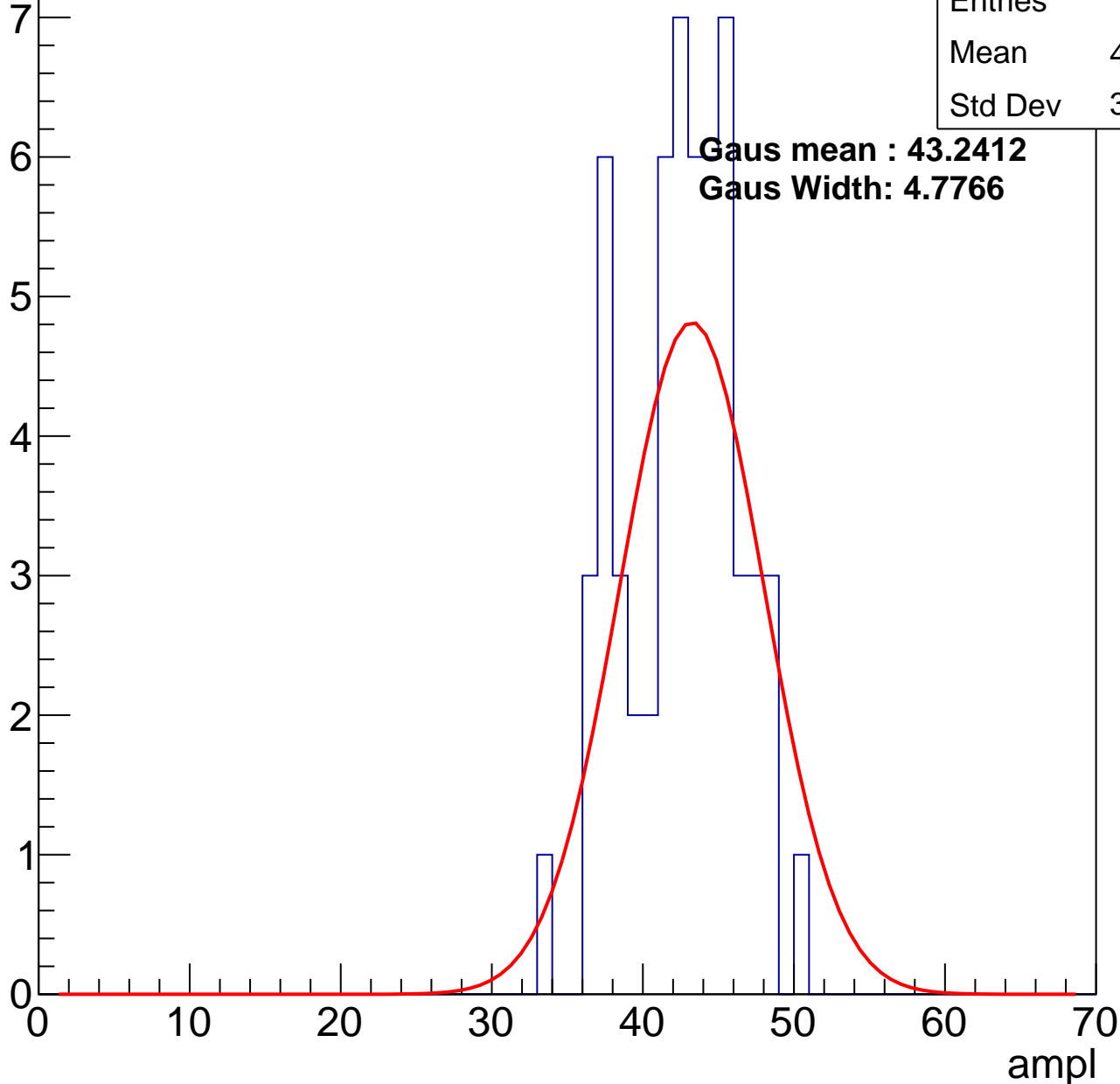
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.12
Std Dev	3.696

Gaus mean : 43.2412

Gaus Width: 4.7766

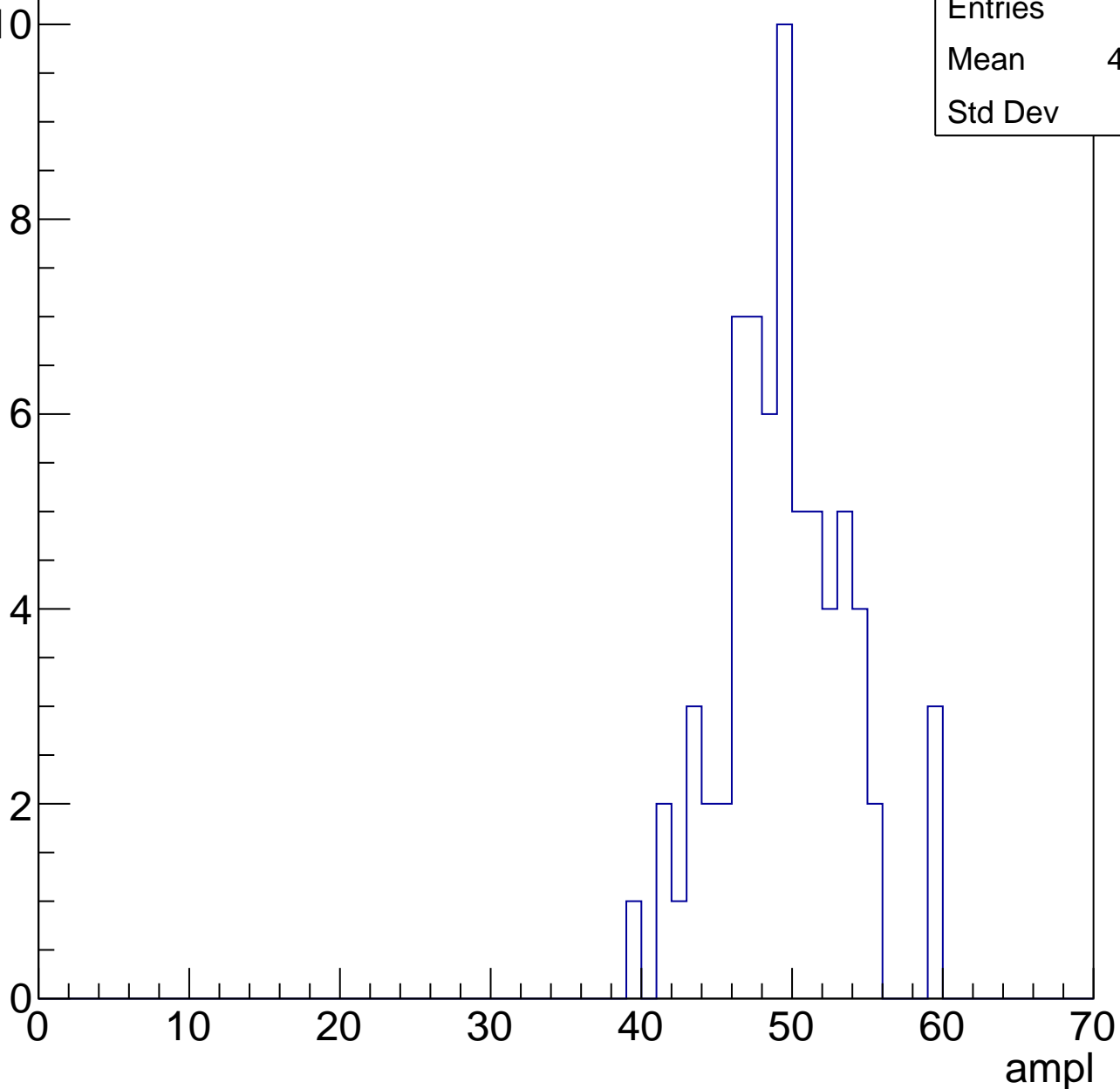


B1L103S, U20-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.99
Std Dev	4.13

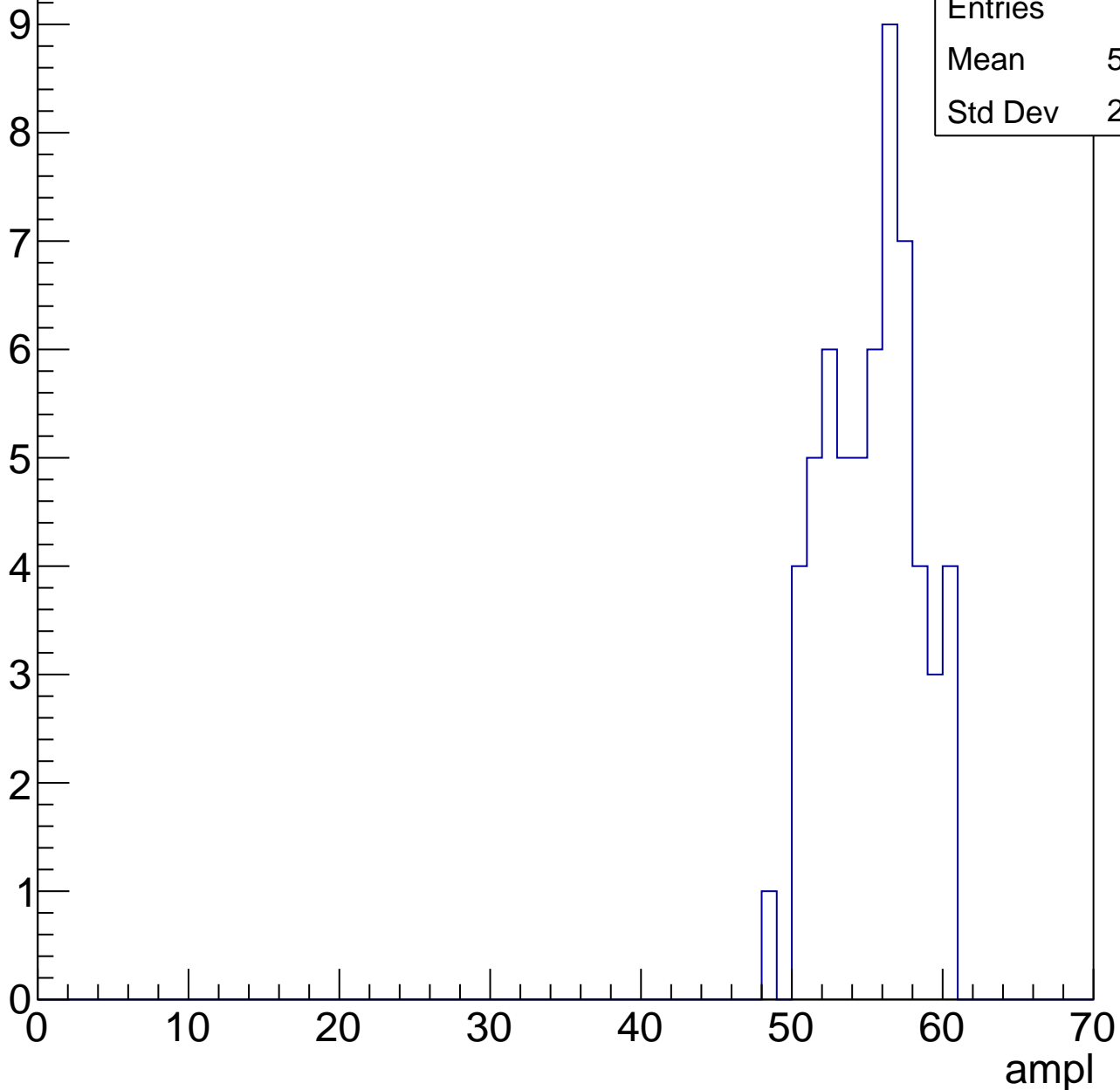


B1L103S, U20-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

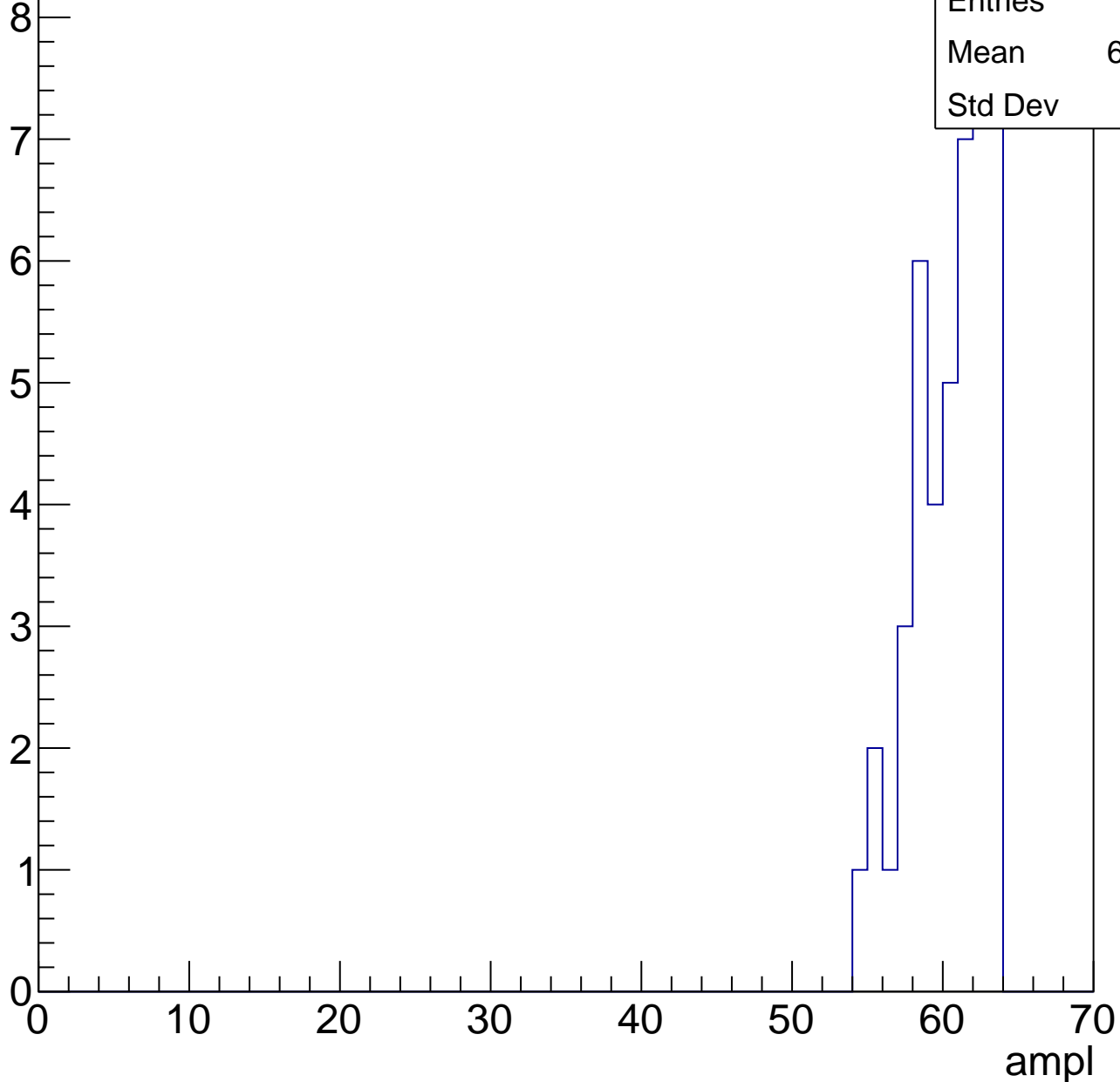
Entries	59
Mean	54.78
Std Dev	2.986



B1L103S, U20-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

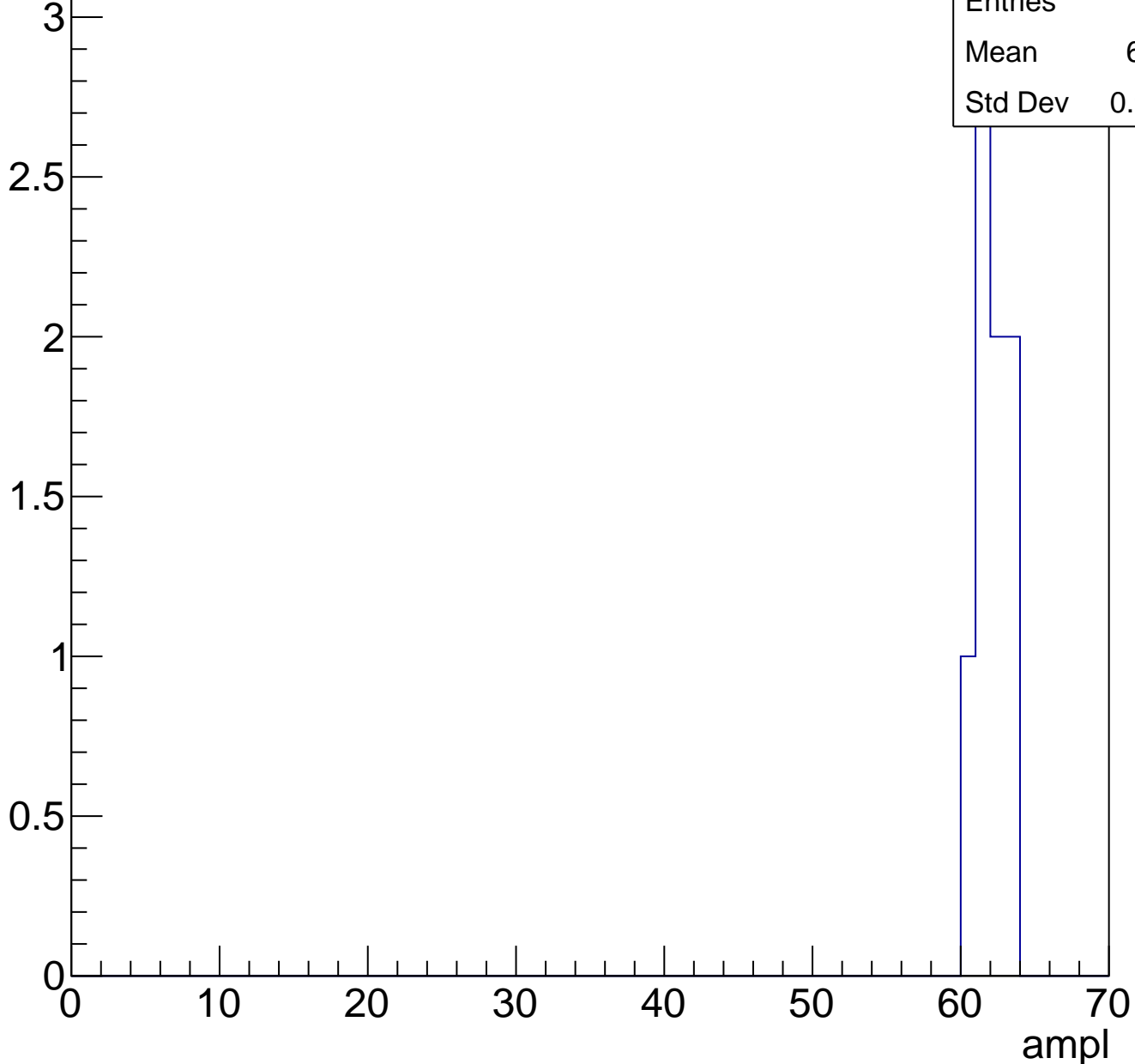


Entries	45
Mean	60.04
Std Dev	2.44

B1L103S, U20-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	8
Mean	61.62
Std Dev	0.9922

B1L103S, U20-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch102, adc0

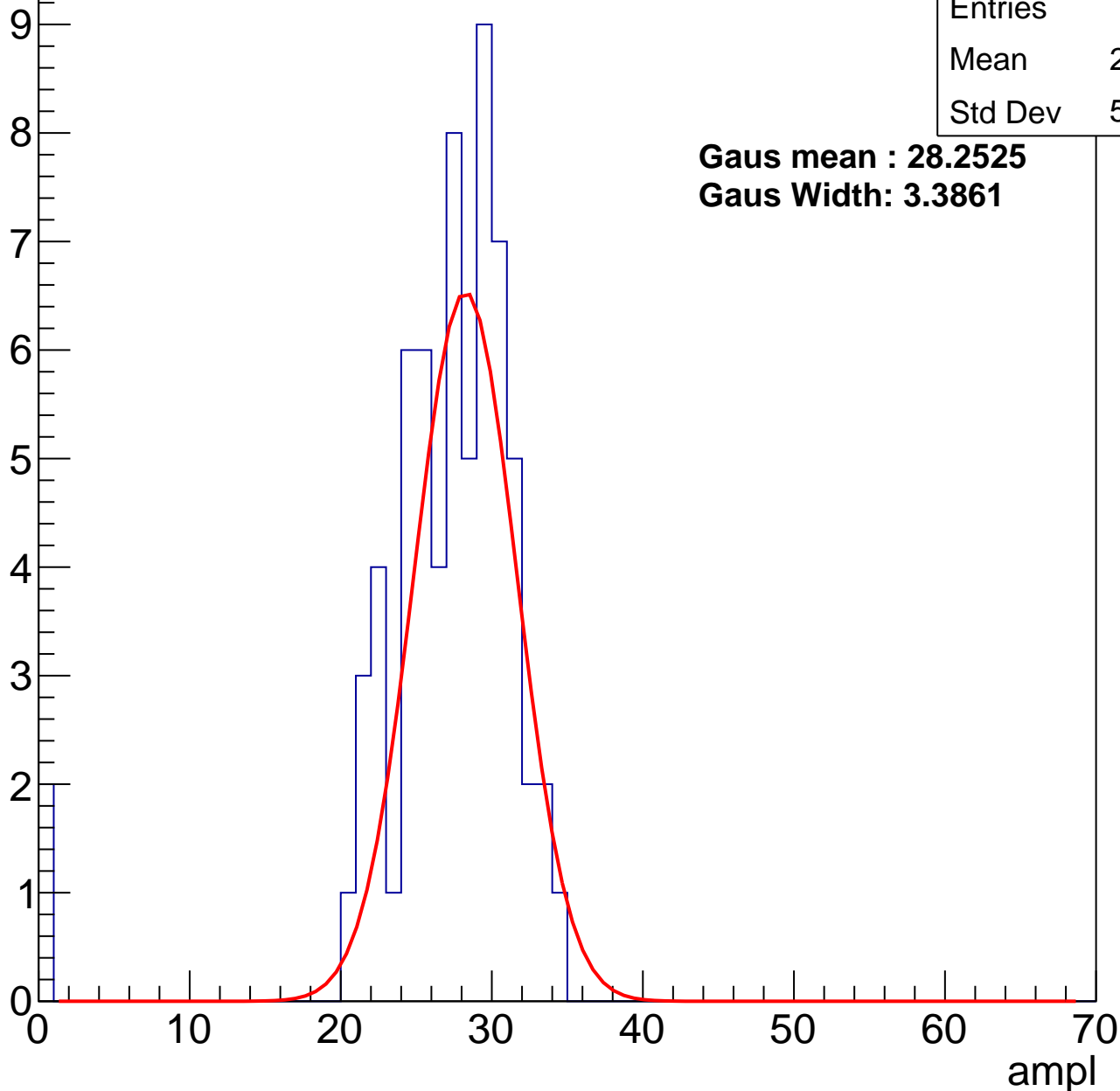
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	26.33
Std Dev	5.692

Gaus mean : 28.2525

Gaus Width: 3.3861



B1L103S, U20-ch102, adc1

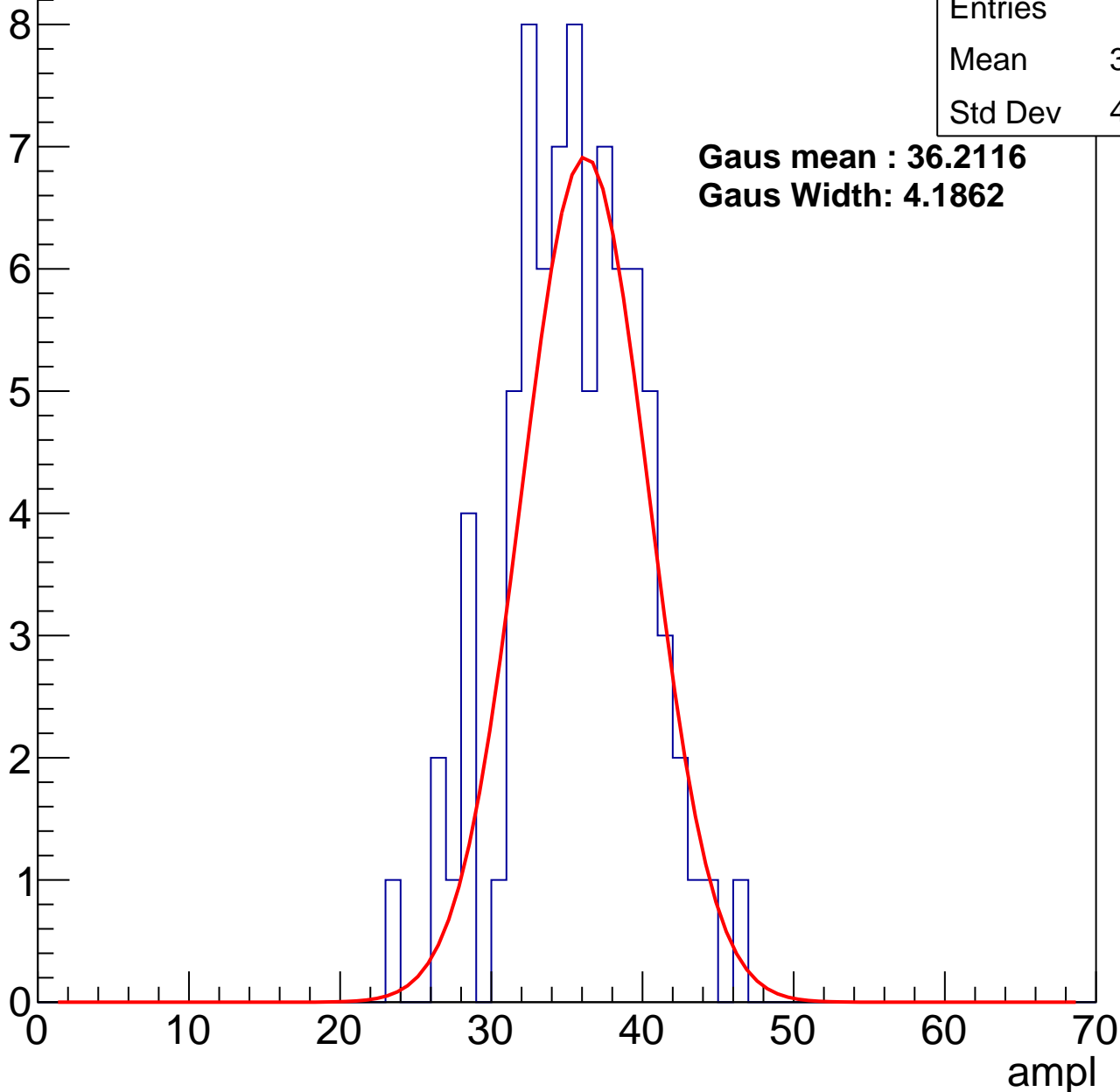
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.15
Std Dev	4.405

Gaus mean : 36.2116

Gaus Width: 4.1862



B1L103S, U20-ch102, adc2

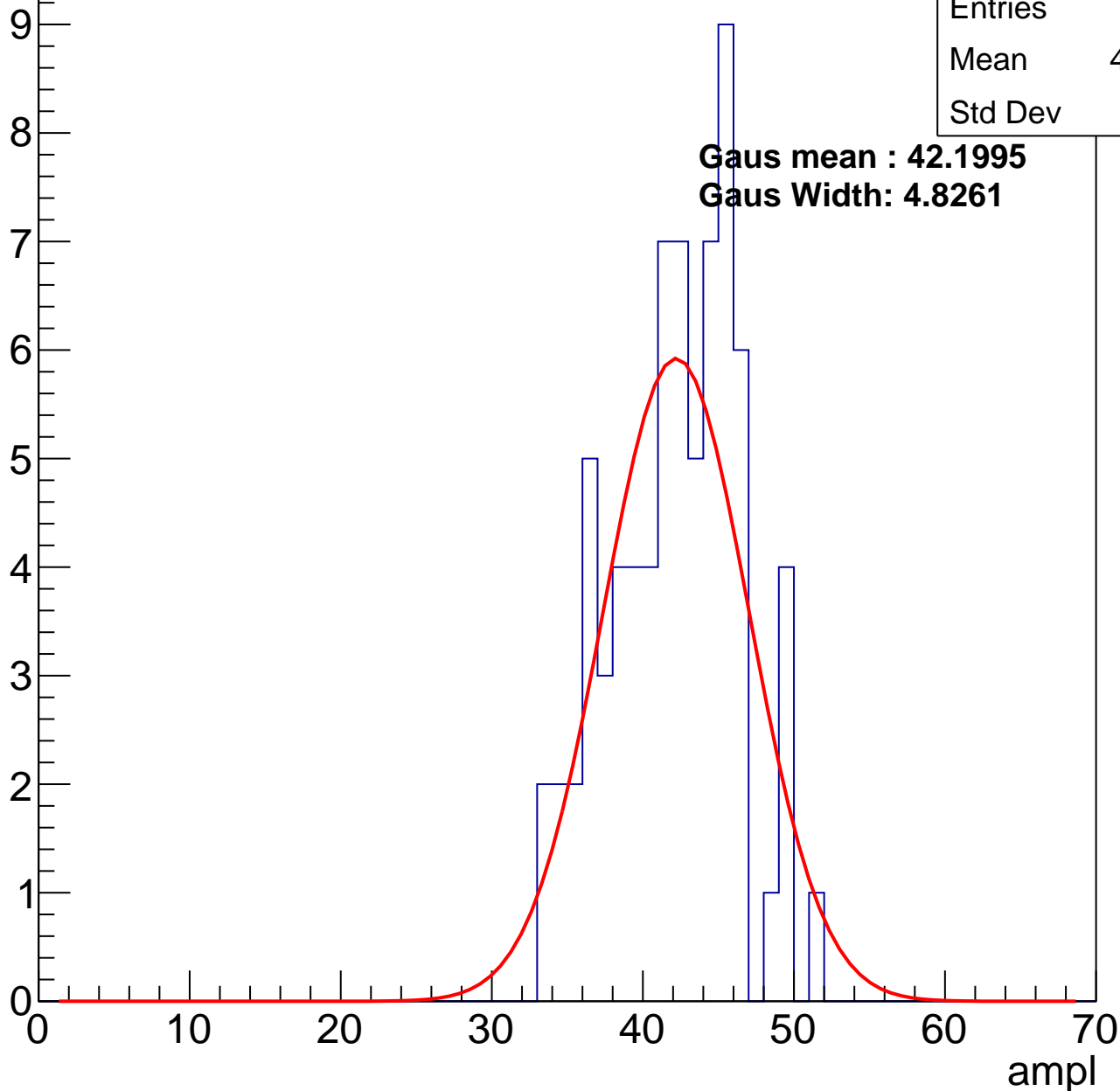
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	41.68
Std Dev	4.19

Gaus mean : 42.1995

Gaus Width: 4.8261

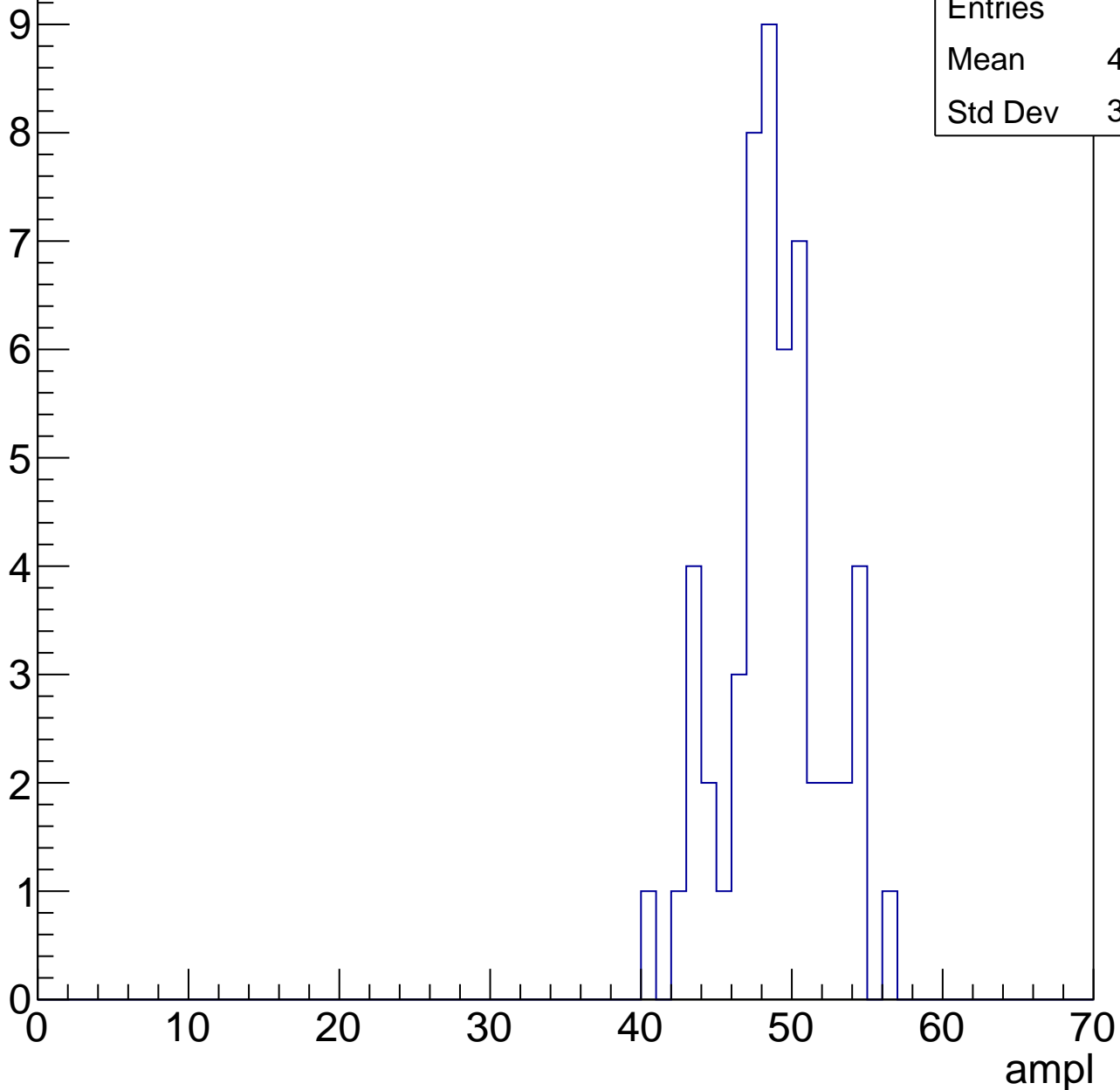


B1L103S, U20-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.32
Std Dev	3.358

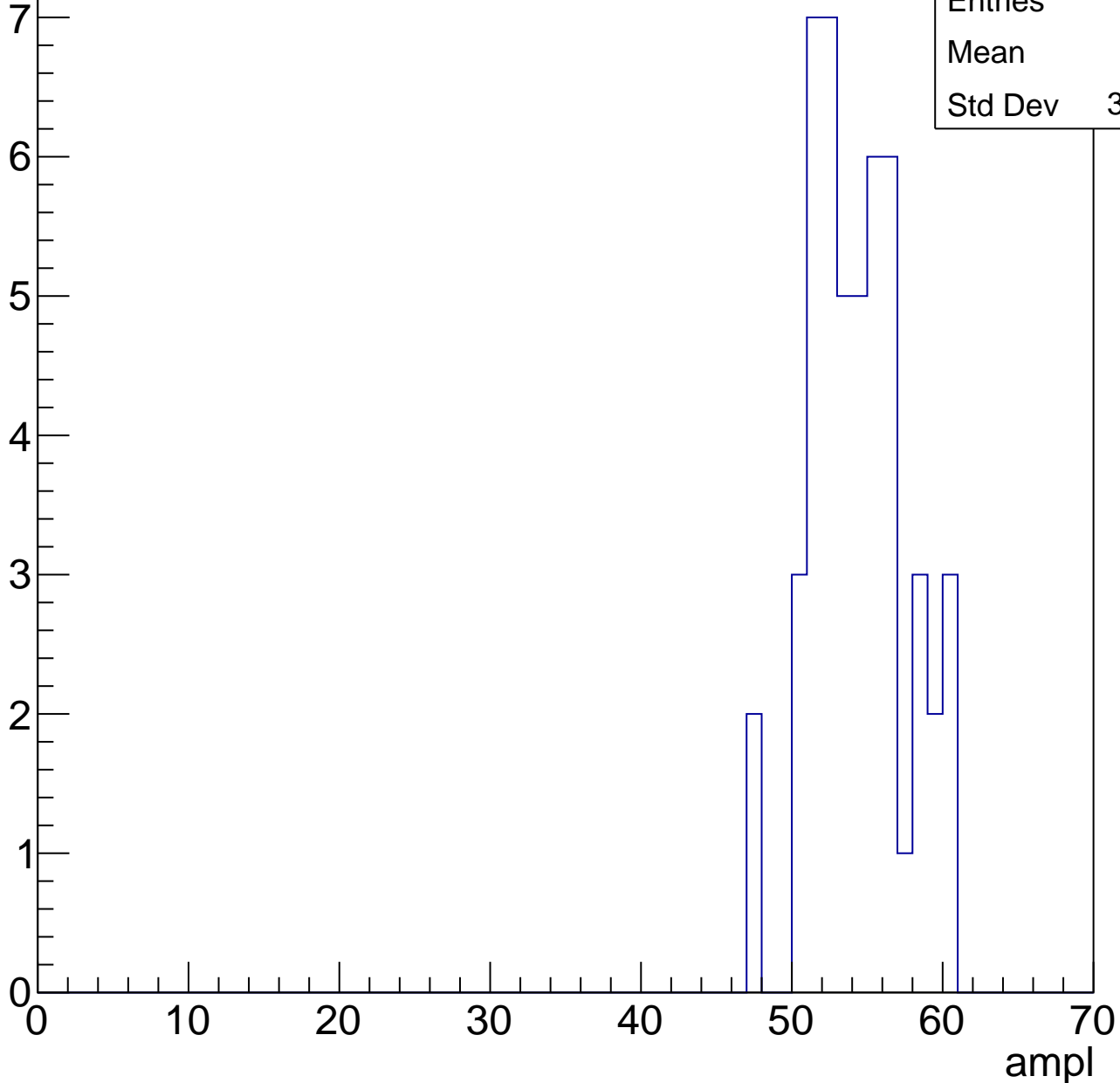


B1L103S, U20-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.9
Std Dev	3.119

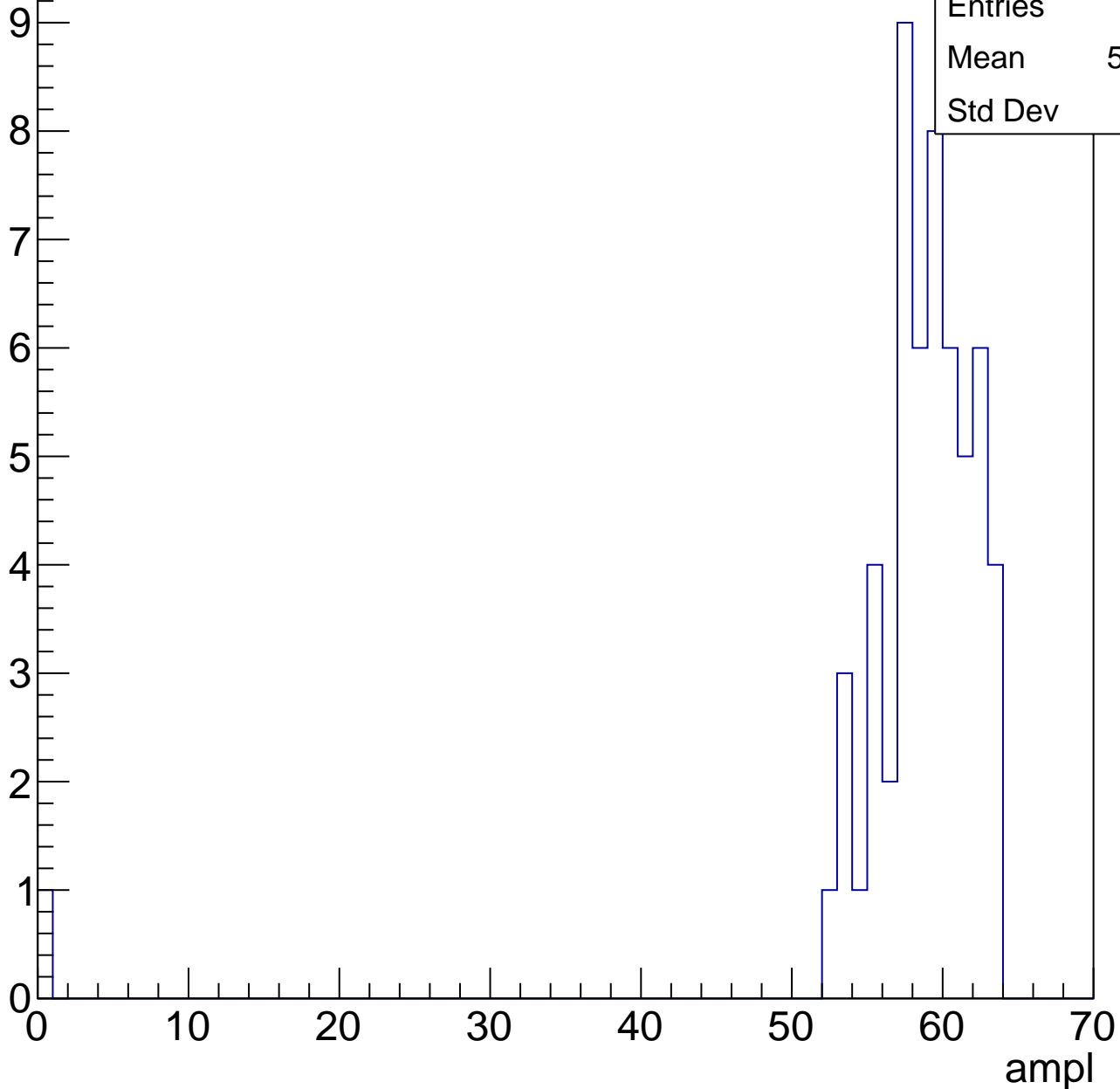


B1L103S, U20-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	57.48
Std Dev	8.24

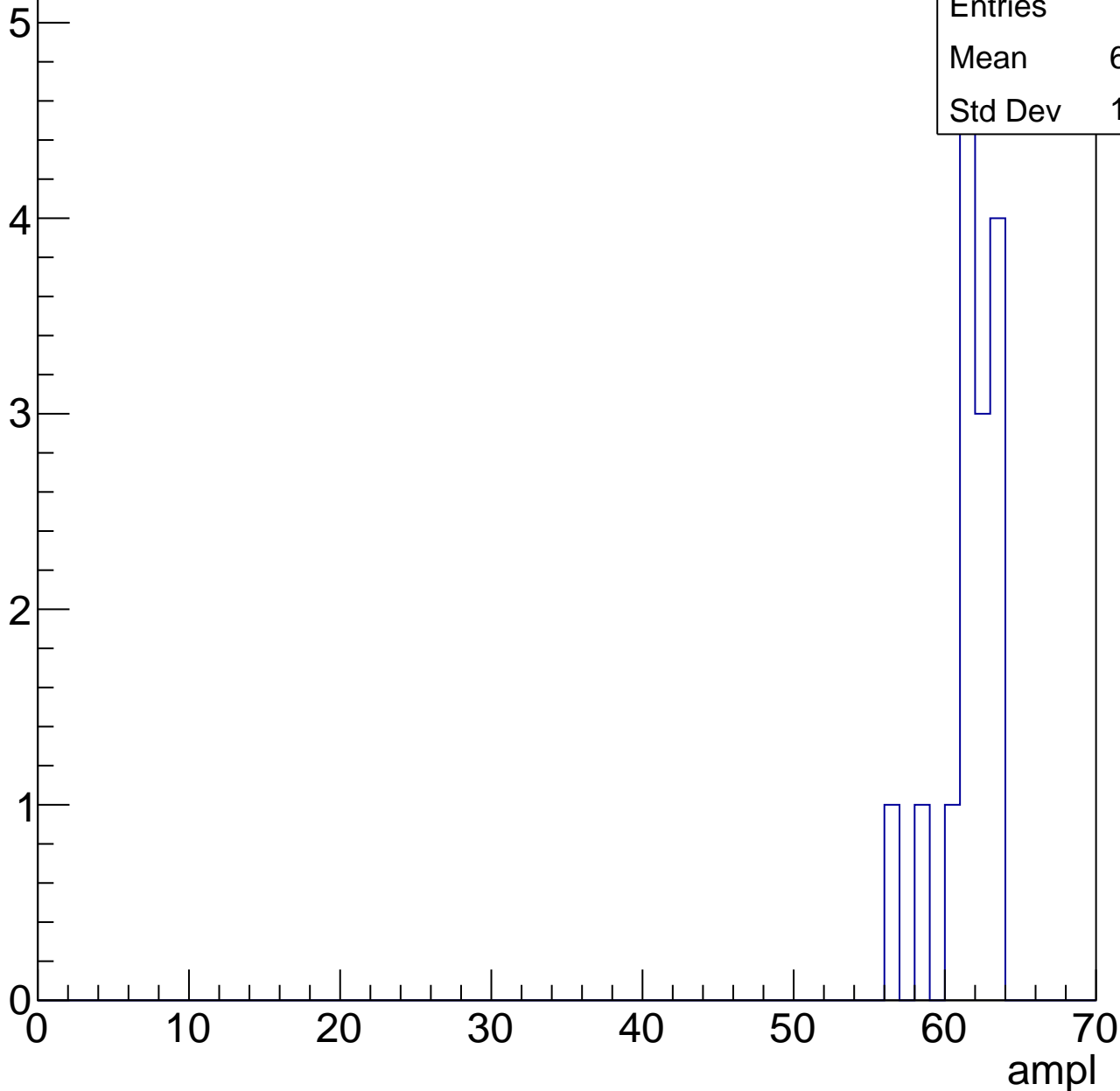


B1L103S, U20-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.13
Std Dev	1.893



B1L103S, U20-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch103, adc0

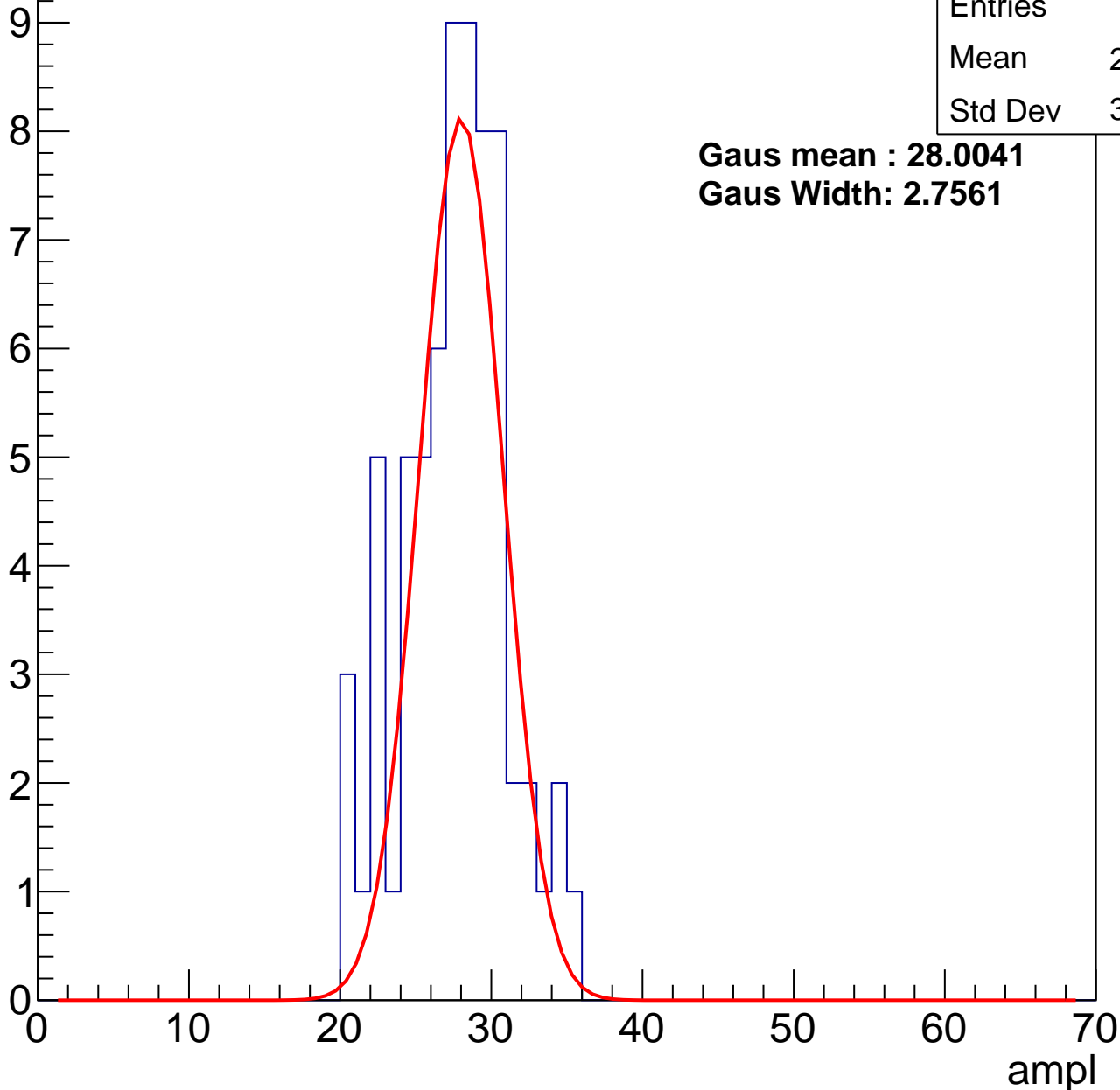
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.12
Std Dev	3.402

Gaus mean : 28.0041

Gaus Width: 2.7561



B1L103S, U20-ch103, adc1

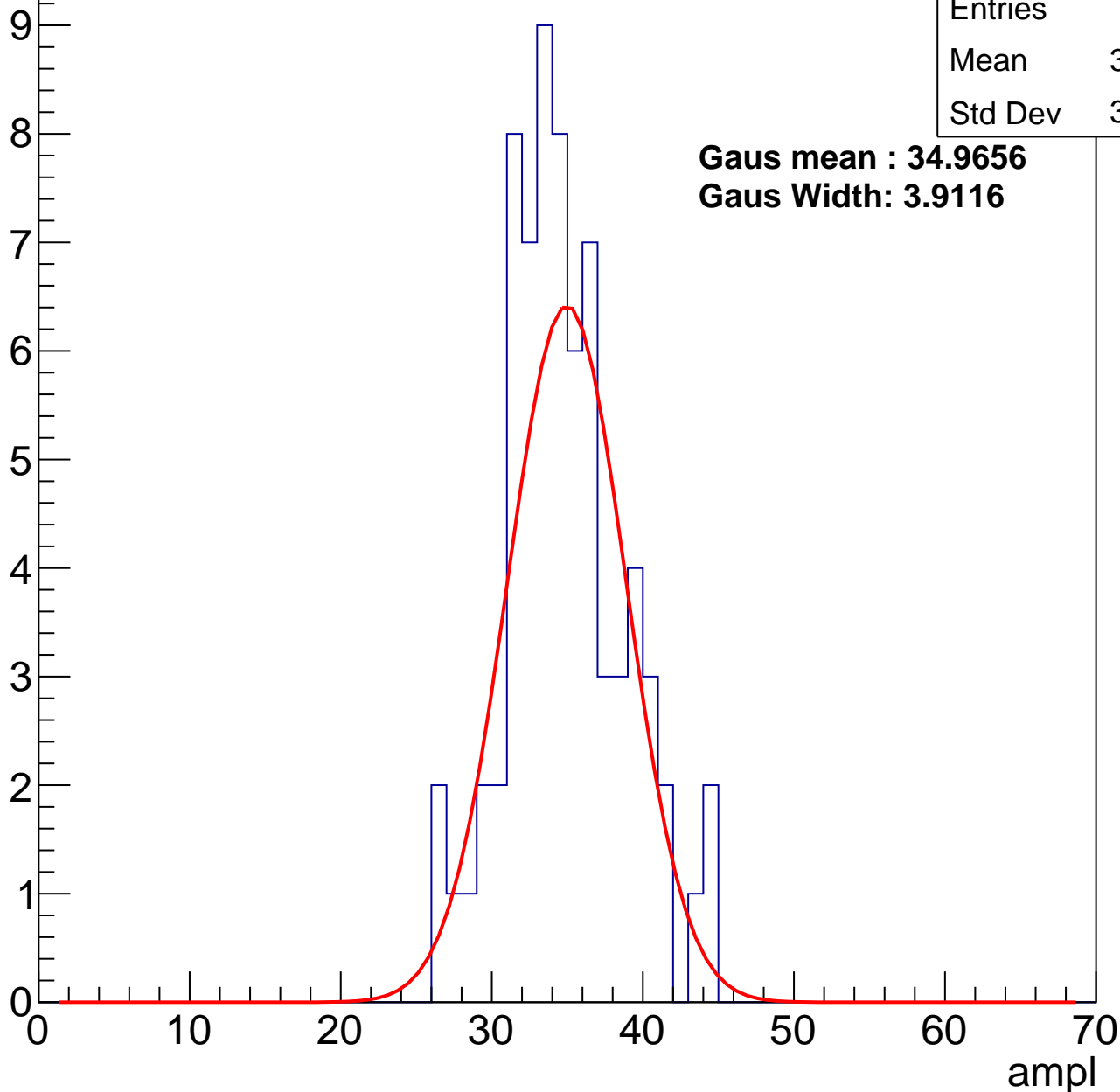
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	34.39
Std Dev	3.916

Gaus mean : 34.9656

Gaus Width: 3.9116



B1L103S, U20-ch103, adc2

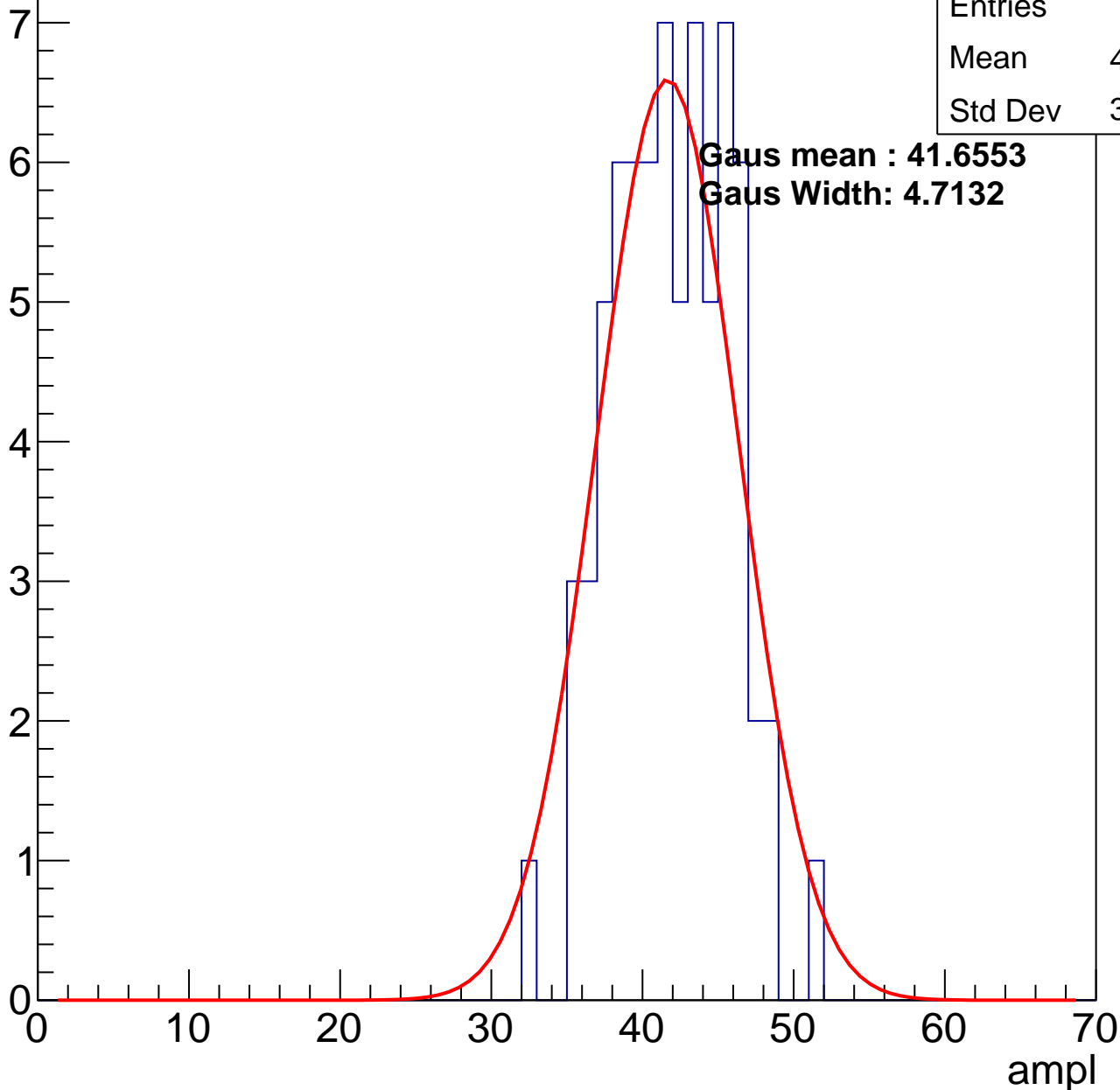
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.42
Std Dev	3.774

Gaus mean : 41.6553

Gaus Width: 4.7132



B1L103S, U20-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

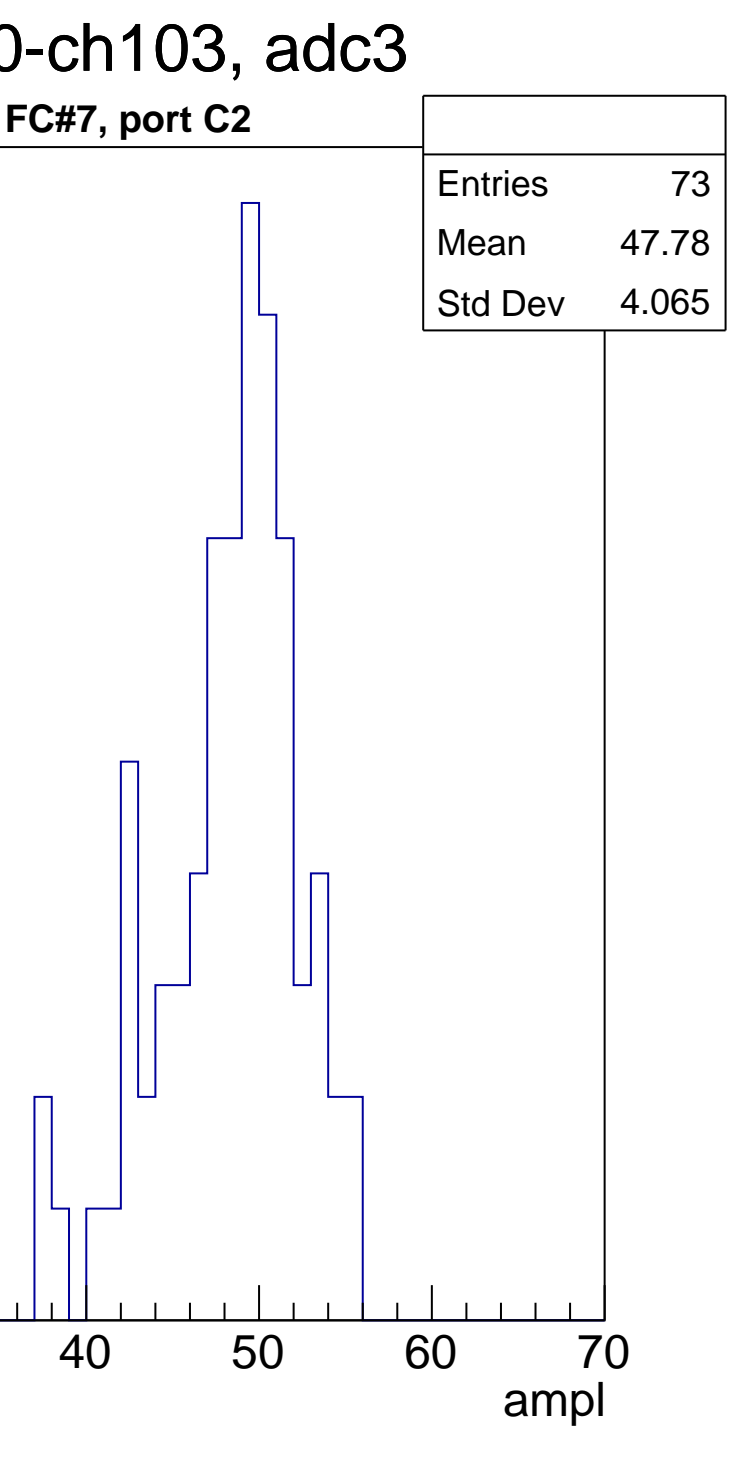
Entries	73
Mean	47.78
Std Dev	4.065

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

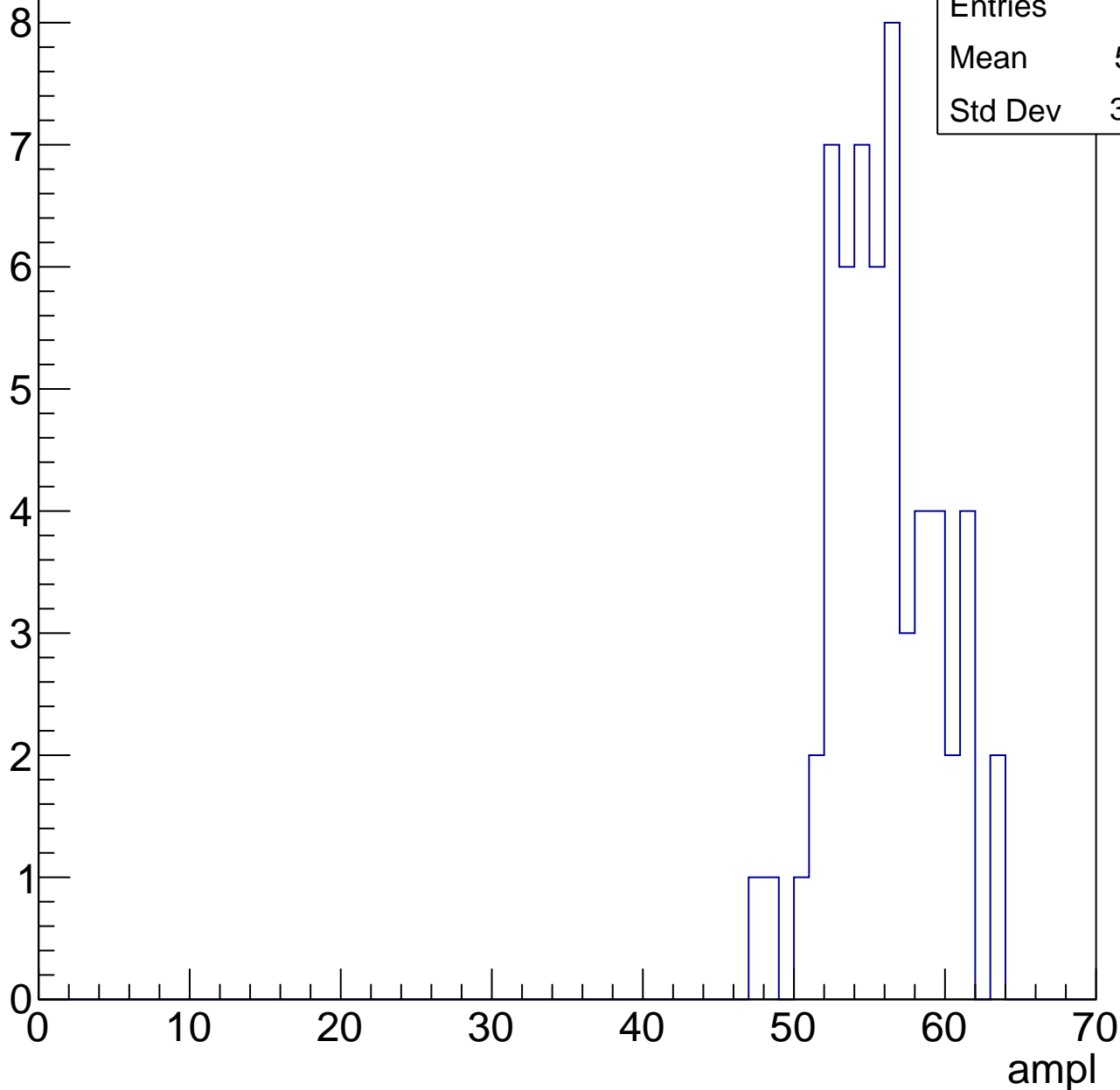


B1L103S, U20-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

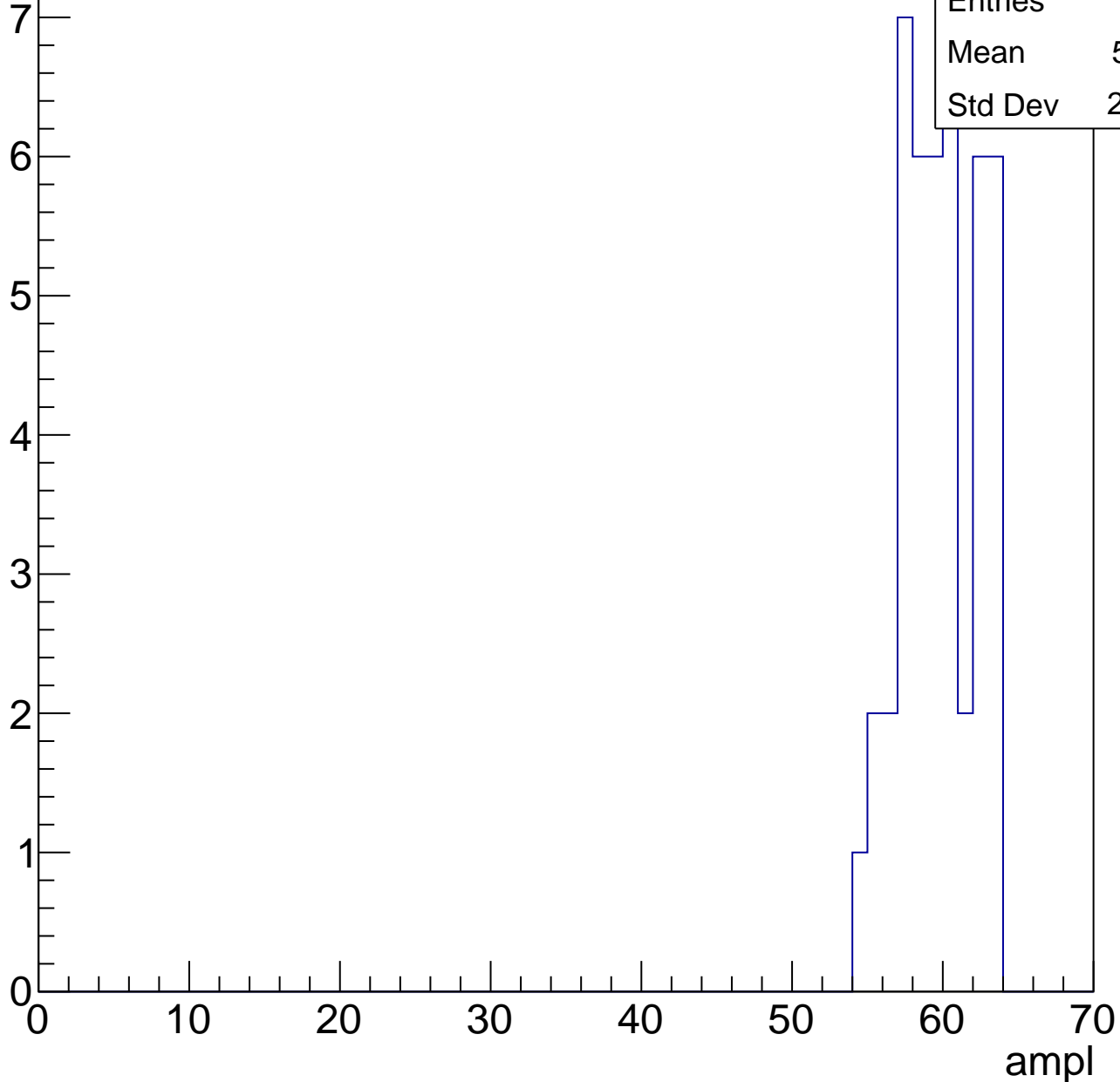
Entries	58
Mean	55.41
Std Dev	3.464



B1L103S, U20-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

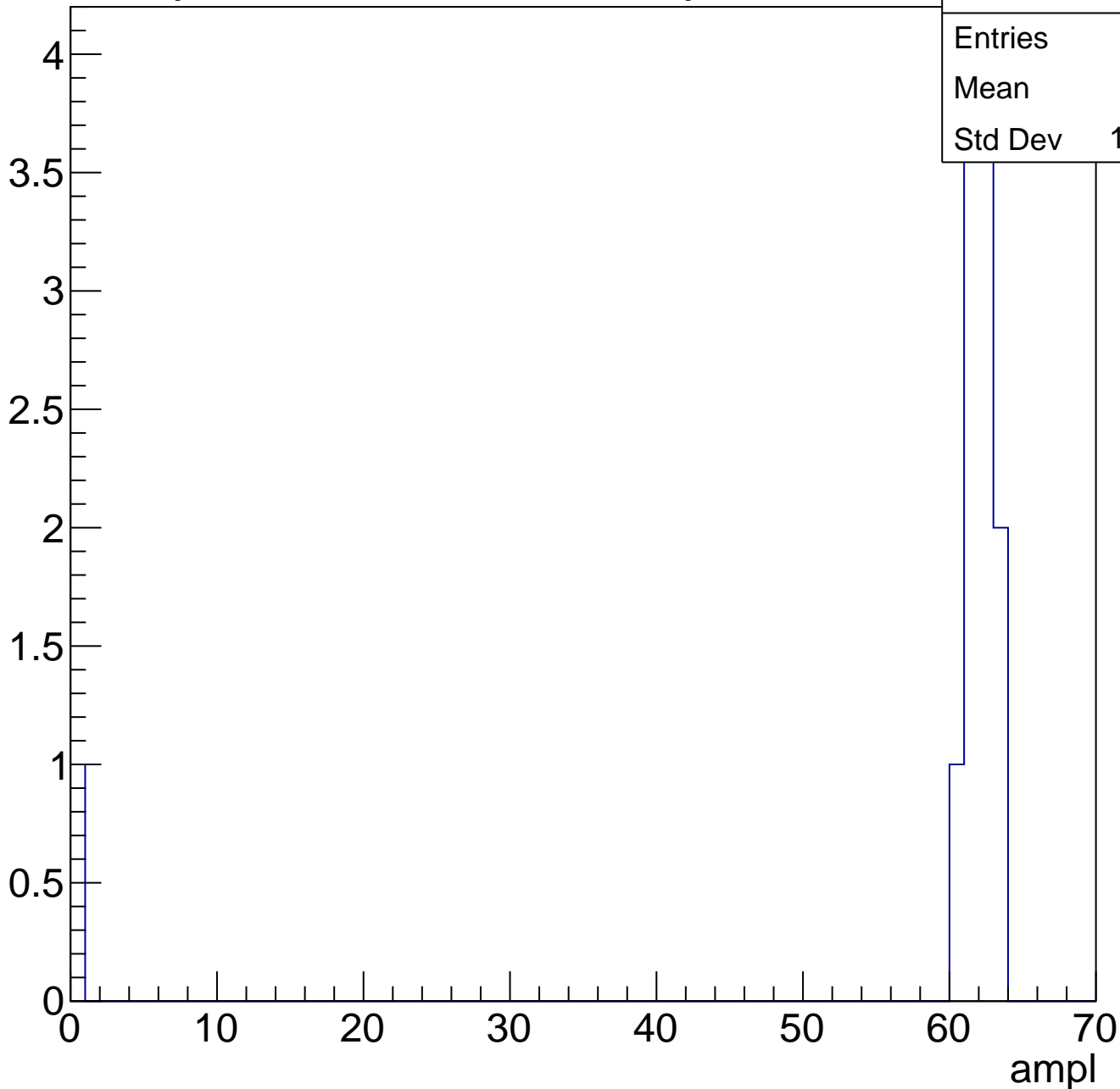
Entry



B1L103S, U20-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	56.5
Std Dev	17.06

B1L103S, U20-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L103S, U20-ch104, adc0

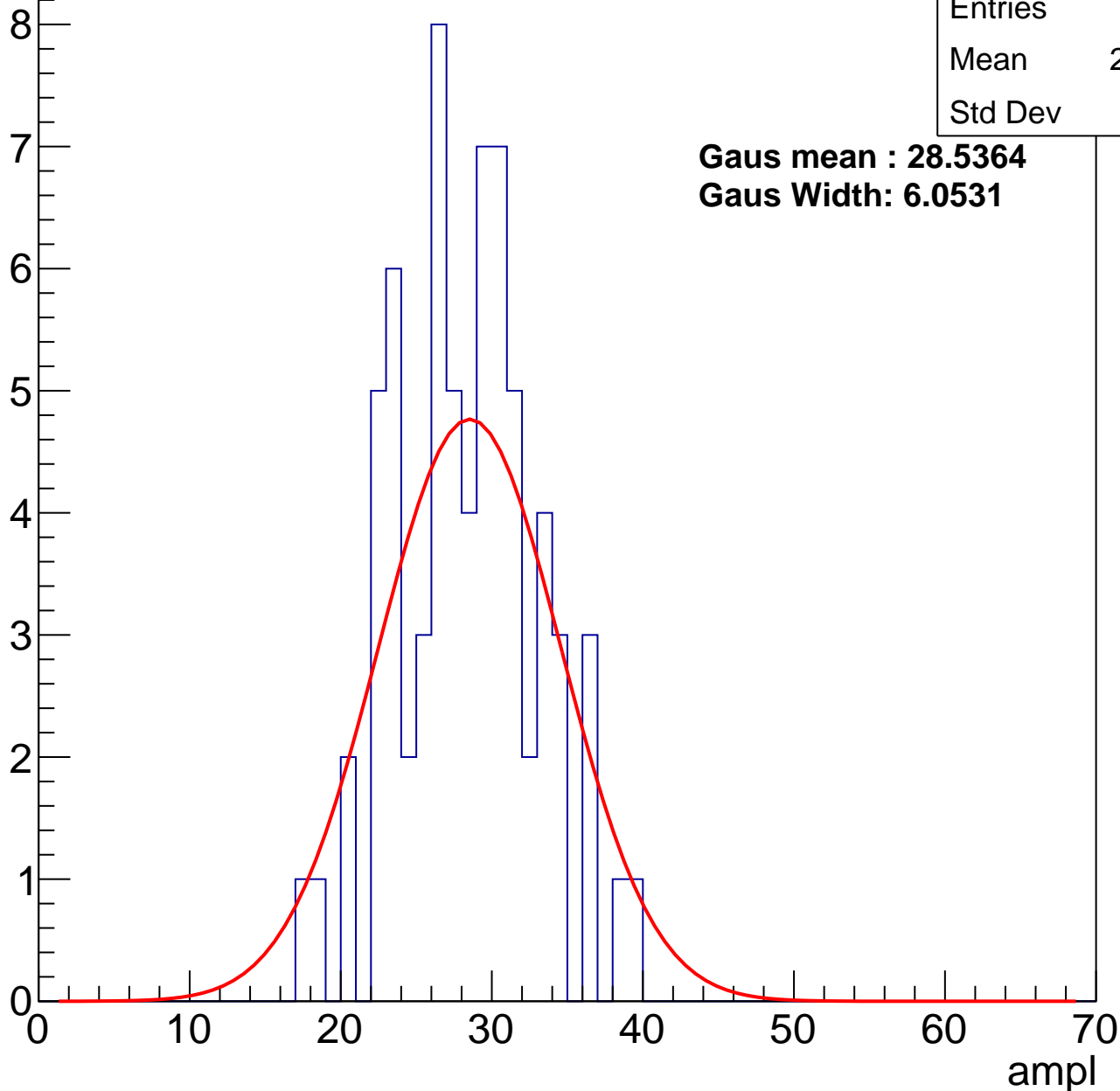
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	27.89
Std Dev	4.64

Gaus mean : 28.5364

Gaus Width: 6.0531



B1L103S, U20-ch104, adc1

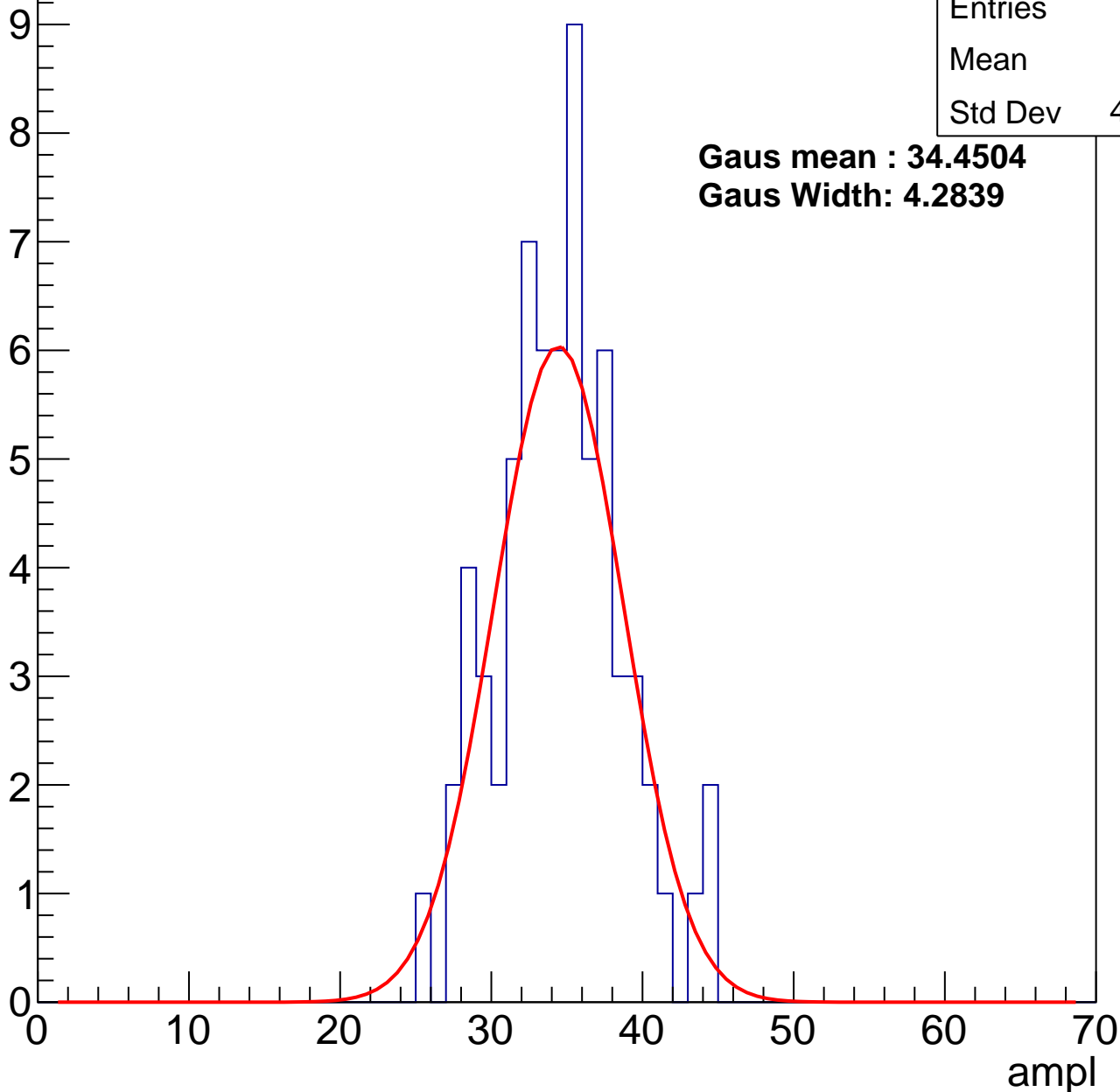
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.1
Std Dev	4.052

Gaus mean : 34.4504

Gaus Width: 4.2839



B1L103S, U20-ch104, adc2

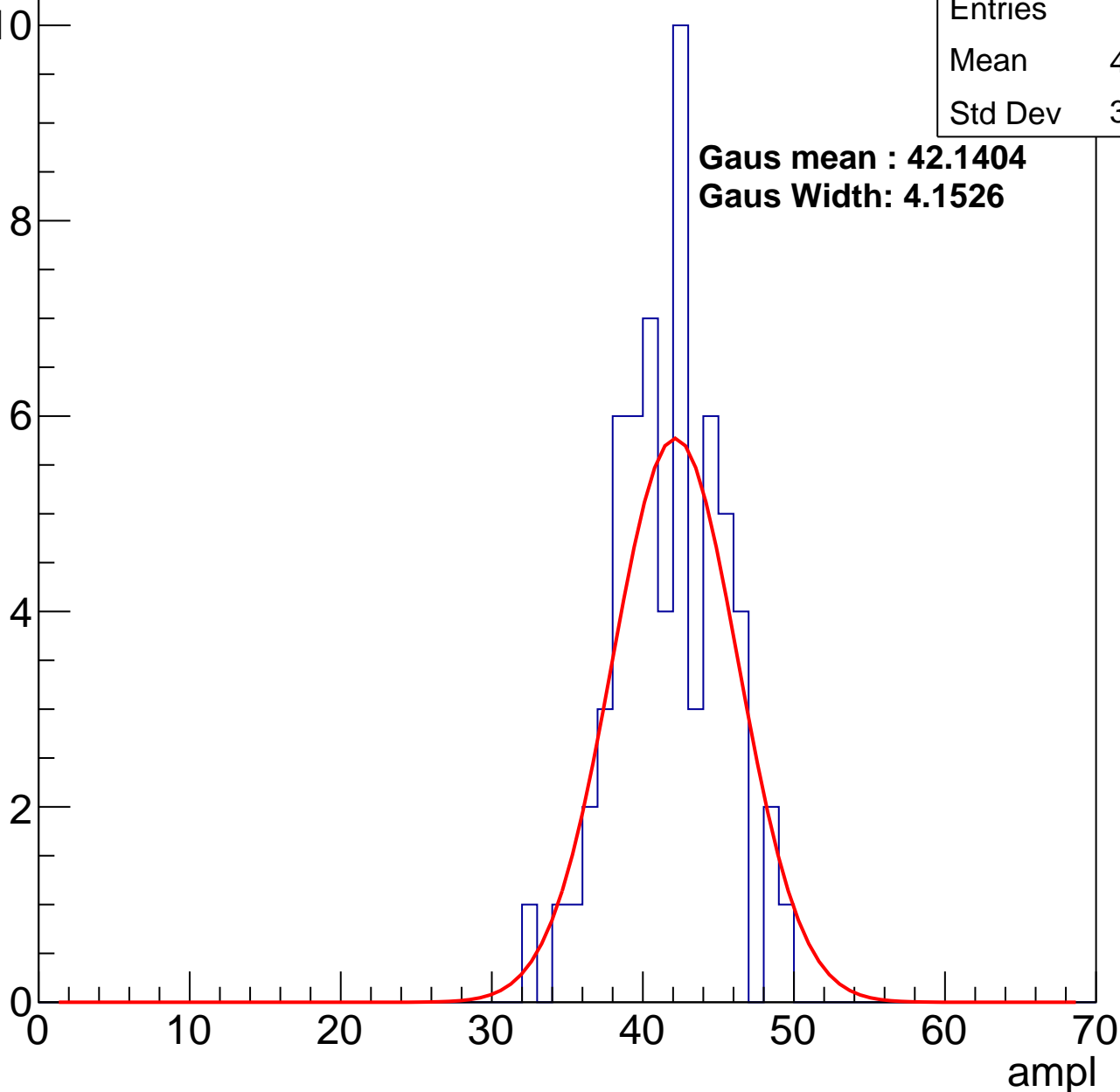
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.24
Std Dev	3.509

Gaus mean : 42.1404

Gaus Width: 4.1526

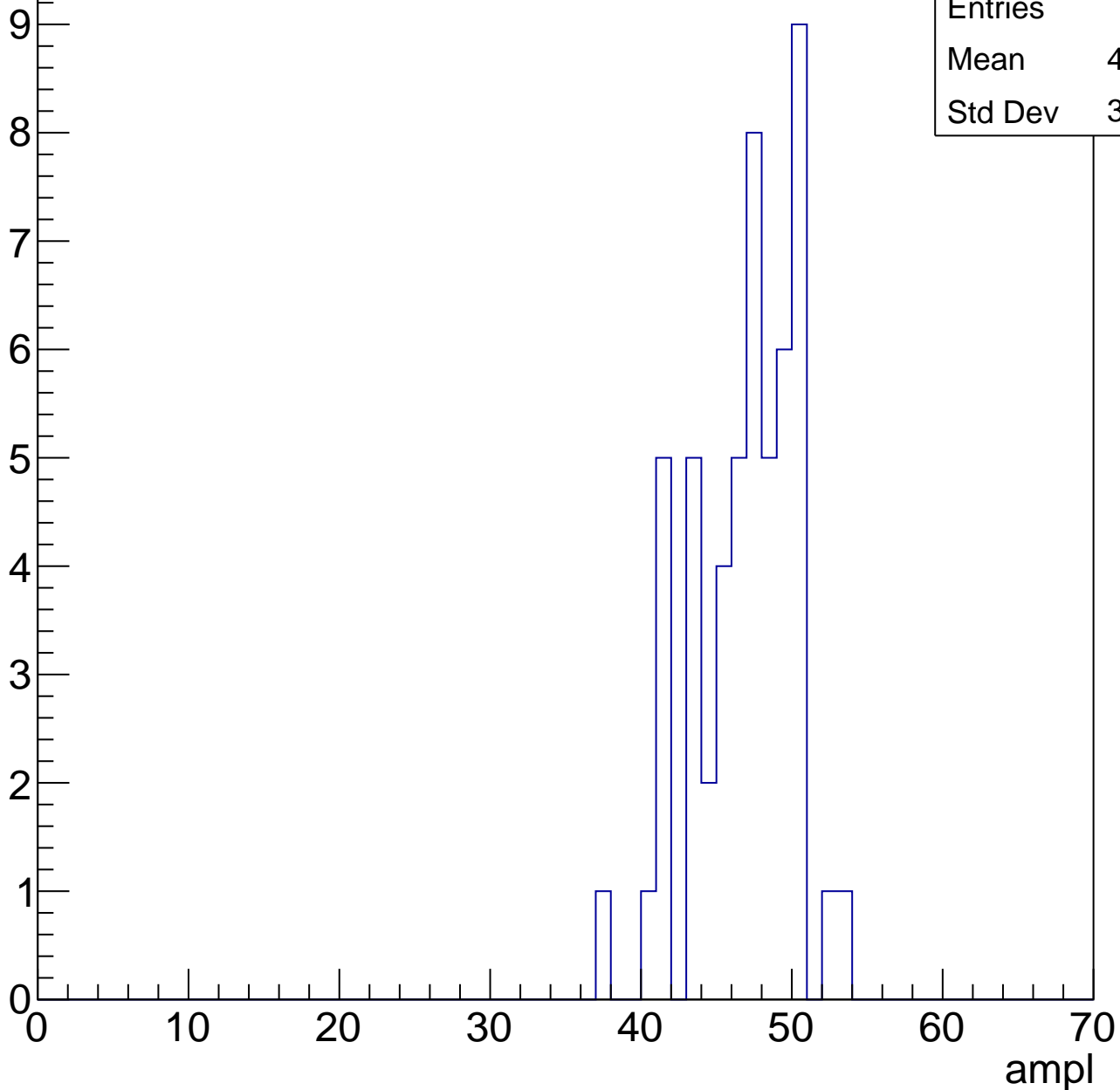


B1L103S, U20-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	46.42
Std Dev	3.367



B1L103S, U20-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	53.84
Std Dev	3.912

Entry

10

8

6

4

2

0

0

10

20

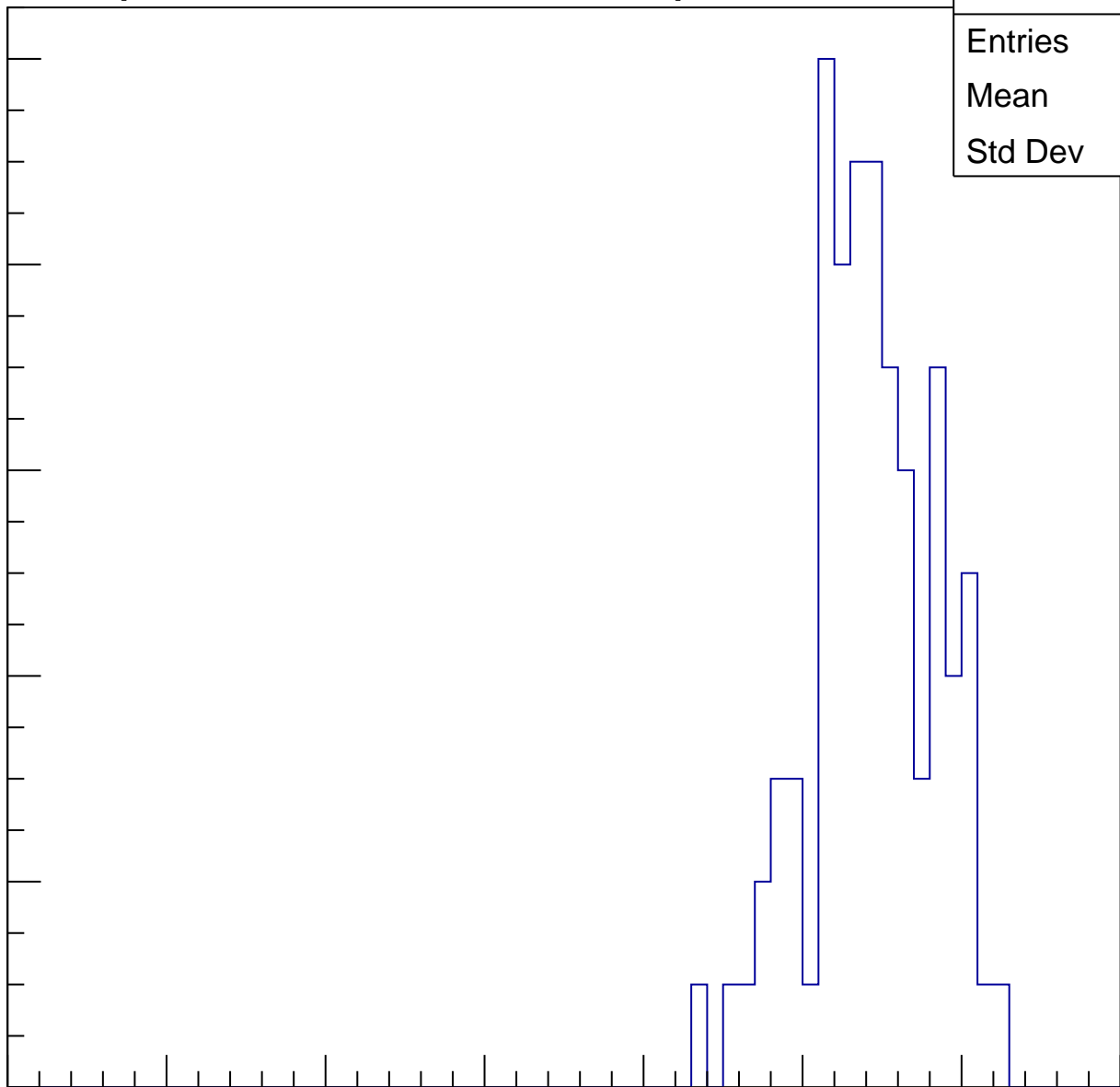
30

40

50

60

ampl

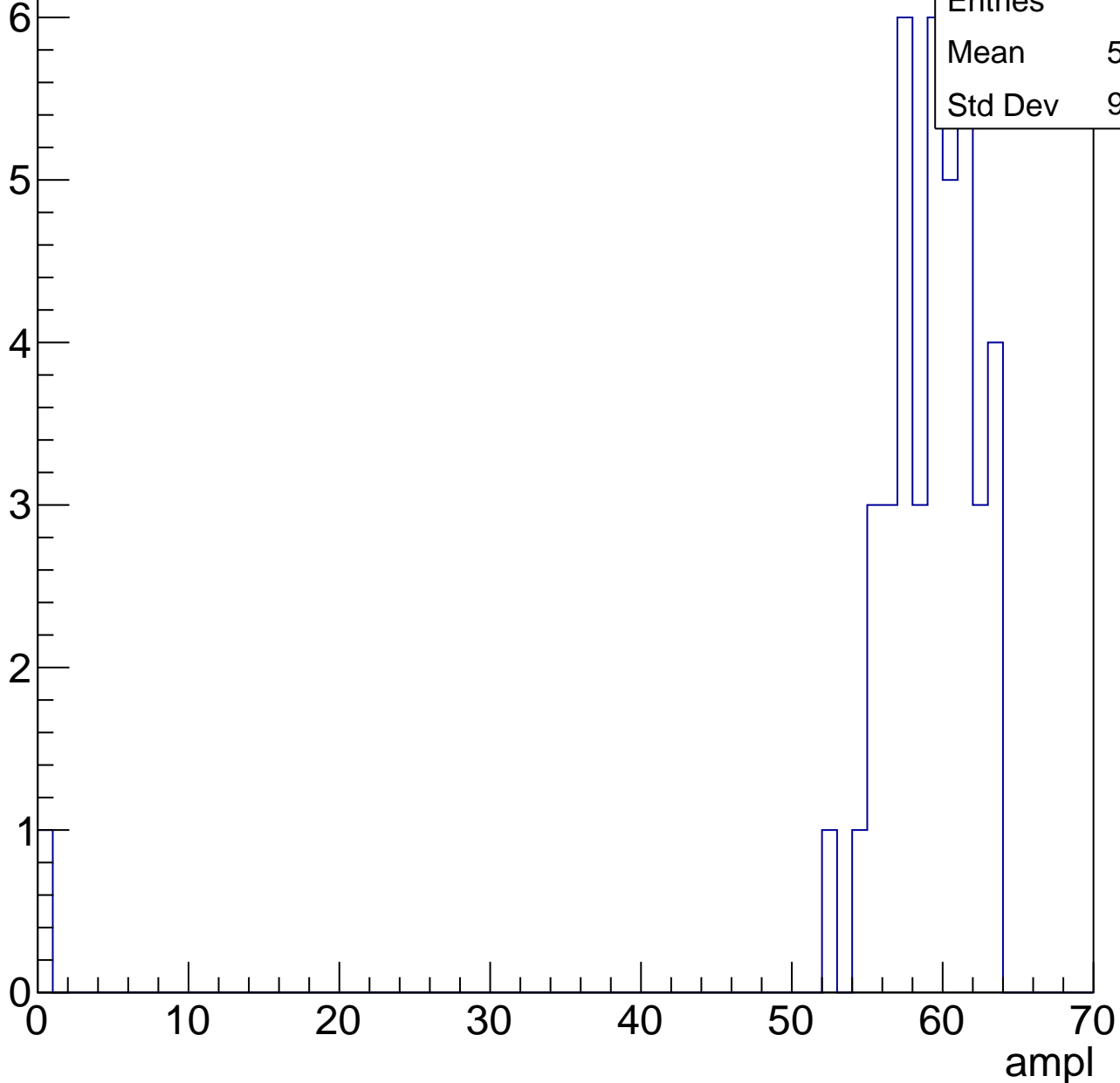


B1L103S, U20-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	57.45
Std Dev	9.356

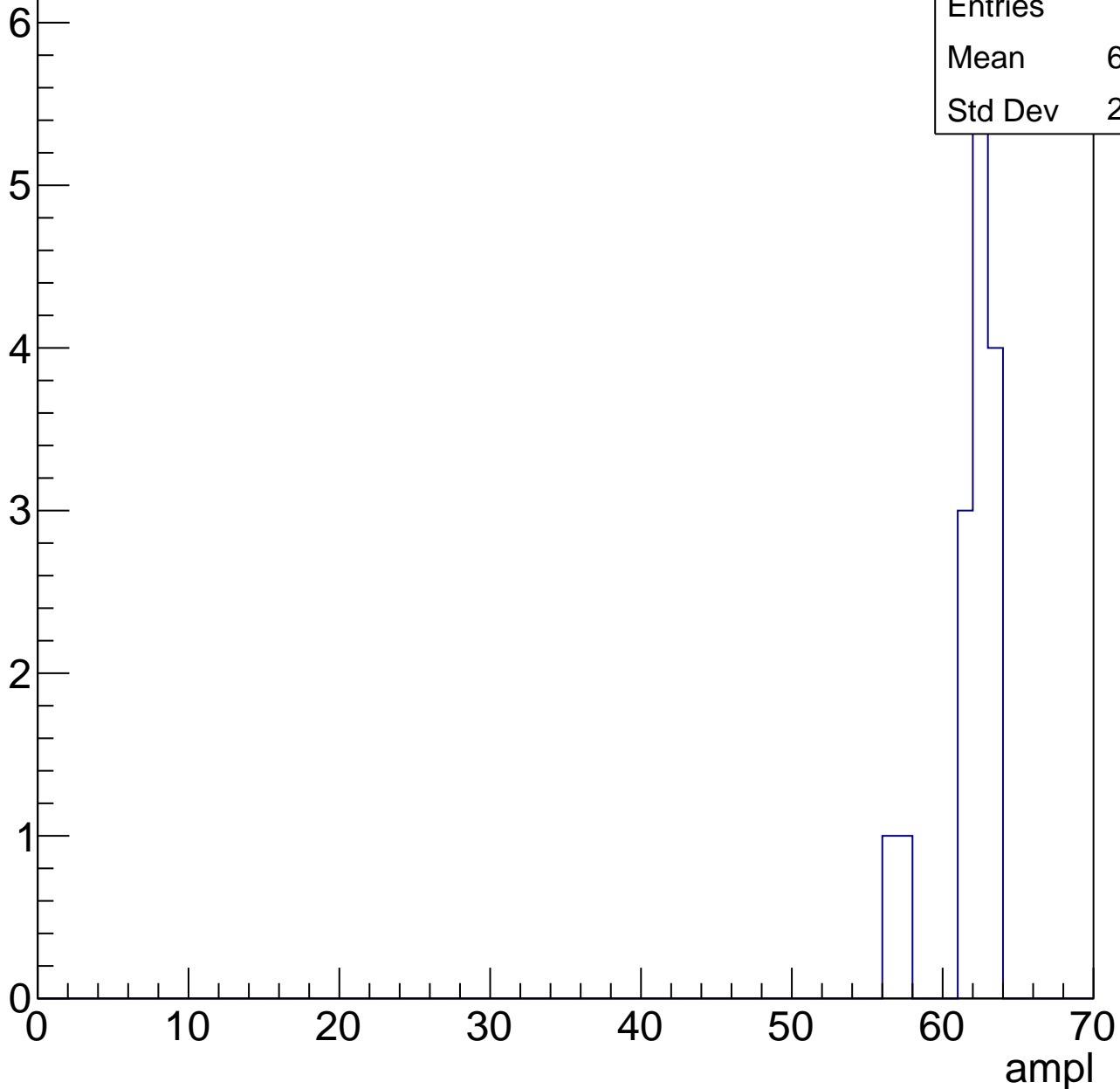


B1L103S, U20-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.33
Std Dev	2.022



B1L103S, U20-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch105, adc0

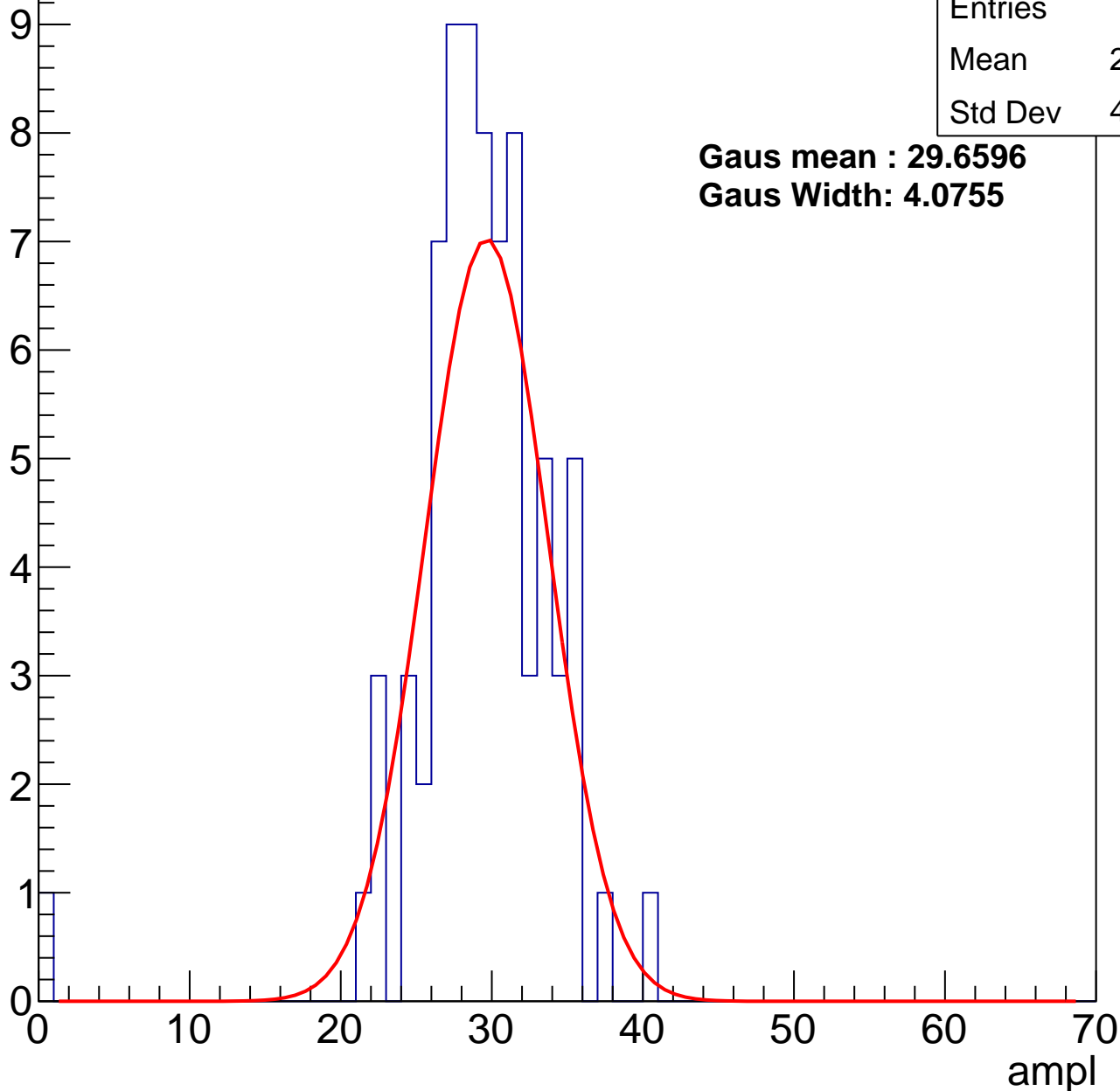
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.83
Std Dev	4.935

Gaus mean : 29.6596

Gaus Width: 4.0755



B1L103S, U20-ch105, adc1

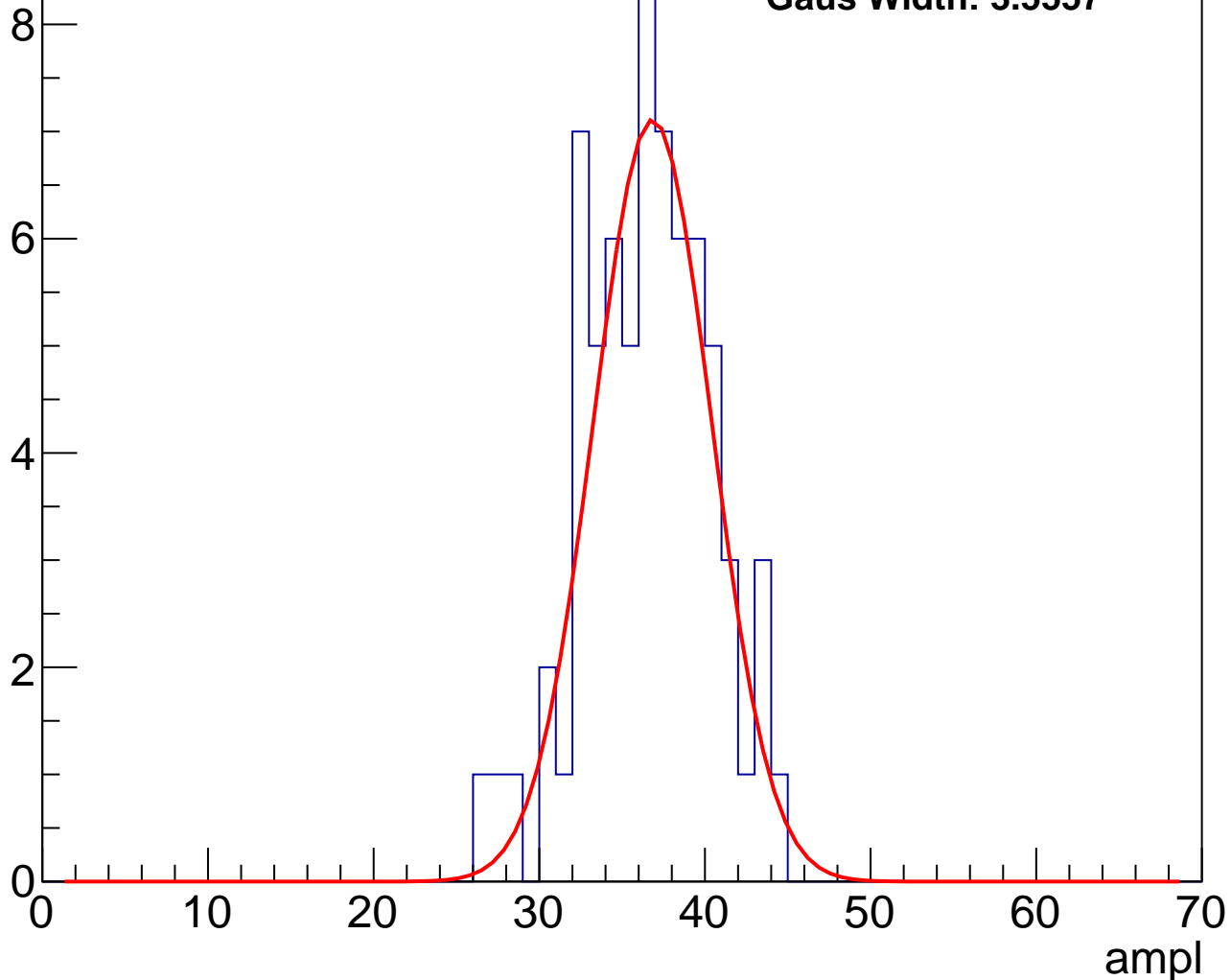
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.04
Std Dev	3.781

Gaus mean : 36.8372

Gaus Width: 3.5557



B1L103S, U20-ch105, adc2

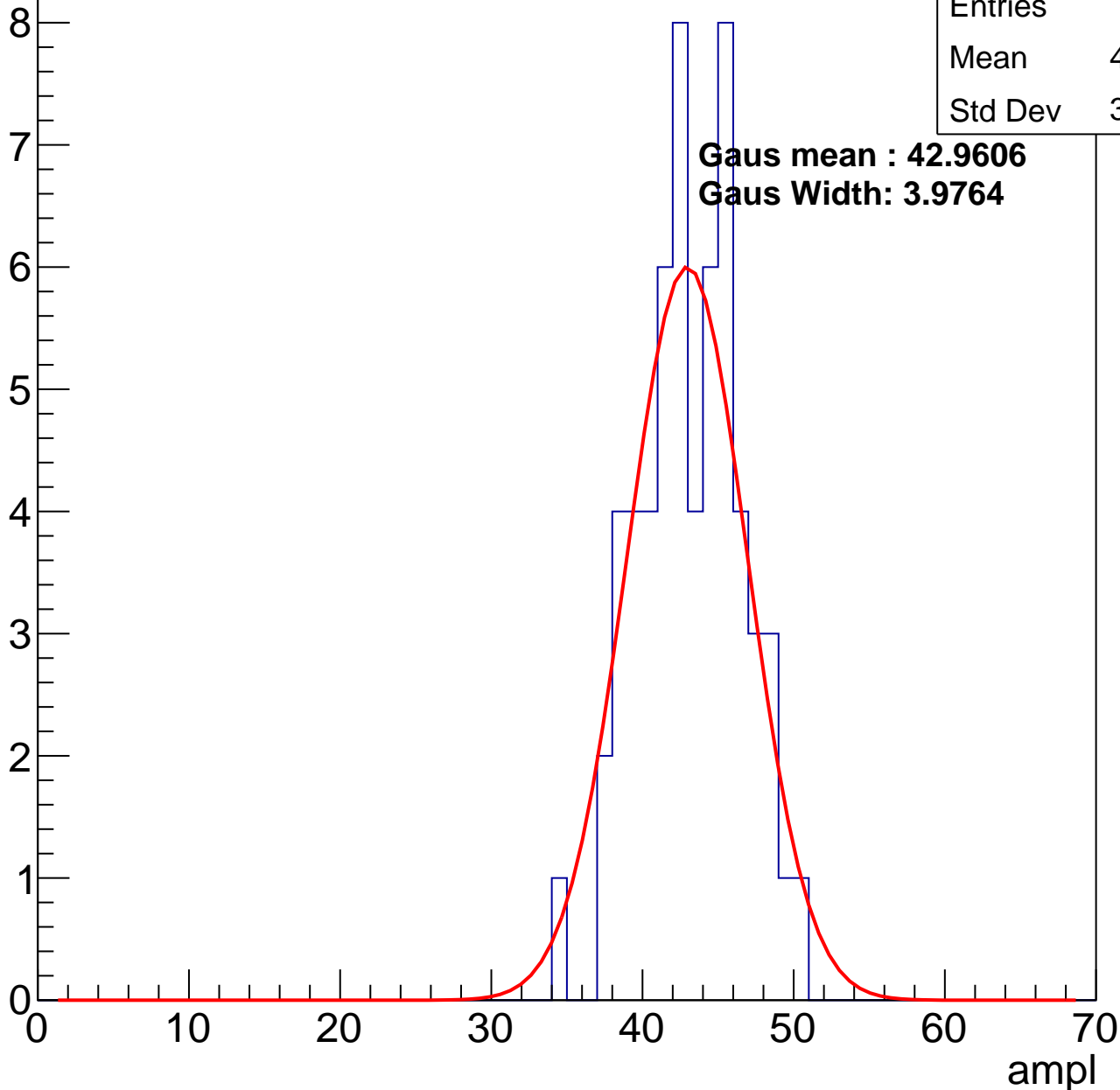
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.75
Std Dev	3.353

Gaus mean : 42.9606

Gaus Width: 3.9764

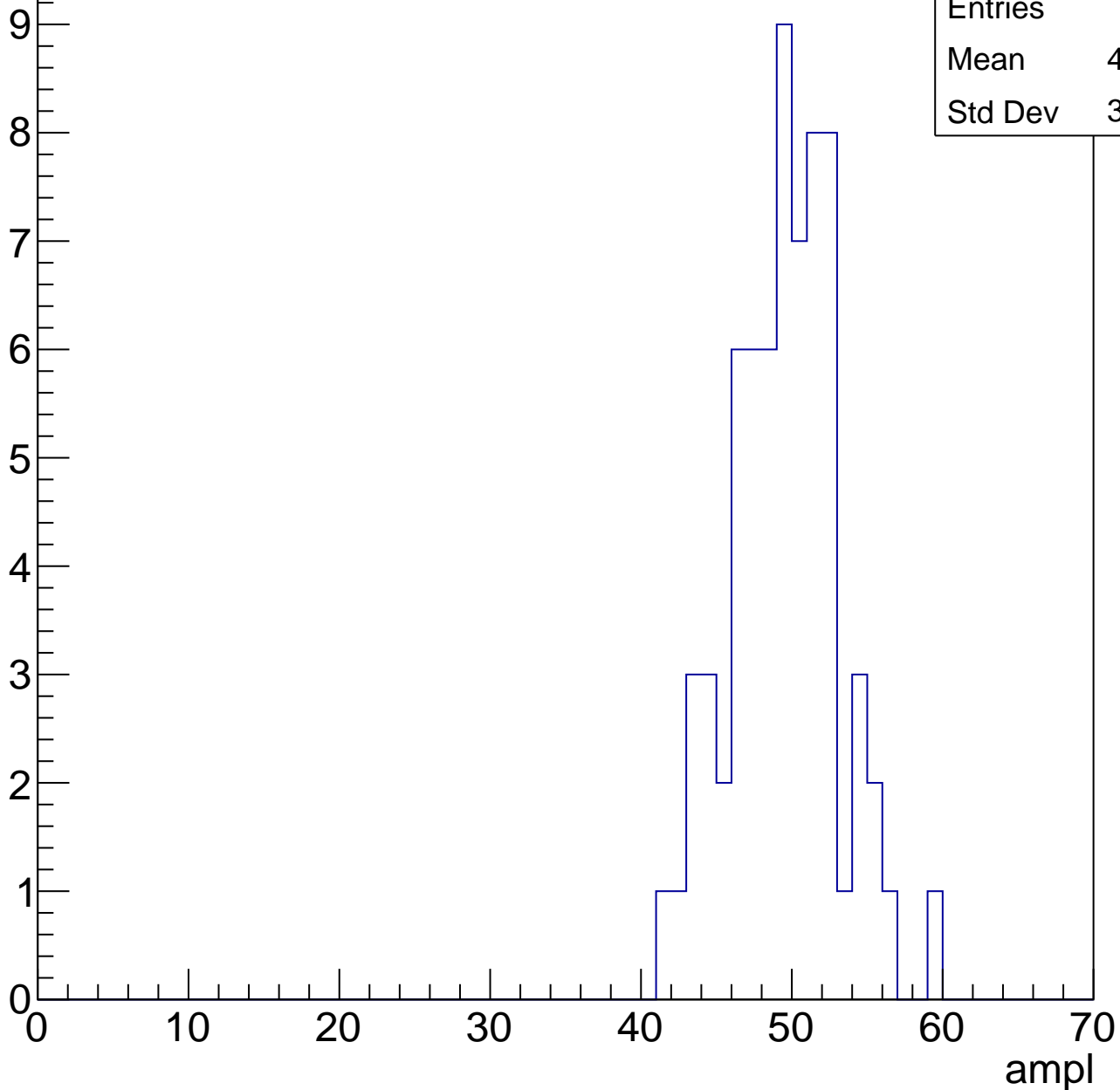


B1L103S, U20-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.04
Std Dev	3.504

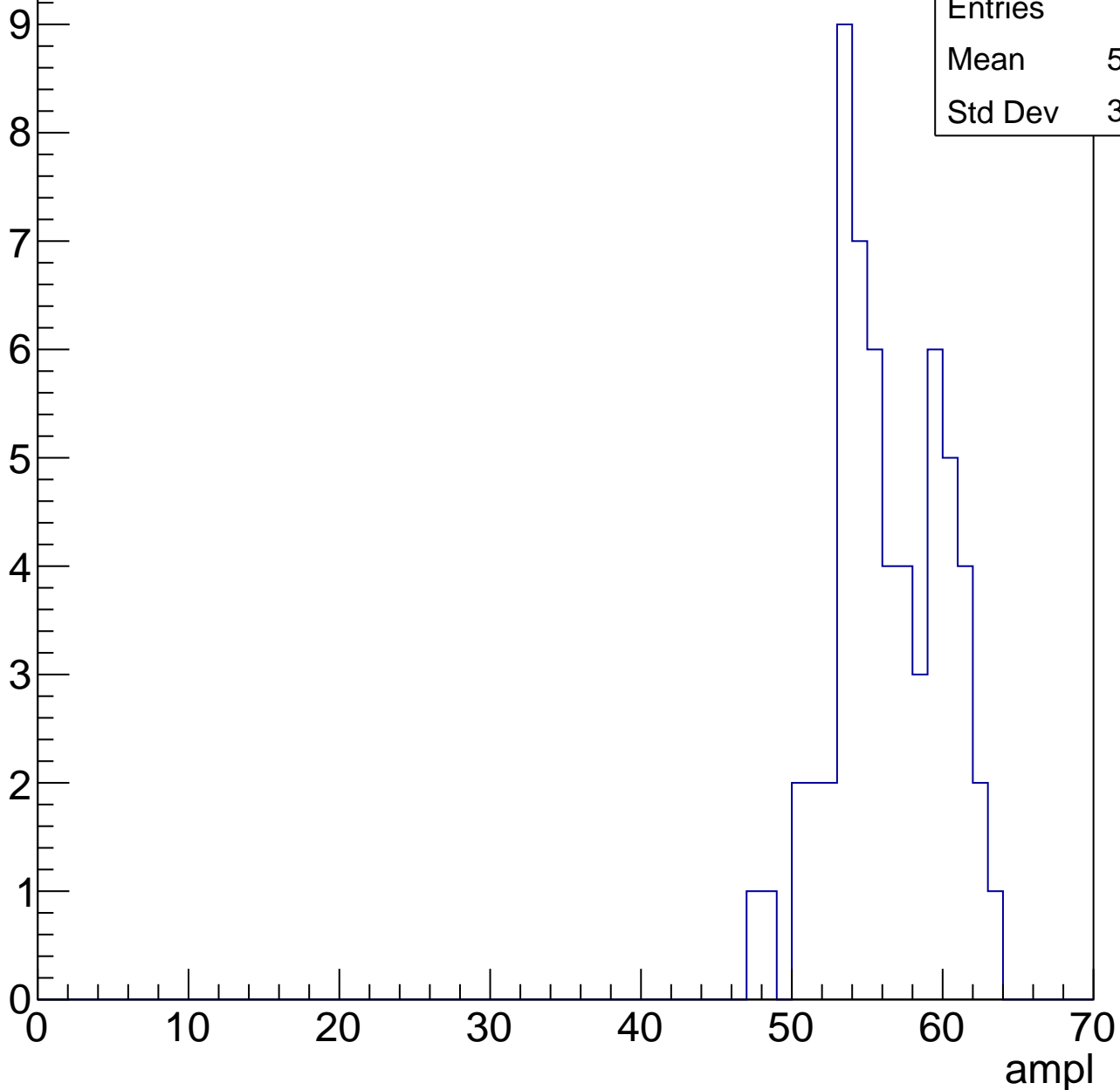


B1L103S, U20-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.88
Std Dev	3.646

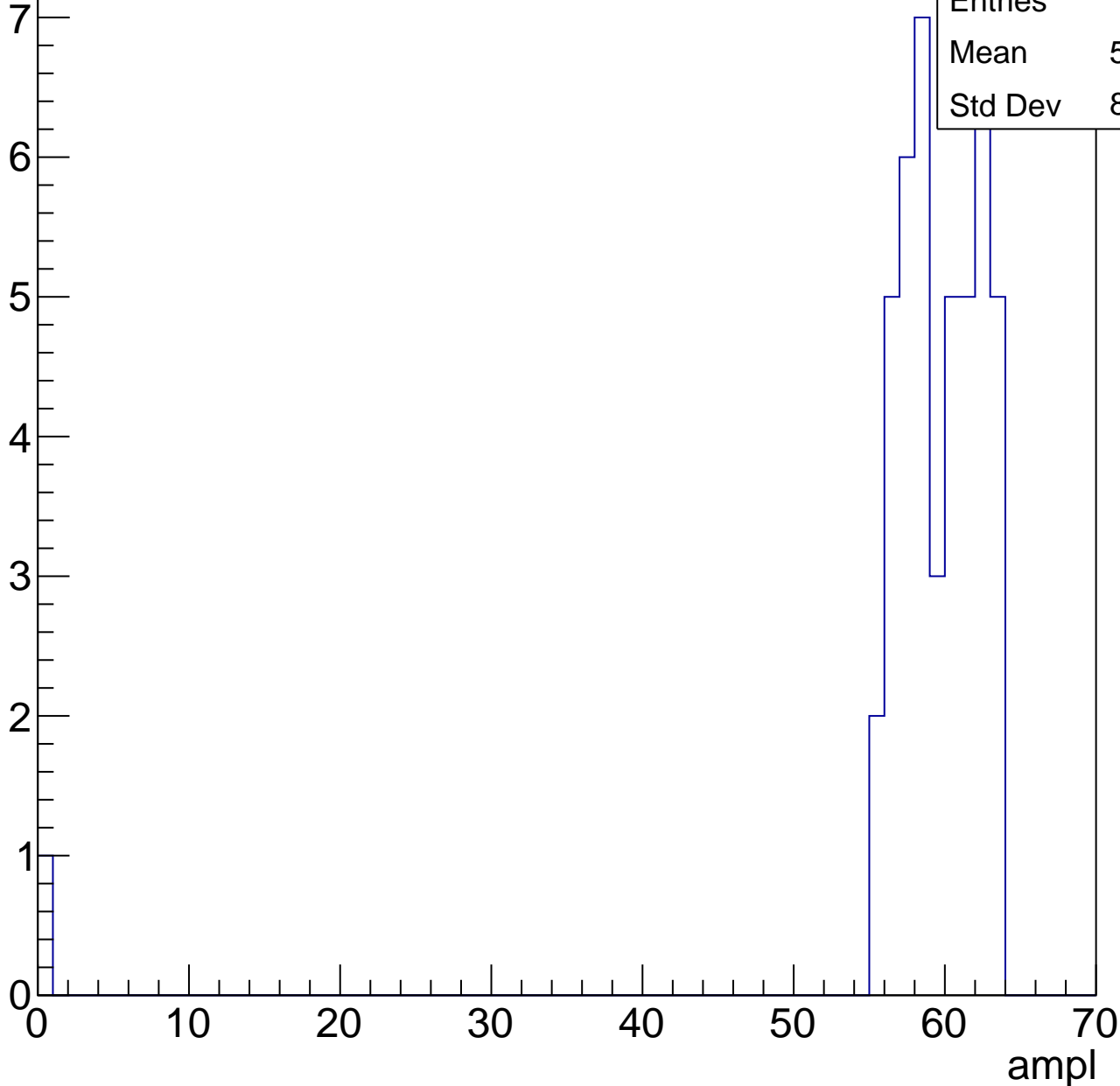


B1L103S, U20-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.02
Std Dev	8.984



B1L103S, U20-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch106, adc0

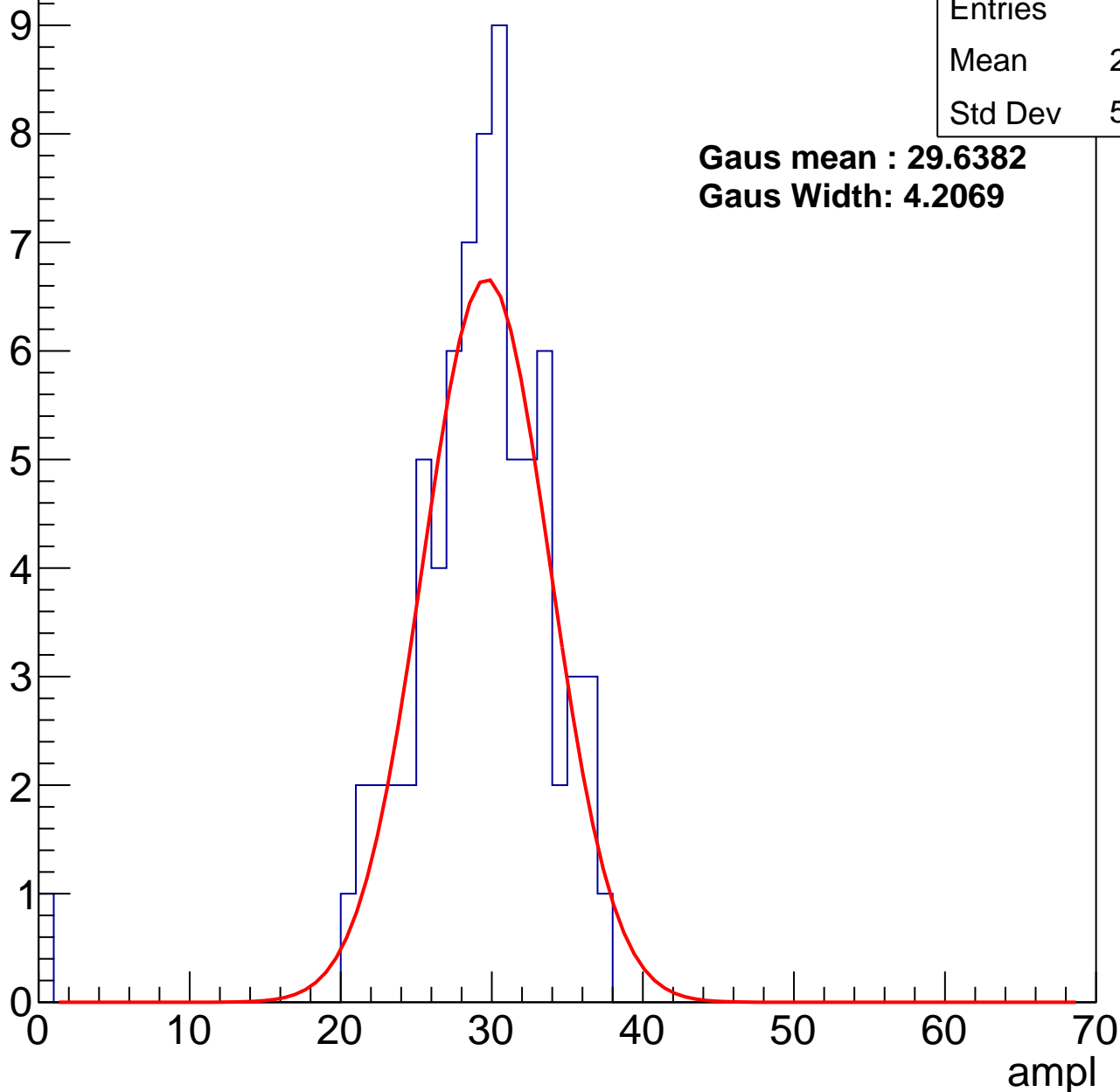
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.65
Std Dev	5.129

Gaus mean : 29.6382

Gaus Width: 4.2069



B1L103S, U20-ch106, adc1

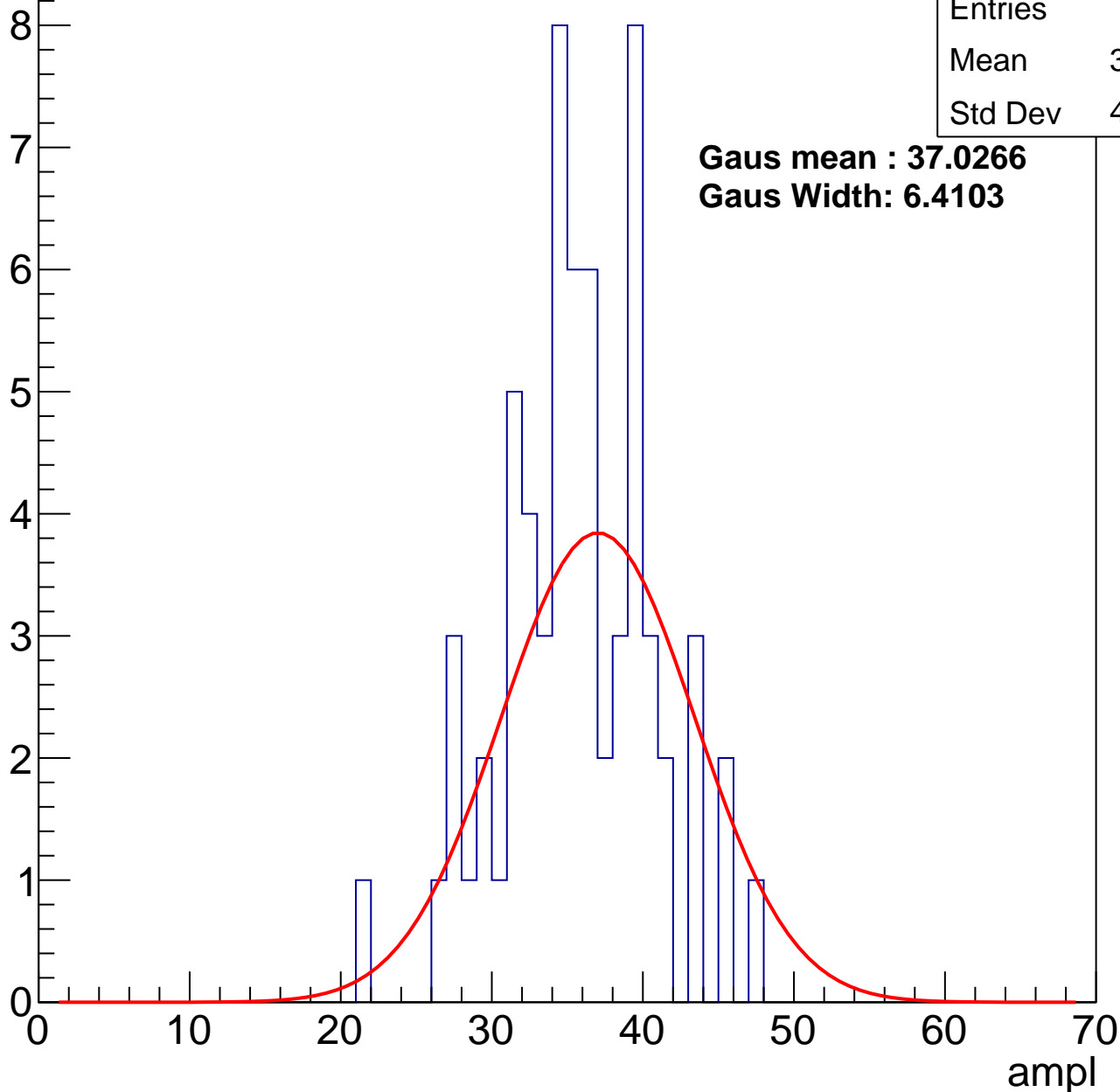
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.26
Std Dev	4.959

Gaus mean : 37.0266

Gaus Width: 6.4103



B1L103S, U20-ch106, adc2

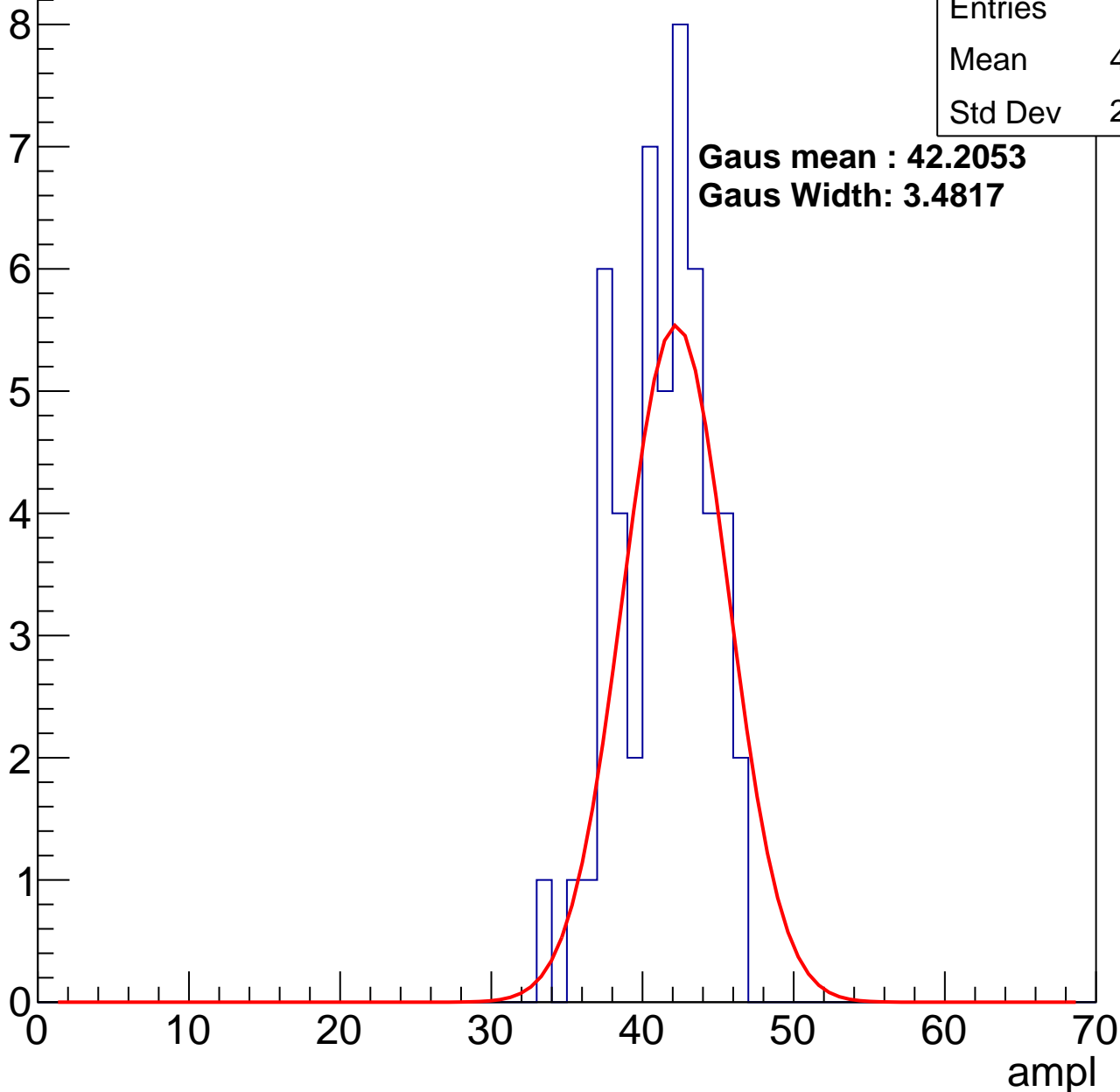
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	40.84
Std Dev	2.979

Gaus mean : 42.2053

Gaus Width: 3.4817

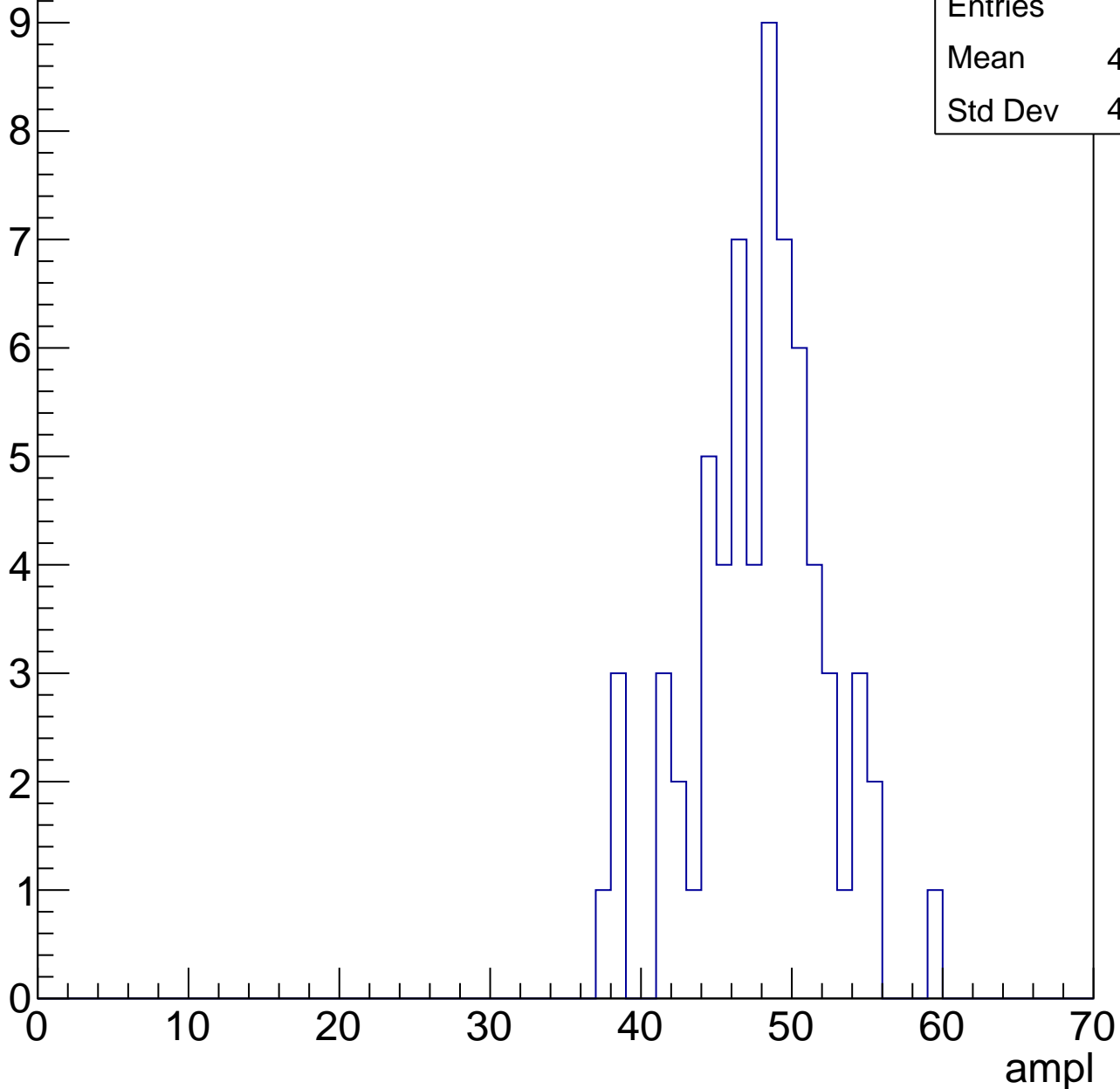


B1L103S, U20-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	47.42
Std Dev	4.366

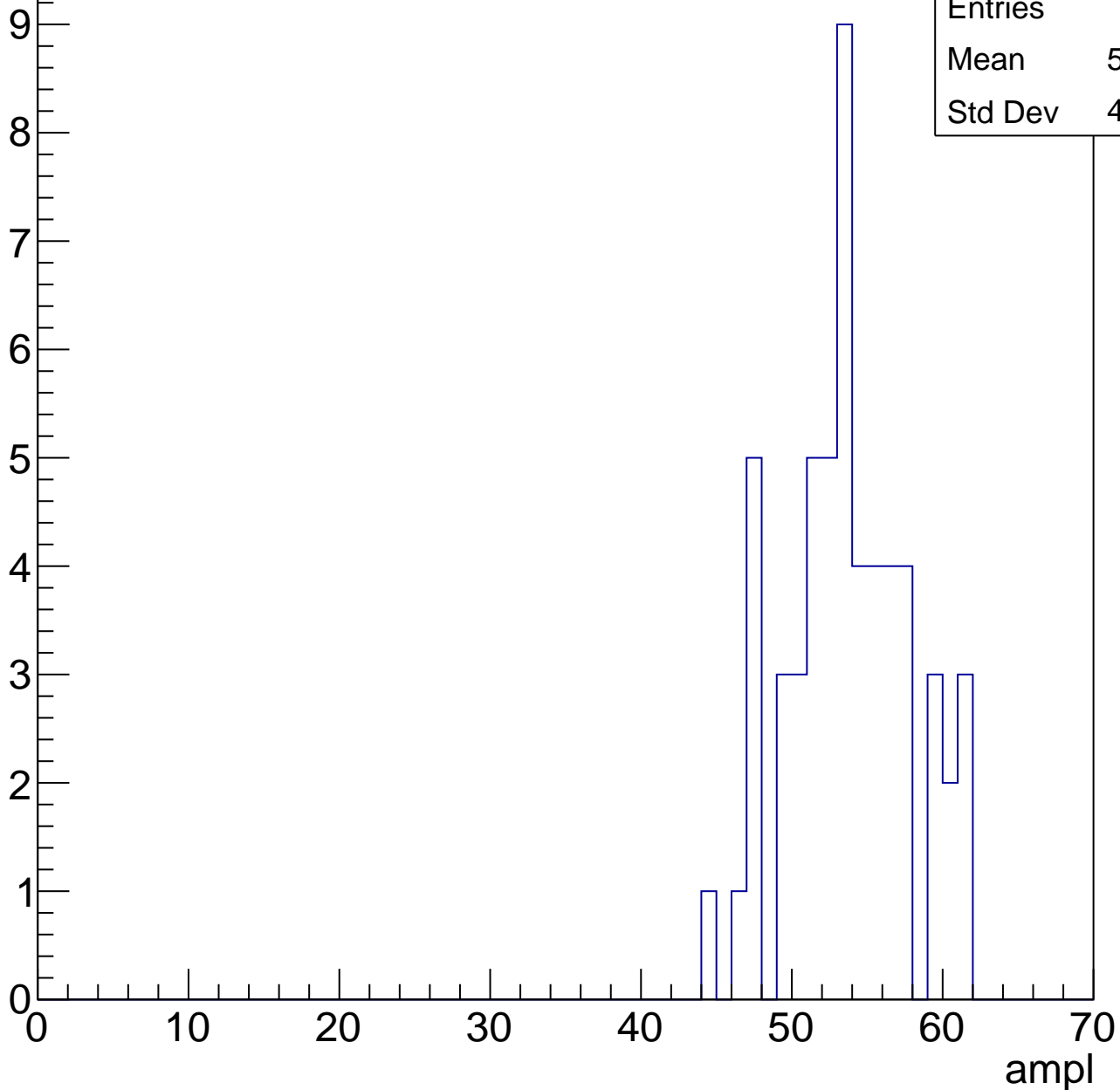


B1L103S, U20-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.25
Std Dev	4.063

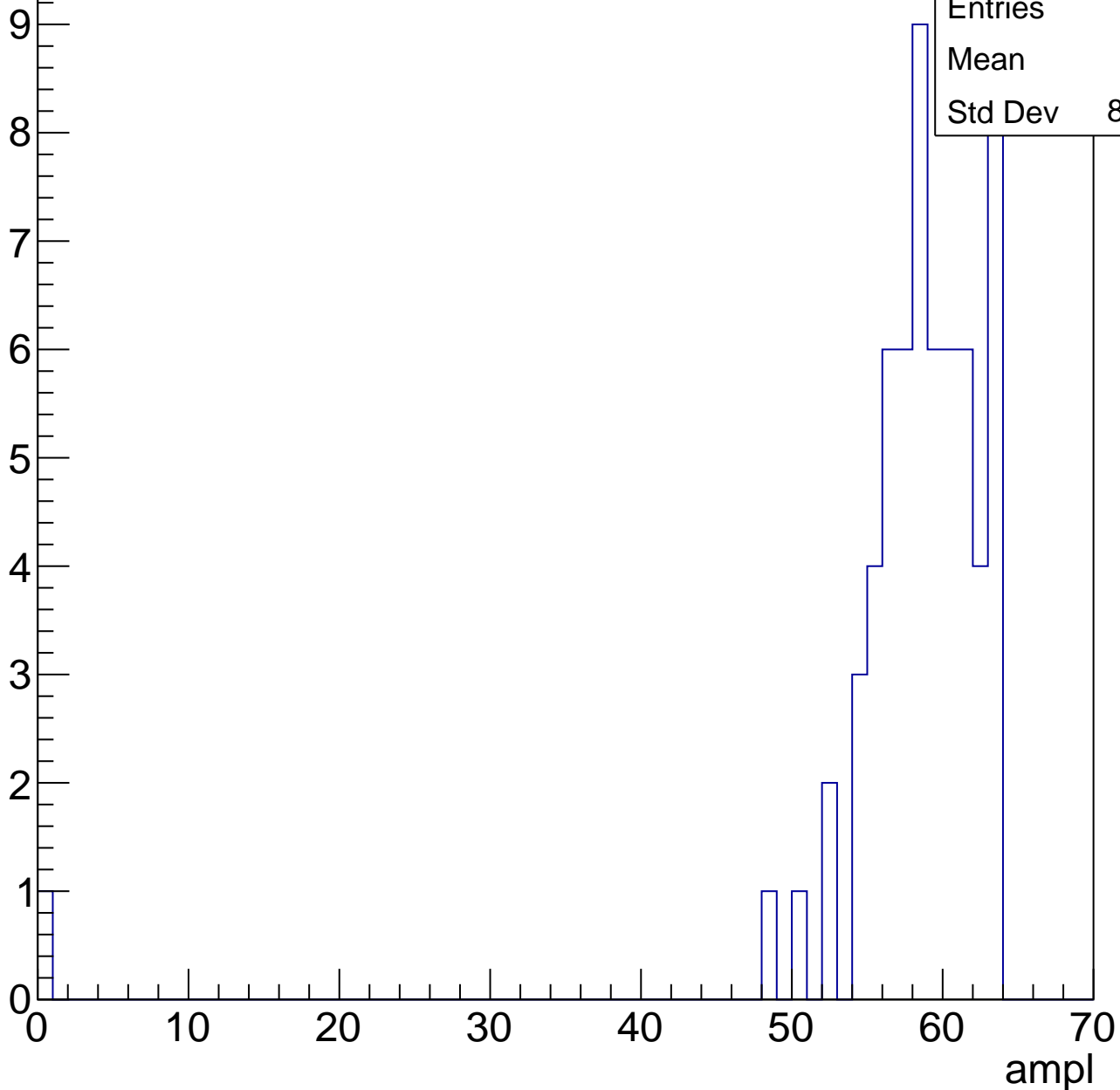


B1L103S, U20-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	57.4
Std Dev	8.007

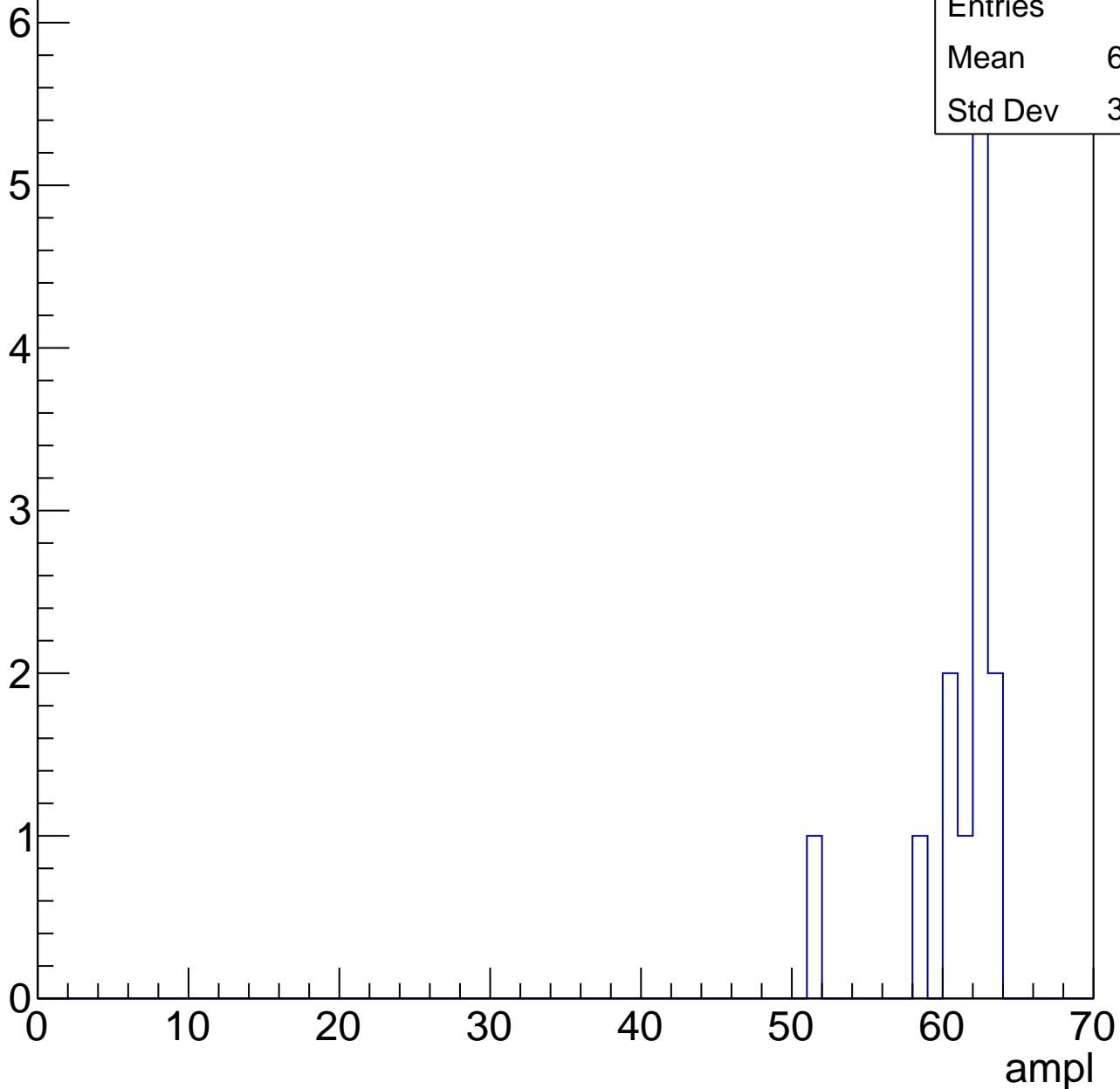


B1L103S, U20-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	60.62
Std Dev	3.077



B1L103S, U20-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch107, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	30.25
Std Dev	4.147

Gaus mean : 30.2827

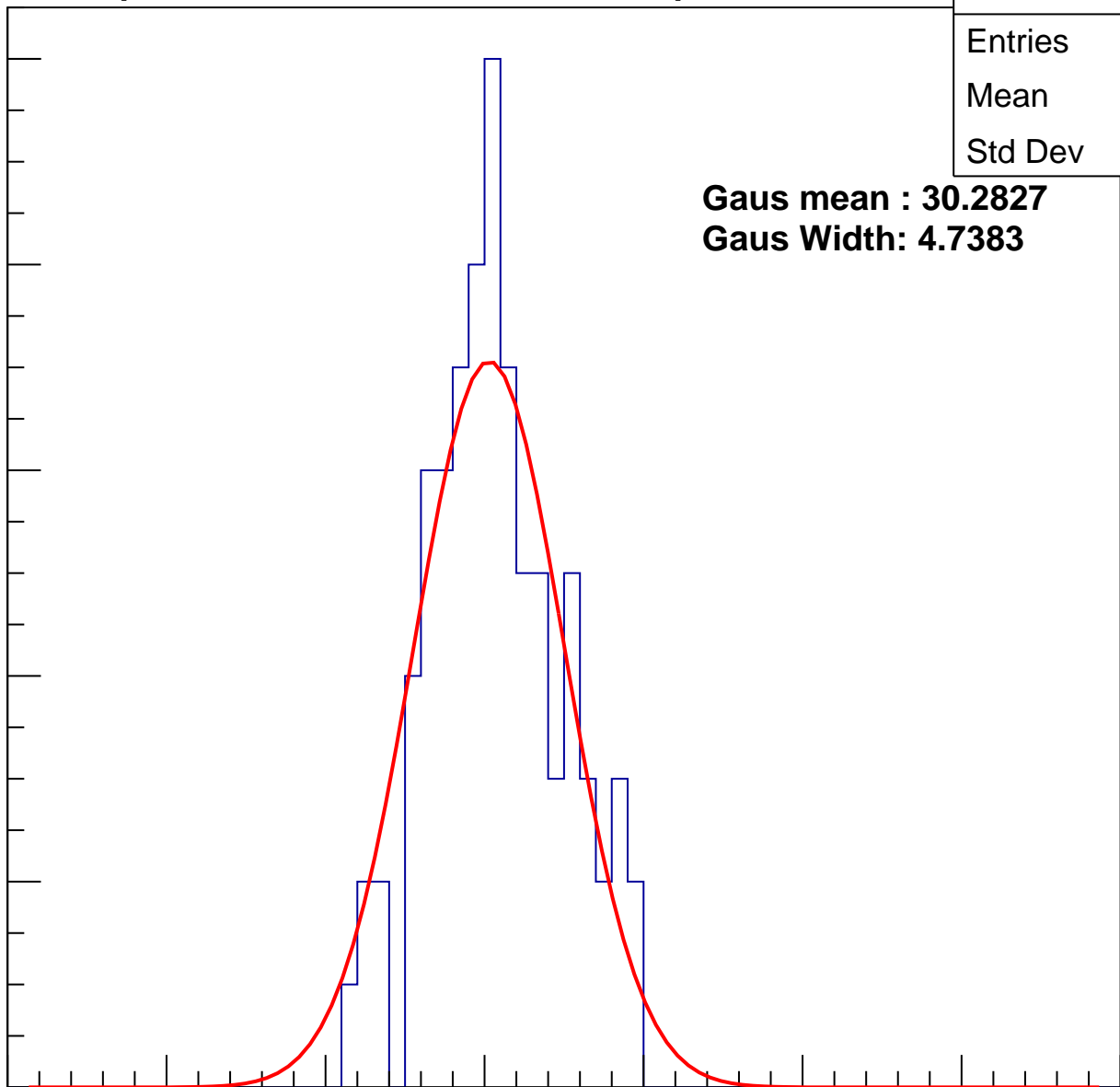
Gaus Width: 4.7383

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch107, adc1

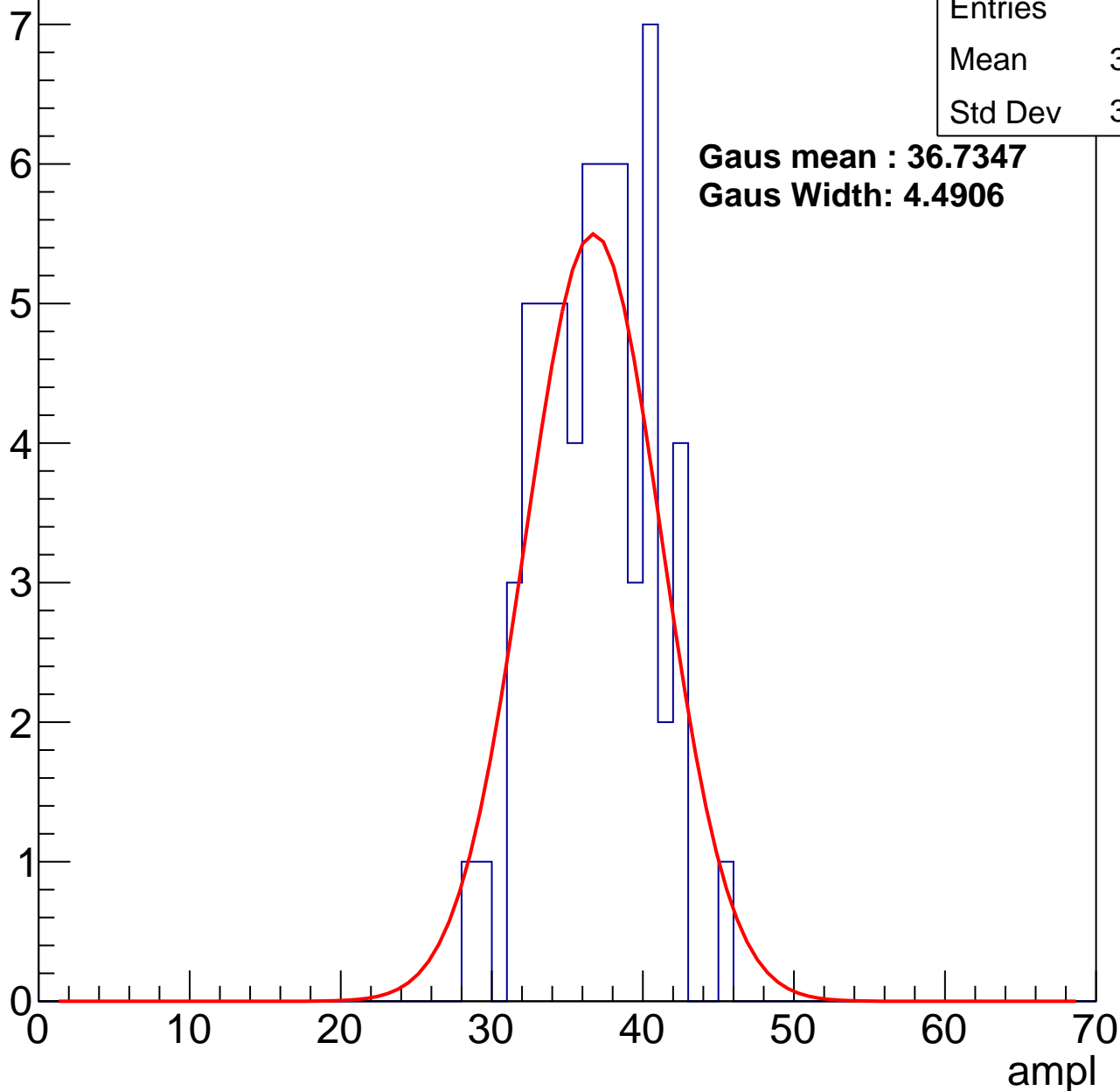
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	36.32
Std Dev	3.624

Gaus mean : 36.7347

Gaus Width: 4.4906



B1L103S, U20-ch107, adc2

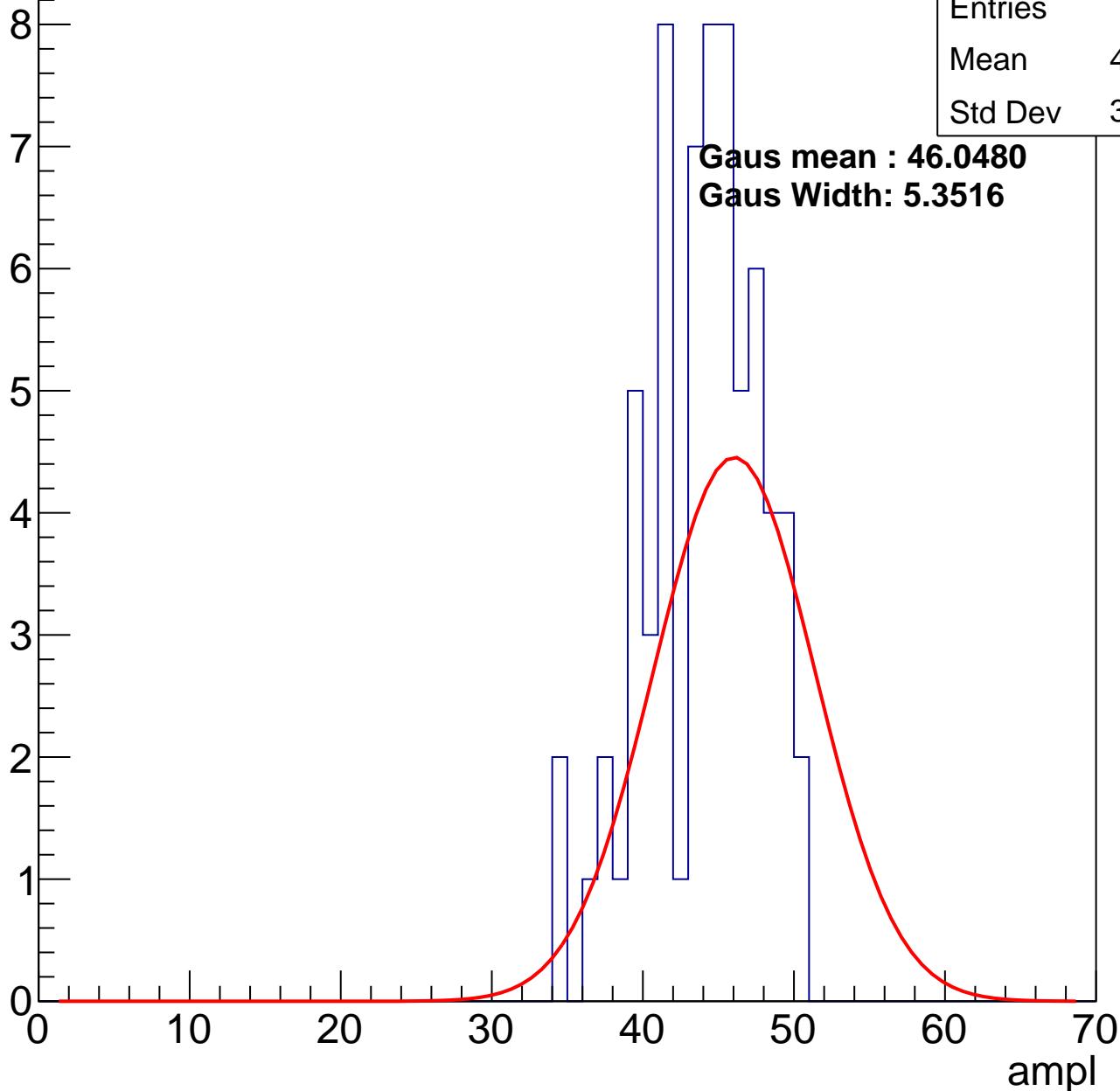
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.49
Std Dev	3.775

Gaus mean : 46.0480

Gaus Width: 5.3516

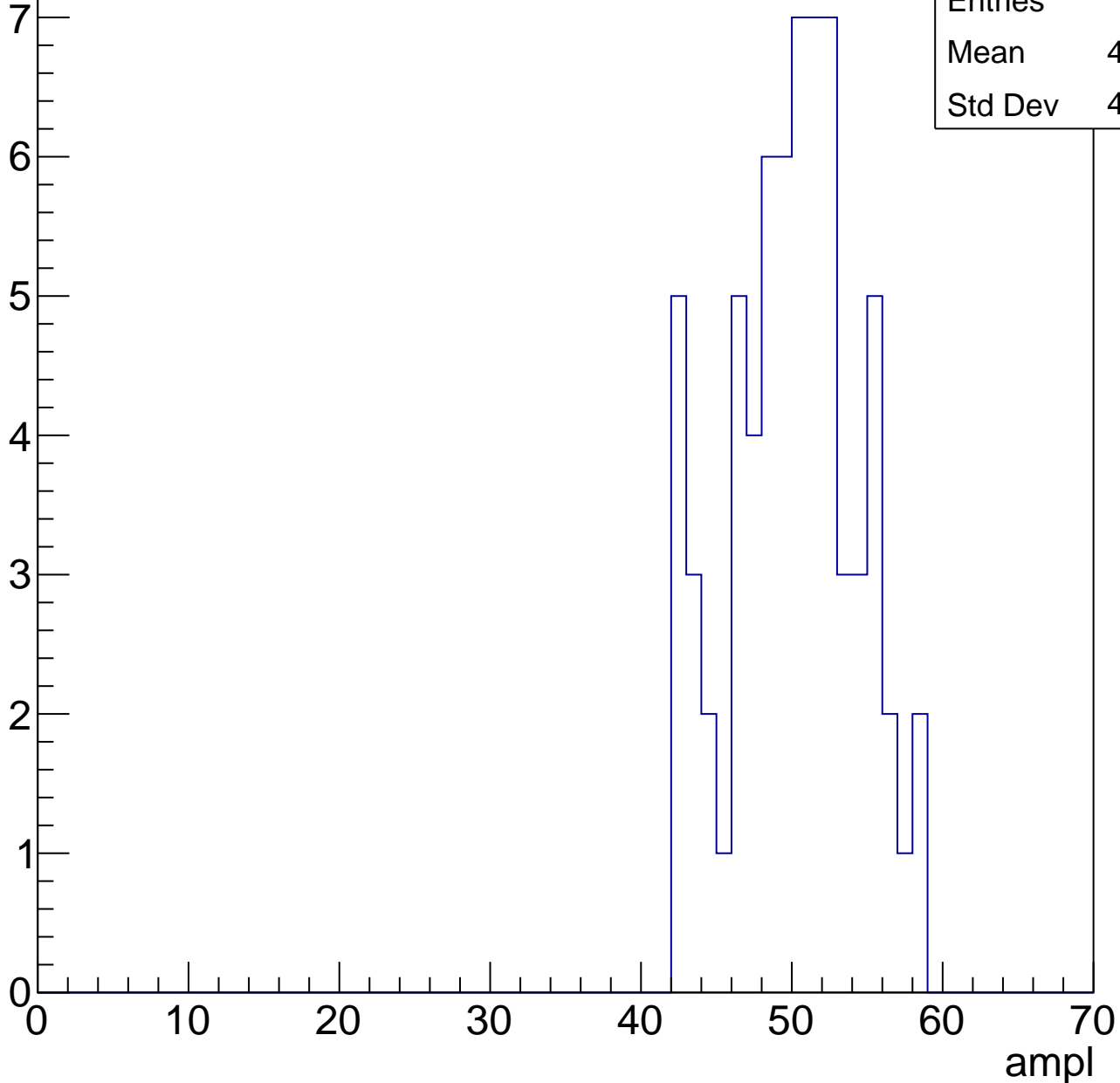


B1L103S, U20-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.62
Std Dev	4.143

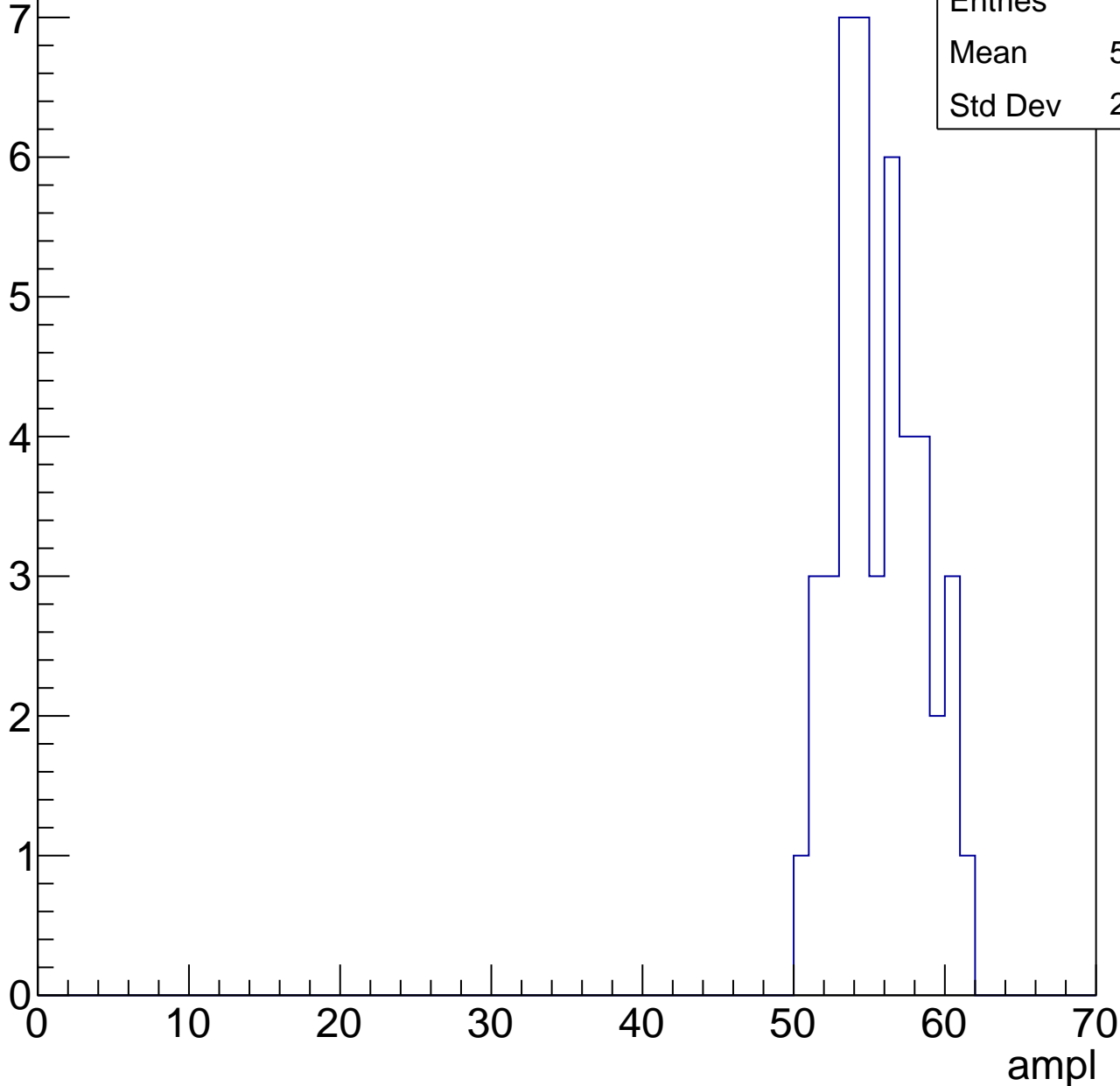


B1L103S, U20-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.18
Std Dev	2.757

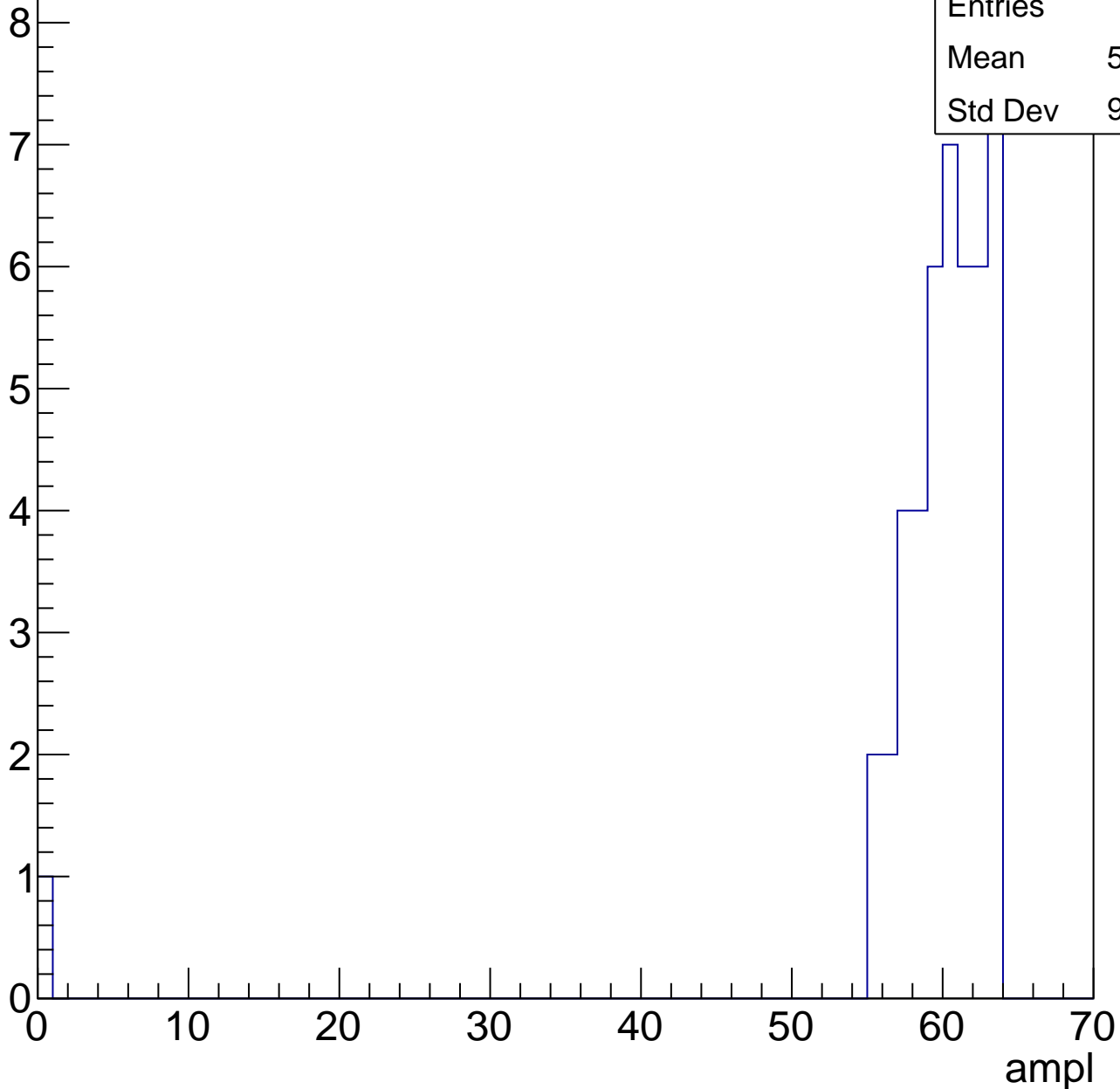


B1L103S, U20-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

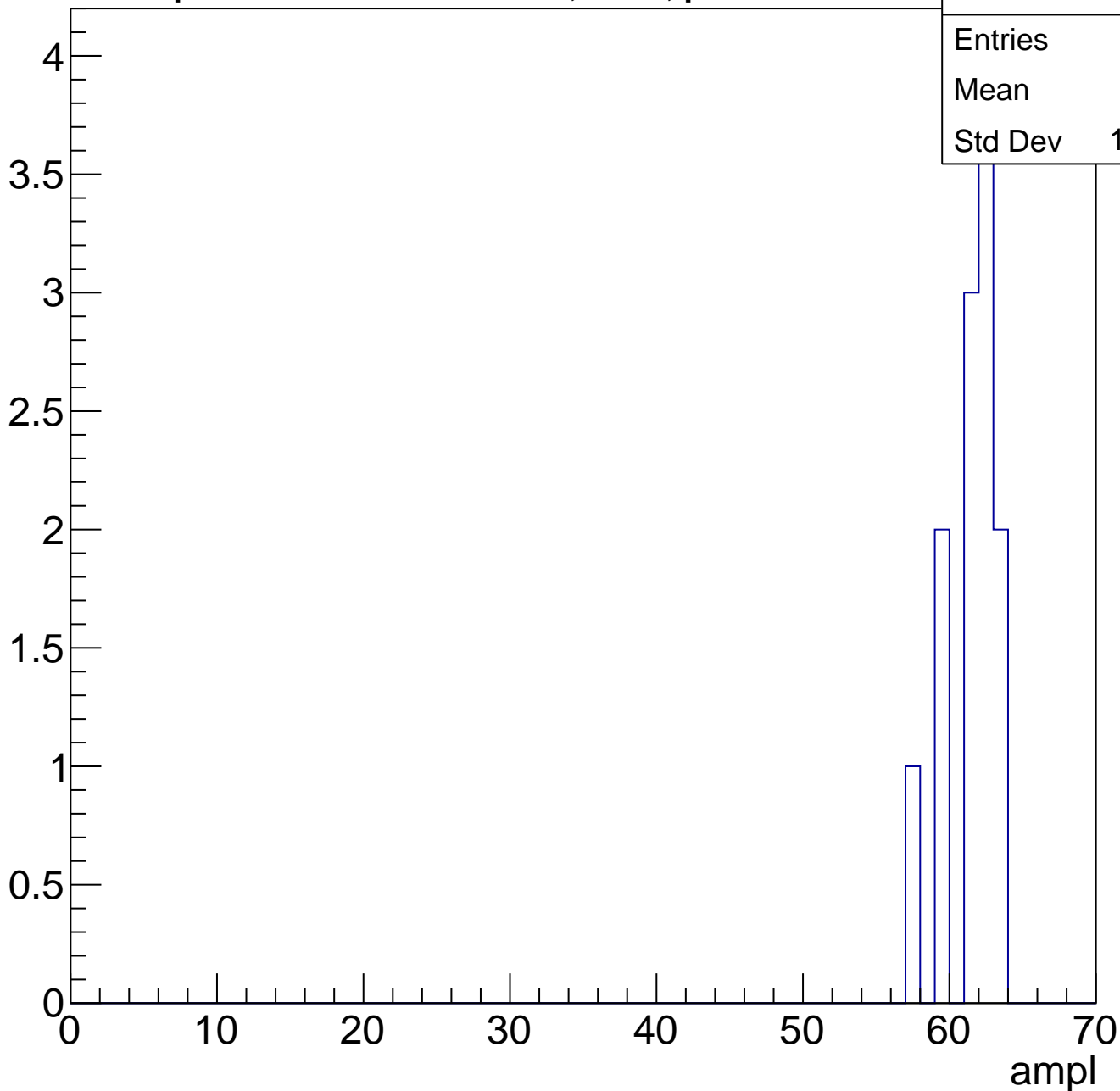
Entries	46
Mean	58.65
Std Dev	9.039



B1L103S, U20-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch108, adc0

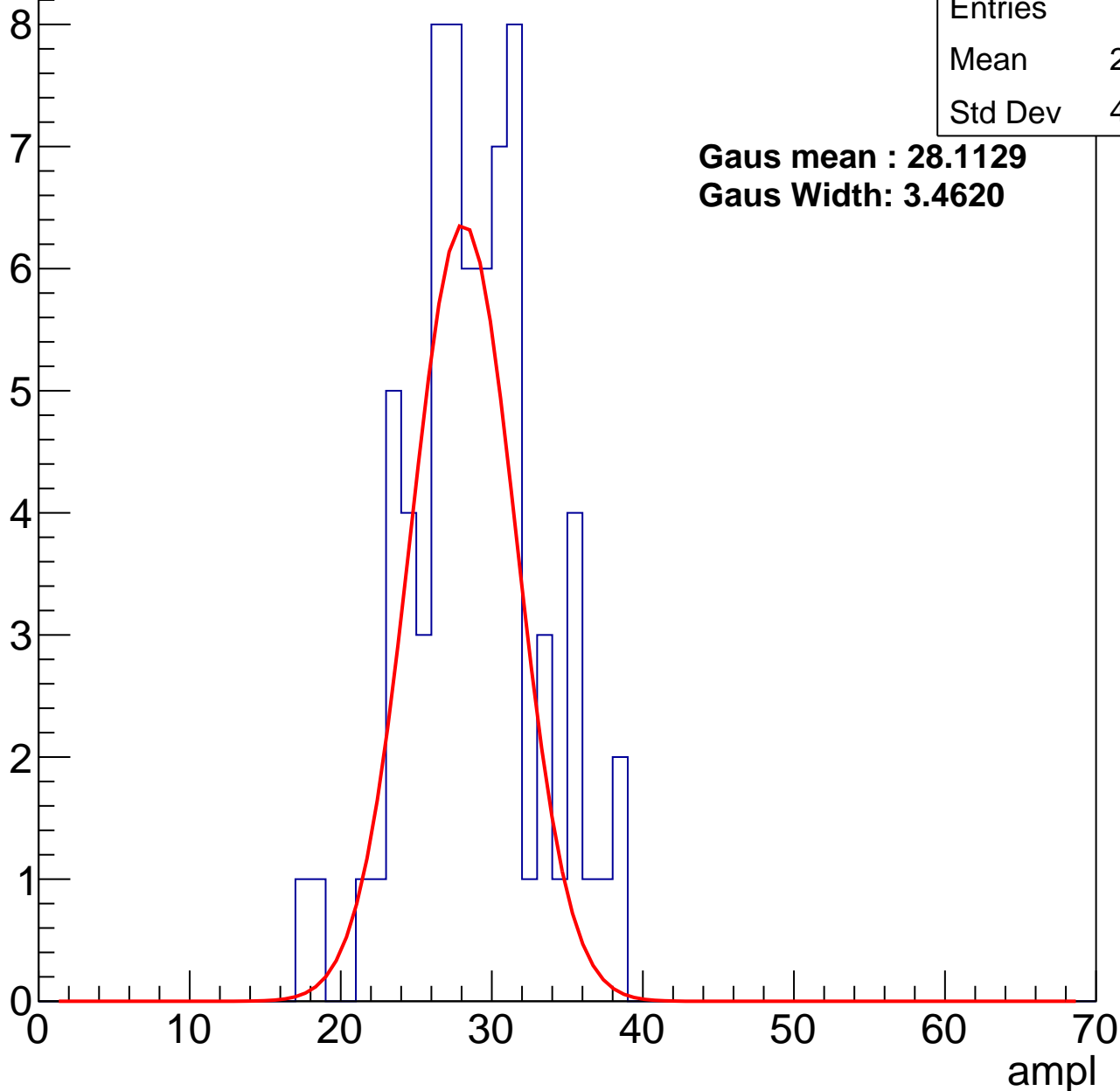
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.36
Std Dev	4.292

Gaus mean : 28.1129

Gaus Width: 3.4620



B1L103S, U20-ch108, adc1

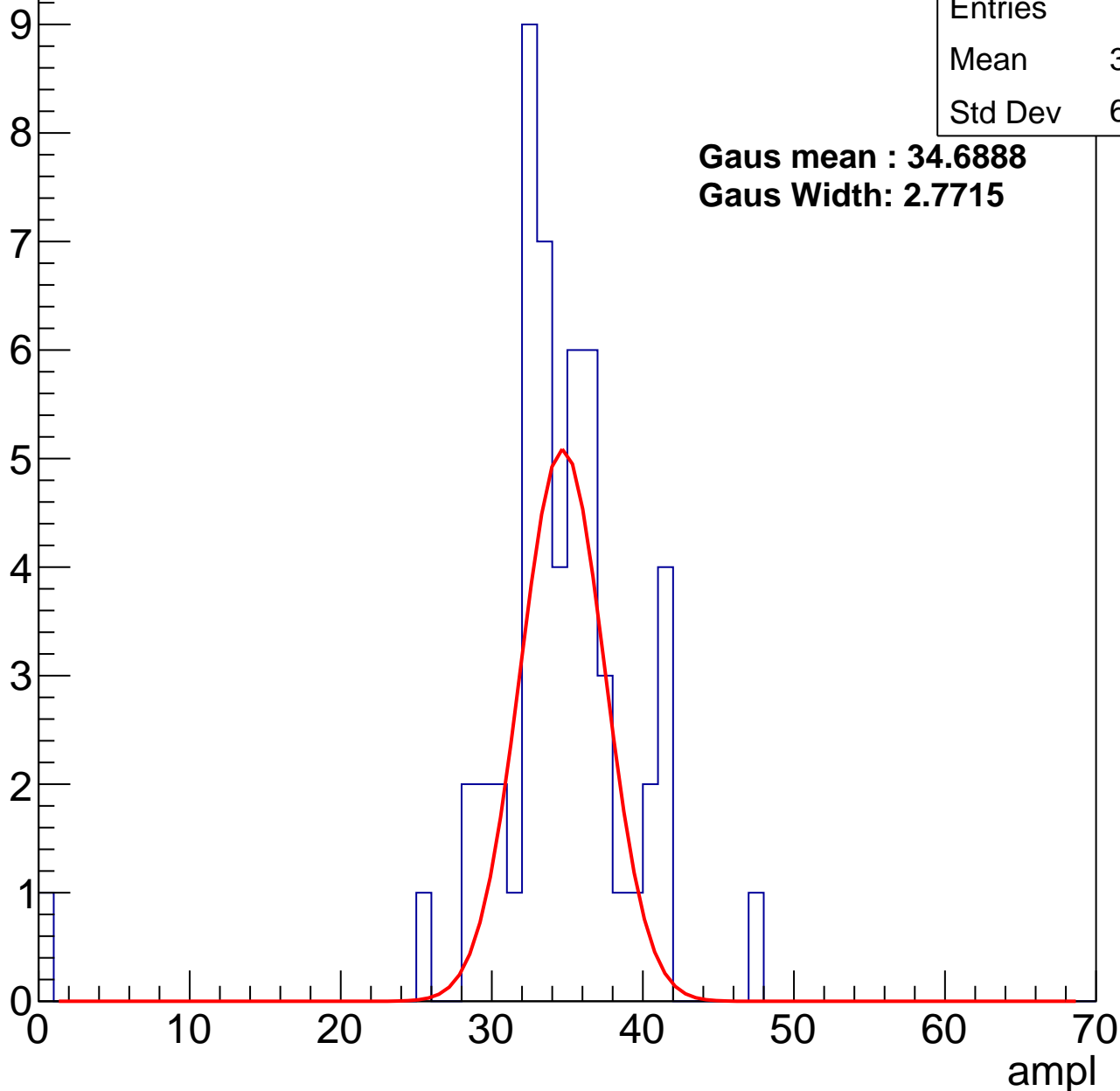
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	33.77
Std Dev	6.105

Gaus mean : 34.6888

Gaus Width: 2.7715



B1L103S, U20-ch108, adc2

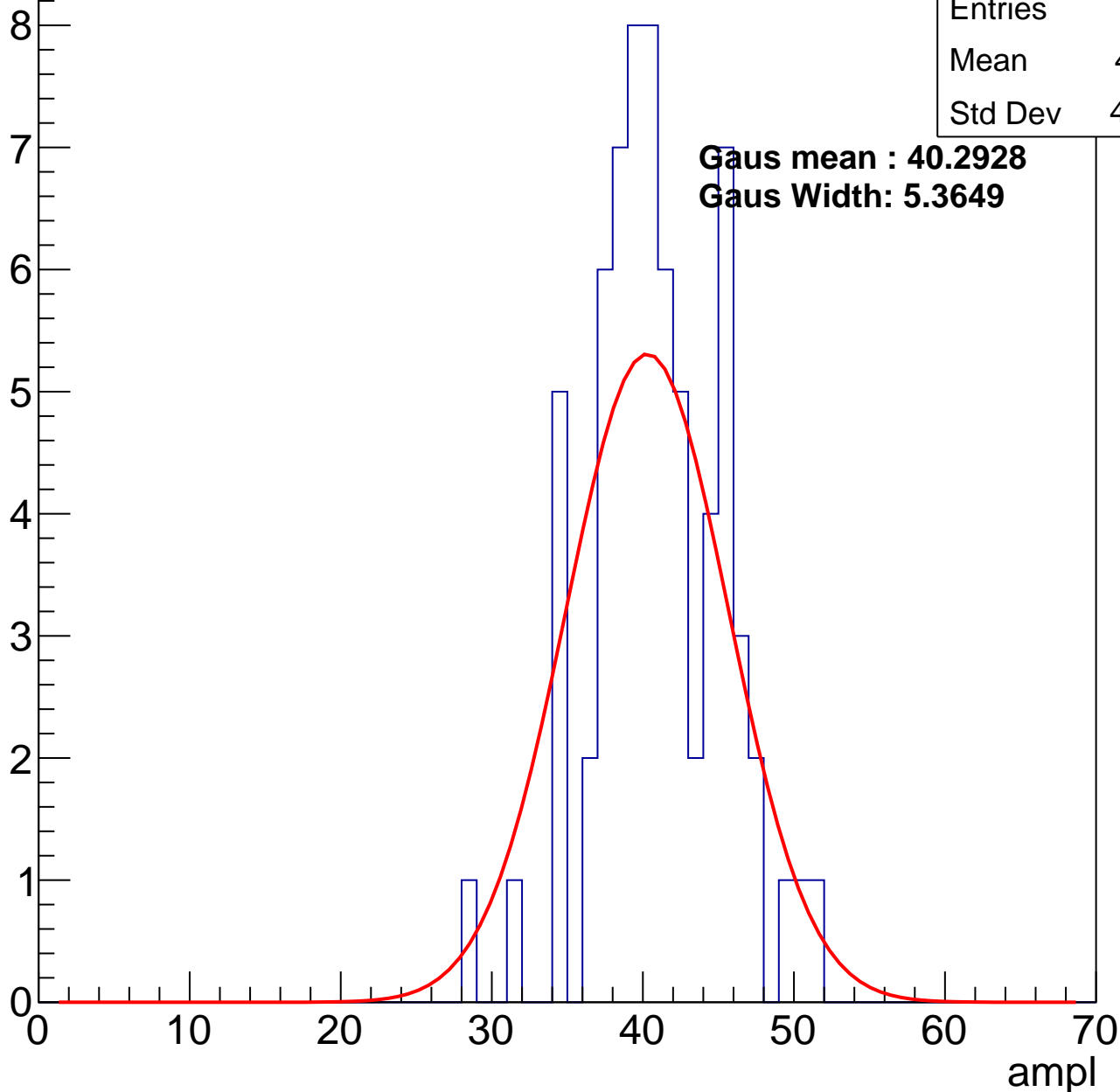
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	40.51
Std Dev	4.309

Gaus mean : 40.2928

Gaus Width: 5.3649

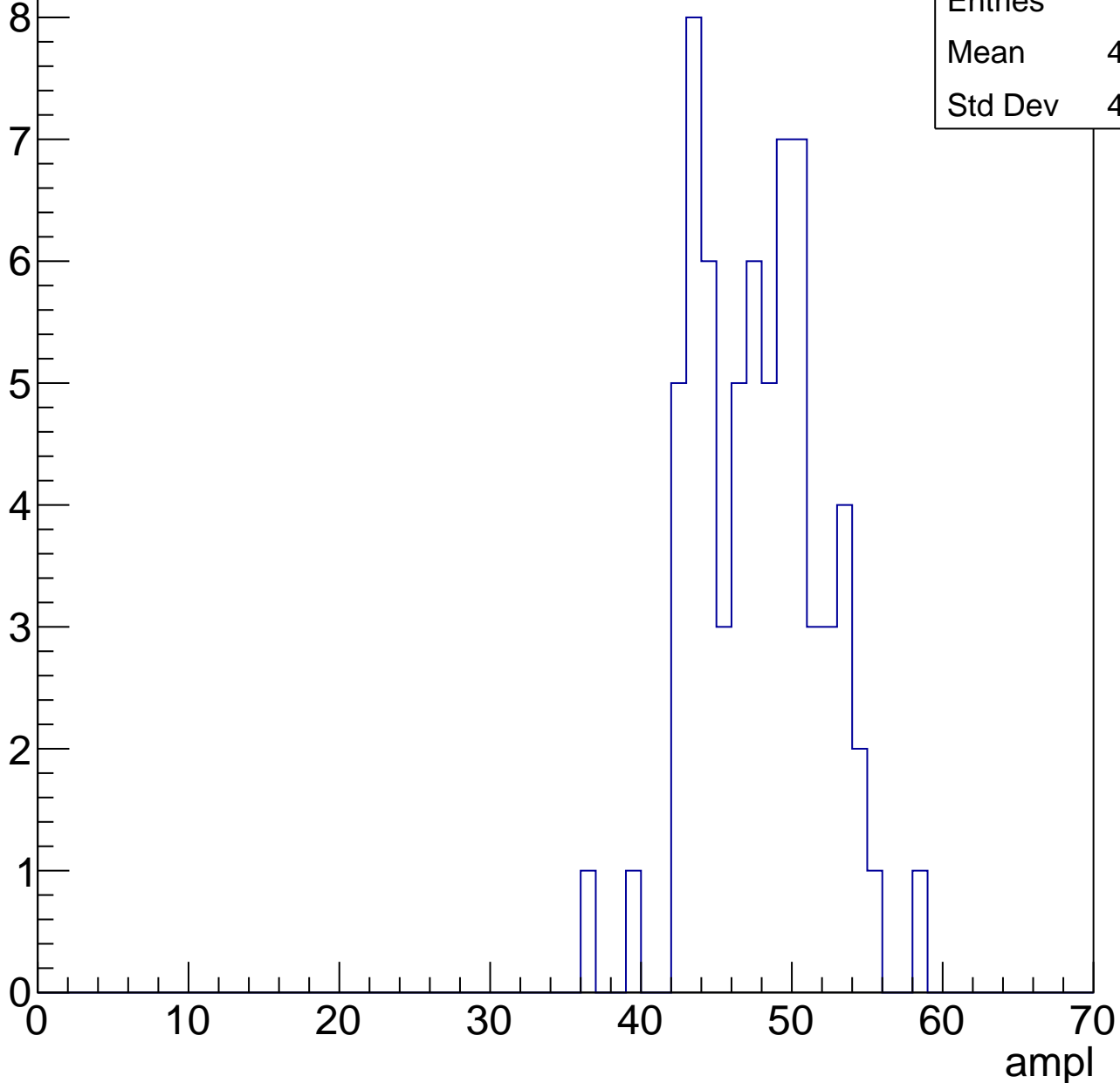


B1L103S, U20-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	47.28
Std Dev	4.112

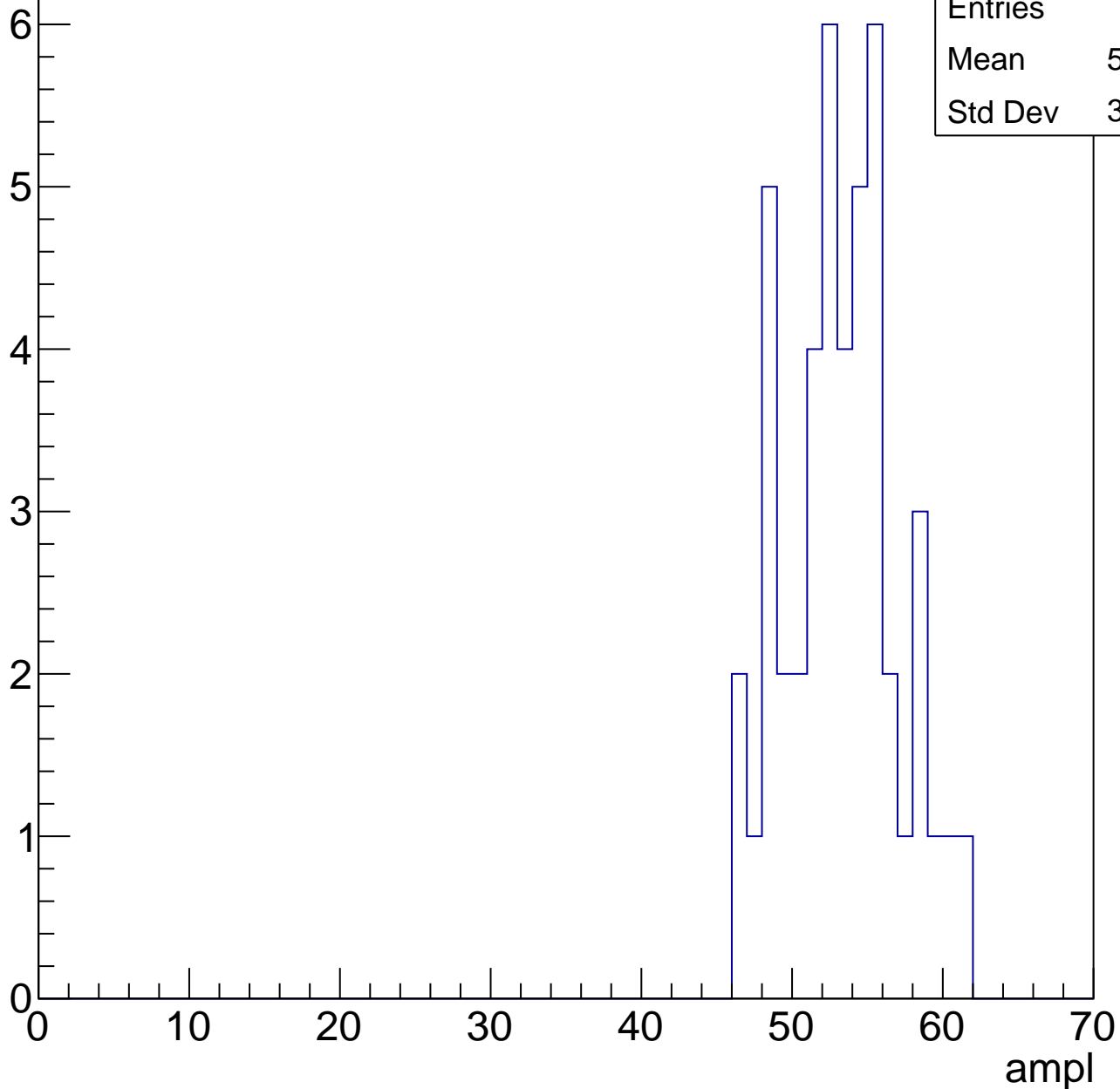


B1L103S, U20-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	52.78
Std Dev	3.659

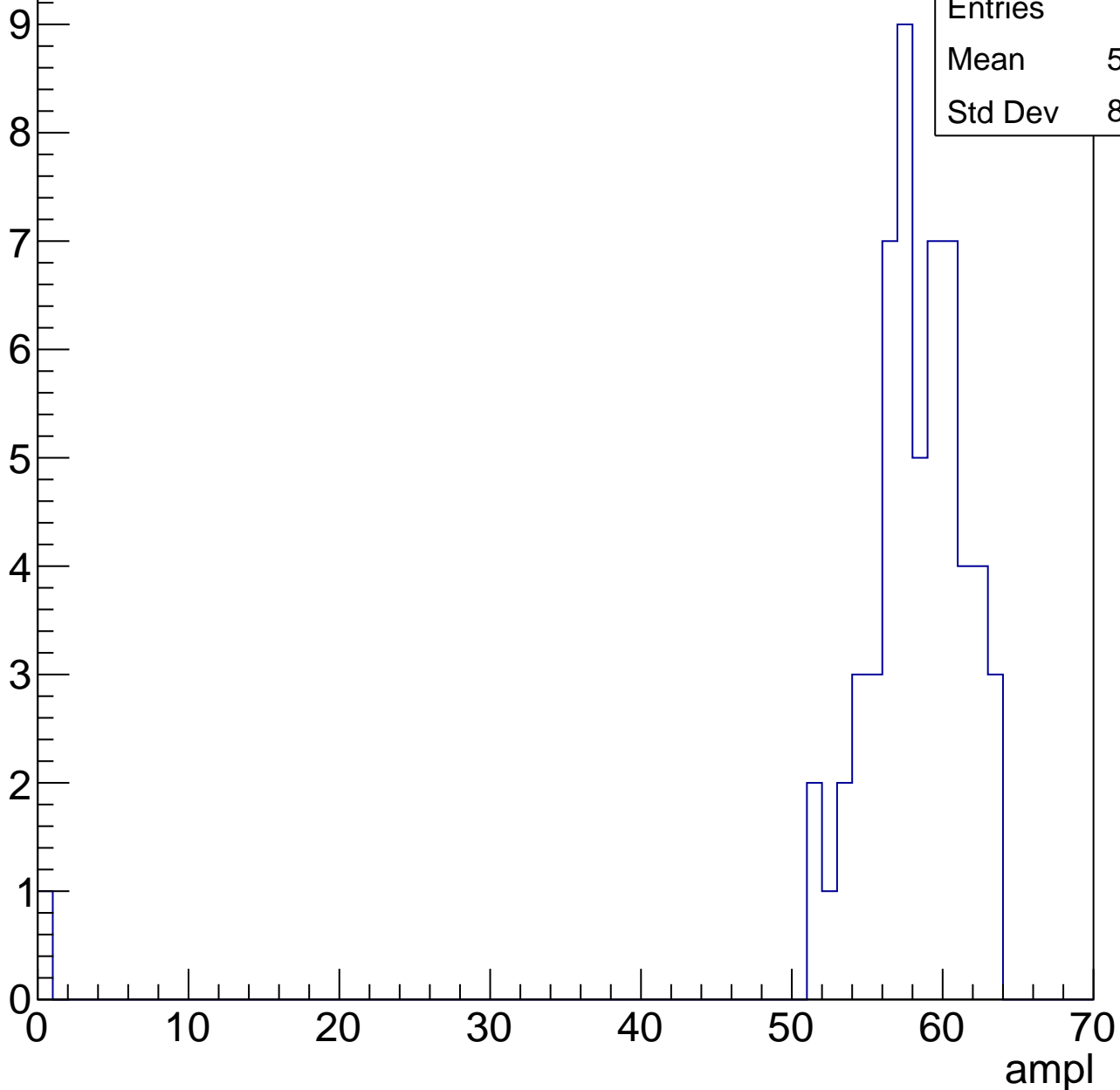


B1L103S, U20-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.83
Std Dev	8.084

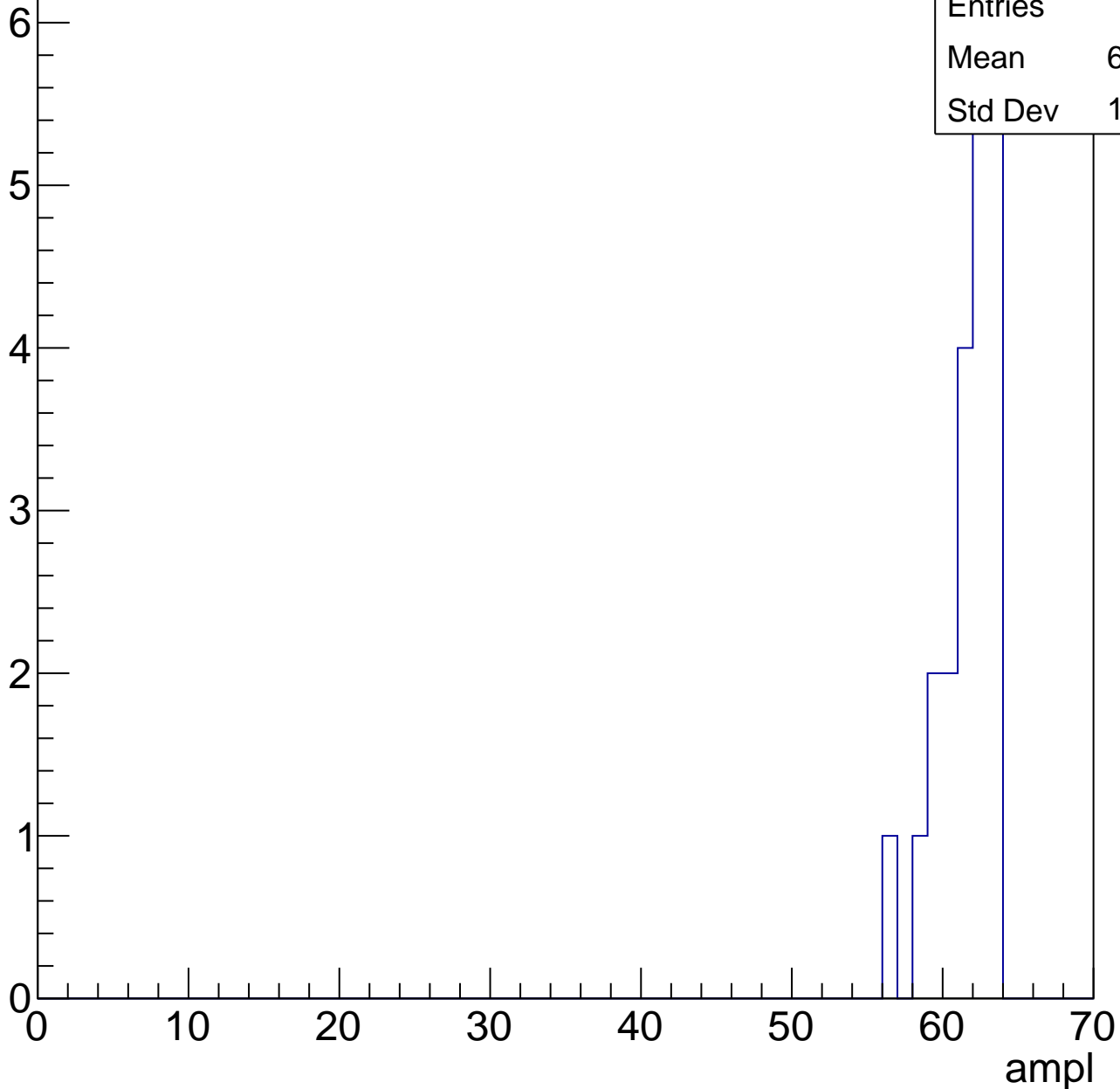


B1L103S, U20-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	22
Mean	61.18
Std Dev	1.825



B1L103S, U20-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch109, adc0

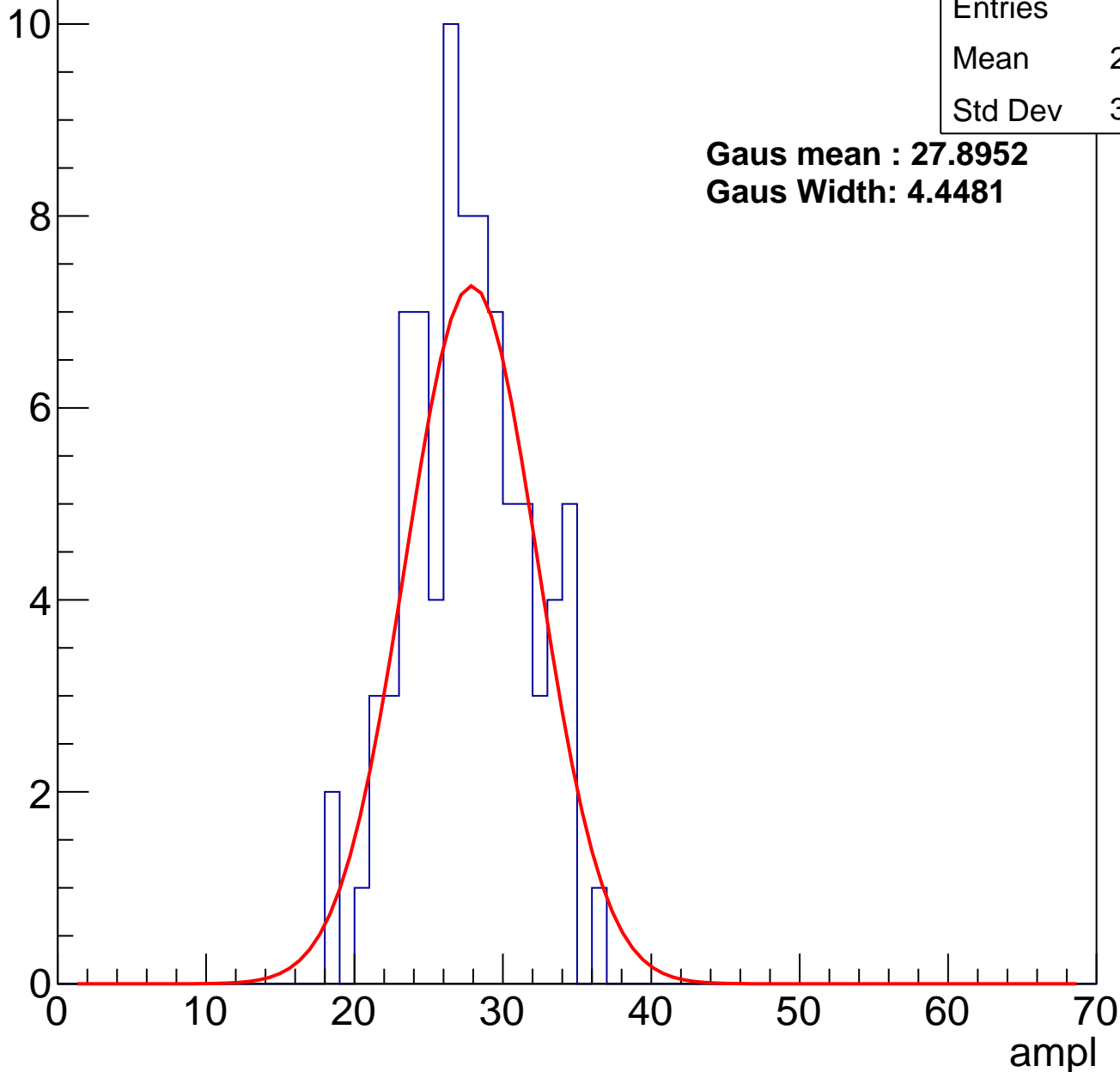
calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	27.18
Std Dev	3.955

Gaus mean : 27.8952

Gaus Width: 4.4481

Entry



B1L103S, U20-ch109, adc1

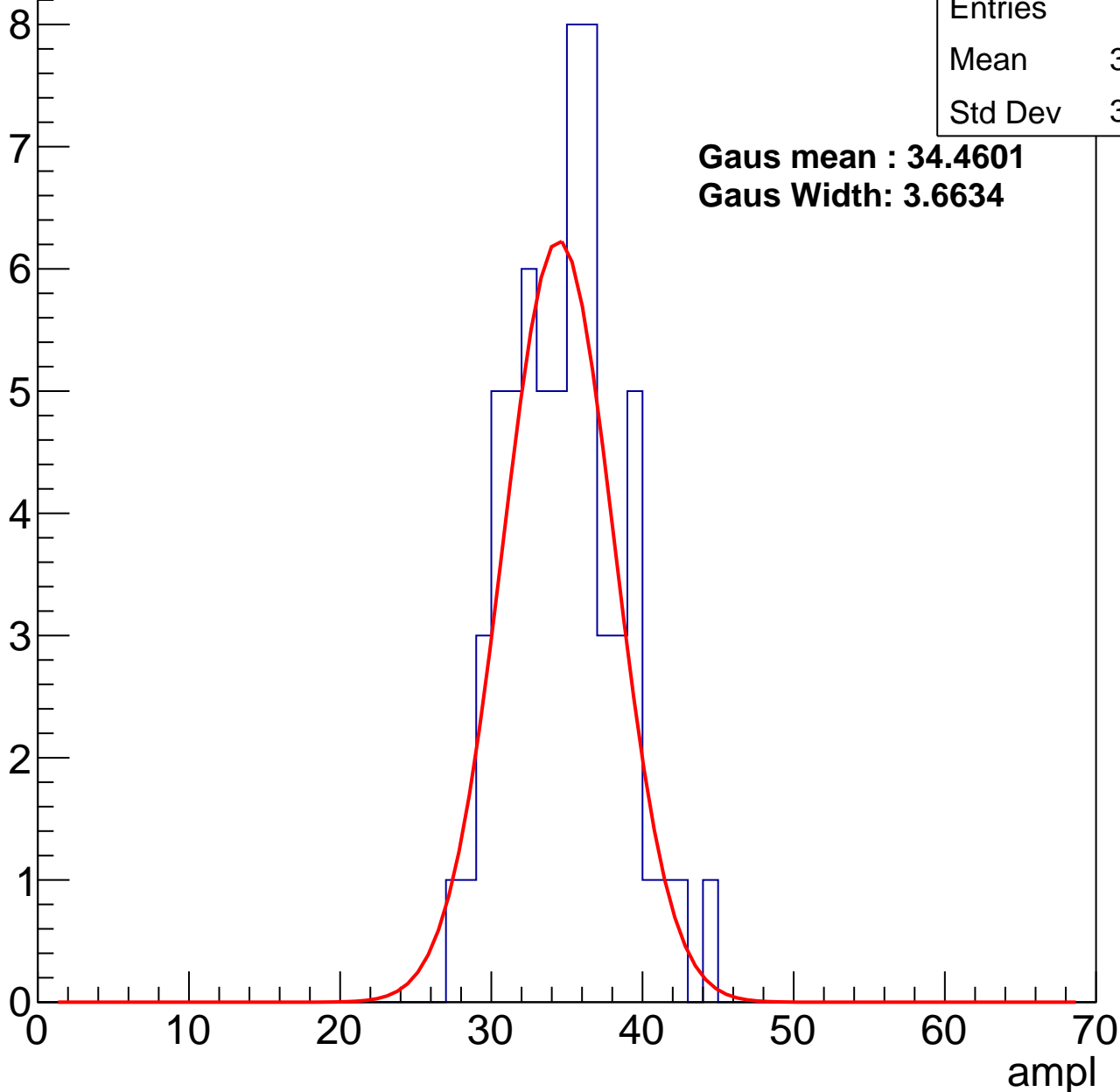
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	34.34
Std Dev	3.583

Gaus mean : 34.4601

Gaus Width: 3.6634



B1L103S, U20-ch109, adc2

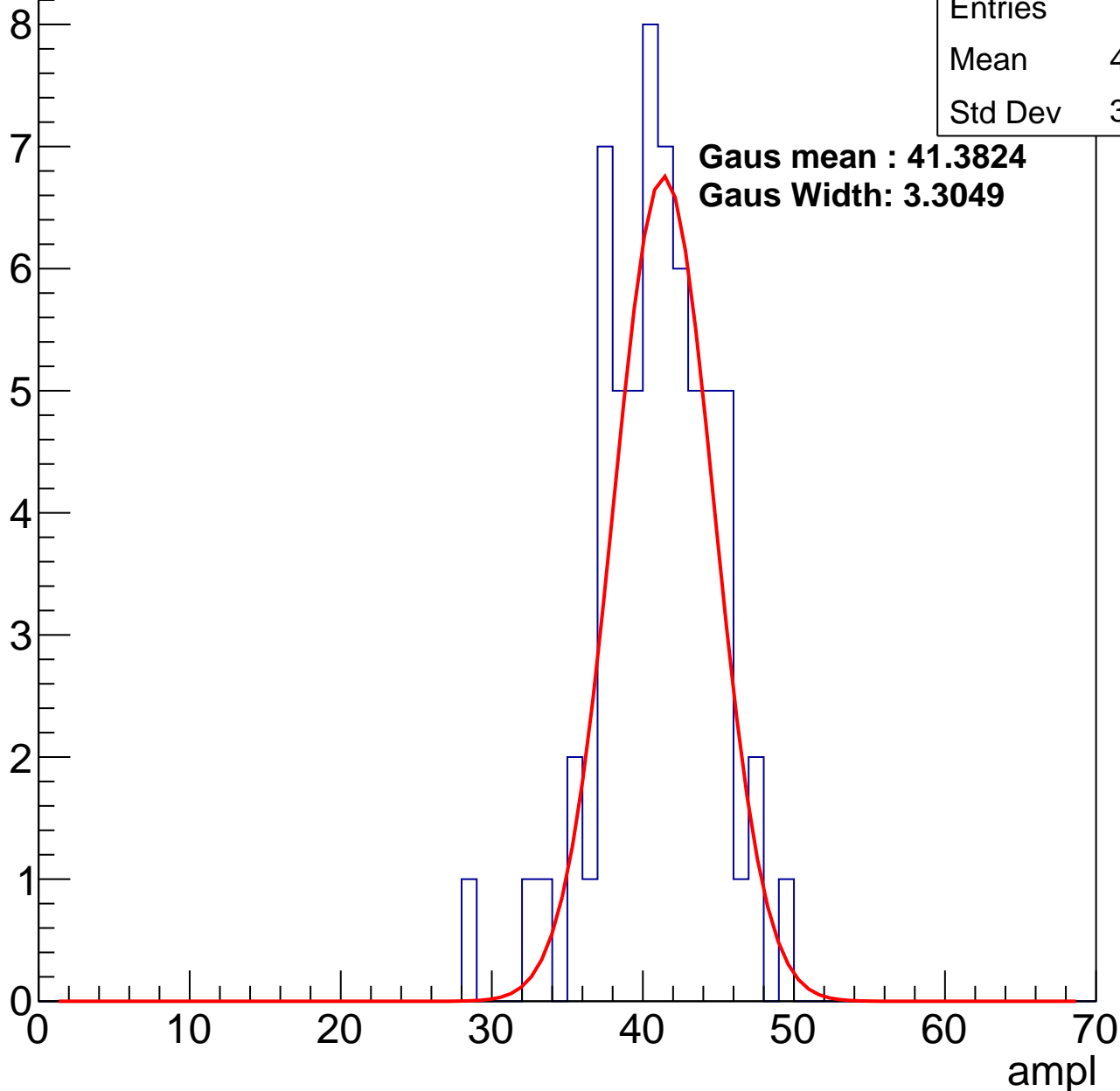
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	40.49
Std Dev	3.762

Gaus mean : 41.3824

Gaus Width: 3.3049

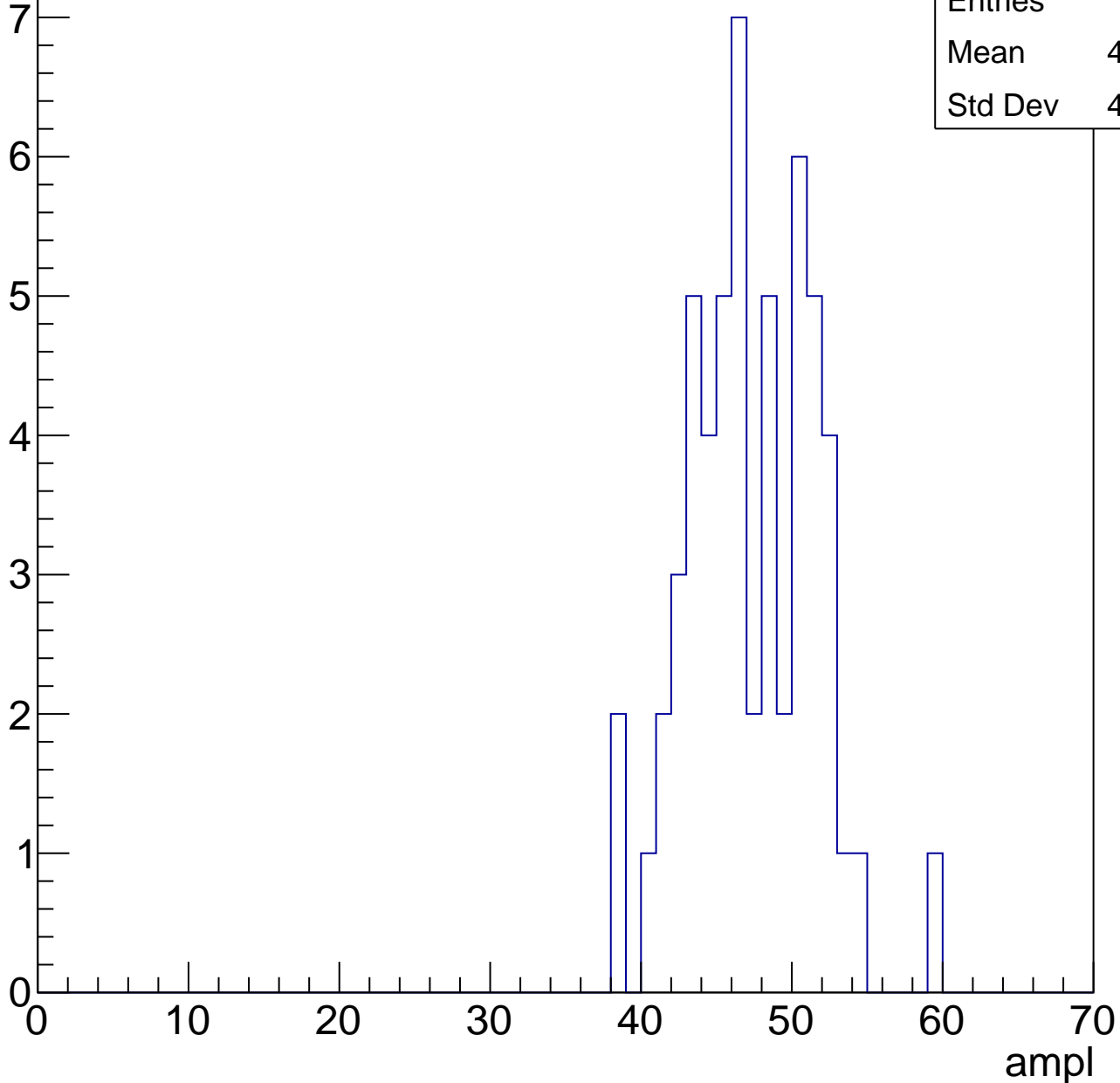


B1L103S, U20-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	46.84
Std Dev	4.174

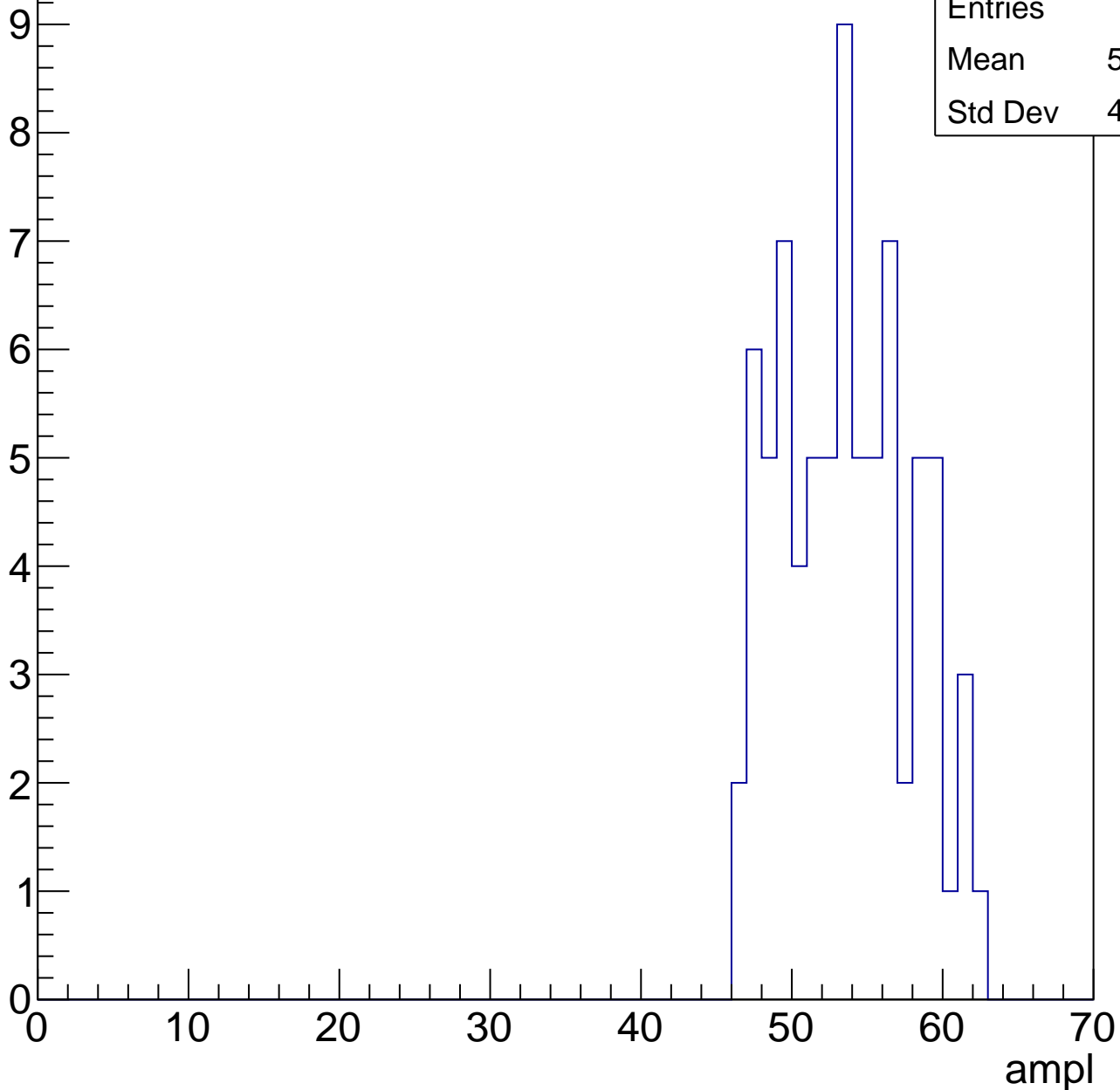


B1L103S, U20-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	53.12
Std Dev	4.199

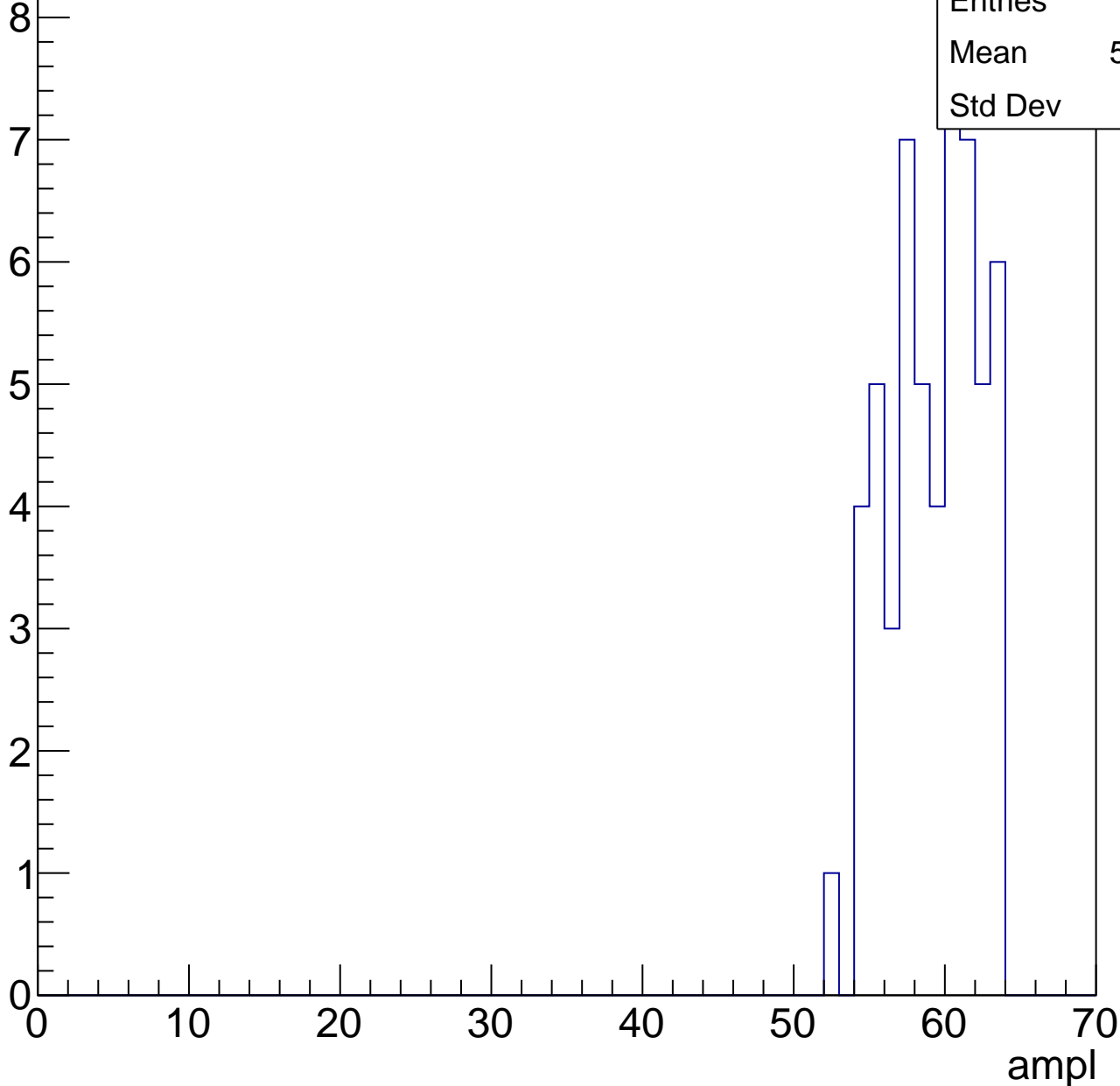


B1L103S, U20-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

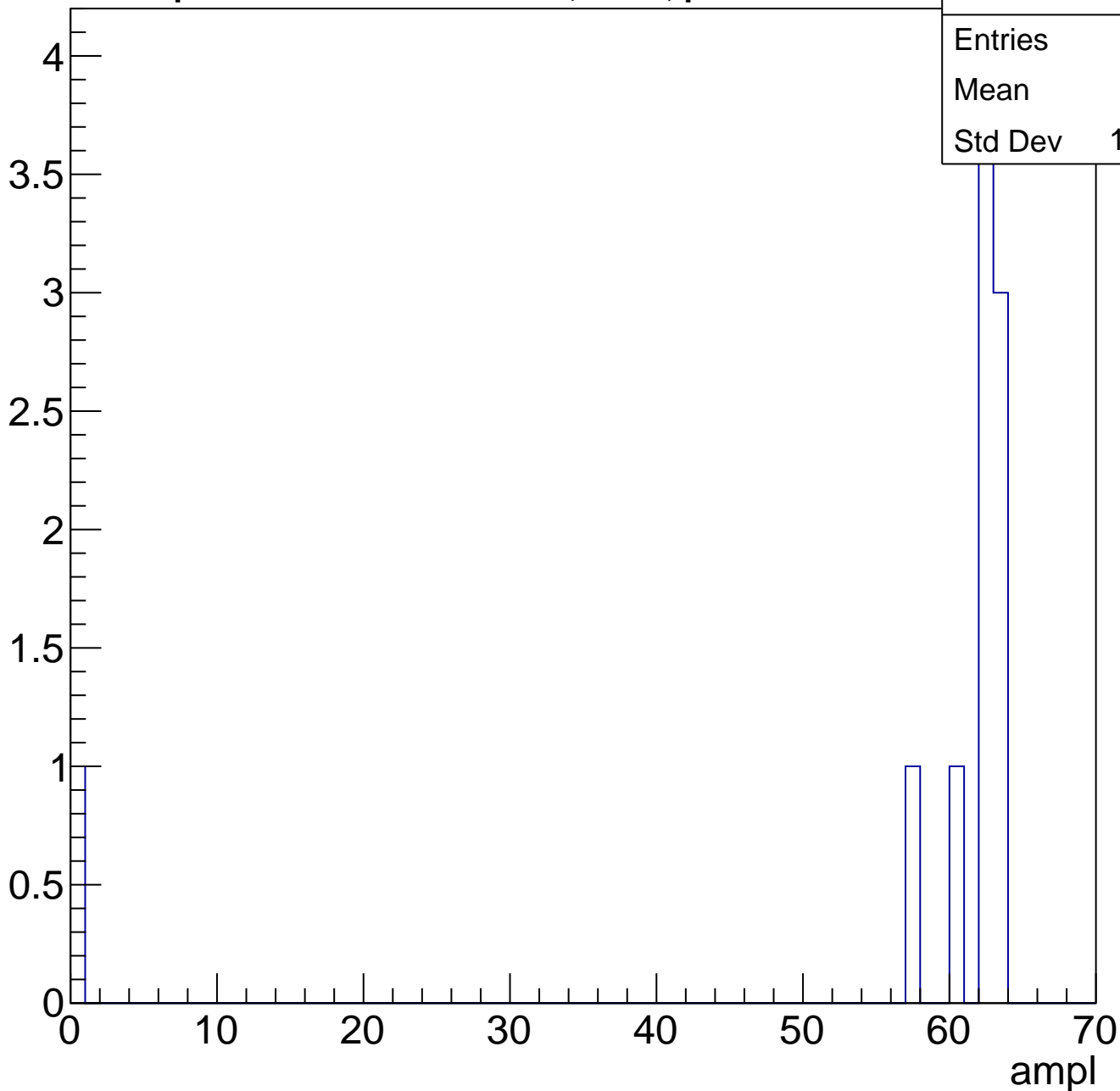
Entries	55
Mean	58.75
Std Dev	2.9



B1L103S, U20-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch110, adc0

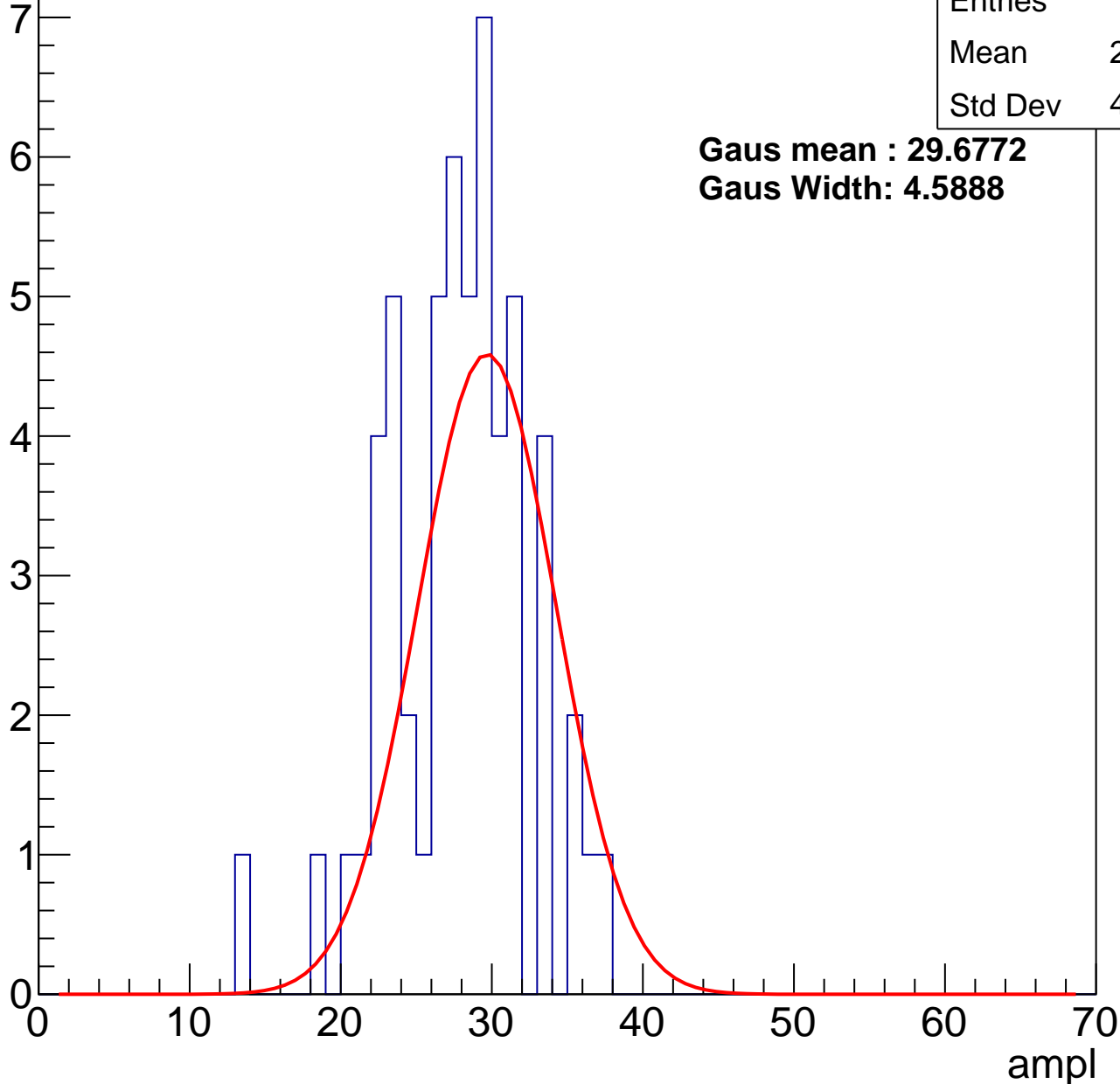
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	27.38
Std Dev	4.557

Gaus mean : 29.6772

Gaus Width: 4.5888



B1L103S, U20-ch110, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	35.66
Std Dev	5.856

Gaus mean : 36.1946

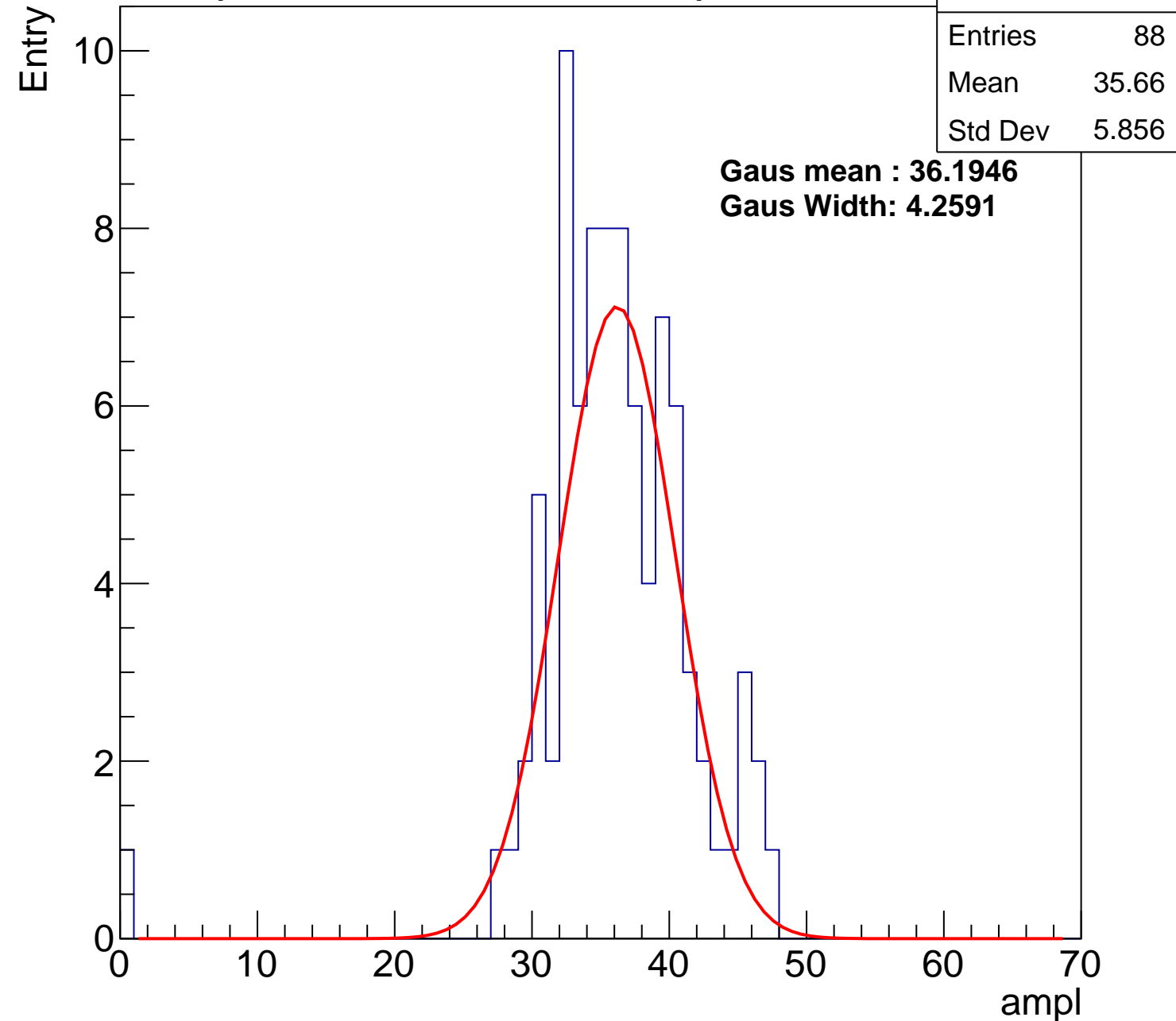
Gaus Width: 4.2591

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch110, adc2

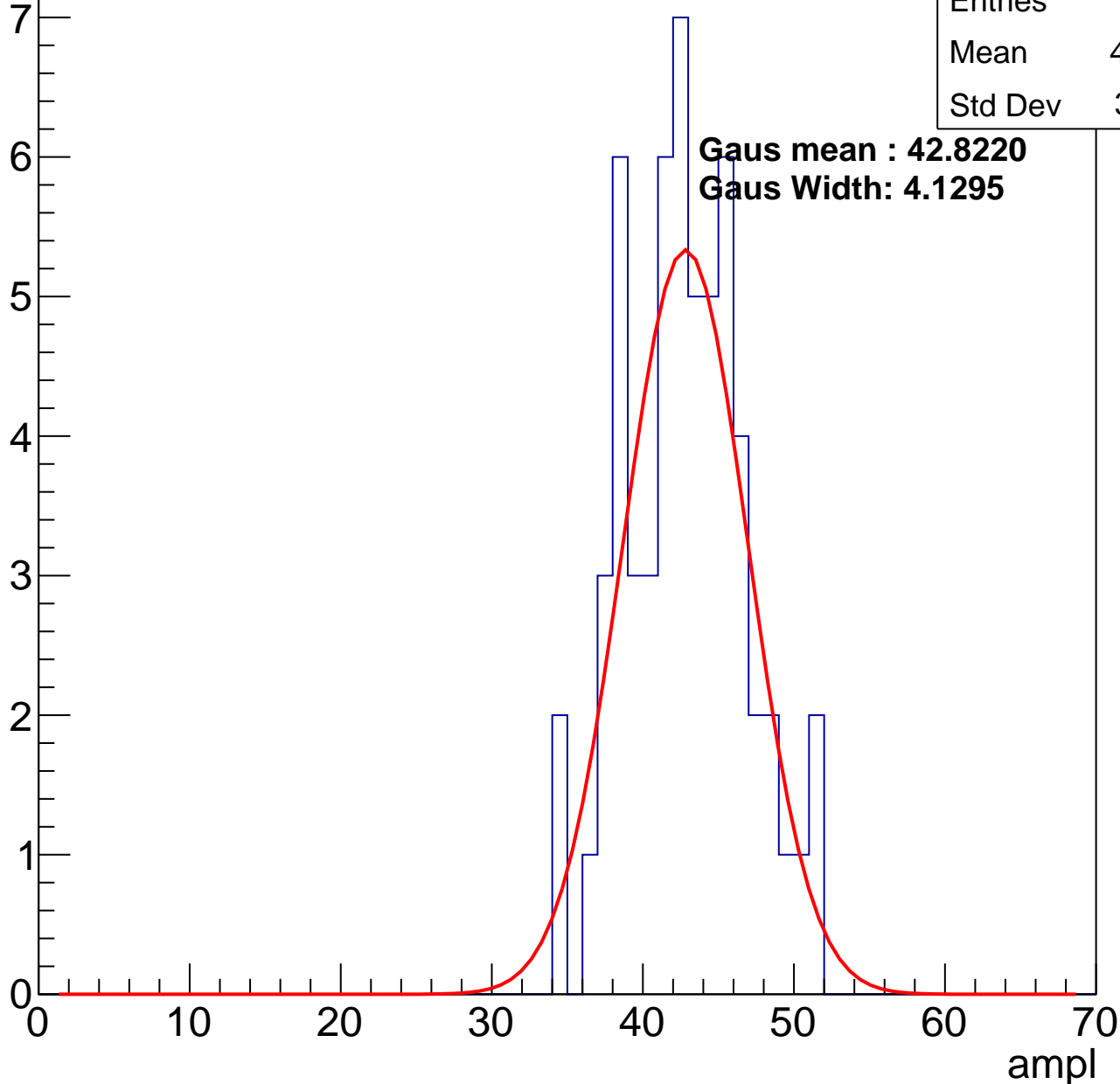
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.37
Std Dev	3.931

Gaus mean : 42.8220

Gaus Width: 4.1295

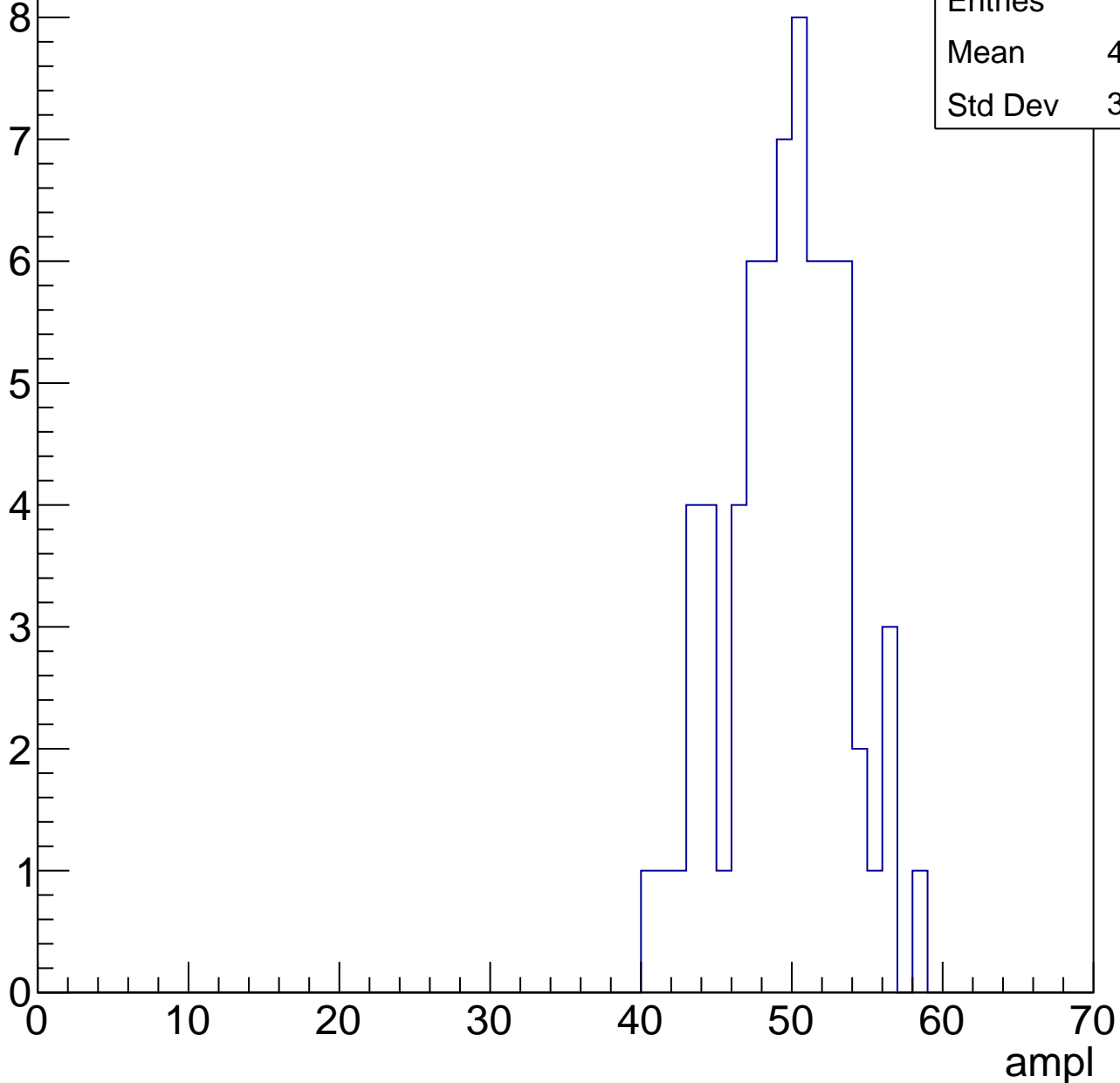


B1L103S, U20-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.09
Std Dev	3.864

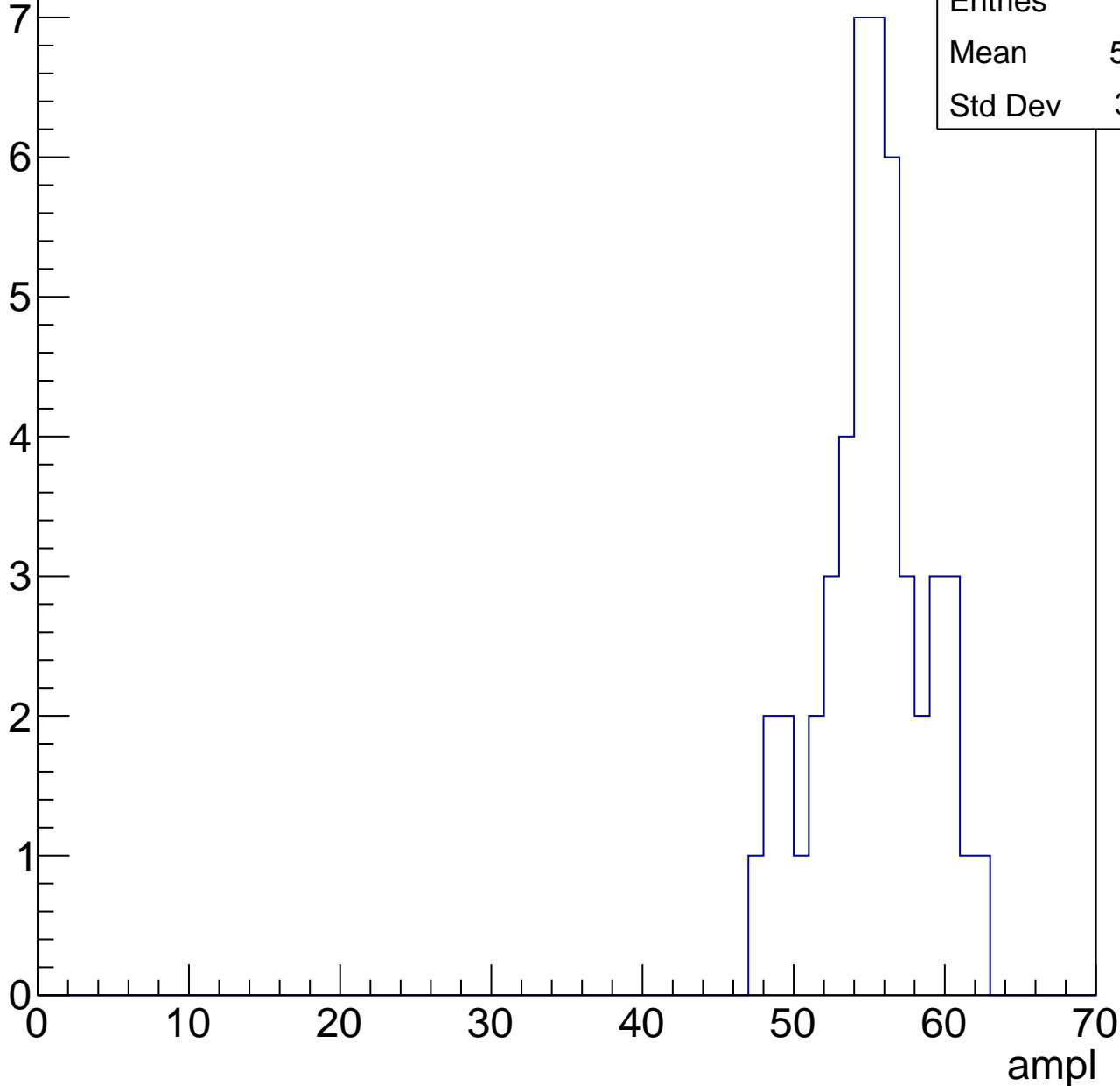


B1L103S, U20-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	54.73
Std Dev	3.481

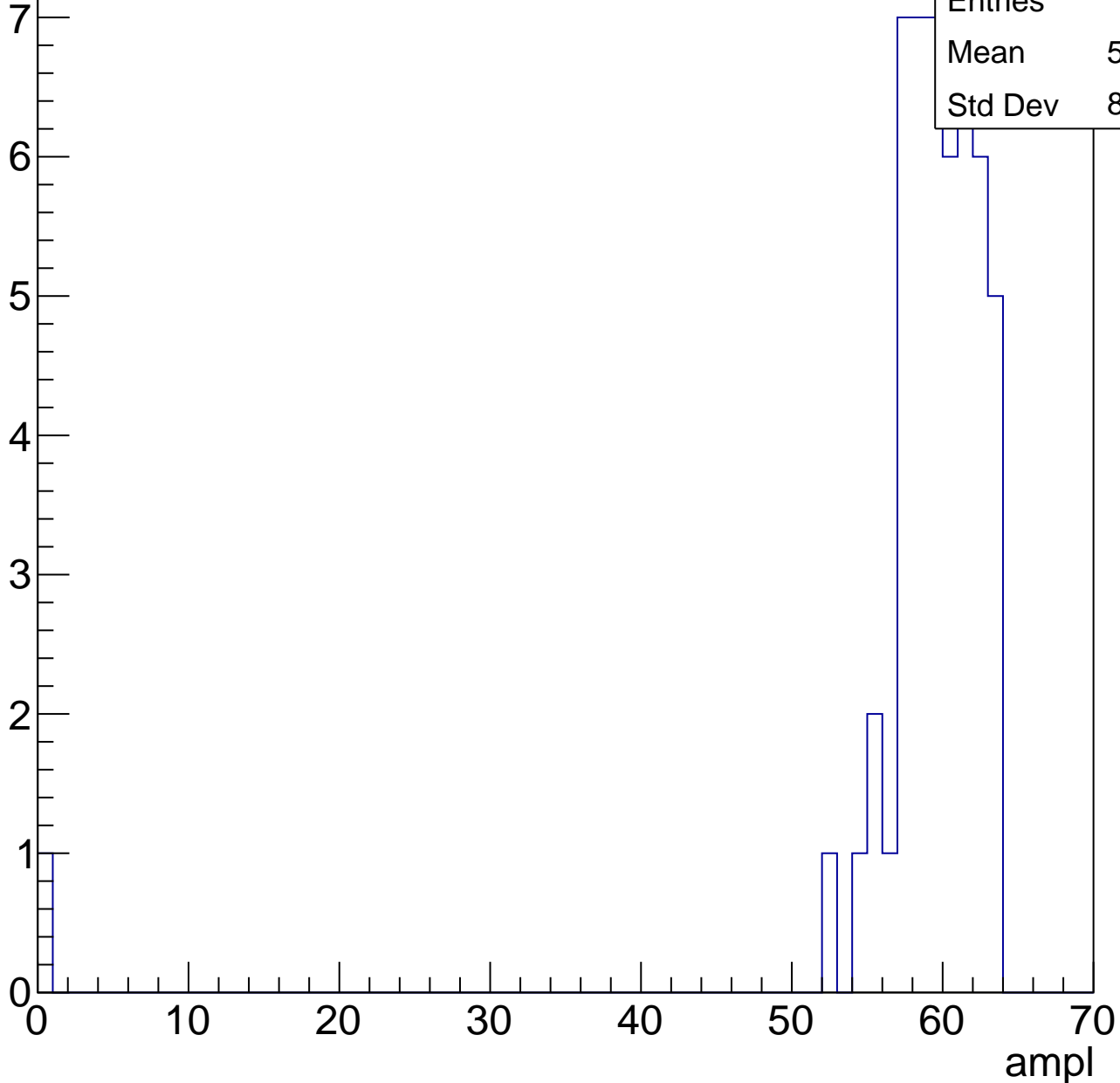


B1L103S, U20-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.12
Std Dev	8.586



B1L103S, U20-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	62.11
Std Dev	0.9938

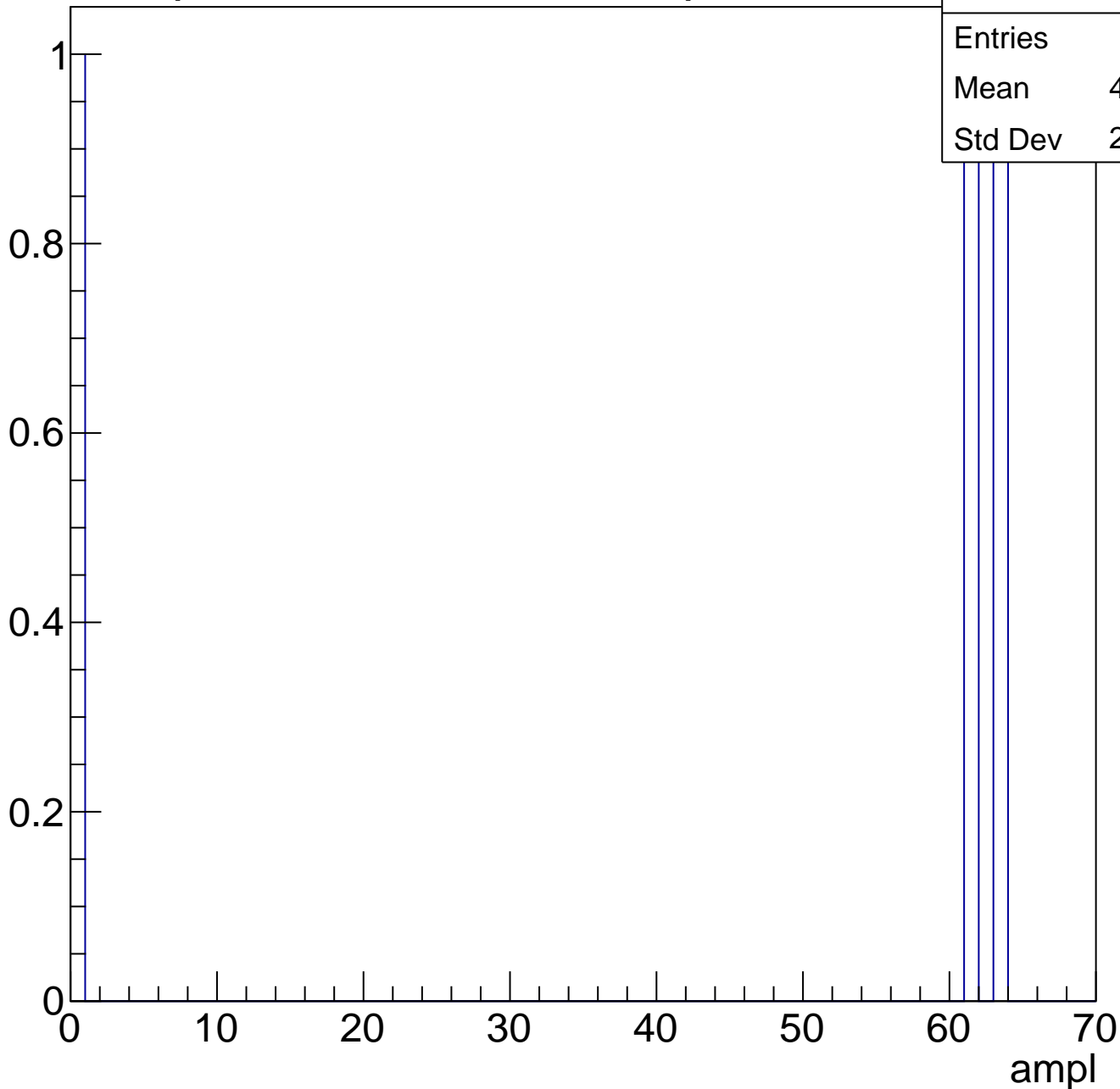
ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch111, adc0

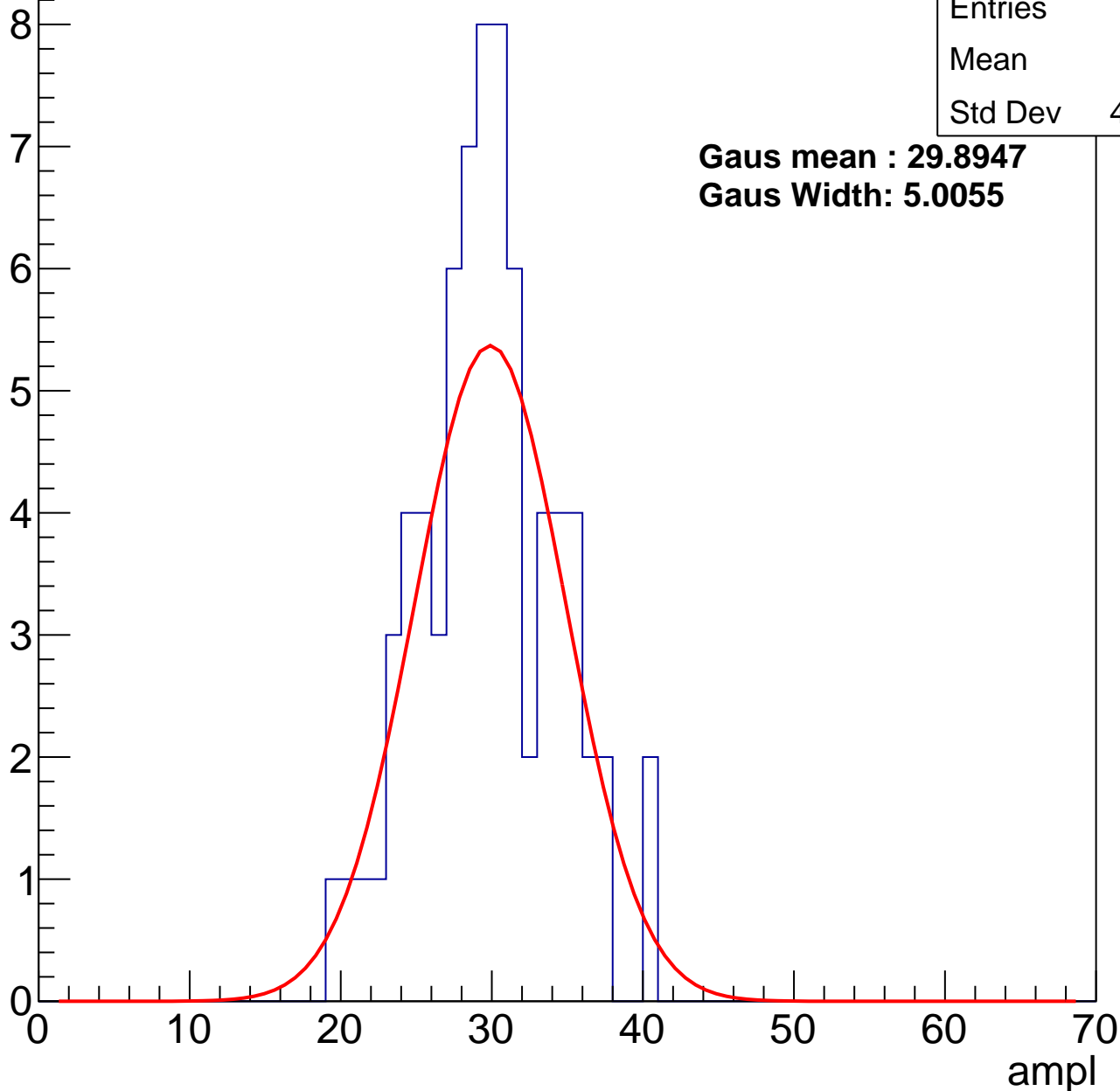
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.3
Std Dev	4.447

Gaus mean : 29.8947

Gaus Width: 5.0055



B1L103S, U20-ch111, adc1

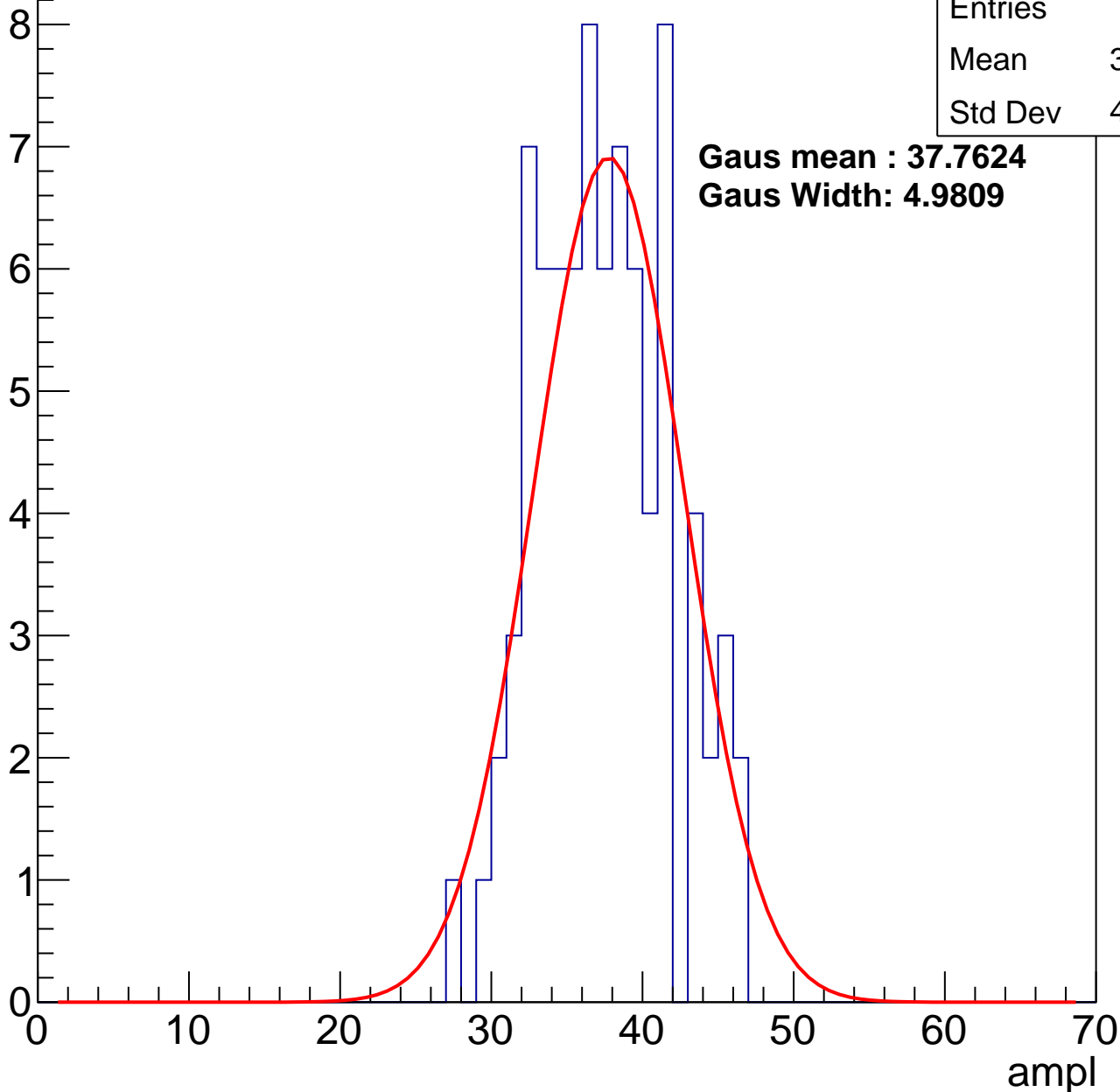
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	36.95
Std Dev	4.302

Gaus mean : 37.7624

Gaus Width: 4.9809



B1L103S, U20-ch111, adc2

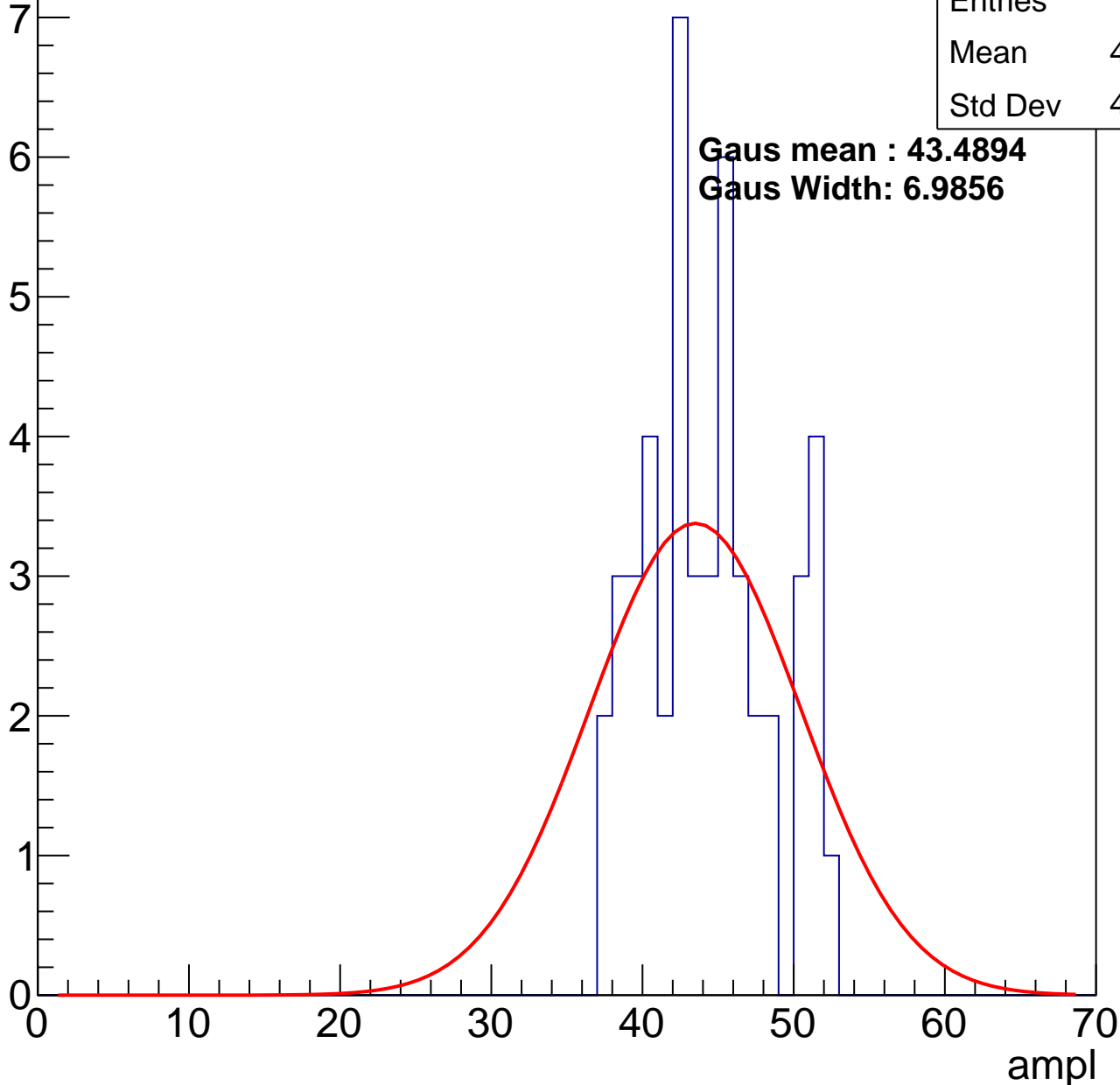
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	43.88
Std Dev	4.156

Gaus mean : 43.4894

Gaus Width: 6.9856

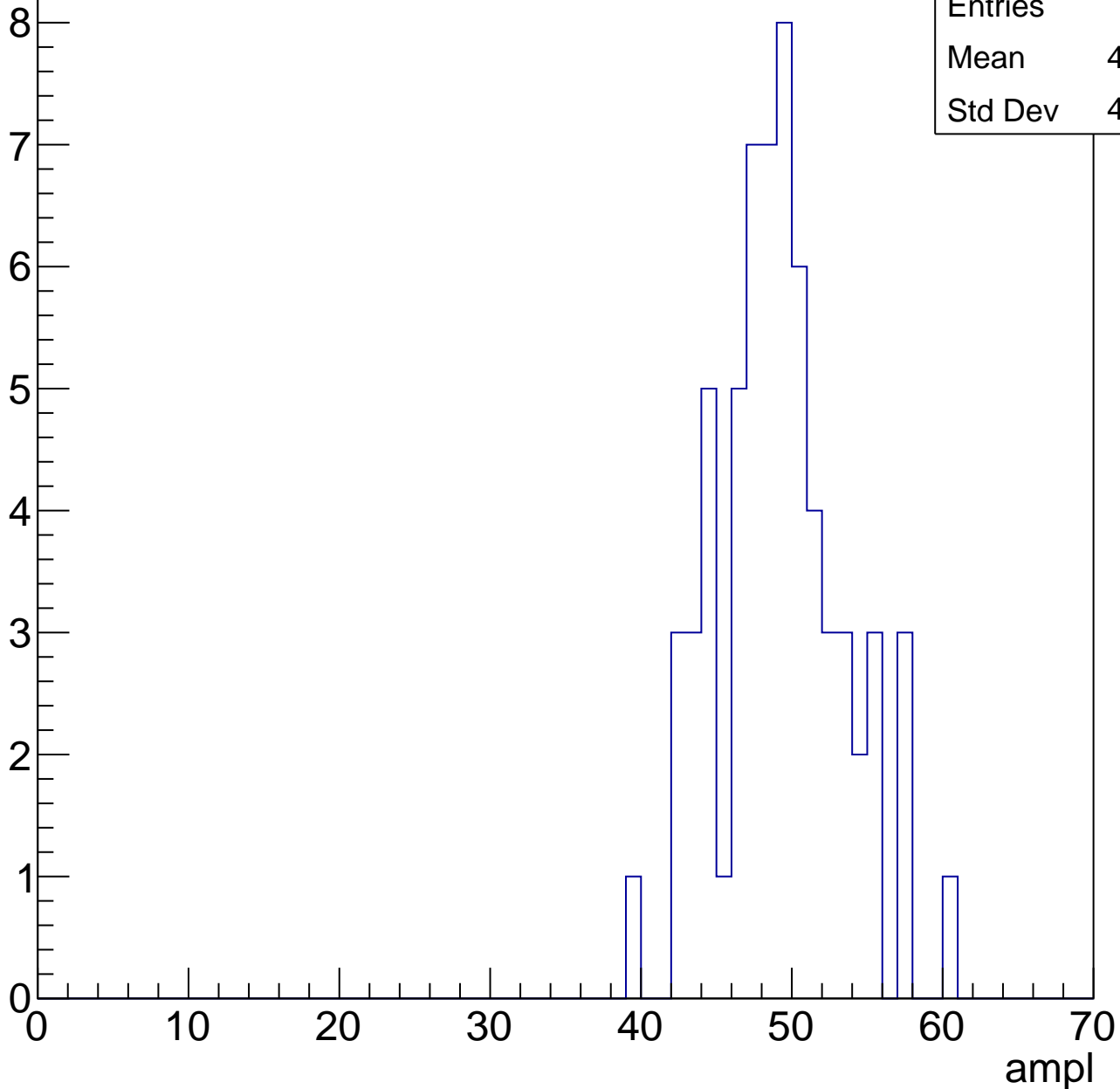


B1L103S, U20-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.75
Std Dev	4.188

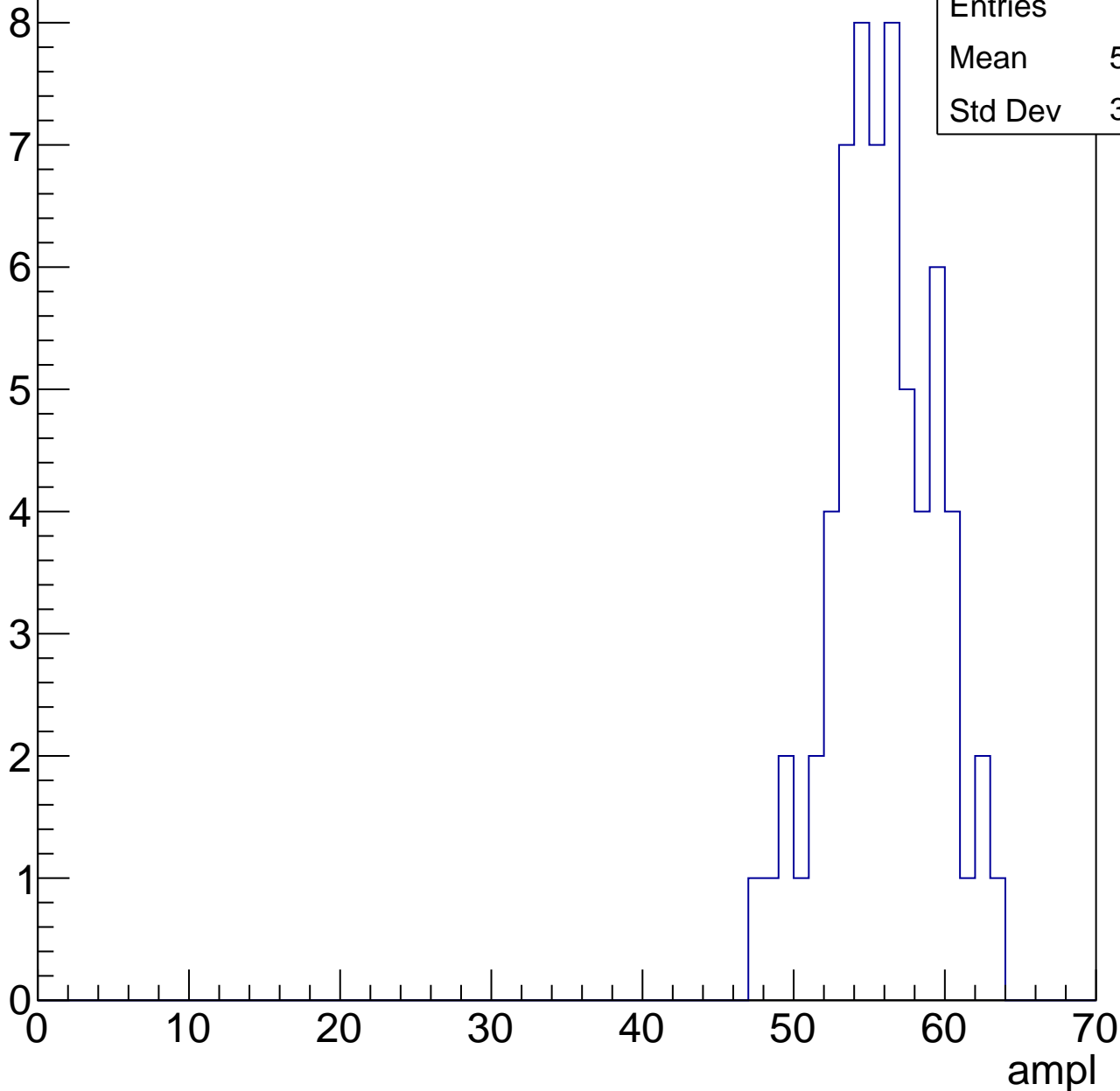


B1L103S, U20-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.44
Std Dev	3.445

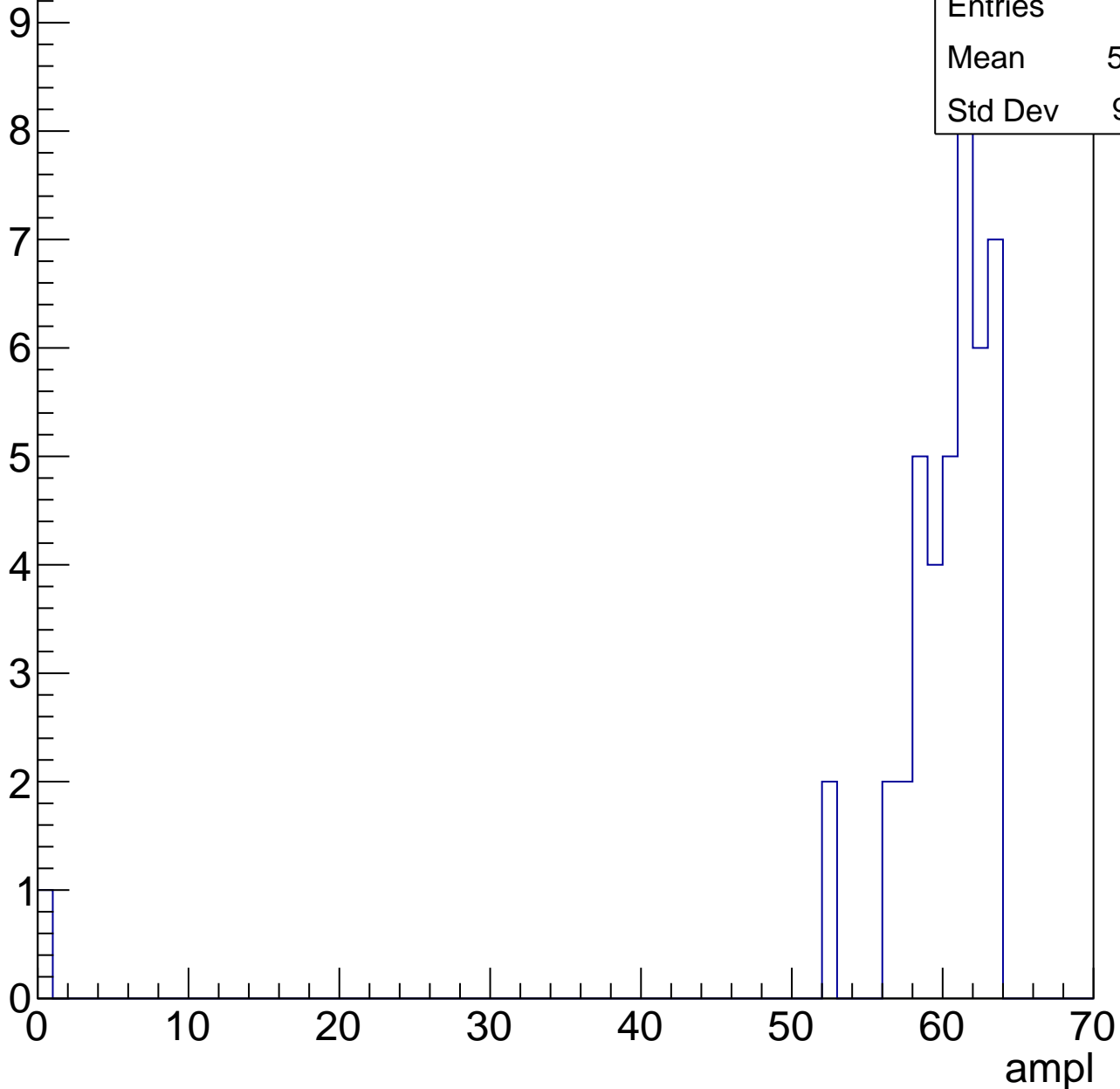


B1L103S, U20-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.56
Std Dev	9.411



B1L103S, U20-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

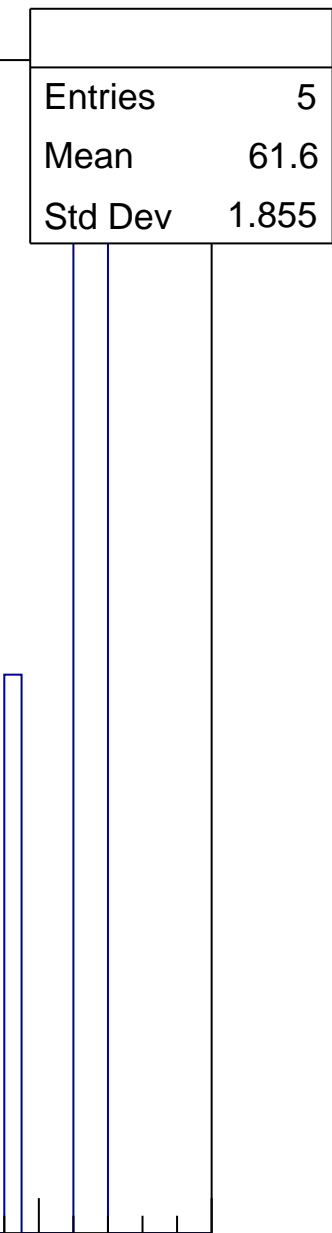
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.855

0 10 20 30 40 50 60 70

ampl



B1L103S, U20-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch112, adc0

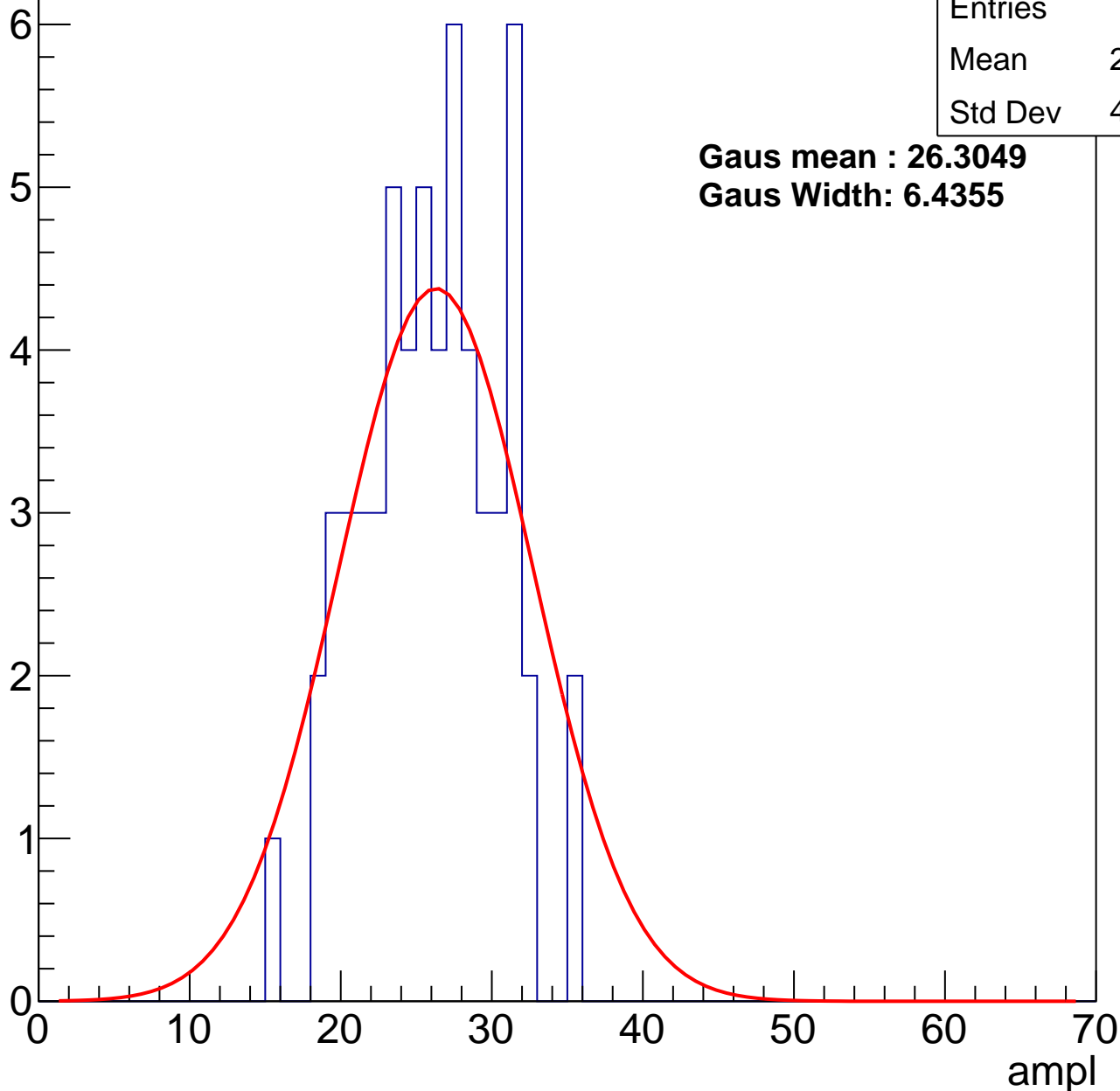
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	25.56
Std Dev	4.439

Gaus mean : 26.3049

Gaus Width: 6.4355



B1L103S, U20-ch112, adc1

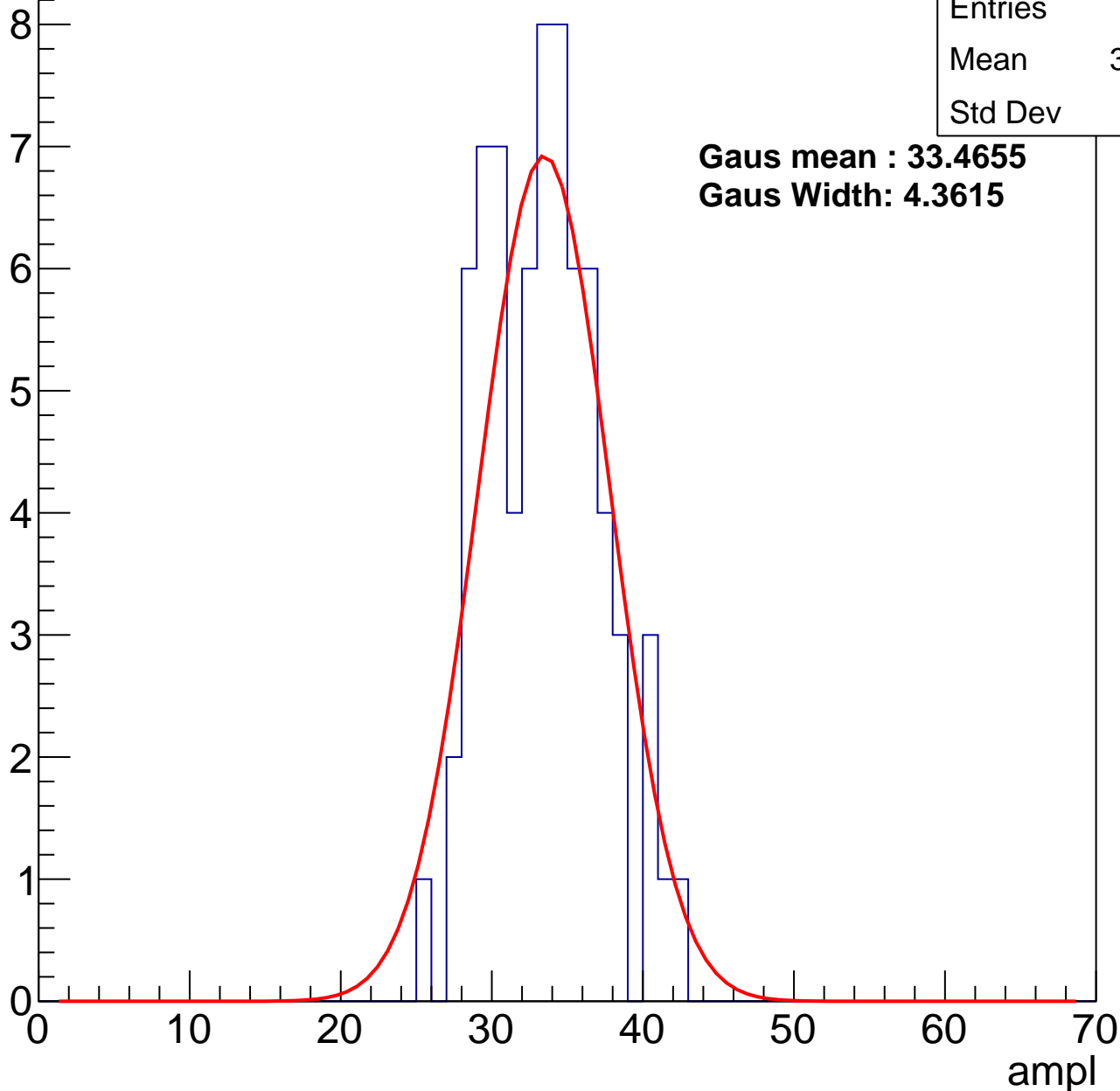
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	32.92
Std Dev	3.7

Gaus mean : 33.4655

Gaus Width: 4.3615



B1L103S, U20-ch112, adc2

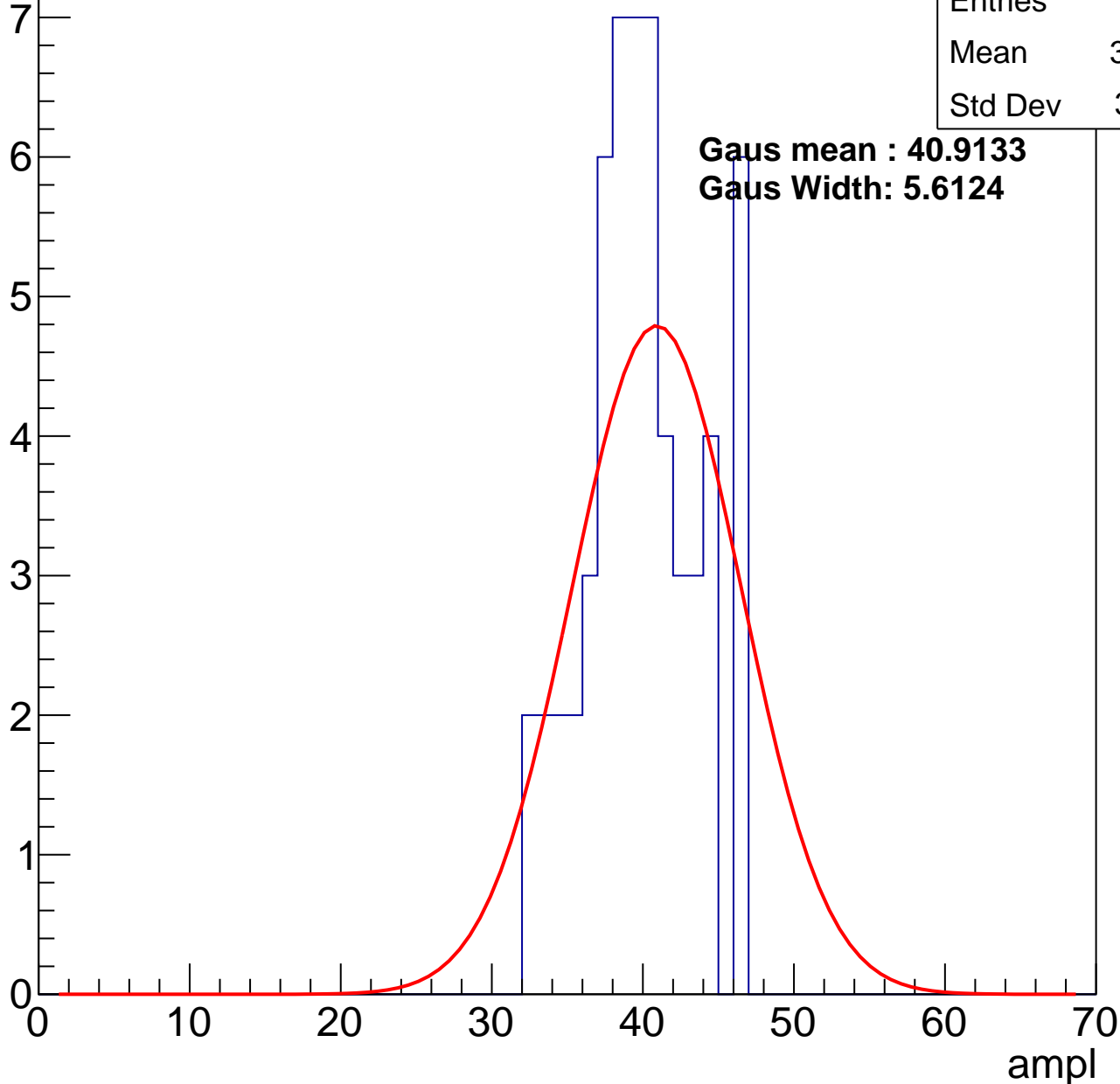
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	39.45
Std Dev	3.691

Gaus mean : 40.9133

Gaus Width: 5.6124

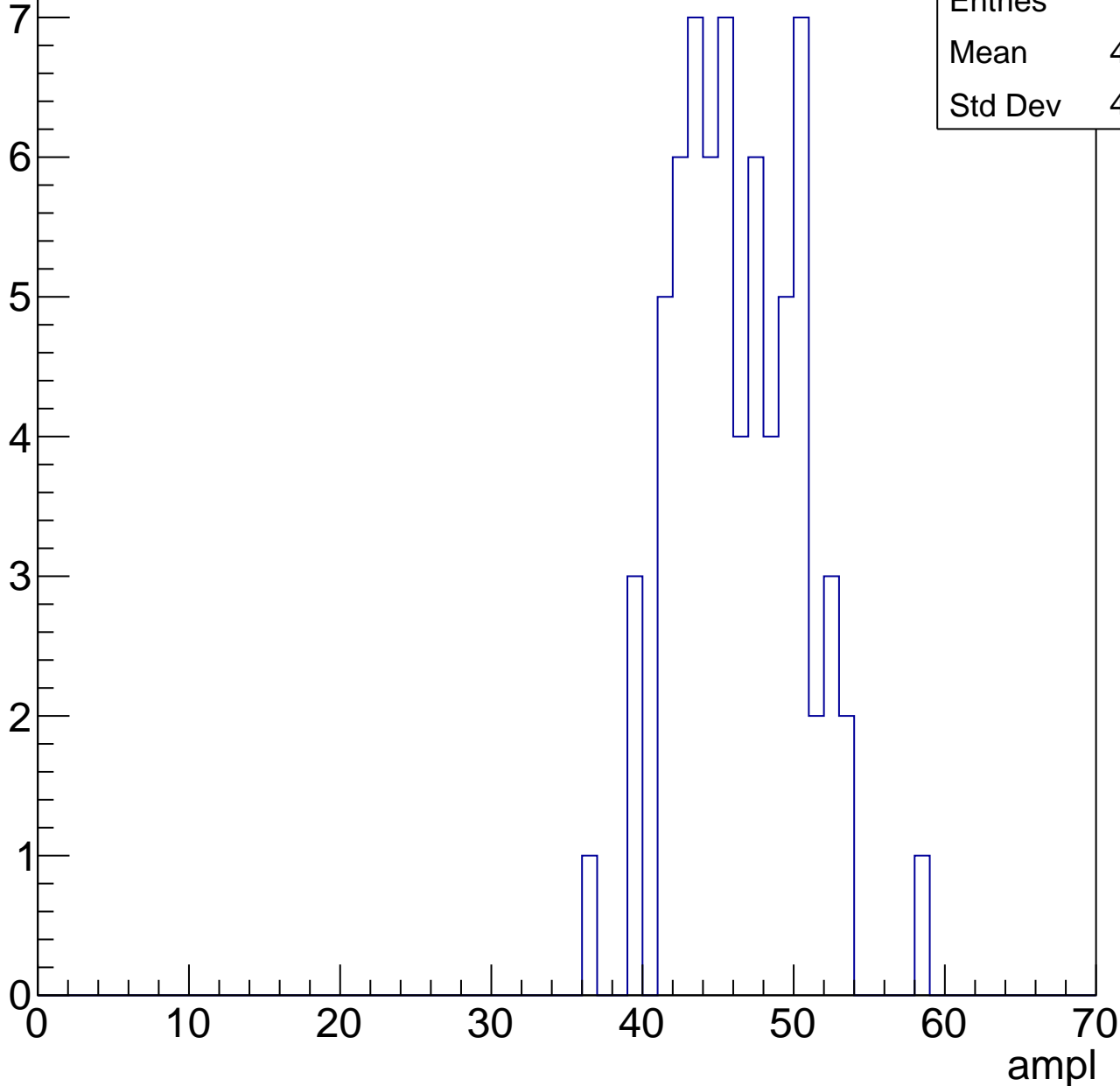


B1L103S, U20-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	45.87
Std Dev	4.064

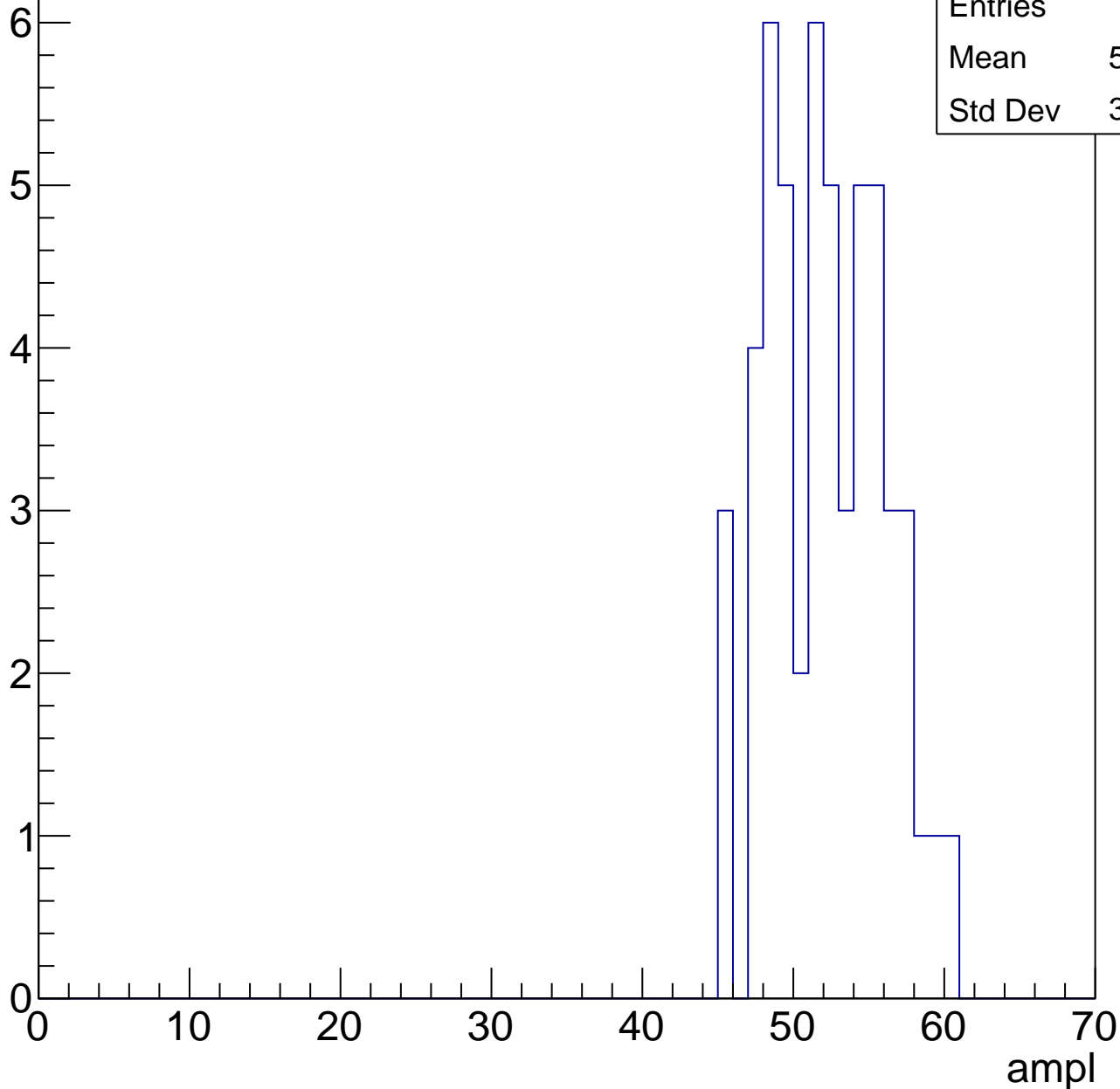


B1L103S, U20-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	51.74
Std Dev	3.732

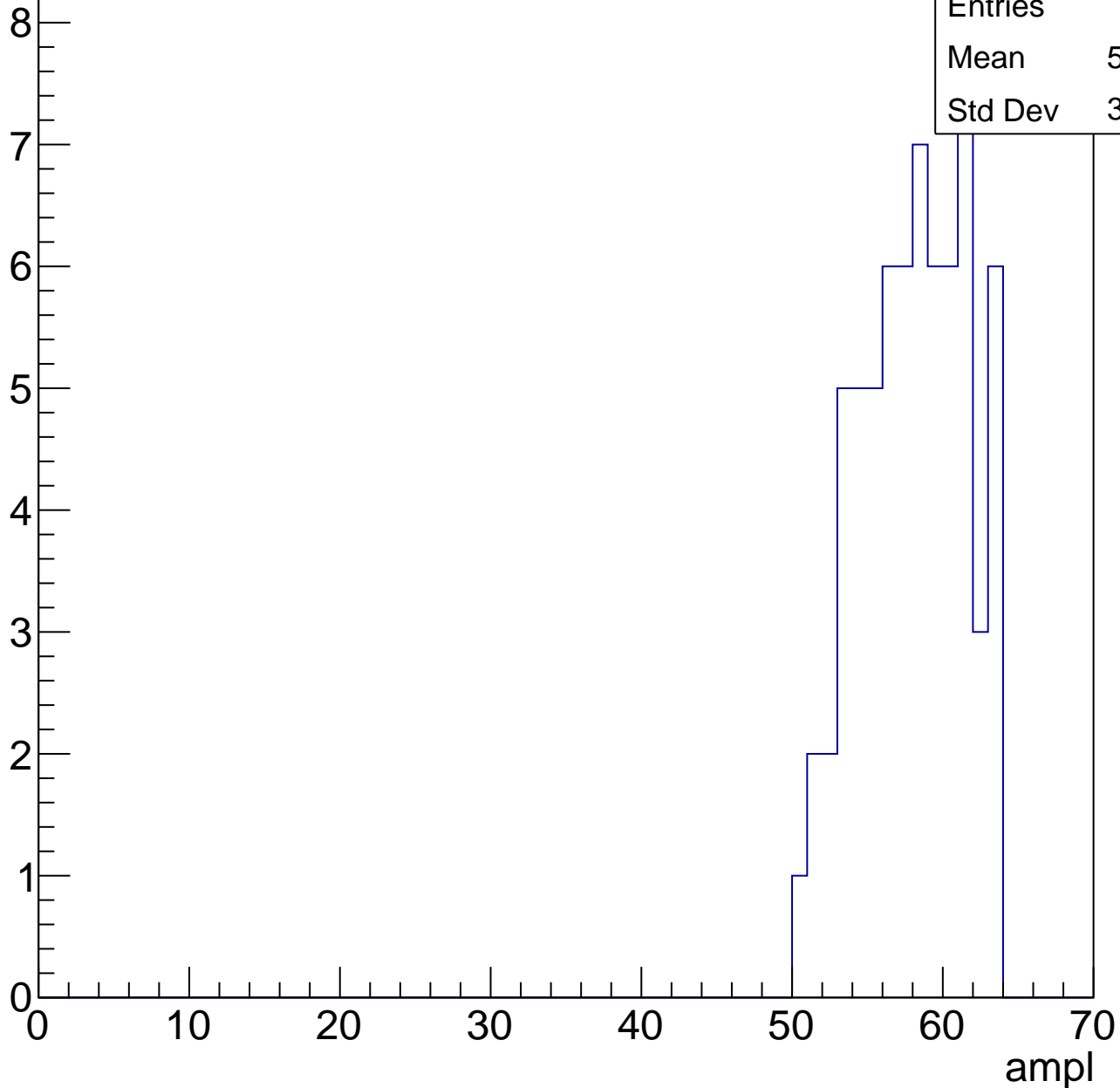


B1L103S, U20-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	57.59
Std Dev	3.435

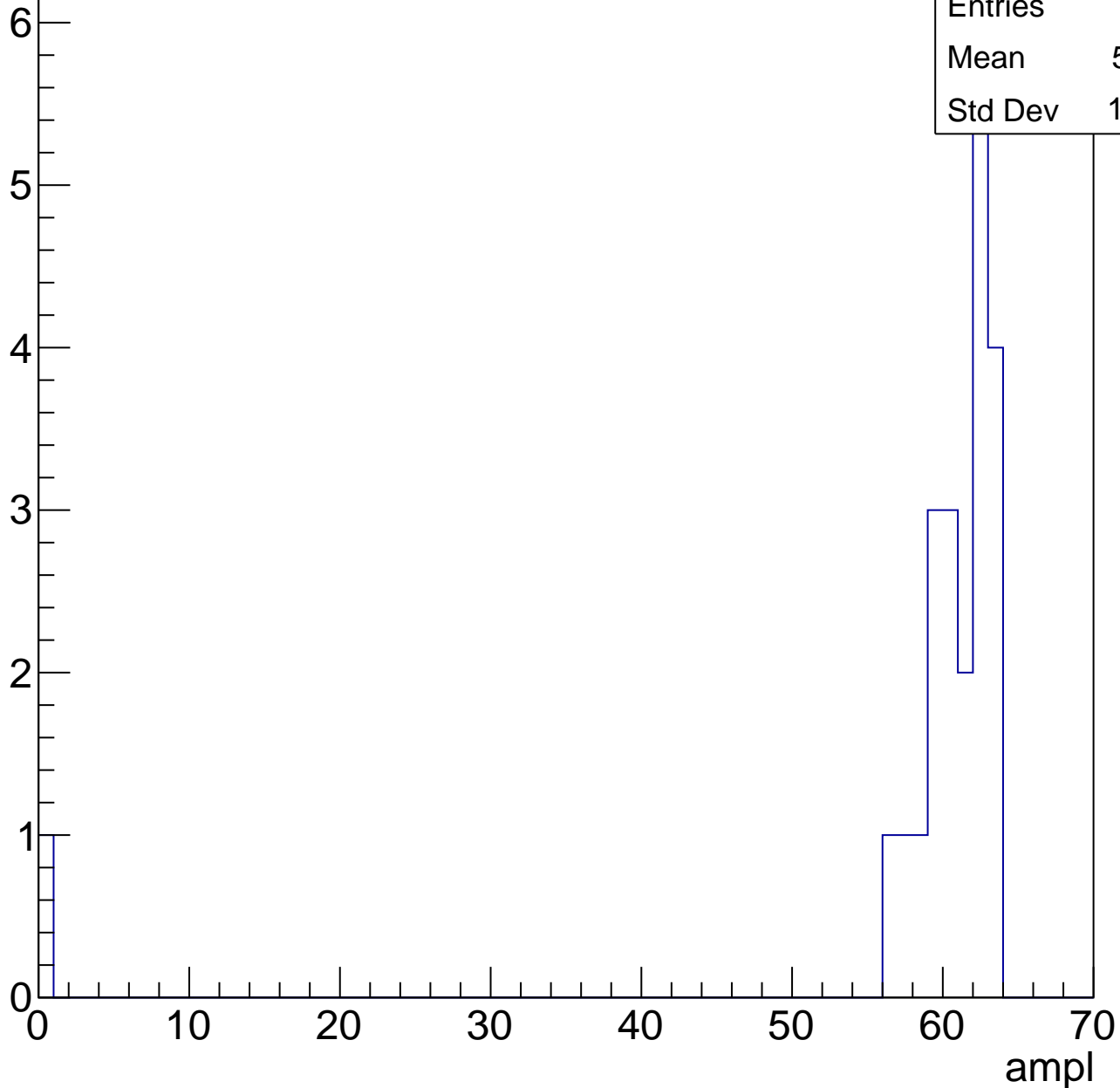


B1L103S, U20-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	22
Mean	57.91
Std Dev	12.79



B1L103S, U20-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch113, adc0

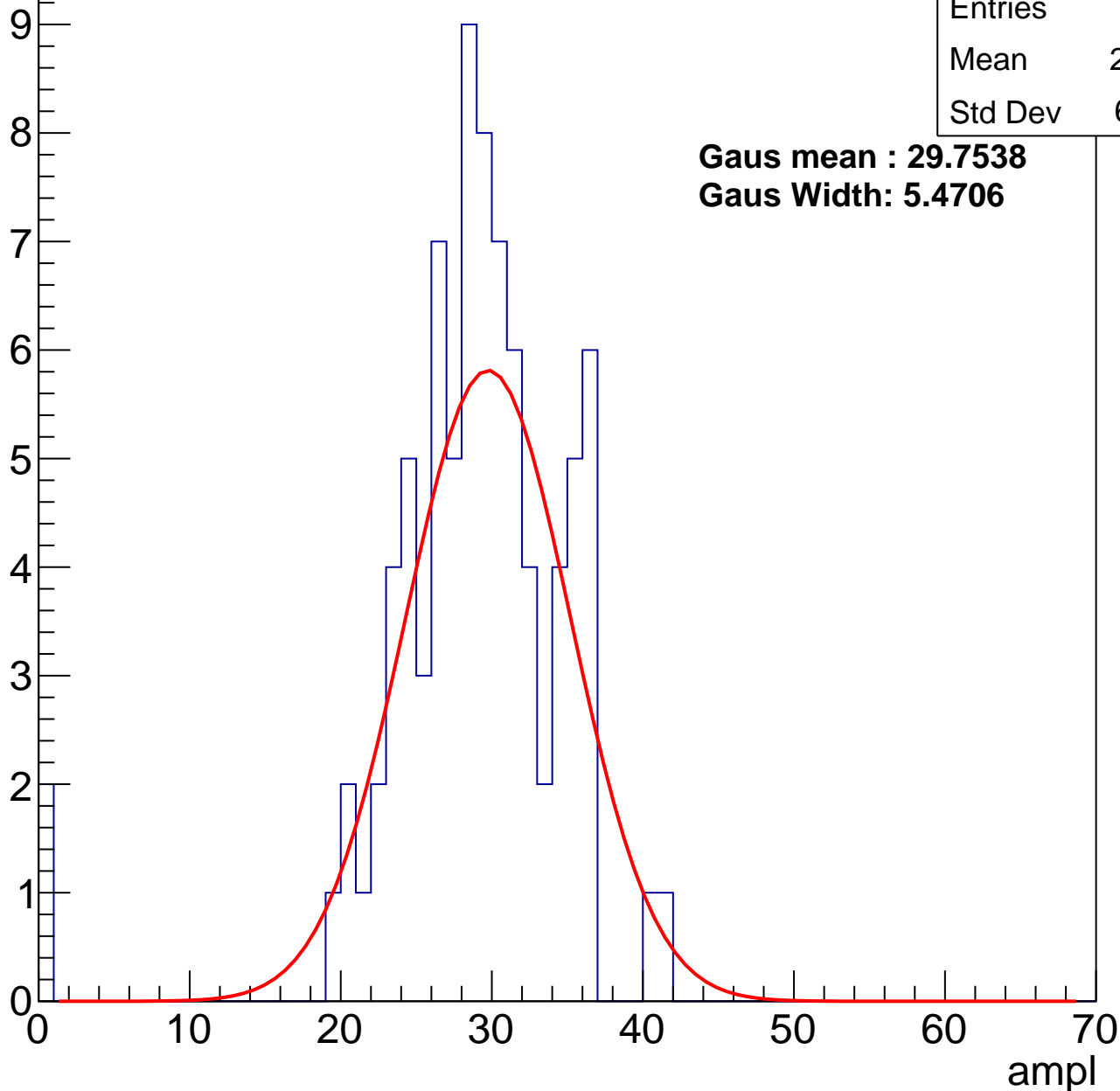
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	28.35
Std Dev	6.341

Gaus mean : 29.7538

Gaus Width: 5.4706



B1L103S, U20-ch113, adc1

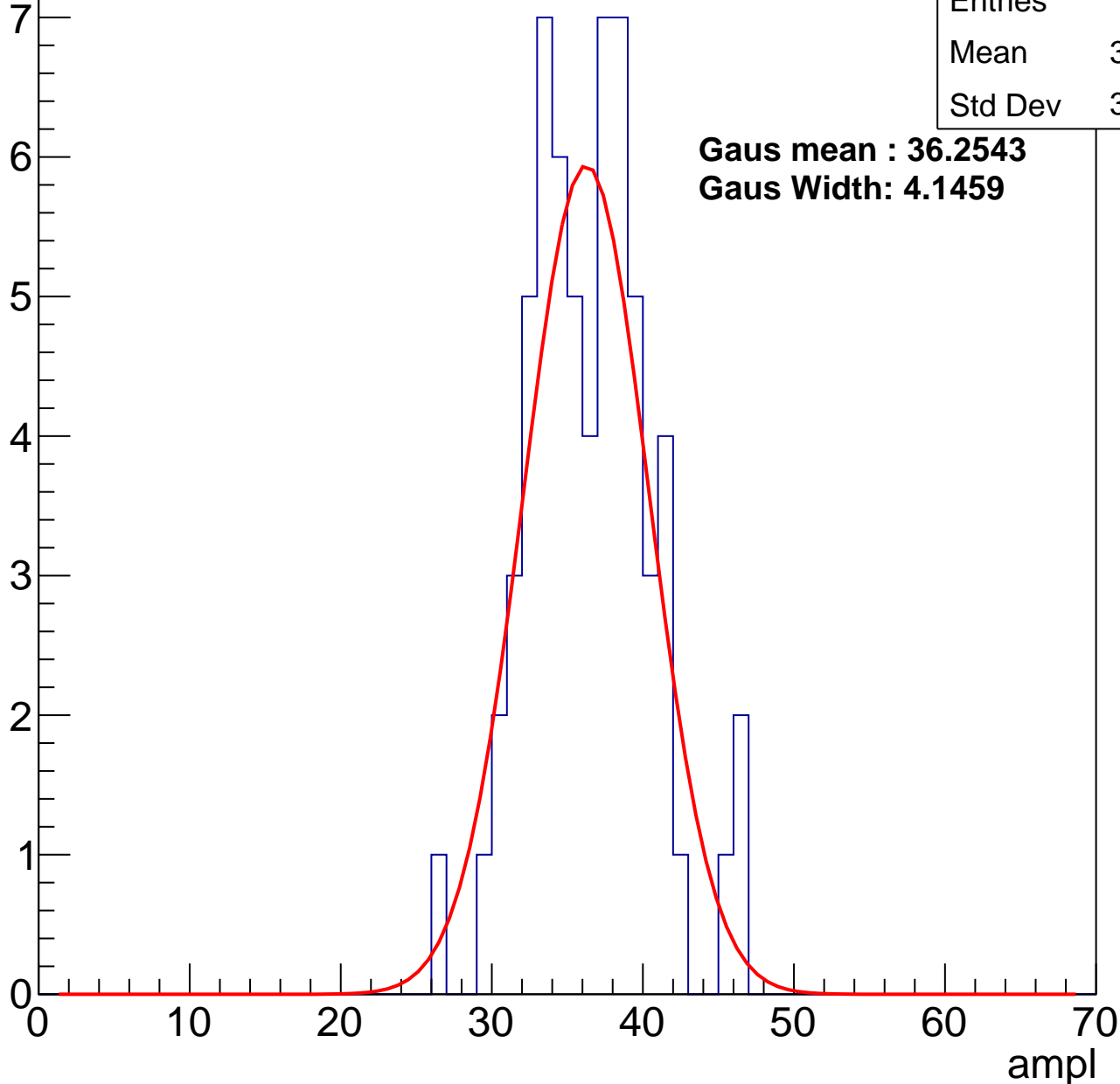
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.02
Std Dev	3.978

Gaus mean : 36.2543

Gaus Width: 4.1459



B1L103S, U20-ch113, adc2

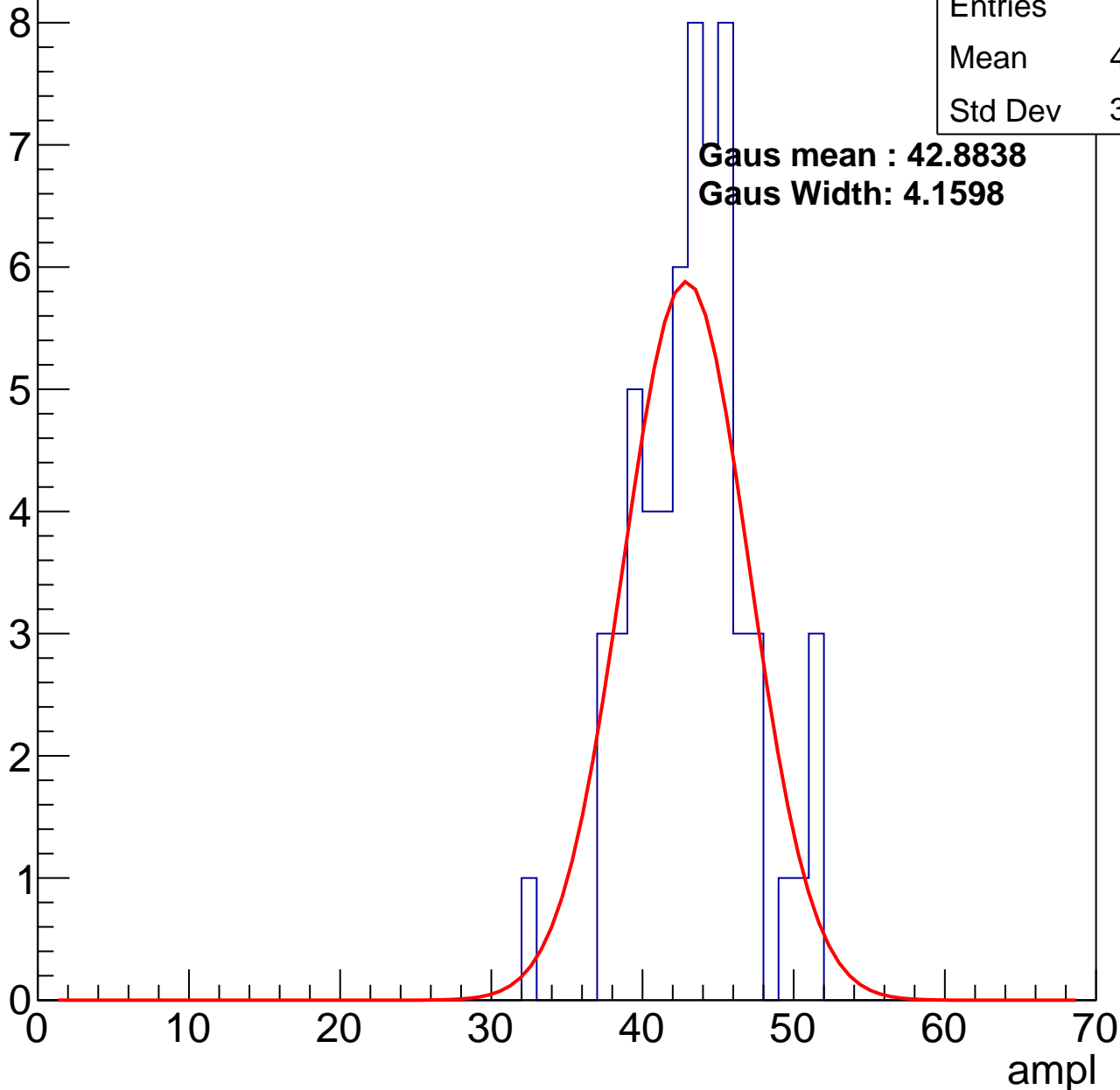
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.85
Std Dev	3.723

Gaus mean : 42.8838

Gaus Width: 4.1598

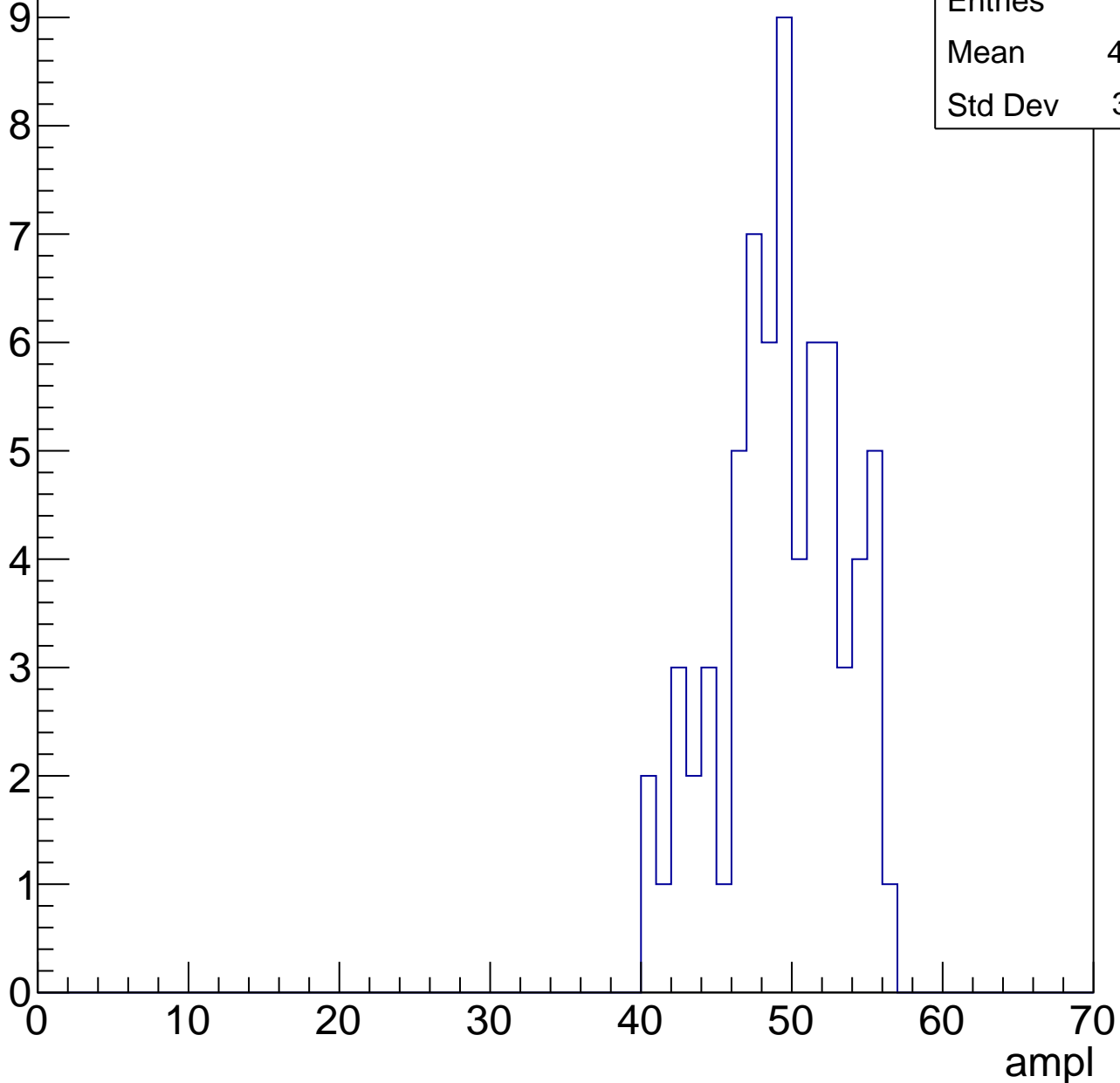


B1L103S, U20-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

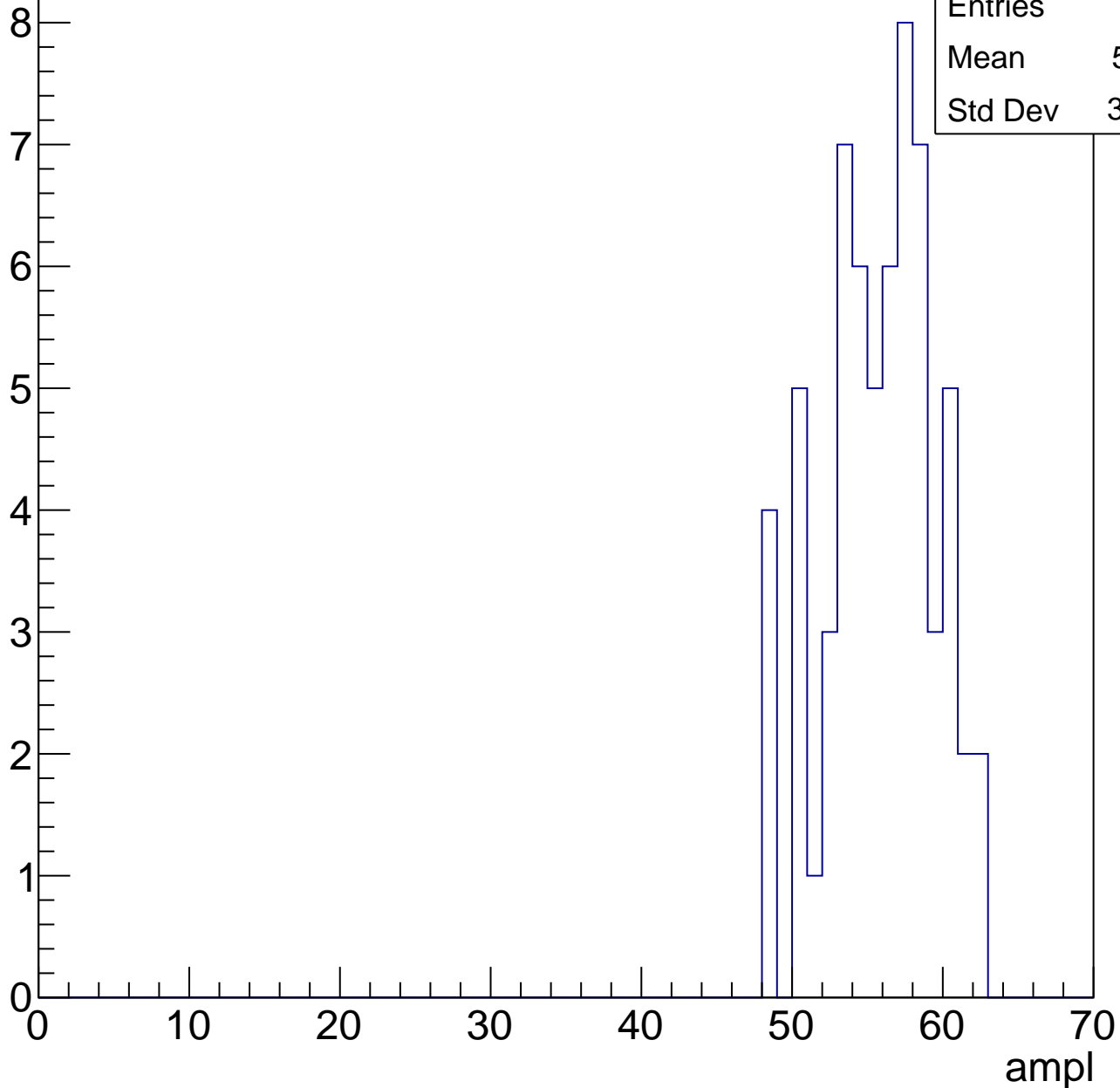
Entries	68
Mean	48.85
Std Dev	3.971



B1L103S, U20-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



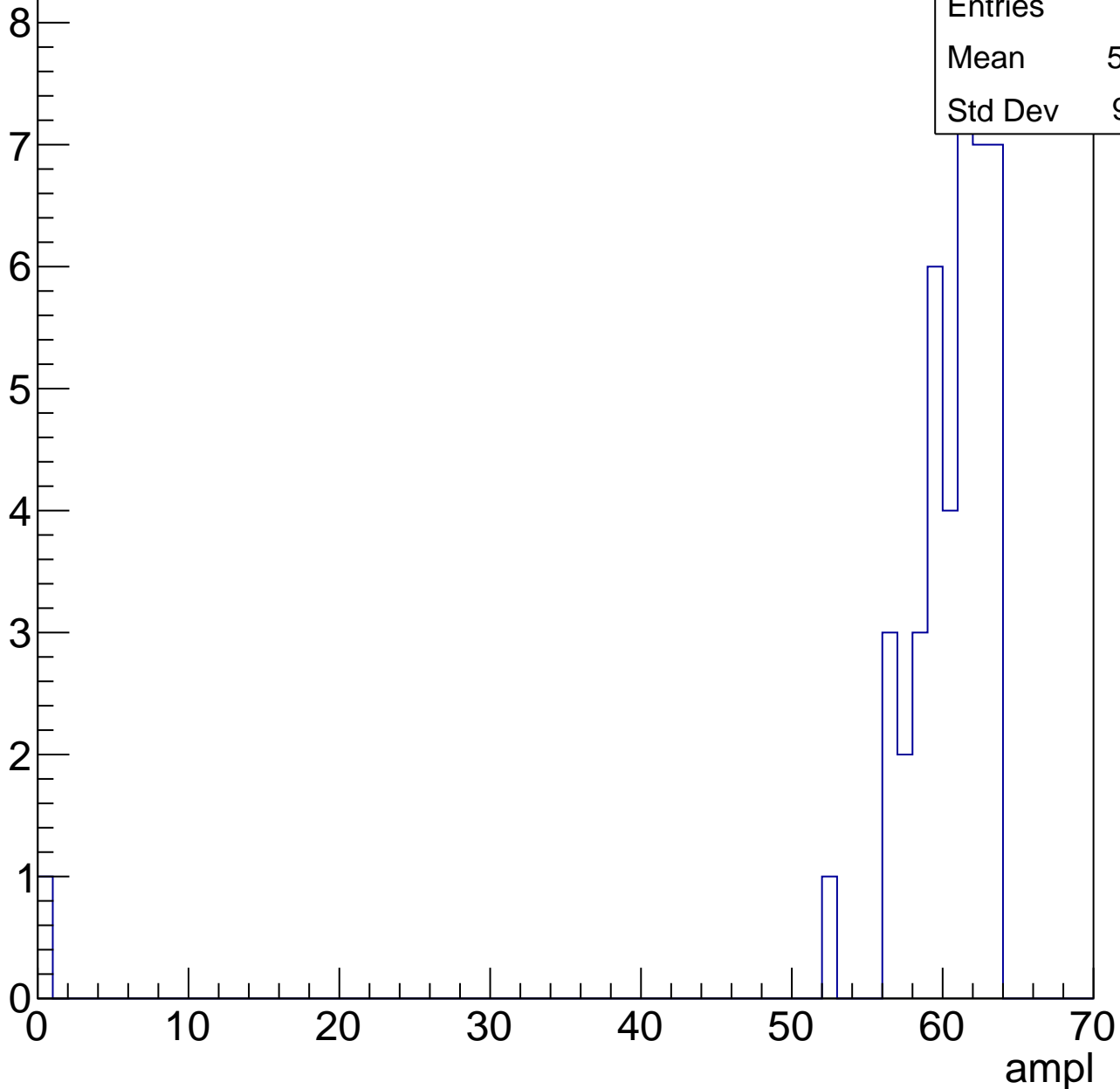
Entries	64
Mean	55.31
Std Dev	3.614

B1L103S, U20-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

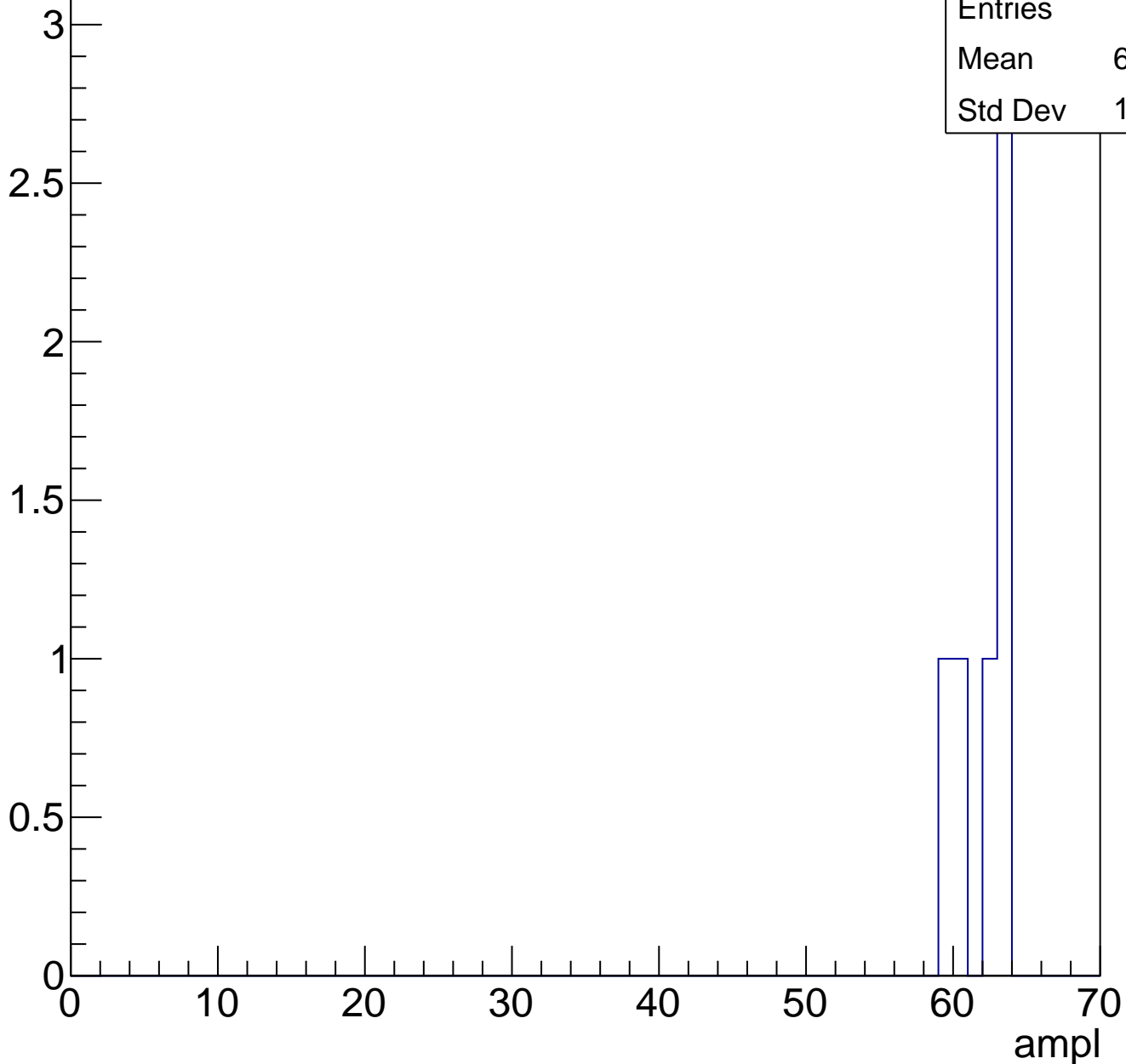
Entries	42
Mean	58.69
Std Dev	9.481



B1L103S, U20-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch114, adc0

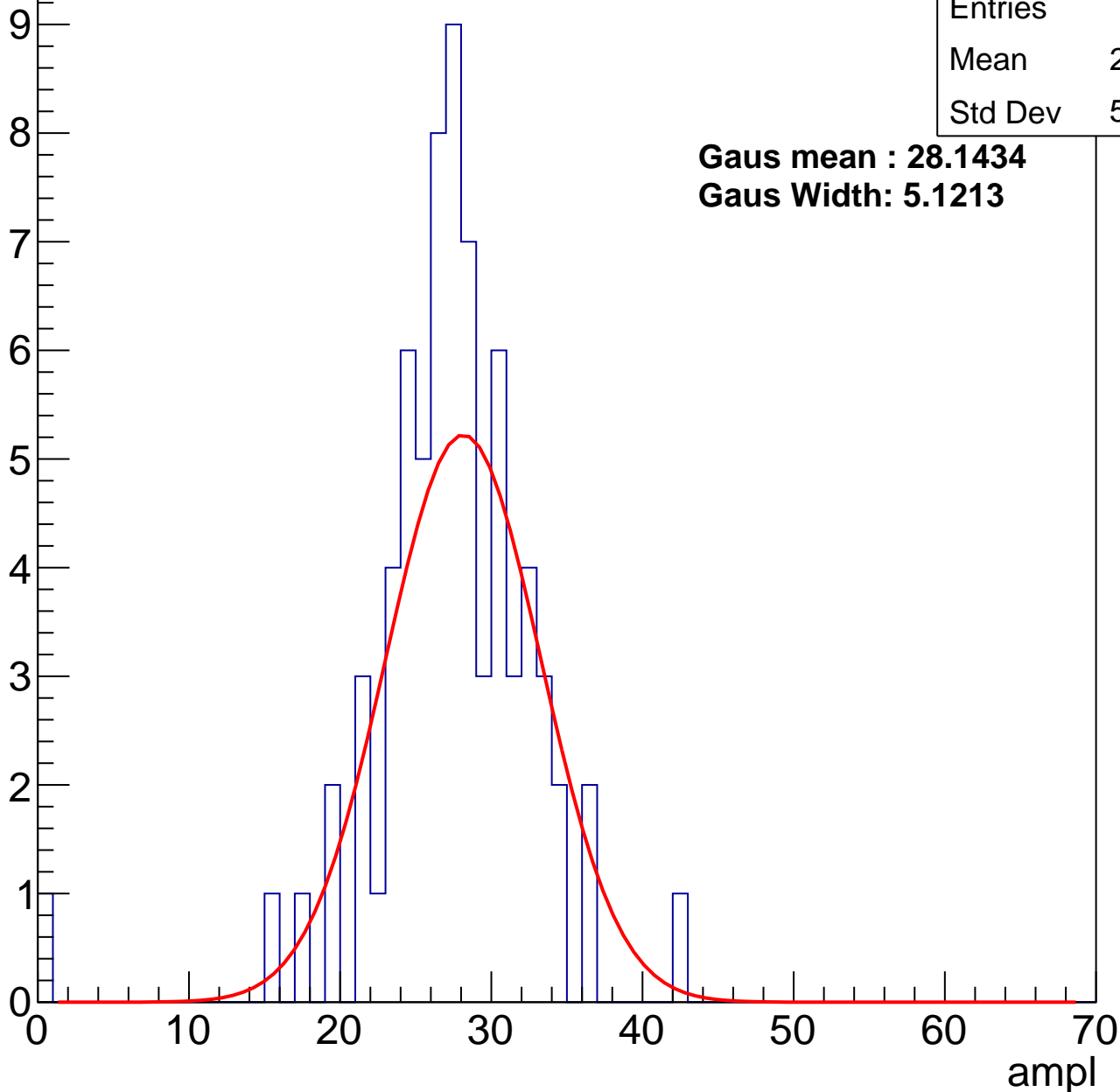
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	26.83
Std Dev	5.518

Gaus mean : 28.1434

Gaus Width: 5.1213



B1L103S, U20-ch114, adc1

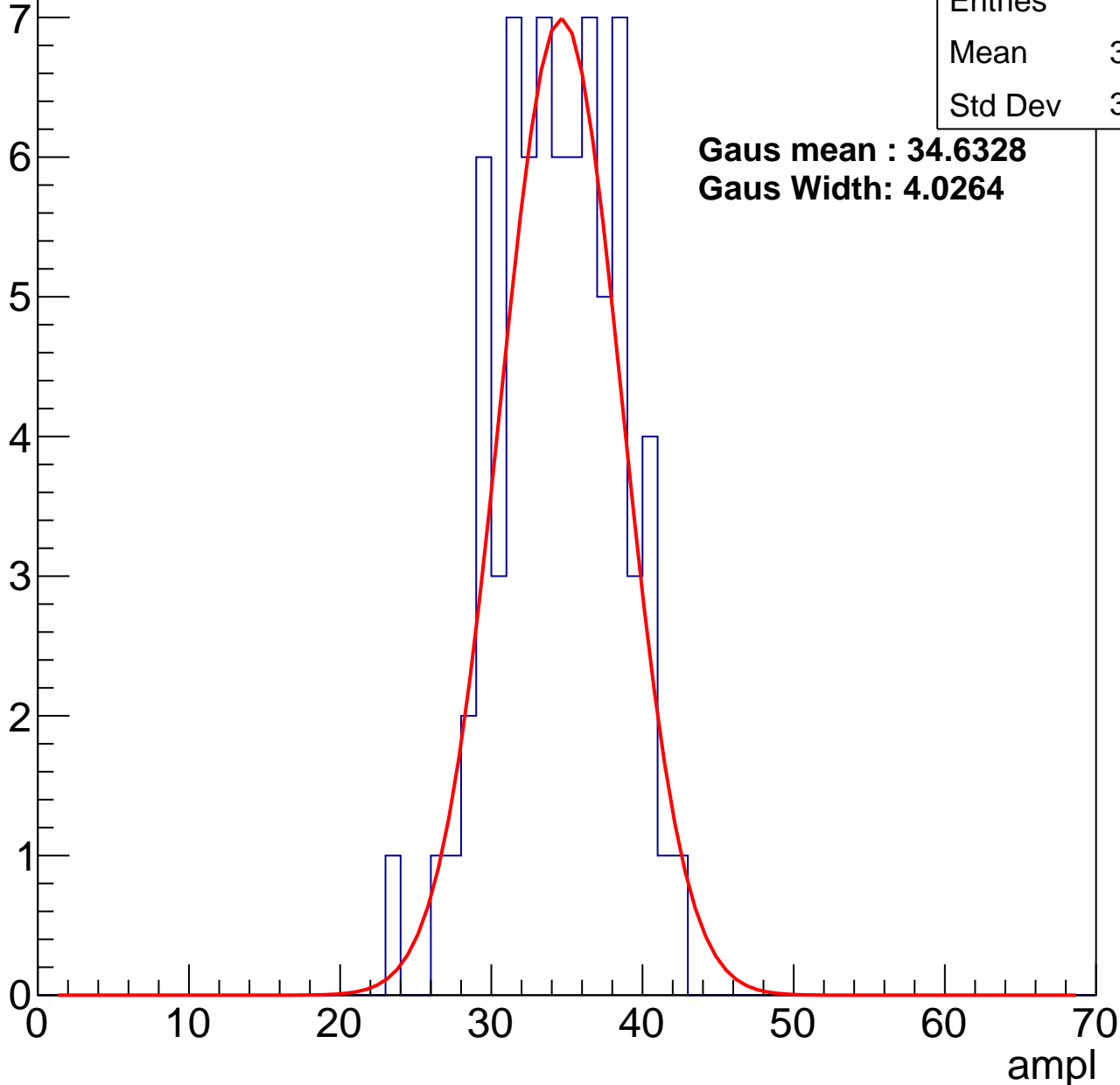
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	33.96
Std Dev	3.892

Gaus mean : 34.6328

Gaus Width: 4.0264



B1L103S, U20-ch114, adc2

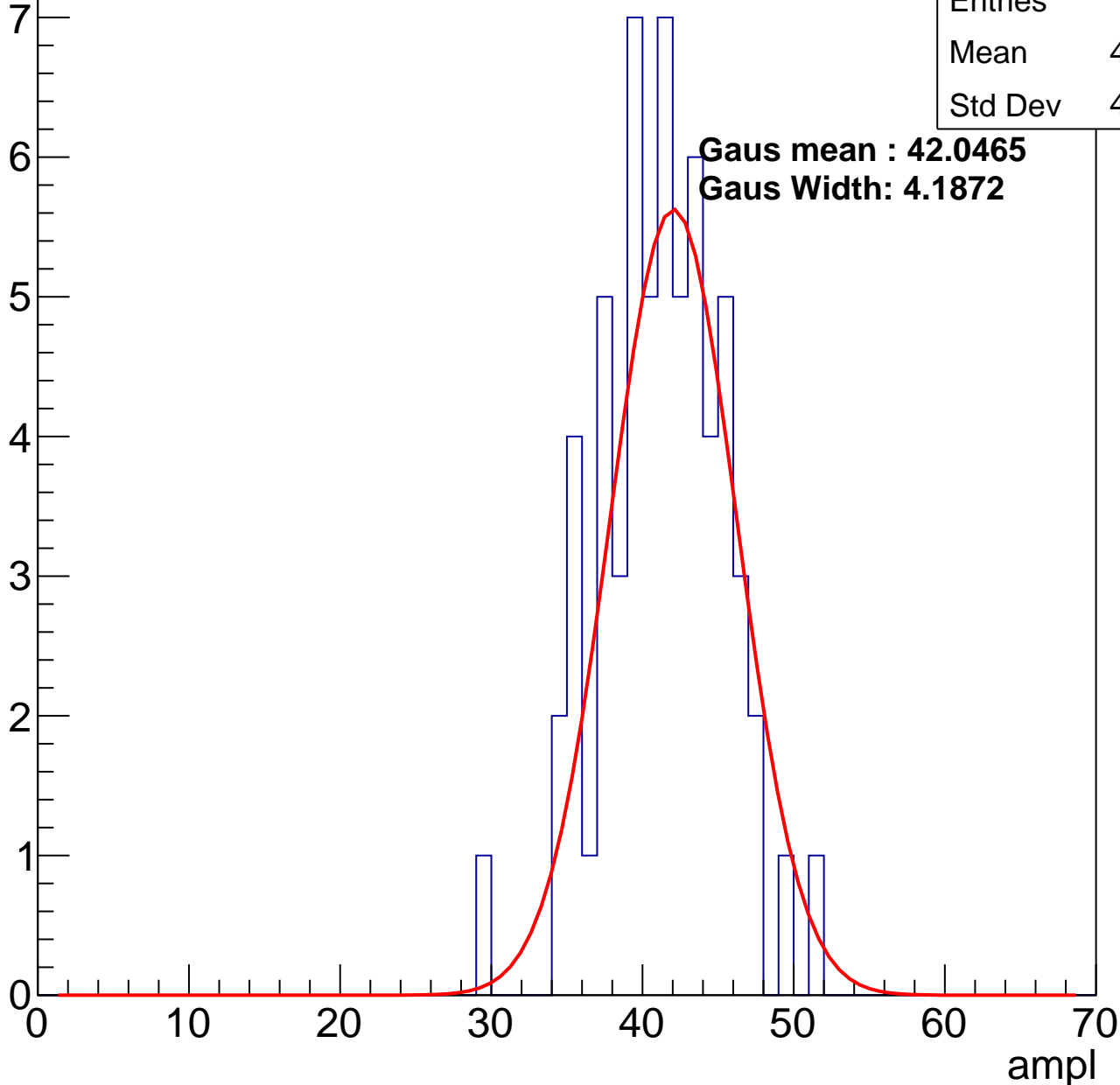
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	40.85
Std Dev	4.028

Gaus mean : 42.0465

Gaus Width: 4.1872

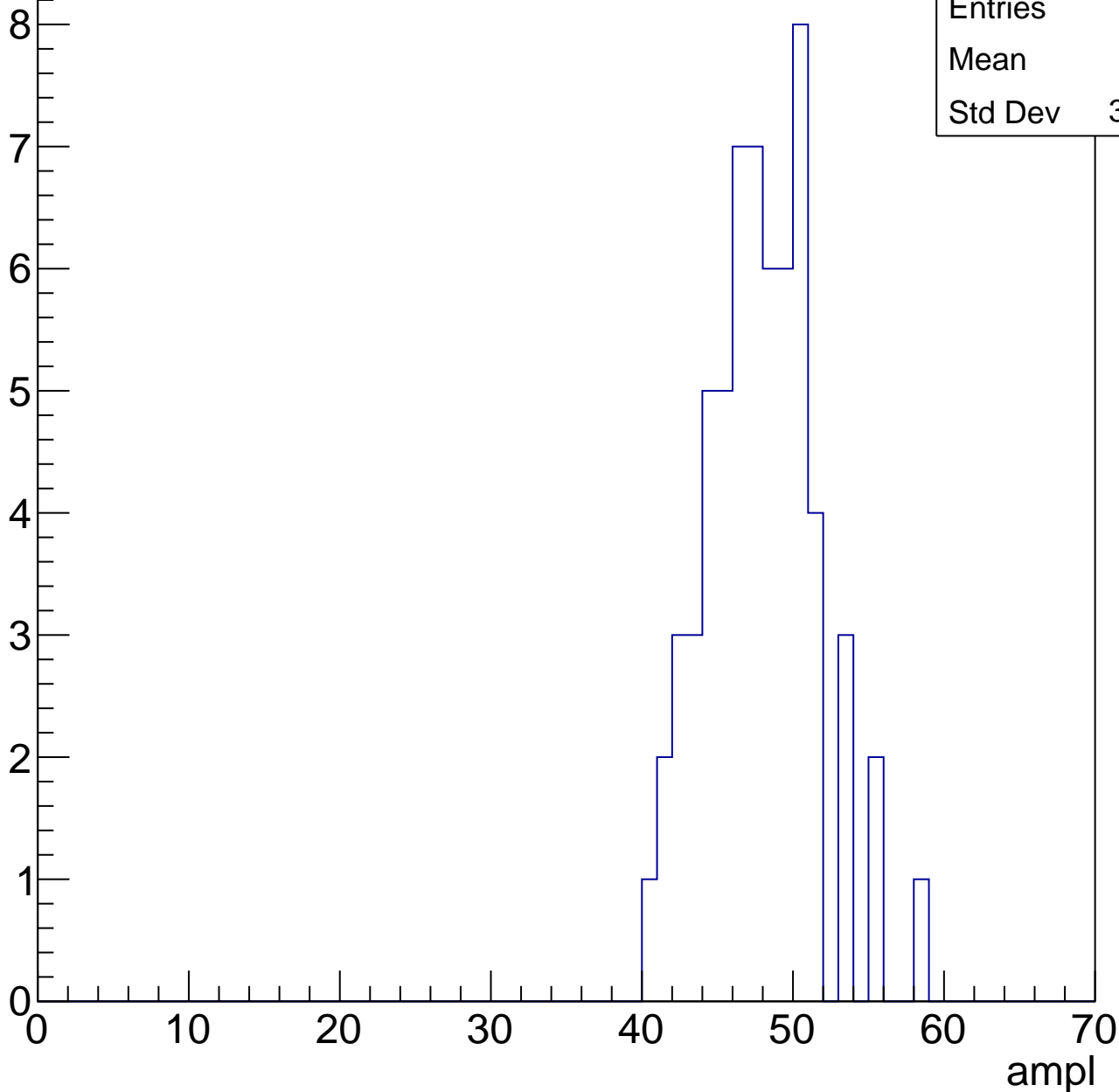


B1L103S, U20-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	47.4
Std Dev	3.628

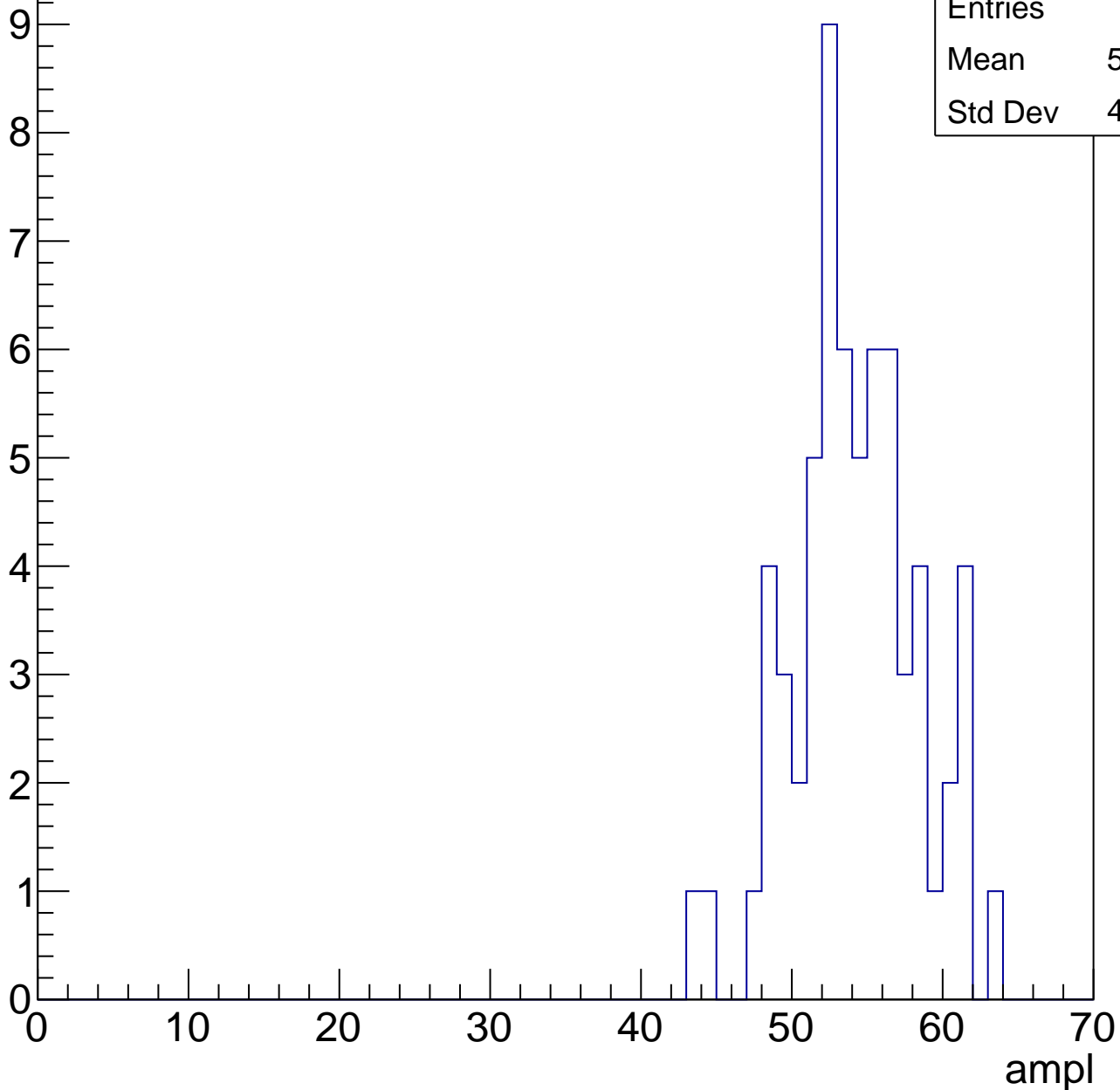


B1L103S, U20-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

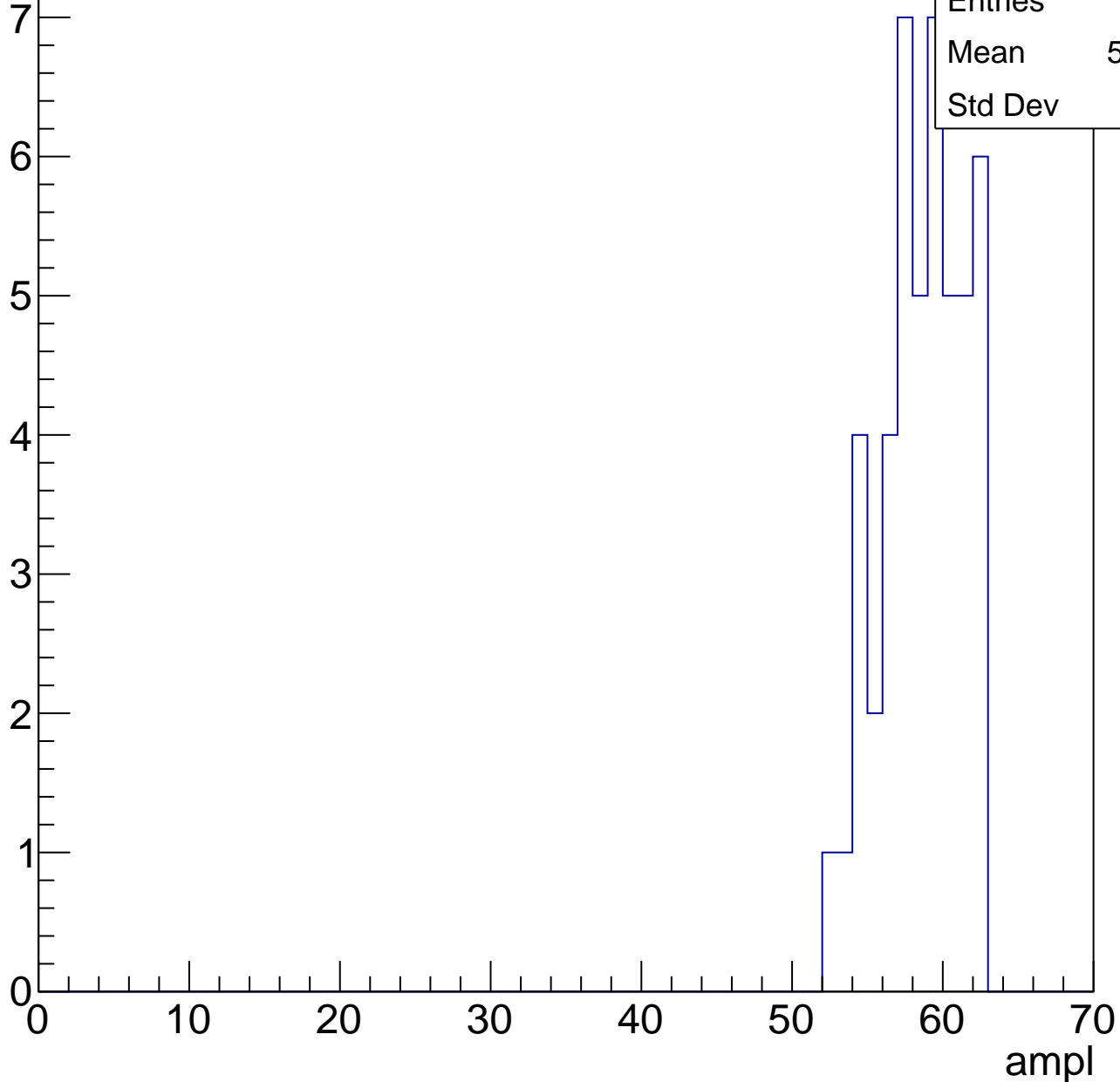
Entries	64
Mean	53.73
Std Dev	4.143



B1L103S, U20-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



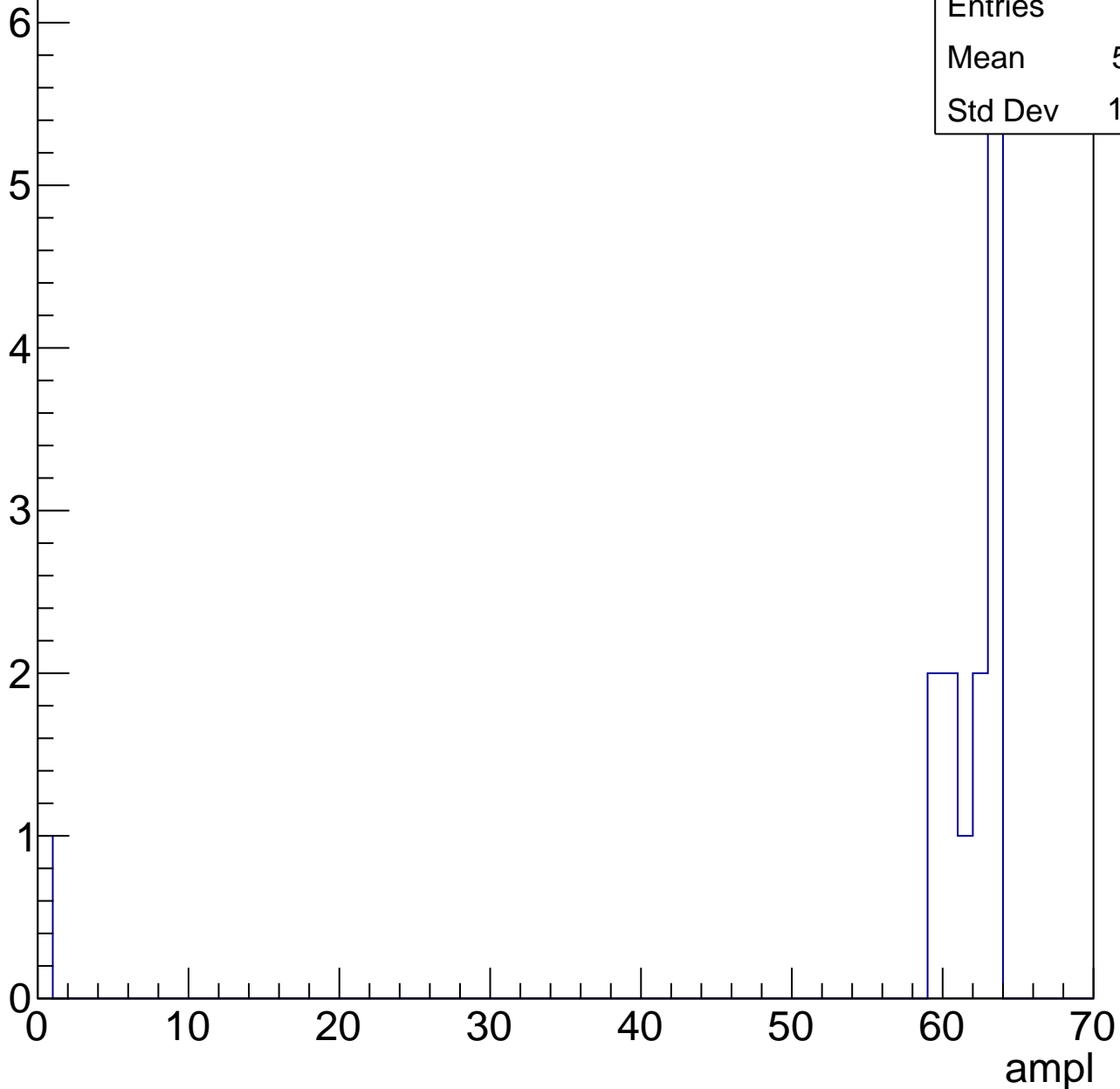
Entries	47
Mean	58.17
Std Dev	2.66

B1L103S, U20-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

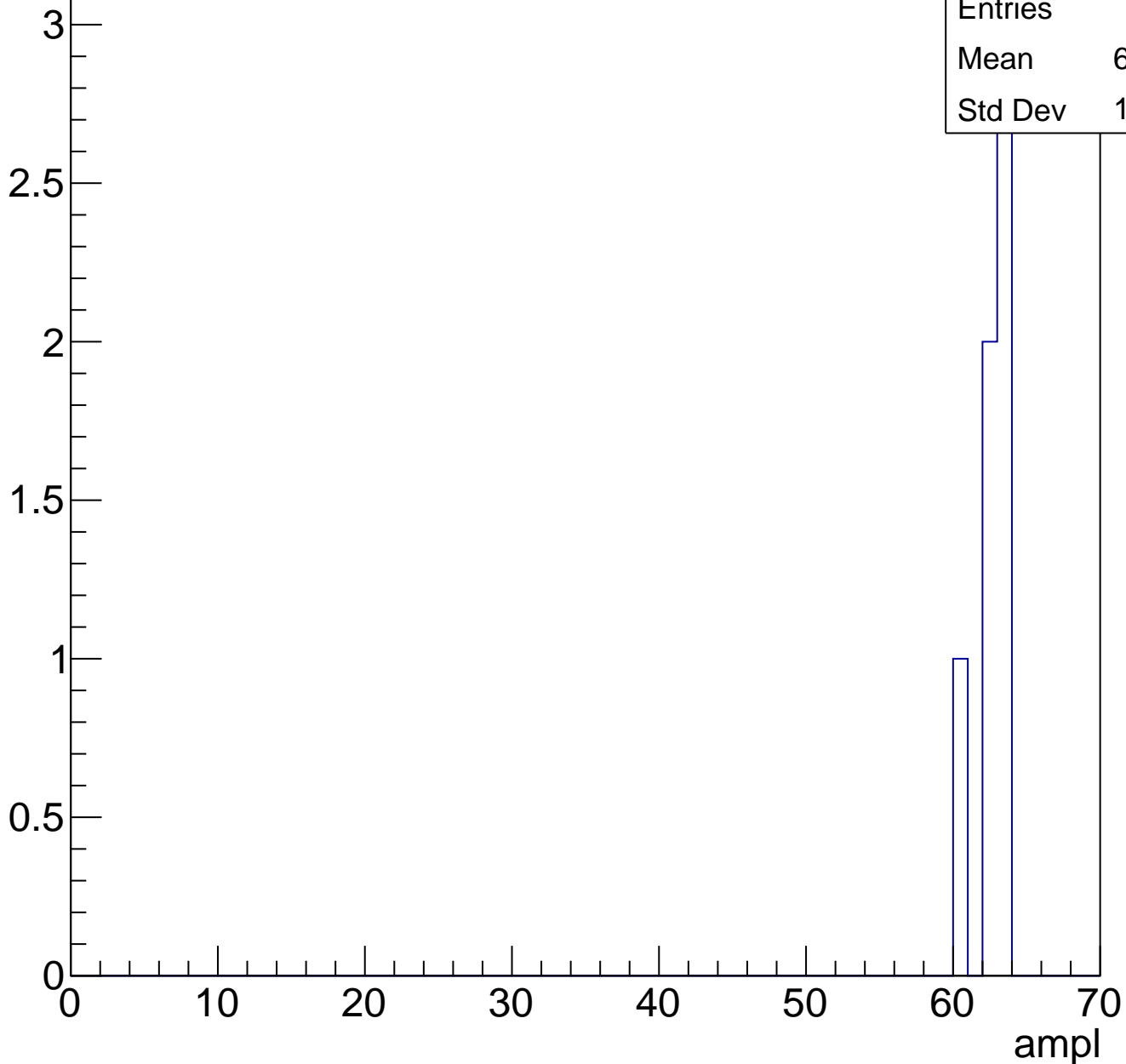
Entries	14
Mean	57.21
Std Dev	15.94



B1L103S, U20-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch115, adc0

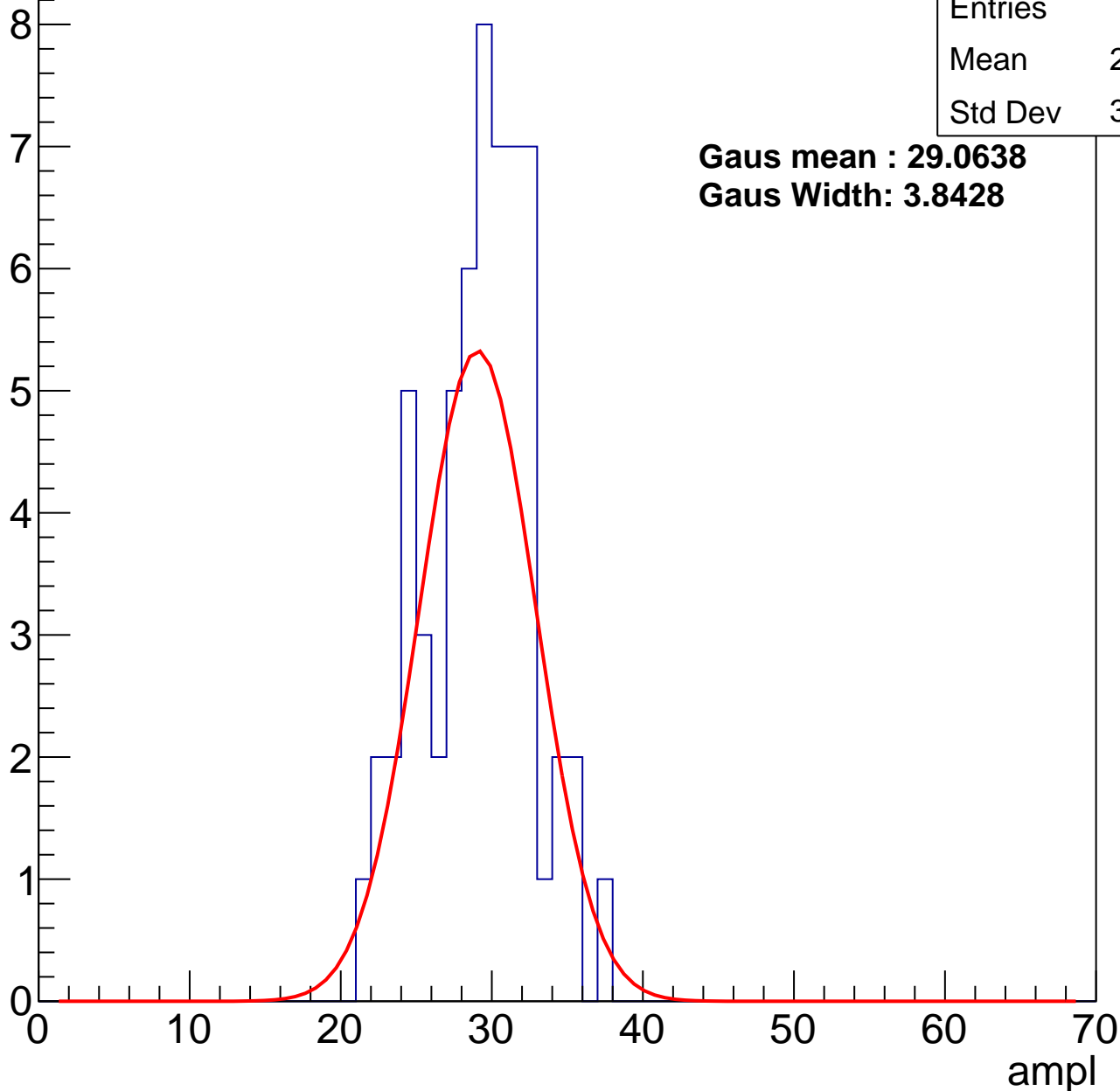
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.72
Std Dev	3.498

Gaus mean : 29.0638

Gaus Width: 3.8428



B1L103S, U20-ch115, adc1

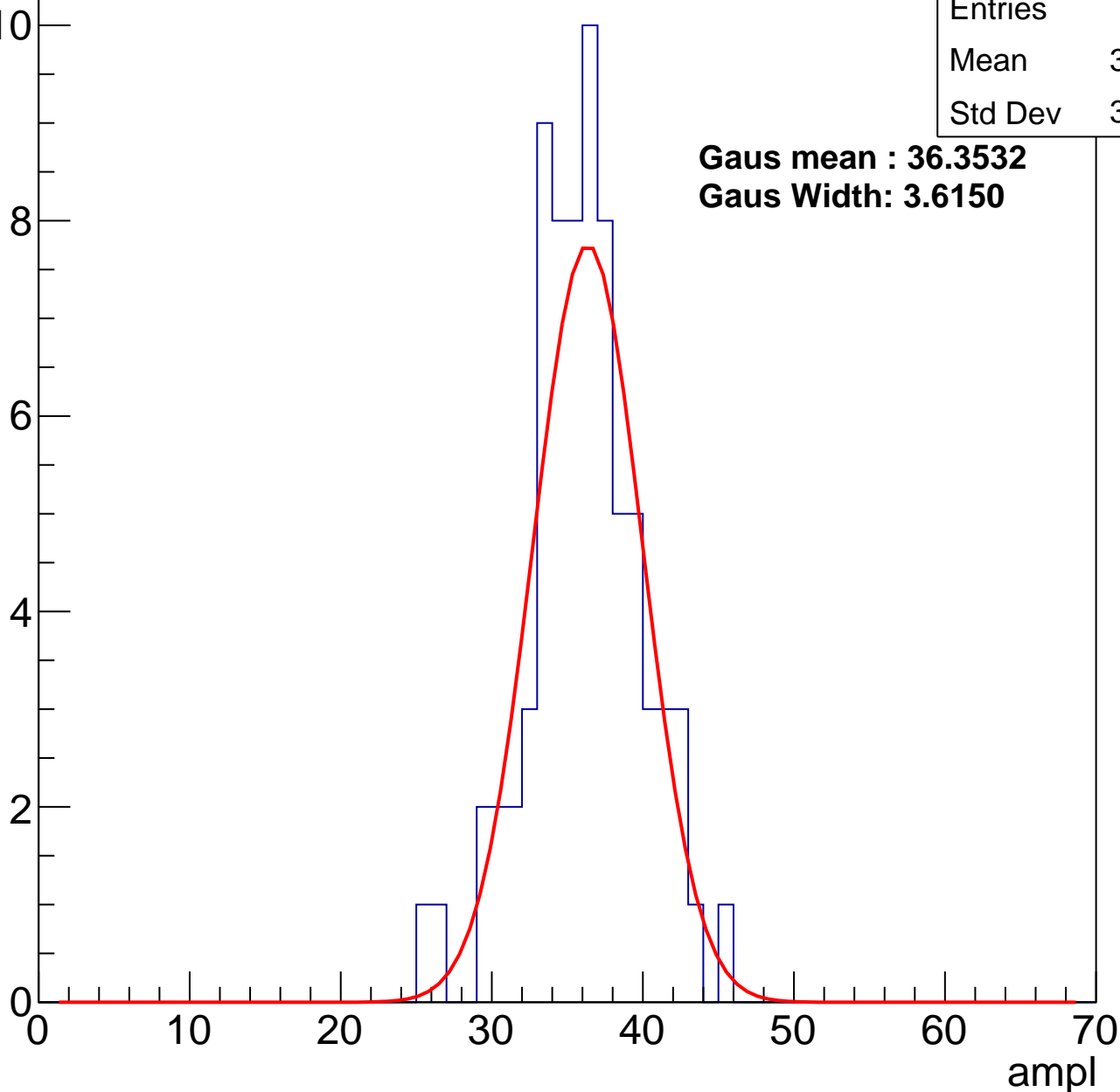
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	35.65
Std Dev	3.715

Gaus mean : 36.3532

Gaus Width: 3.6150

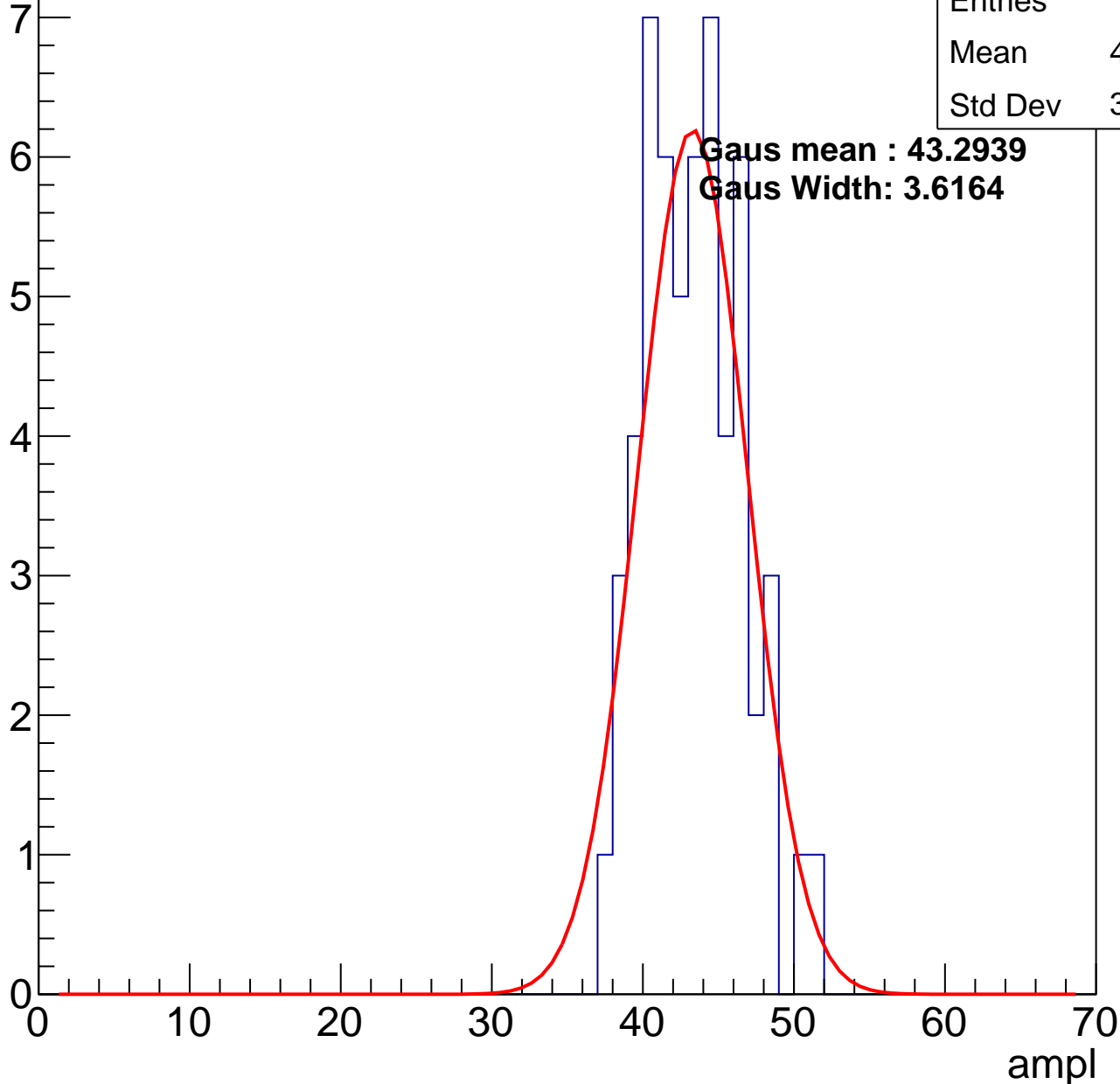


B1L103S, U20-ch115, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.93
Std Dev	3.173

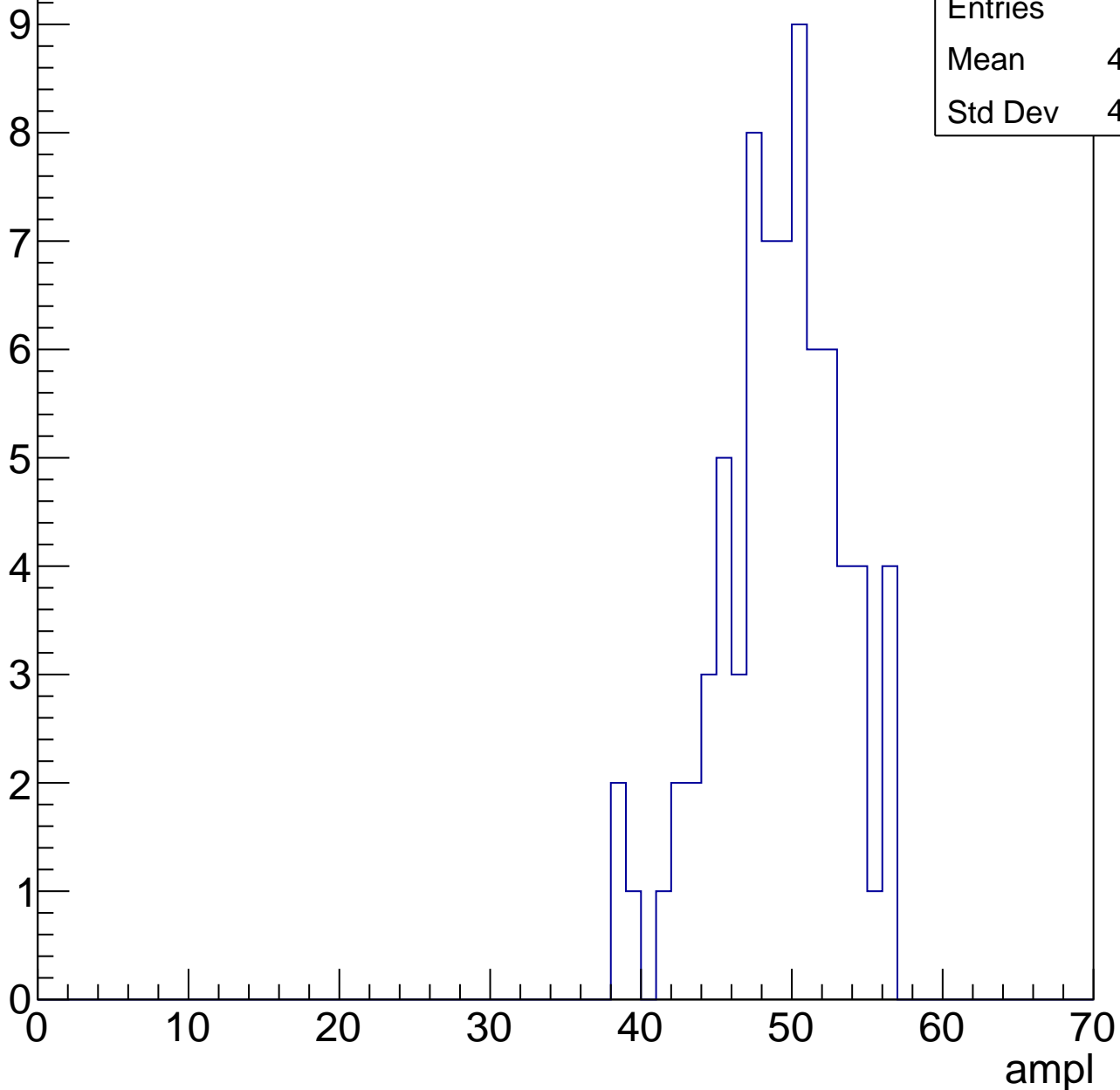


B1L103S, U20-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.68
Std Dev	4.119

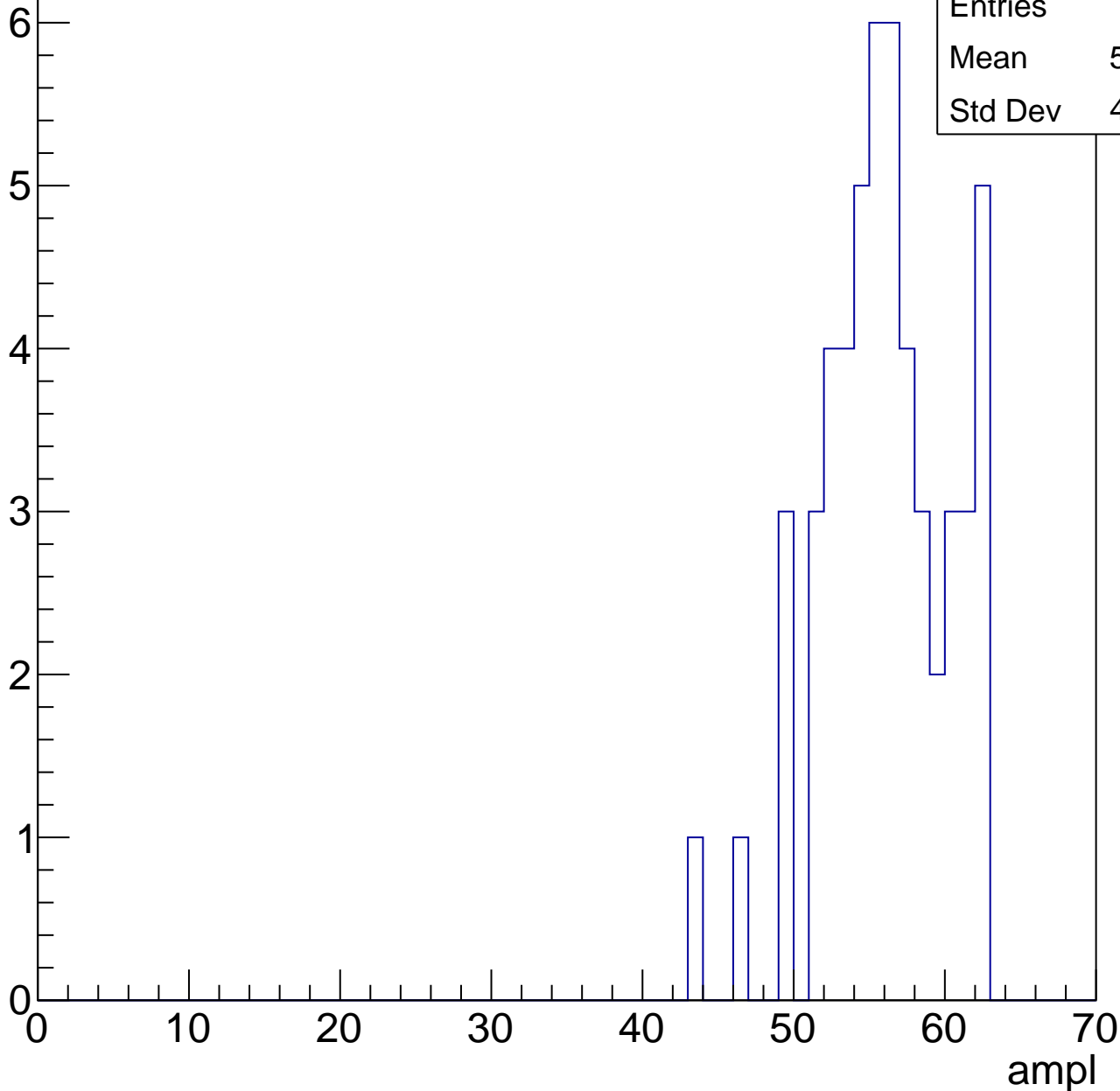


B1L103S, U20-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.43
Std Dev	4.205

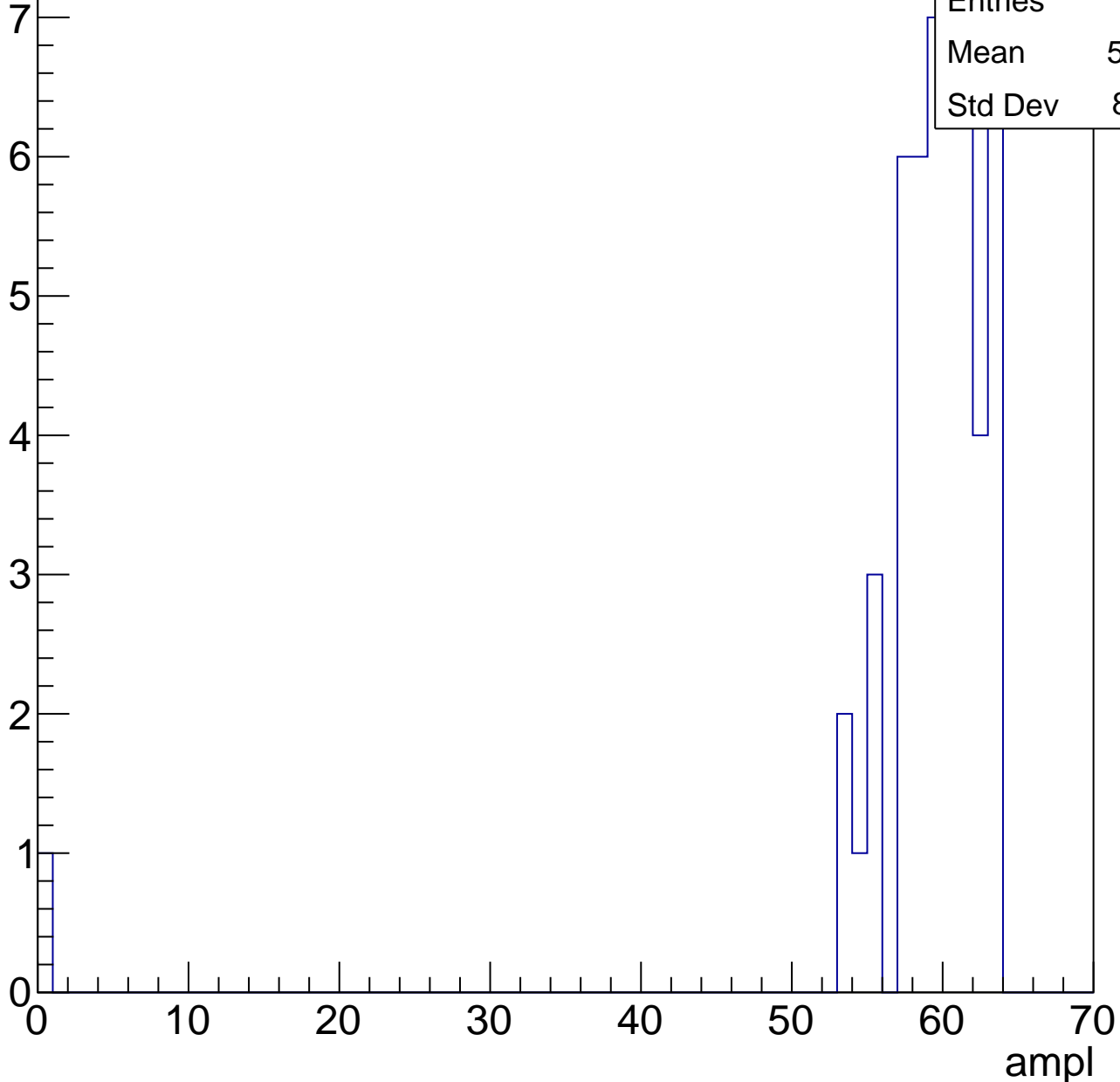


B1L103S, U20-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.12
Std Dev	8.631



B1L103S, U20-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	2.098

ampl

0 10 20 30 40 50 60 70

B1L103S, U20-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U20-ch116, adc0

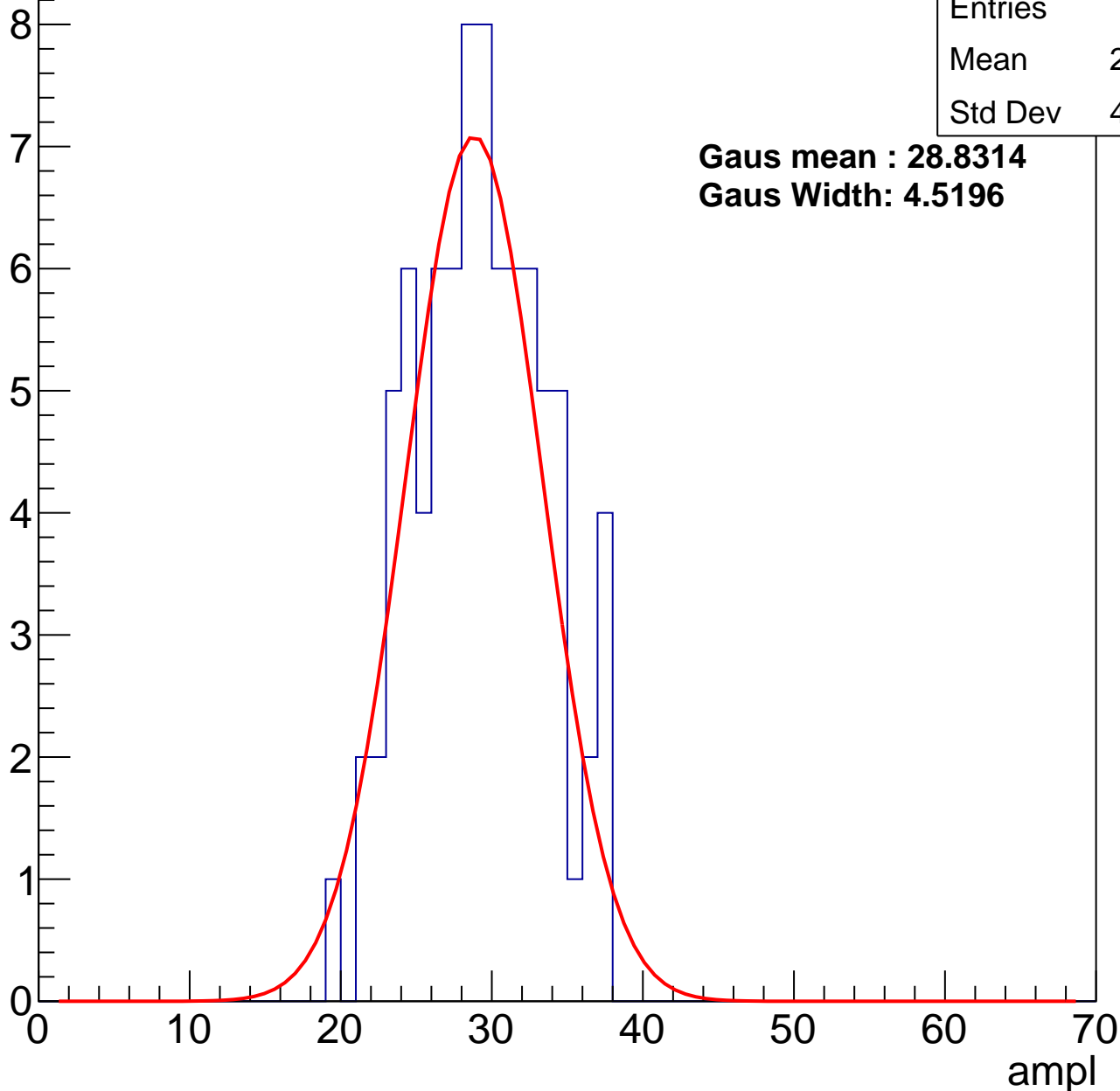
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	28.75
Std Dev	4.222

Gaus mean : 28.8314

Gaus Width: 4.5196



B1L103S, U20-ch116, adc1

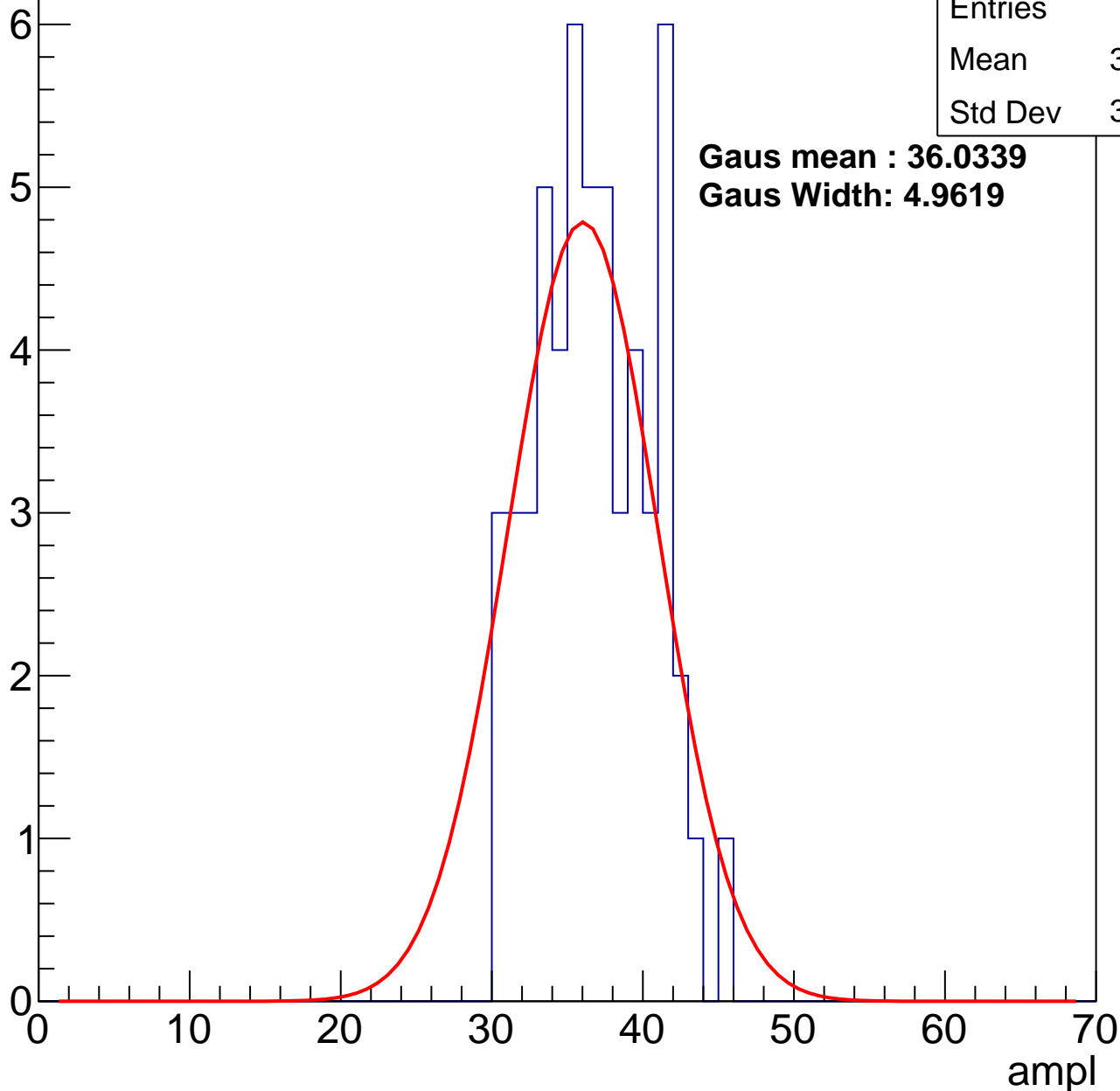
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.35
Std Dev	3.713

Gaus mean : 36.0339

Gaus Width: 4.9619



B1L103S, U20-ch116, adc2

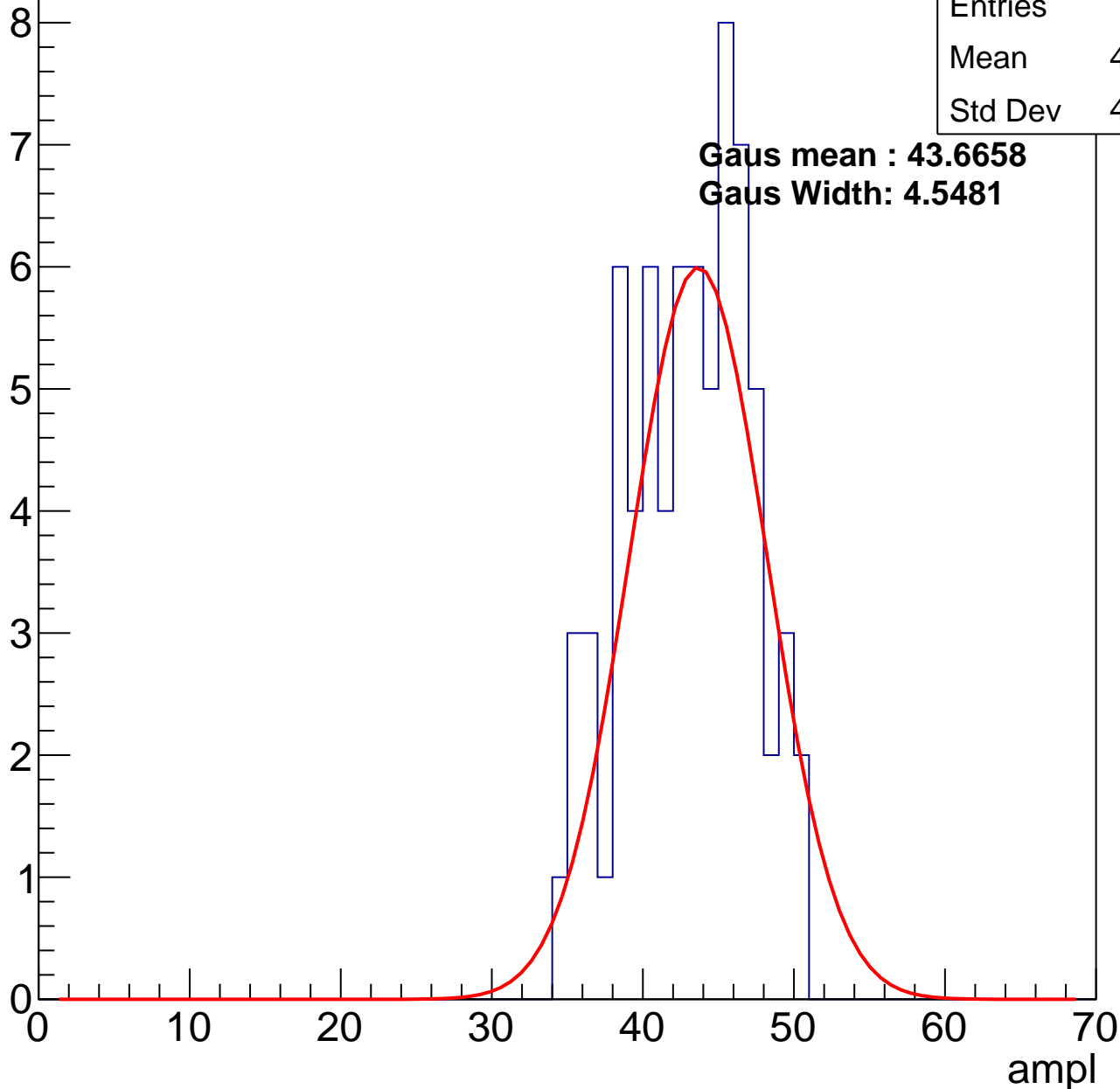
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.53
Std Dev	4.024

Gaus mean : 43.6658

Gaus Width: 4.5481

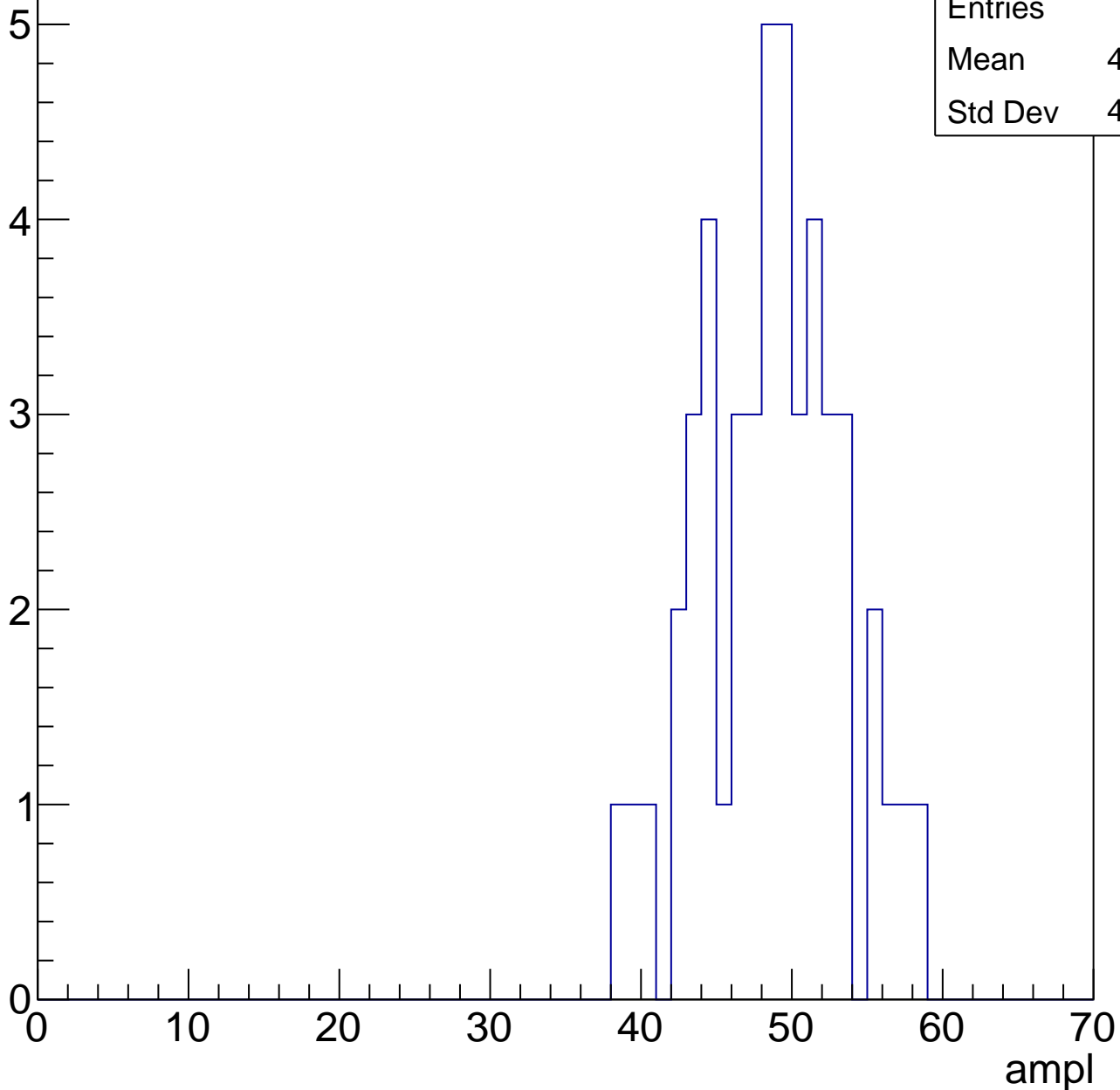


B1L103S, U20-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	48.19
Std Dev	4.606

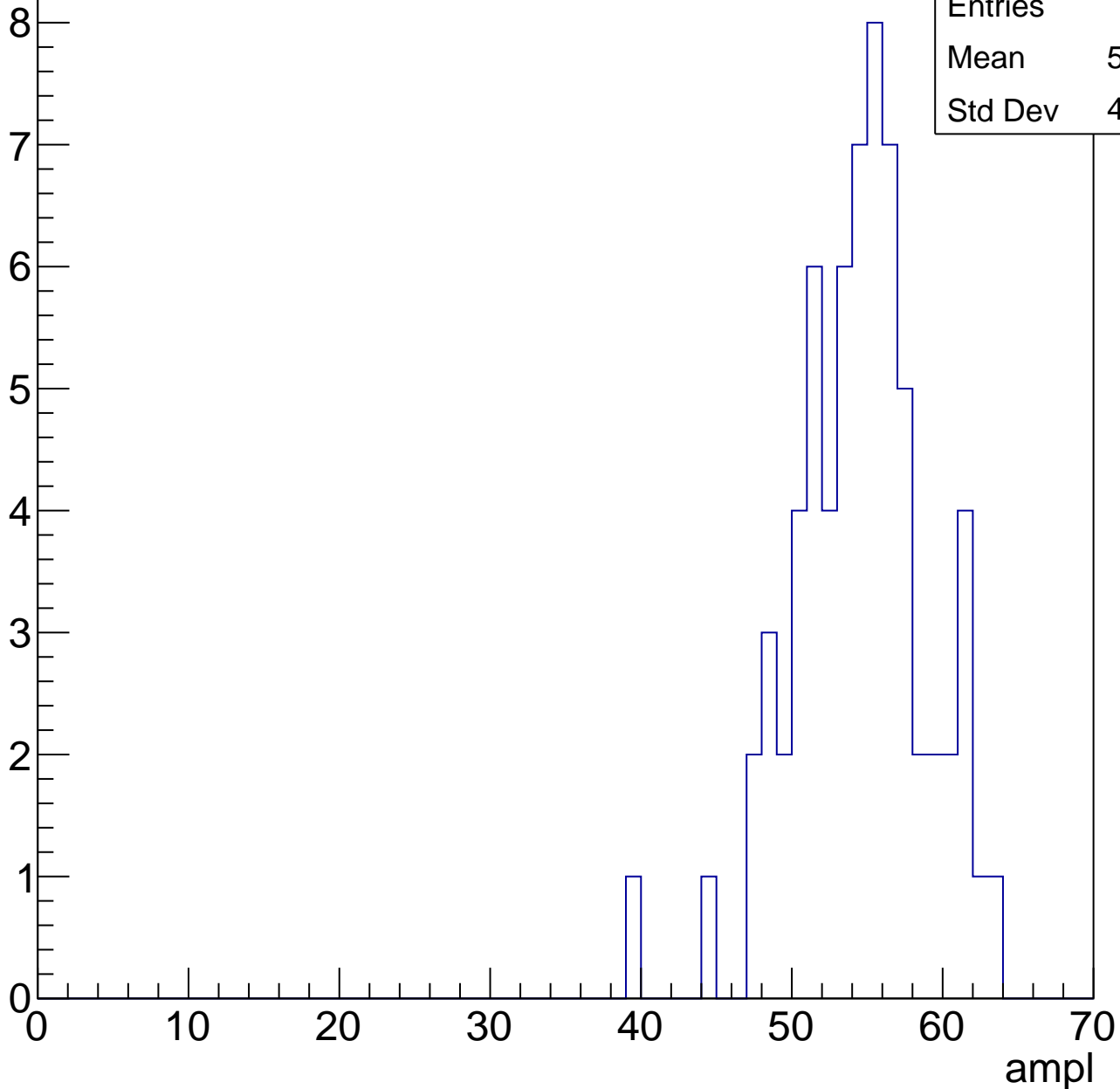


B1L103S, U20-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	53.96
Std Dev	4.367

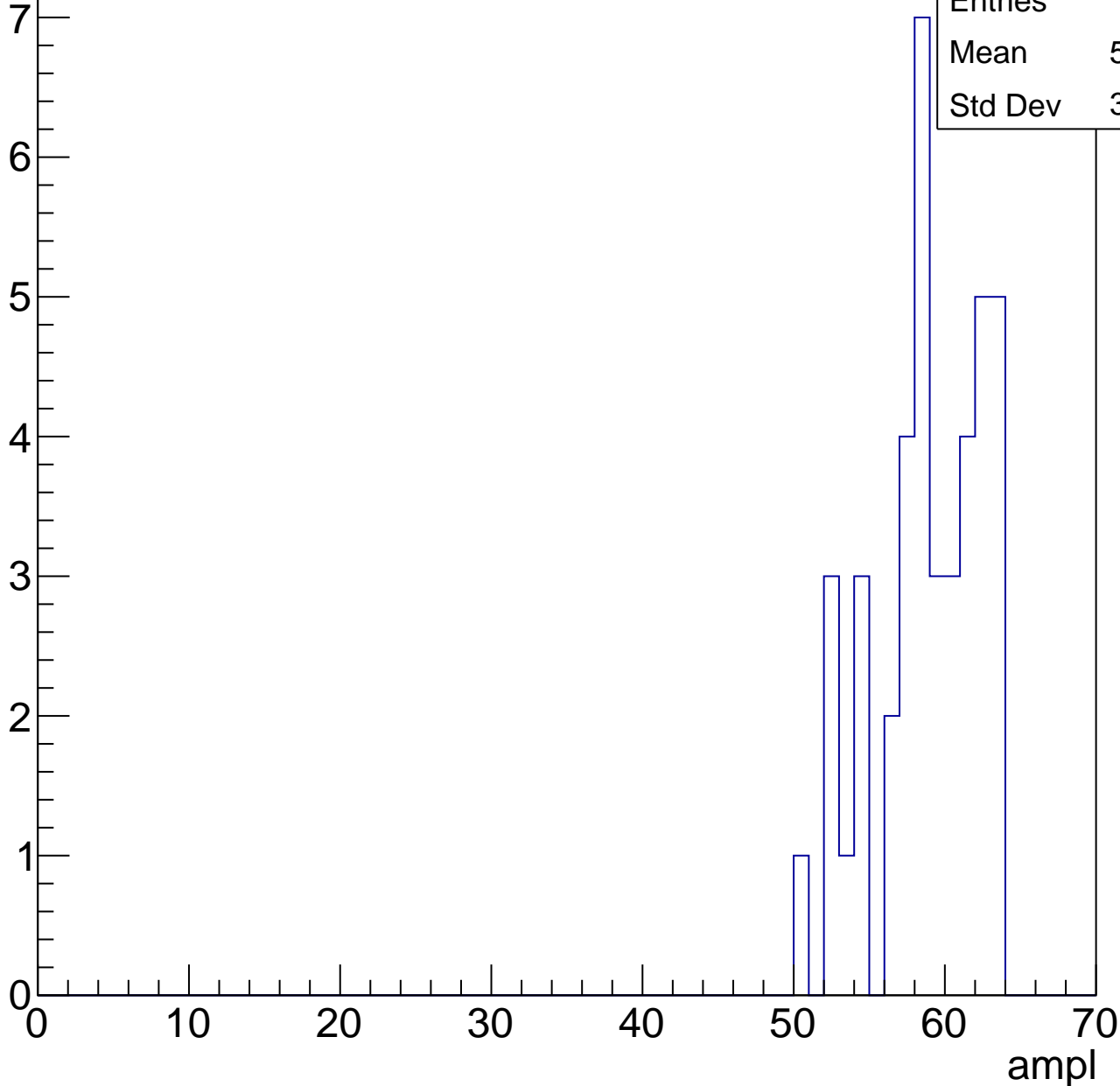


B1L103S, U20-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

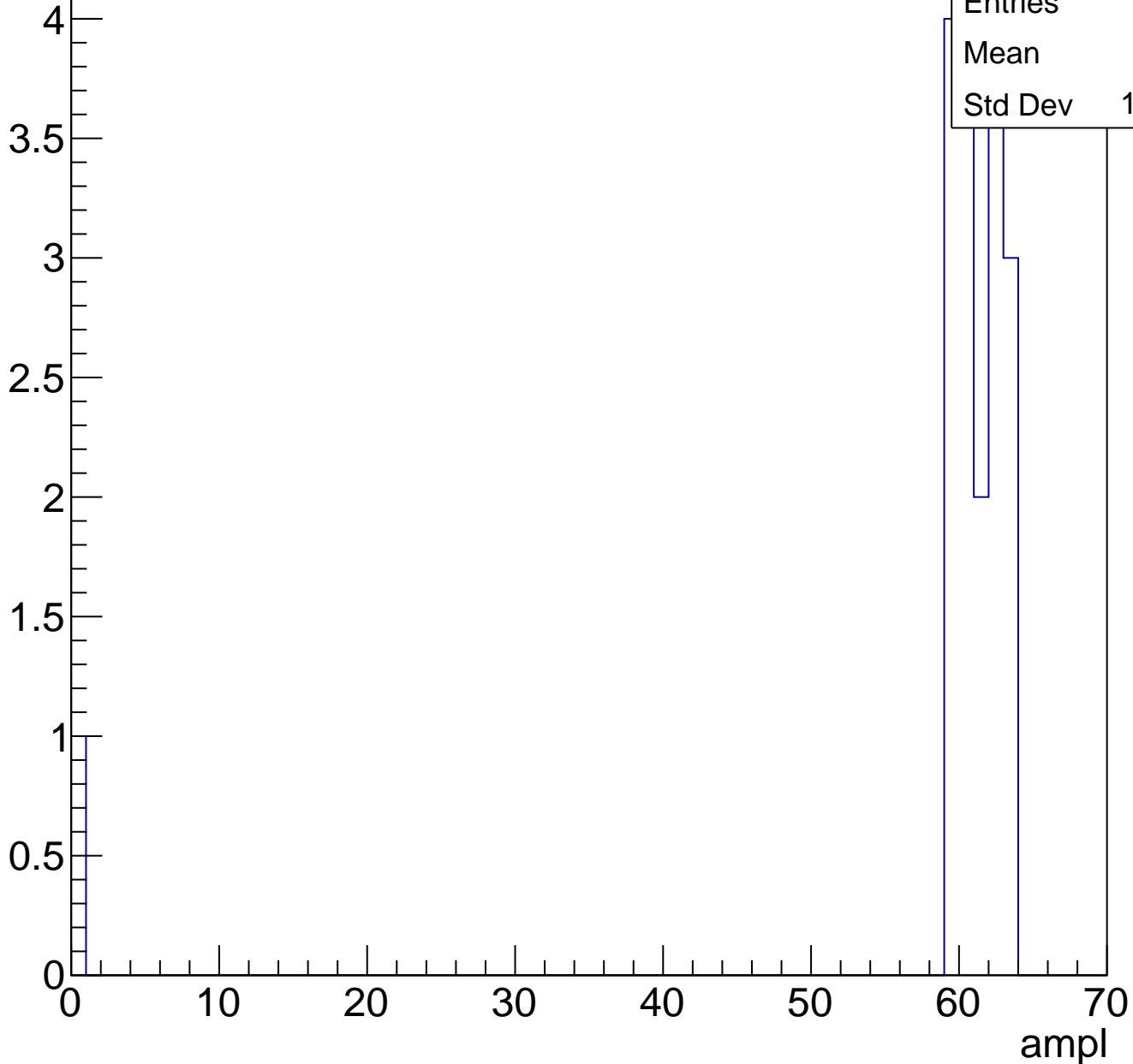
Entries	41
Mean	58.37
Std Dev	3.518



B1L103S, U20-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

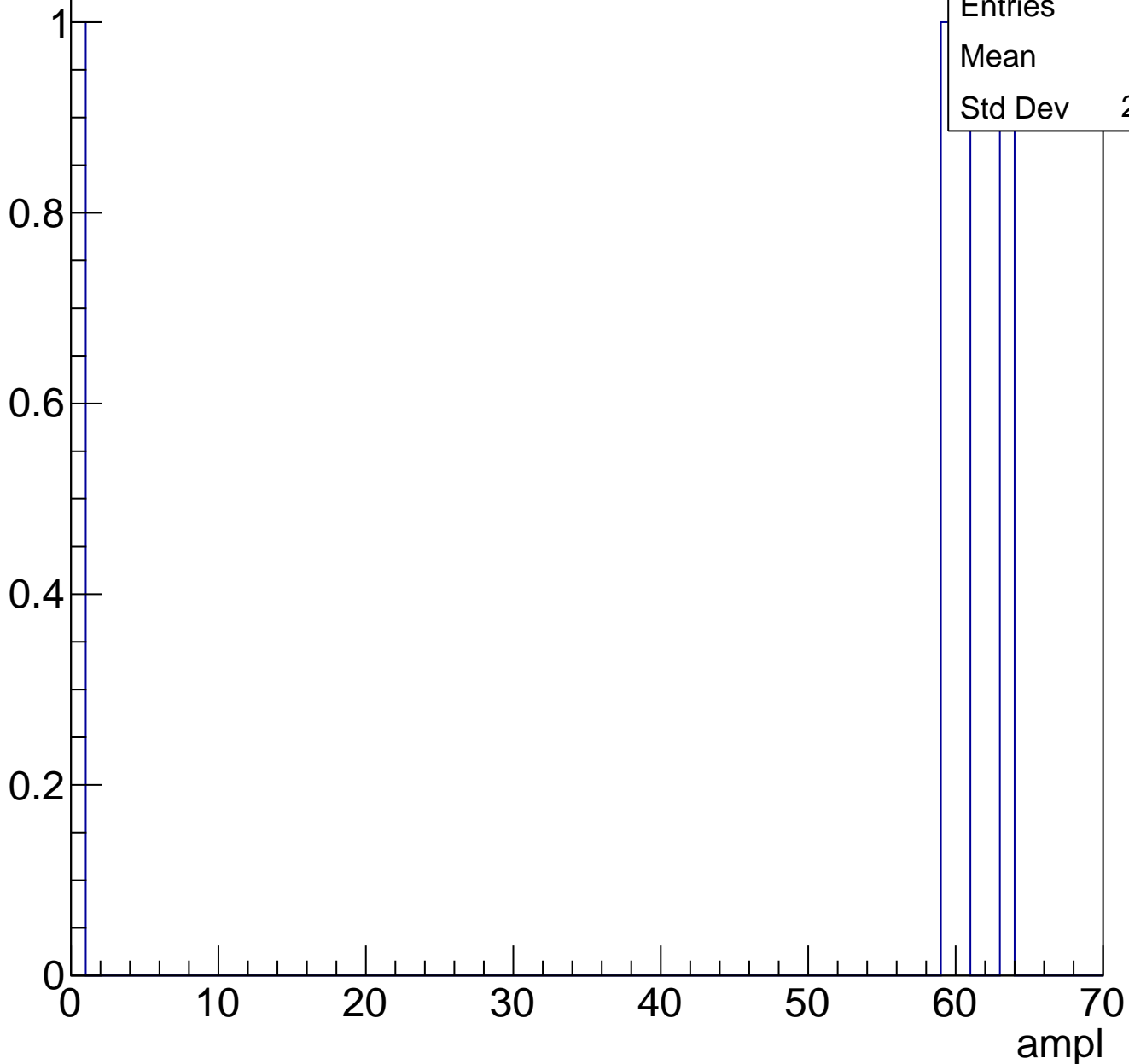
Entry



B1L103S, U20-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch117, adc0

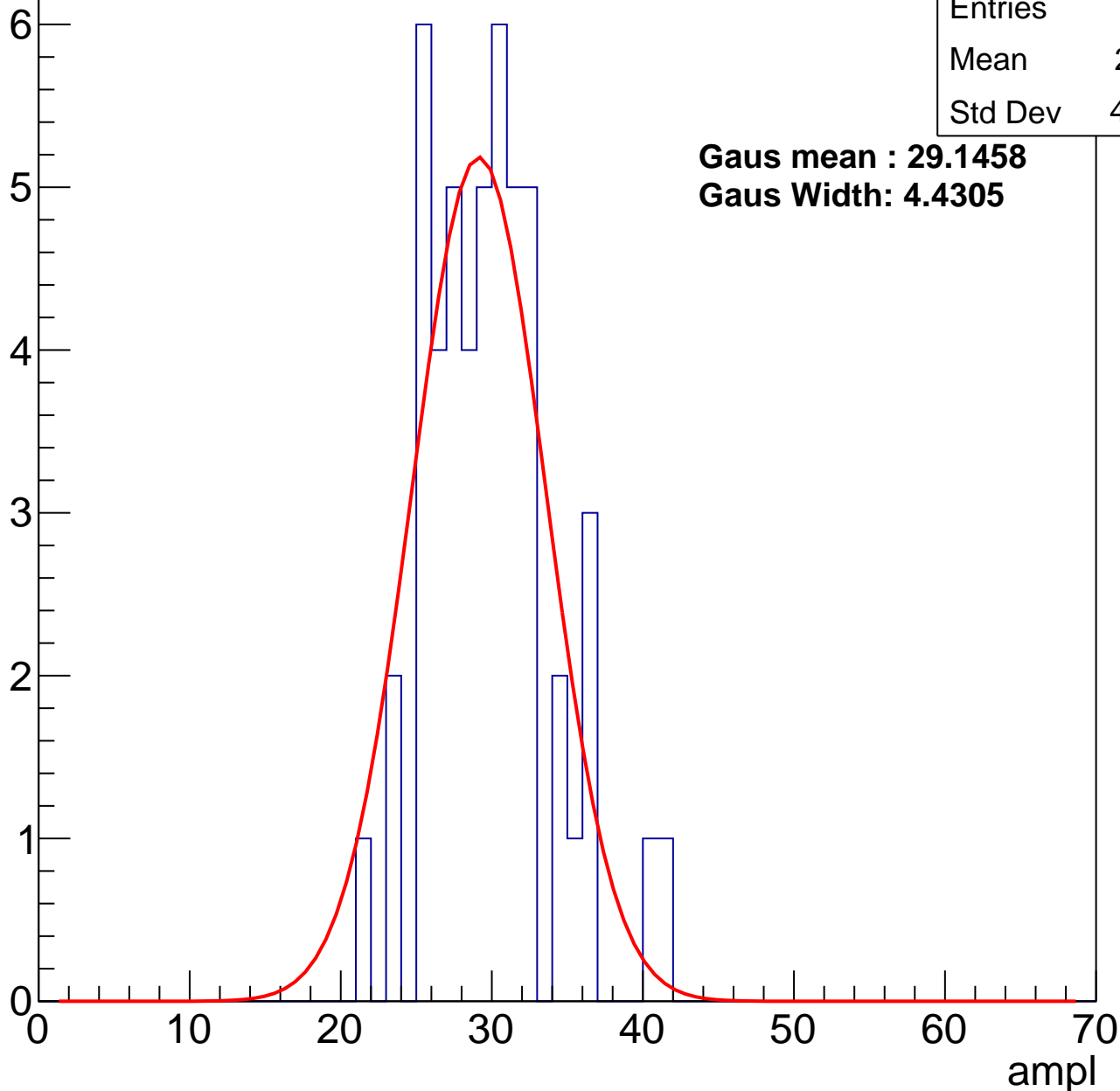
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	29.41
Std Dev	4.088

Gaus mean : 29.1458

Gaus Width: 4.4305



B1L103S, U20-ch117, adc1

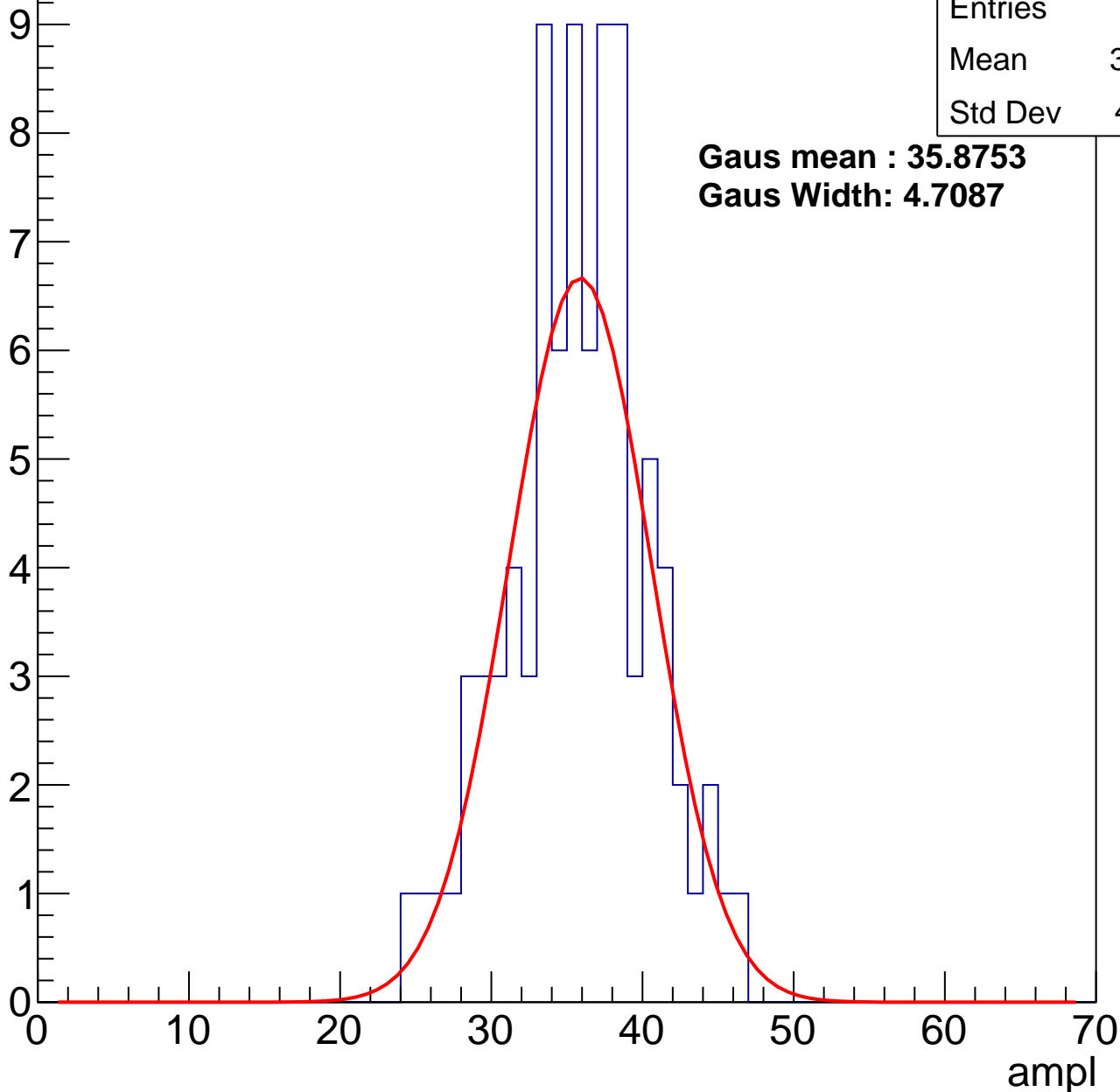
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	35.37
Std Dev	4.551

Gaus mean : 35.8753

Gaus Width: 4.7087



B1L103S, U20-ch117, adc2

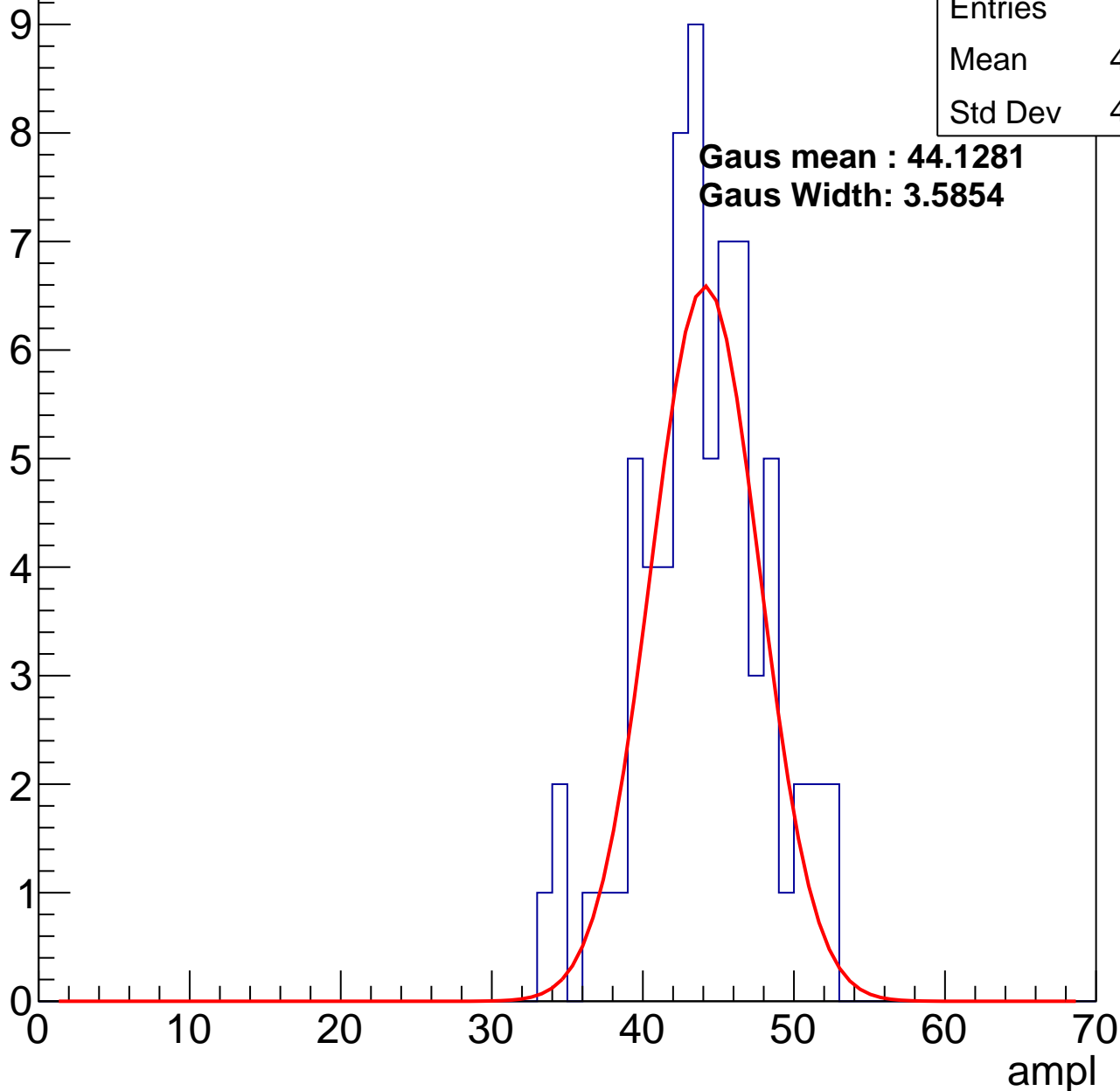
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.53
Std Dev	4.115

Gaus mean : 44.1281

Gaus Width: 3.5854

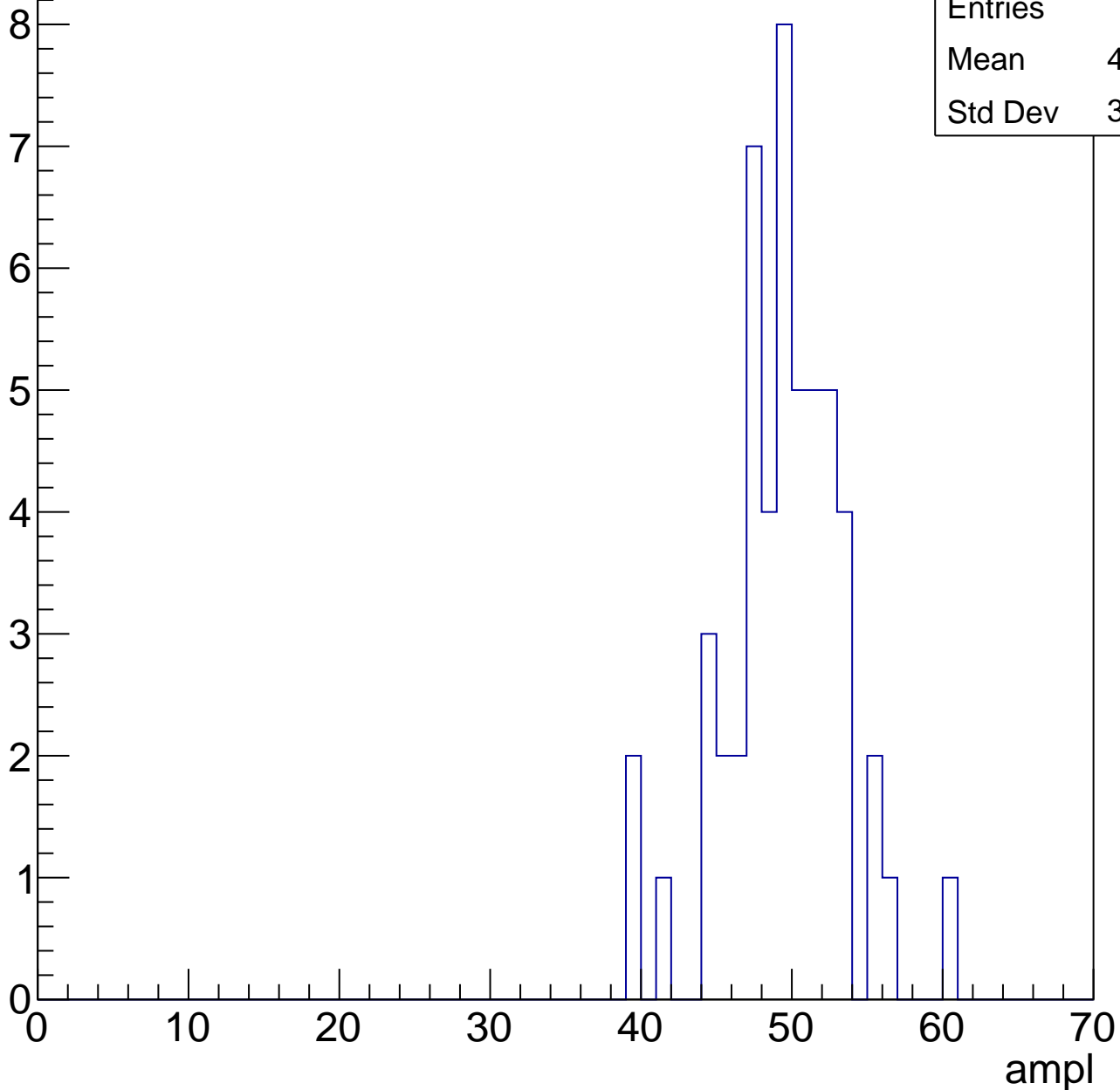


B1L103S, U20-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	49.02
Std Dev	3.915

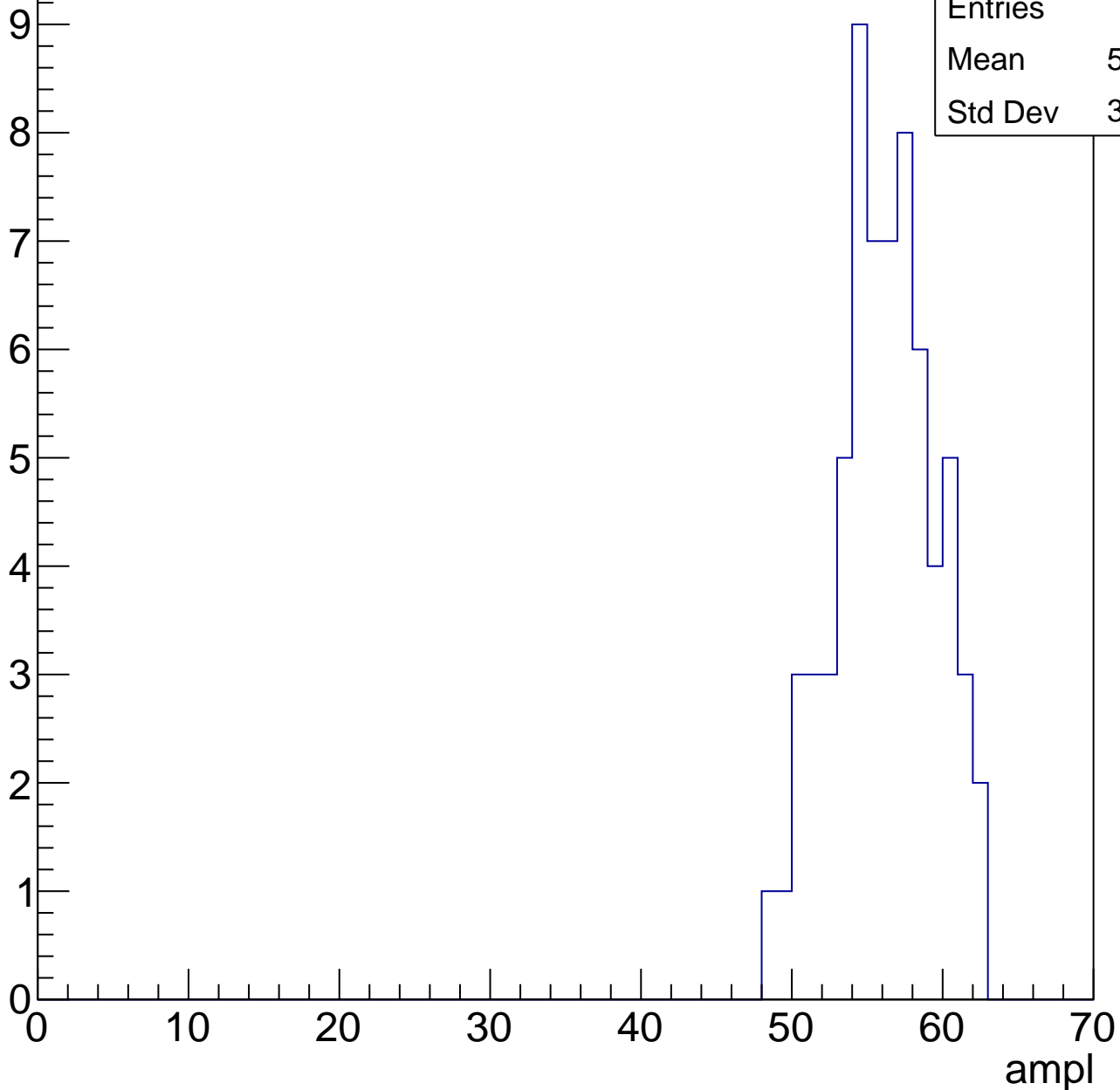


B1L103S, U20-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	55.69
Std Dev	3.279

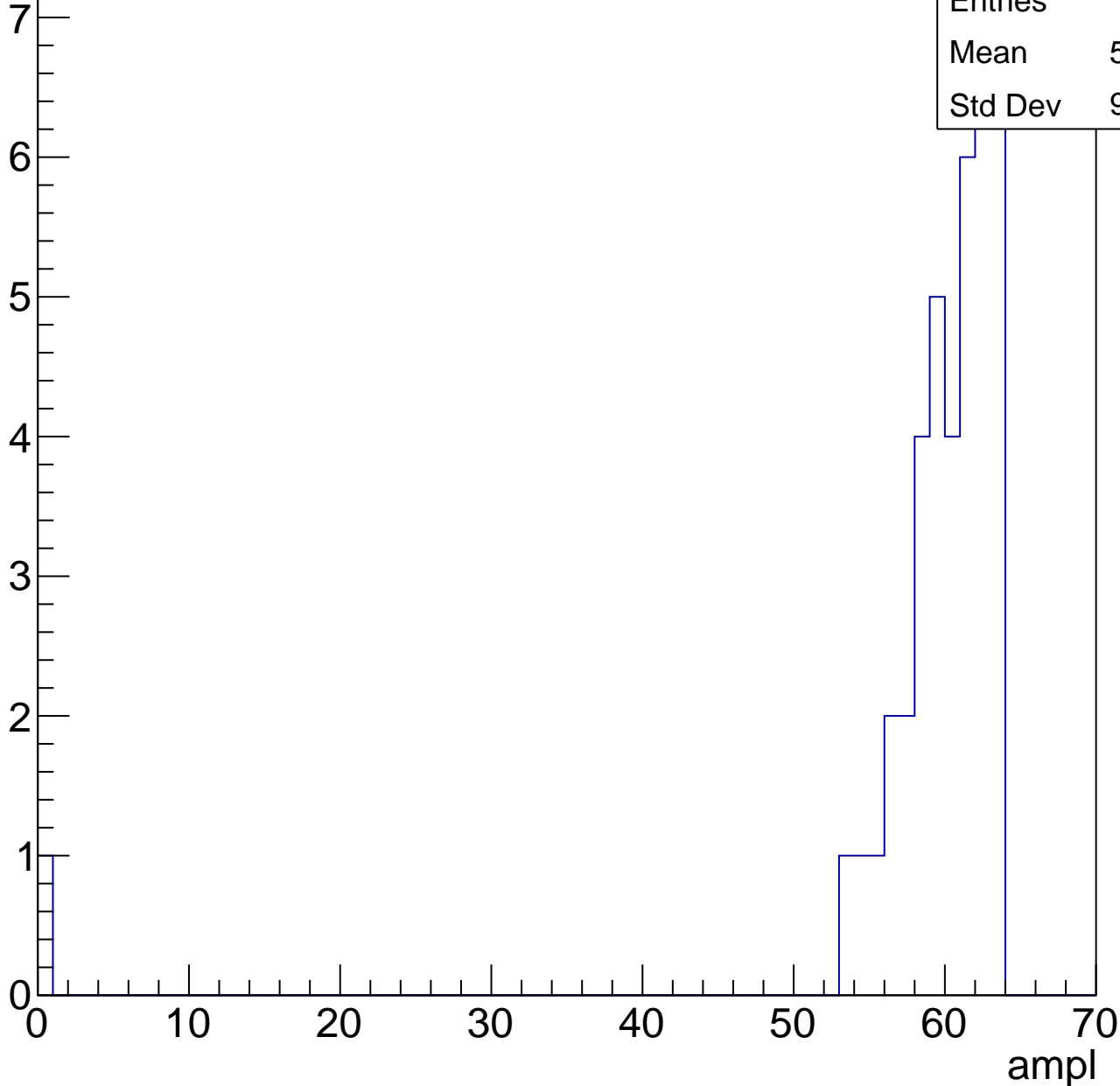


B1L103S, U20-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	58.44
Std Dev	9.599



B1L103S, U20-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L103S, U20-ch118, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	95
Mean	29.57
Std Dev	5.196

Gaus mean : 30.4380

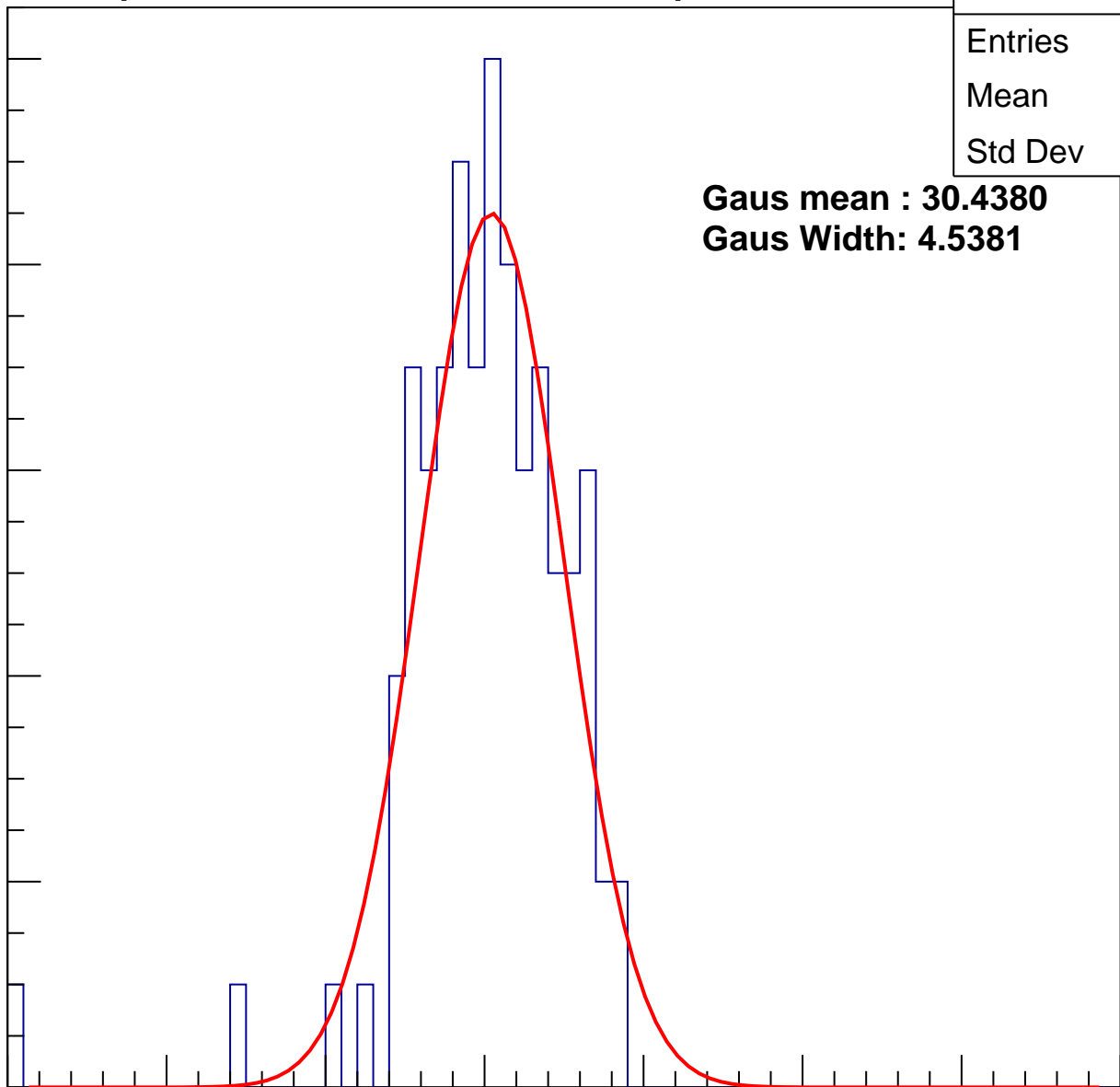
Gaus Width: 4.5381

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch118, adc1

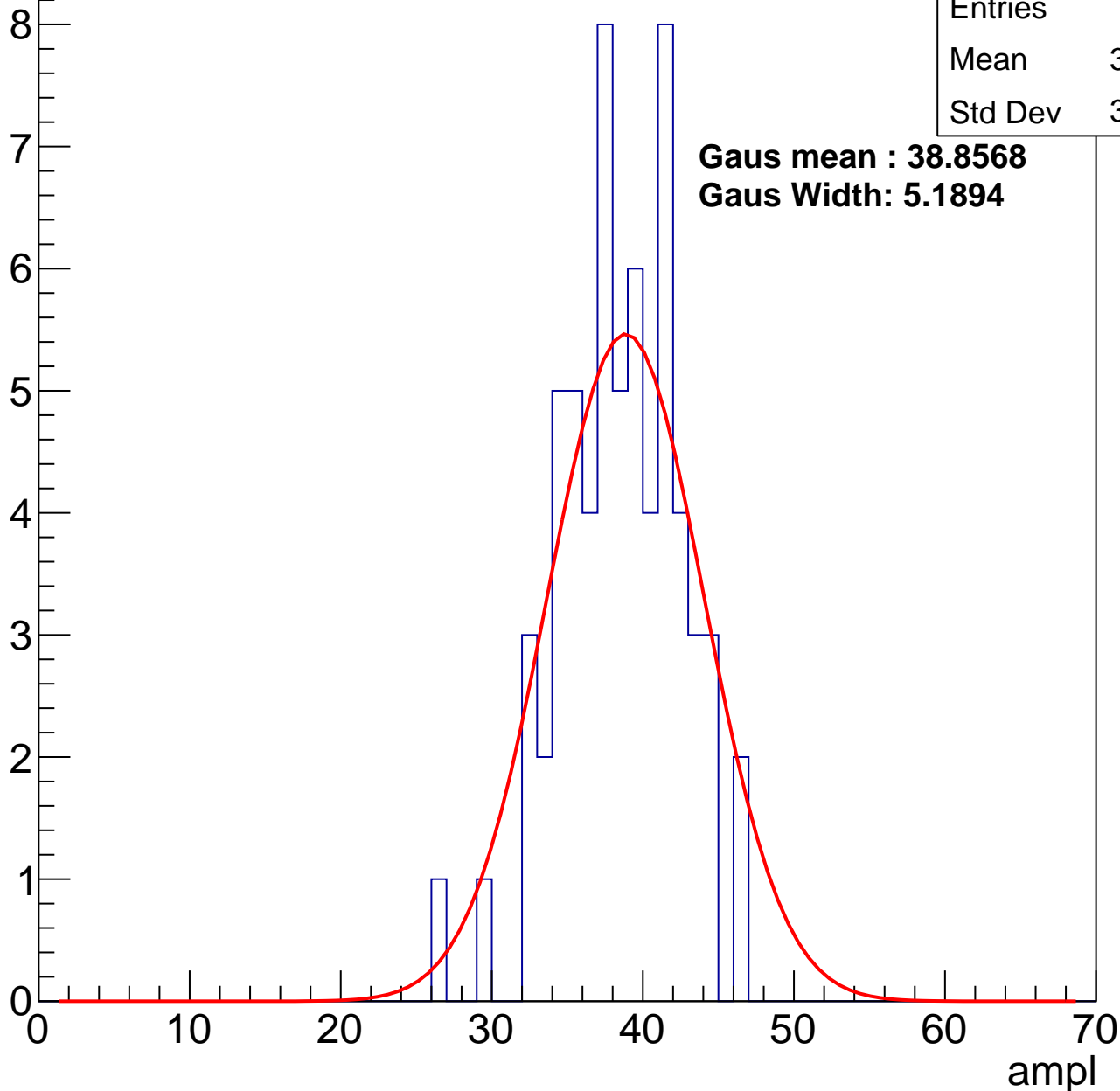
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	38.05
Std Dev	3.955

Gaus mean : 38.8568

Gaus Width: 5.1894



B1L103S, U20-ch118, adc2

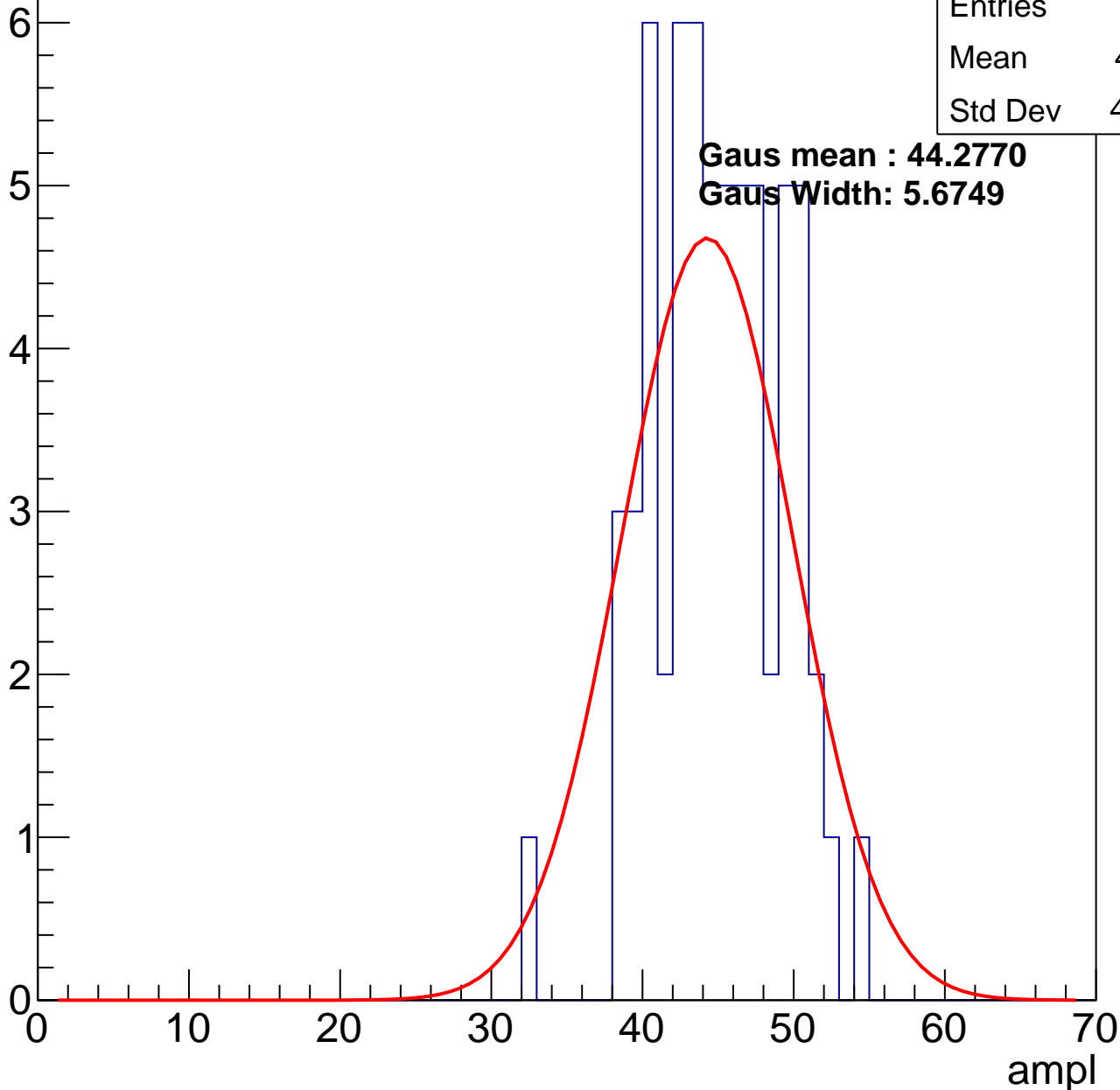
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.51
Std Dev	4.223

Gaus mean : 44.2770

Gaus Width: 5.6749

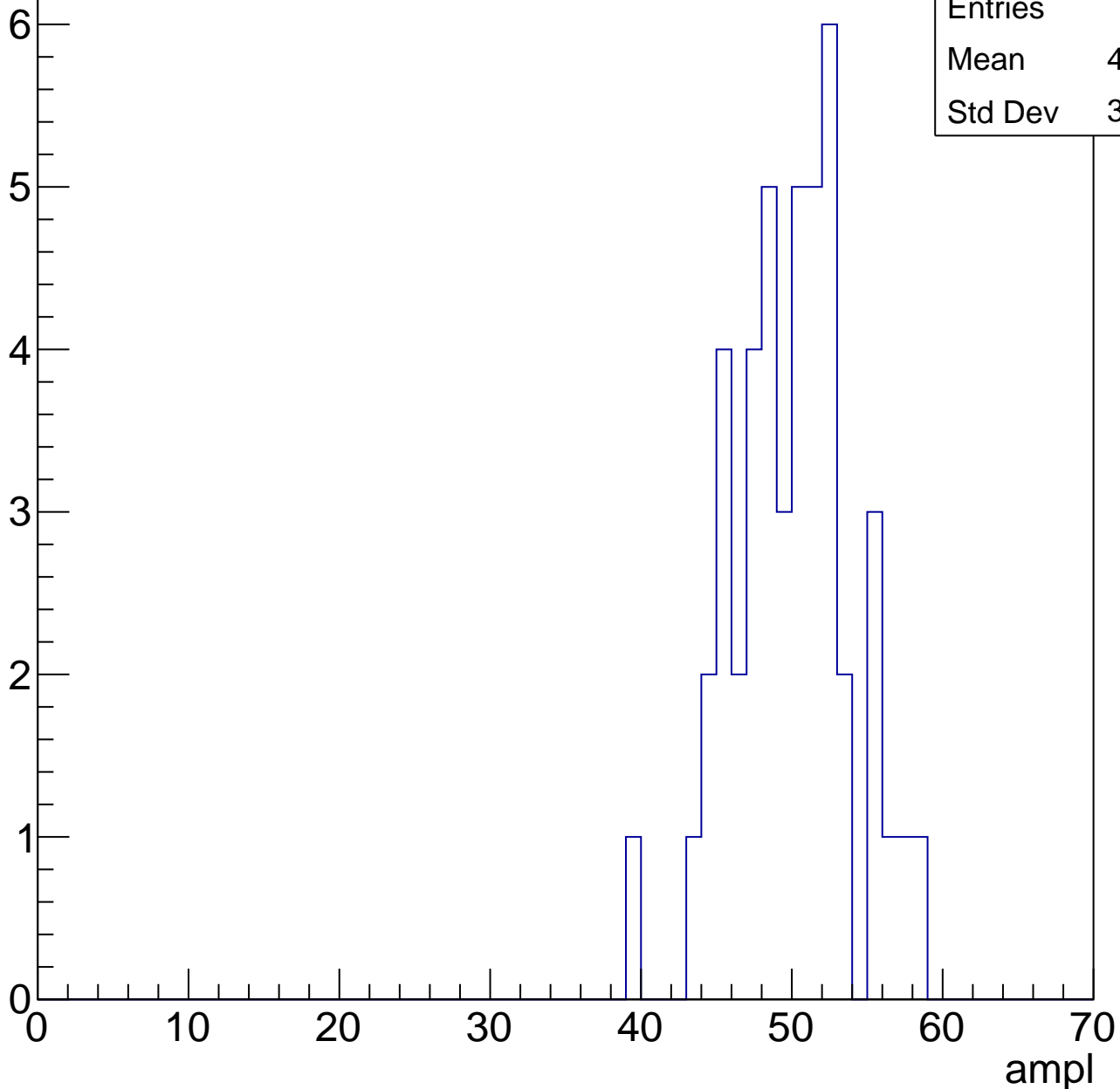


B1L103S, U20-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	49.48
Std Dev	3.877

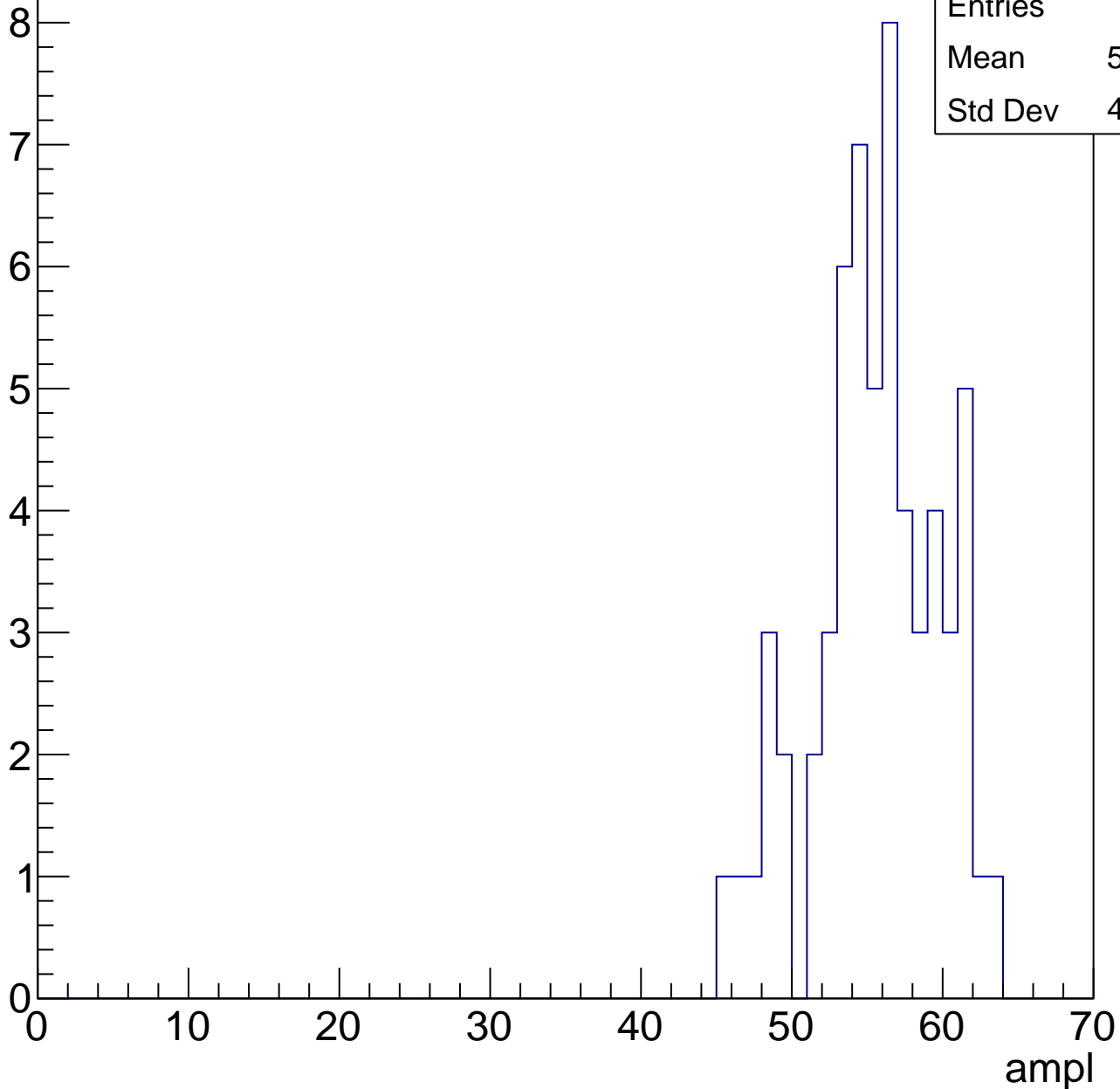


B1L103S, U20-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.08
Std Dev	4.156

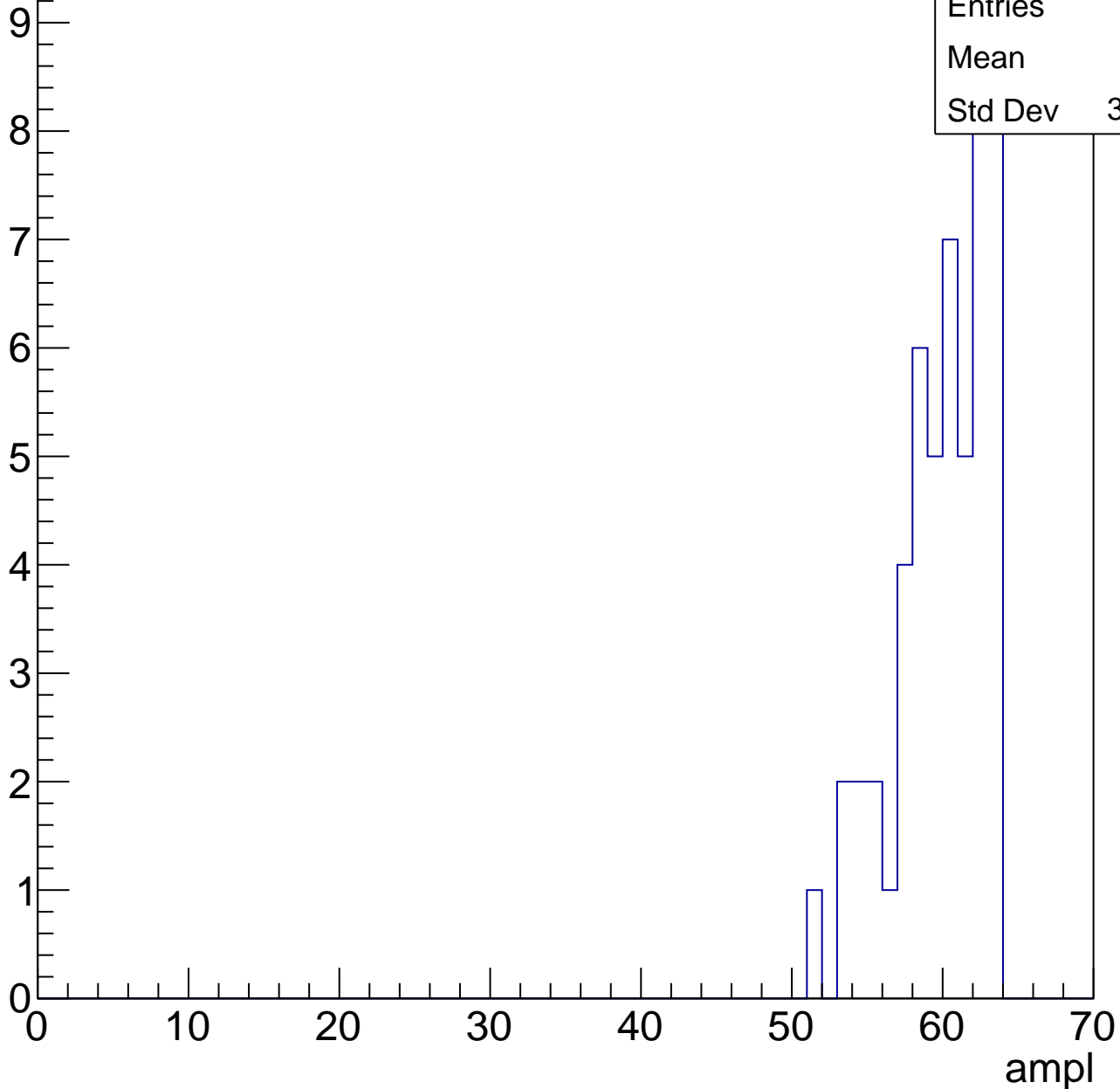


B1L103S, U20-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

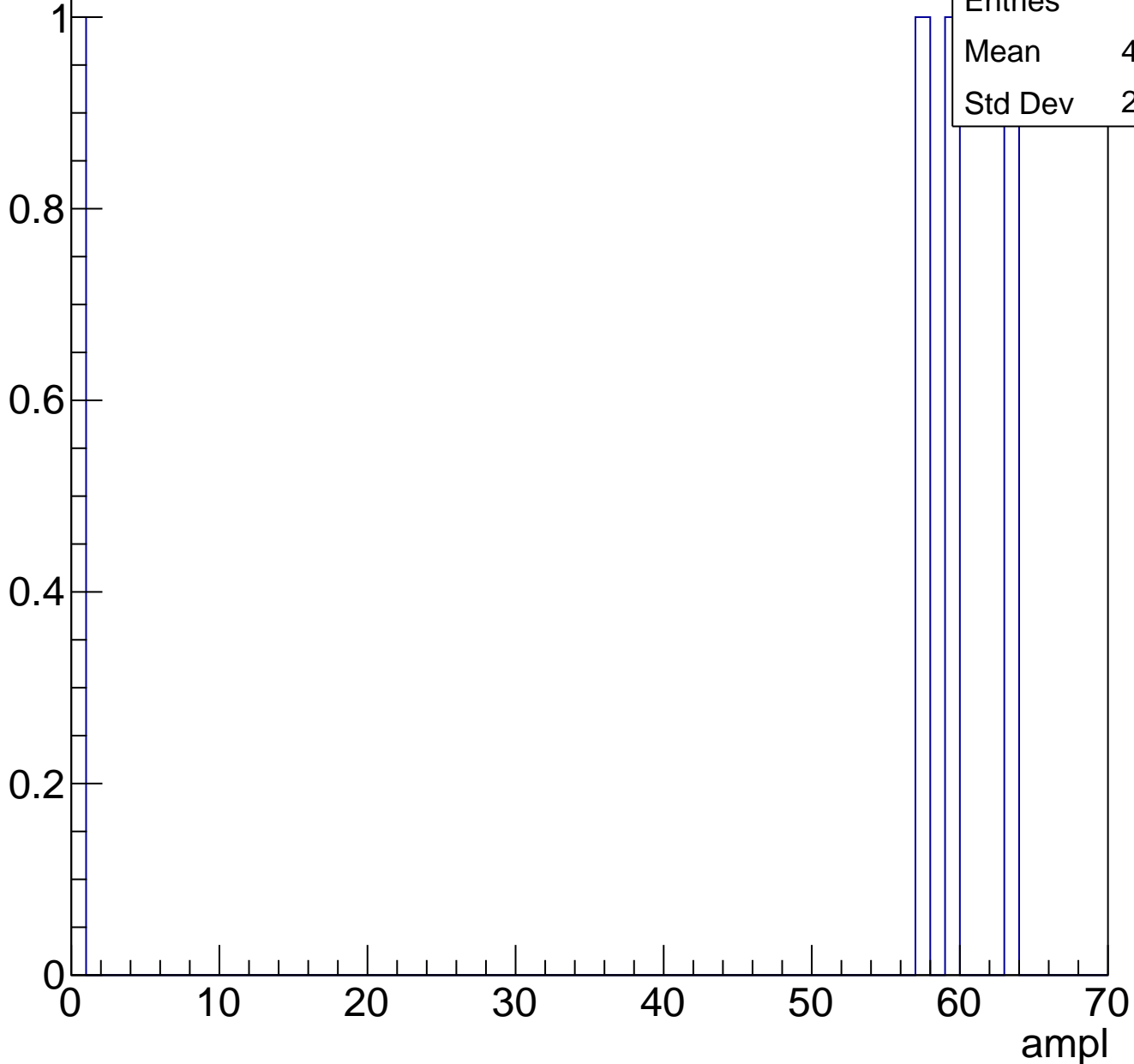
Entries	52
Mean	59.4
Std Dev	3.027



B1L103S, U20-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	44.75
Std Dev	25.93

B1L103S, U20-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch119, adc0

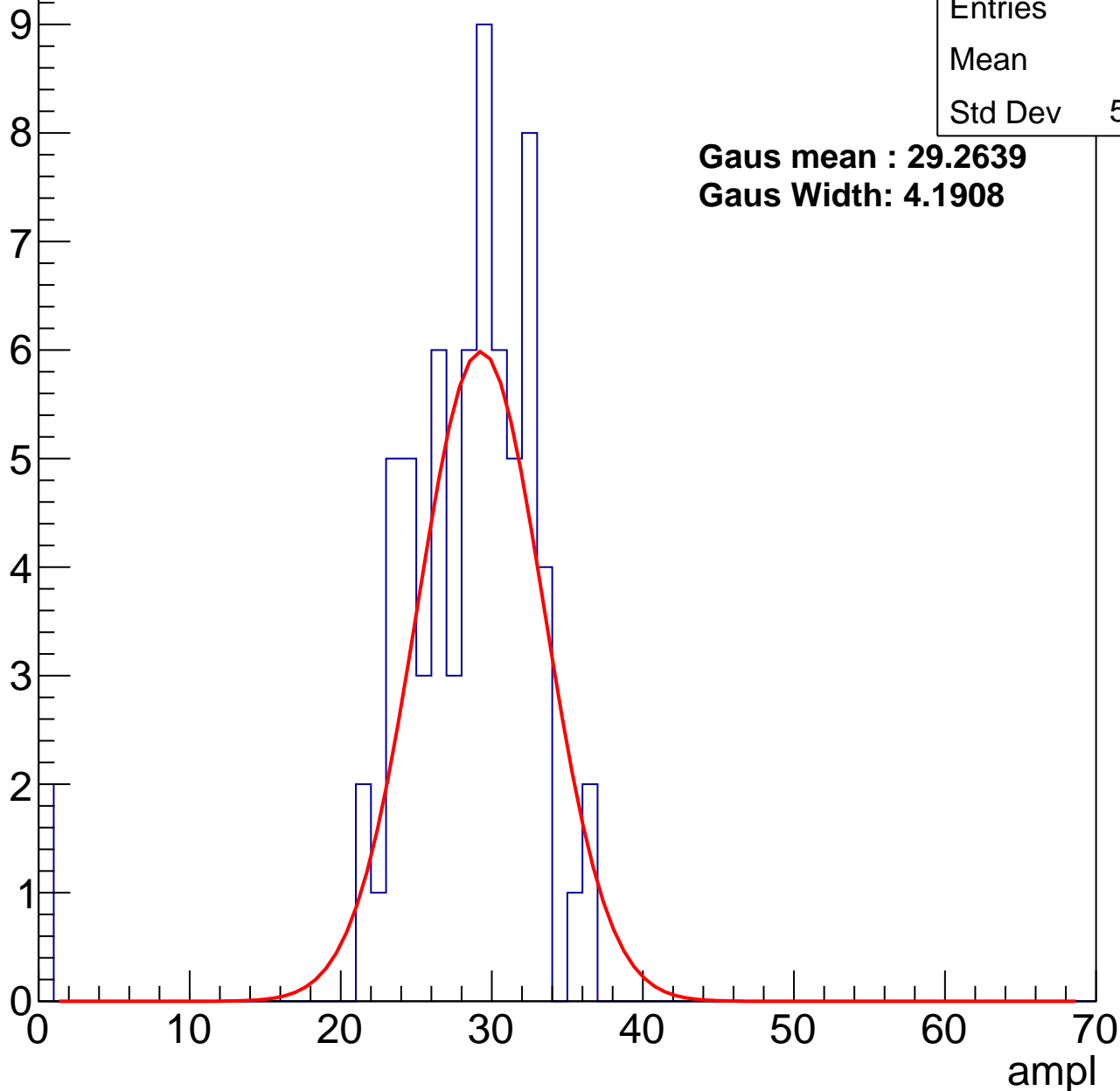
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.5
Std Dev	5.974

Gaus mean : 29.2639

Gaus Width: 4.1908



B1L103S, U20-ch119, adc1

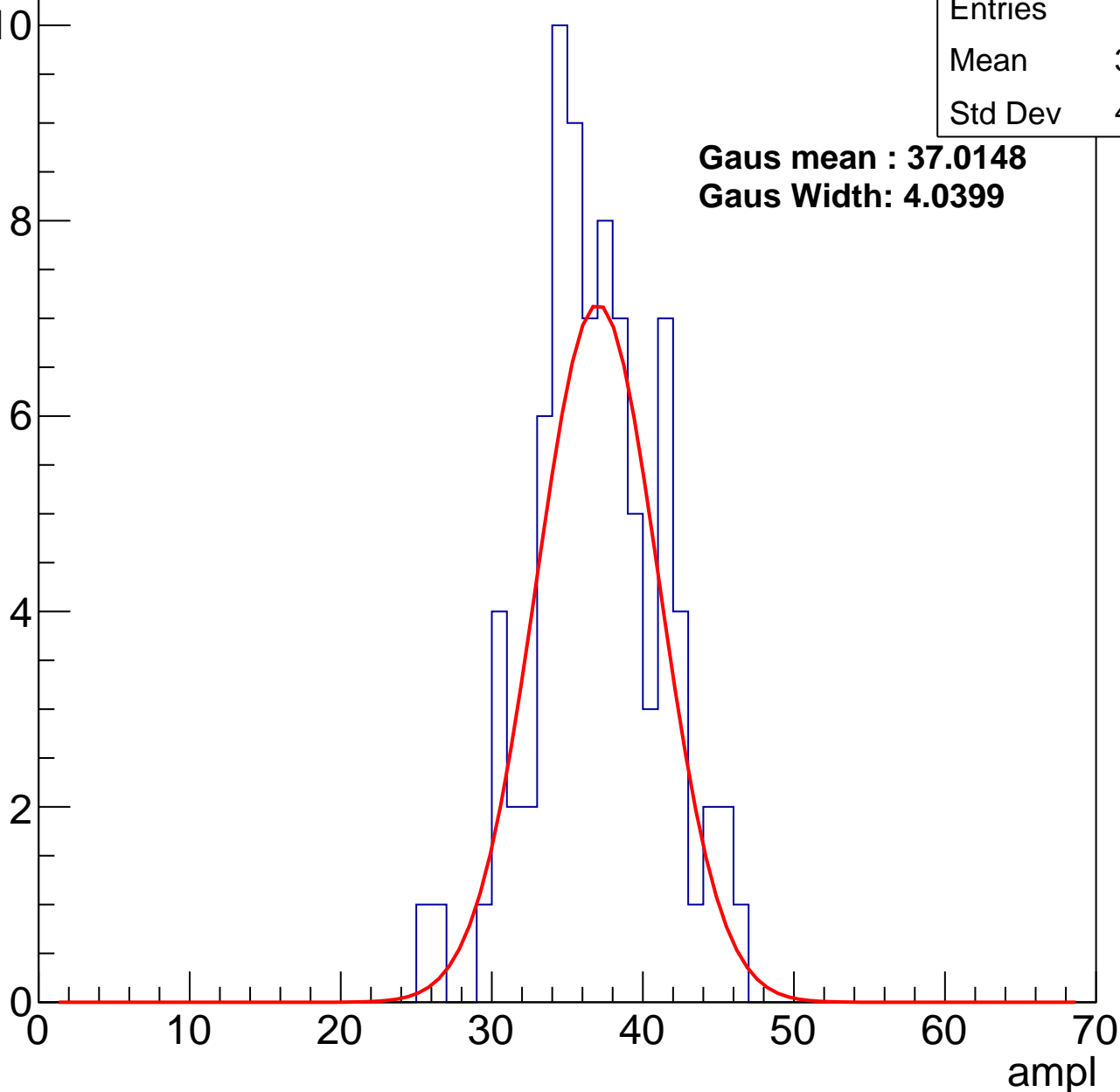
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	36.51
Std Dev	4.201

Gaus mean : 37.0148

Gaus Width: 4.0399



B1L103S, U20-ch119, adc2

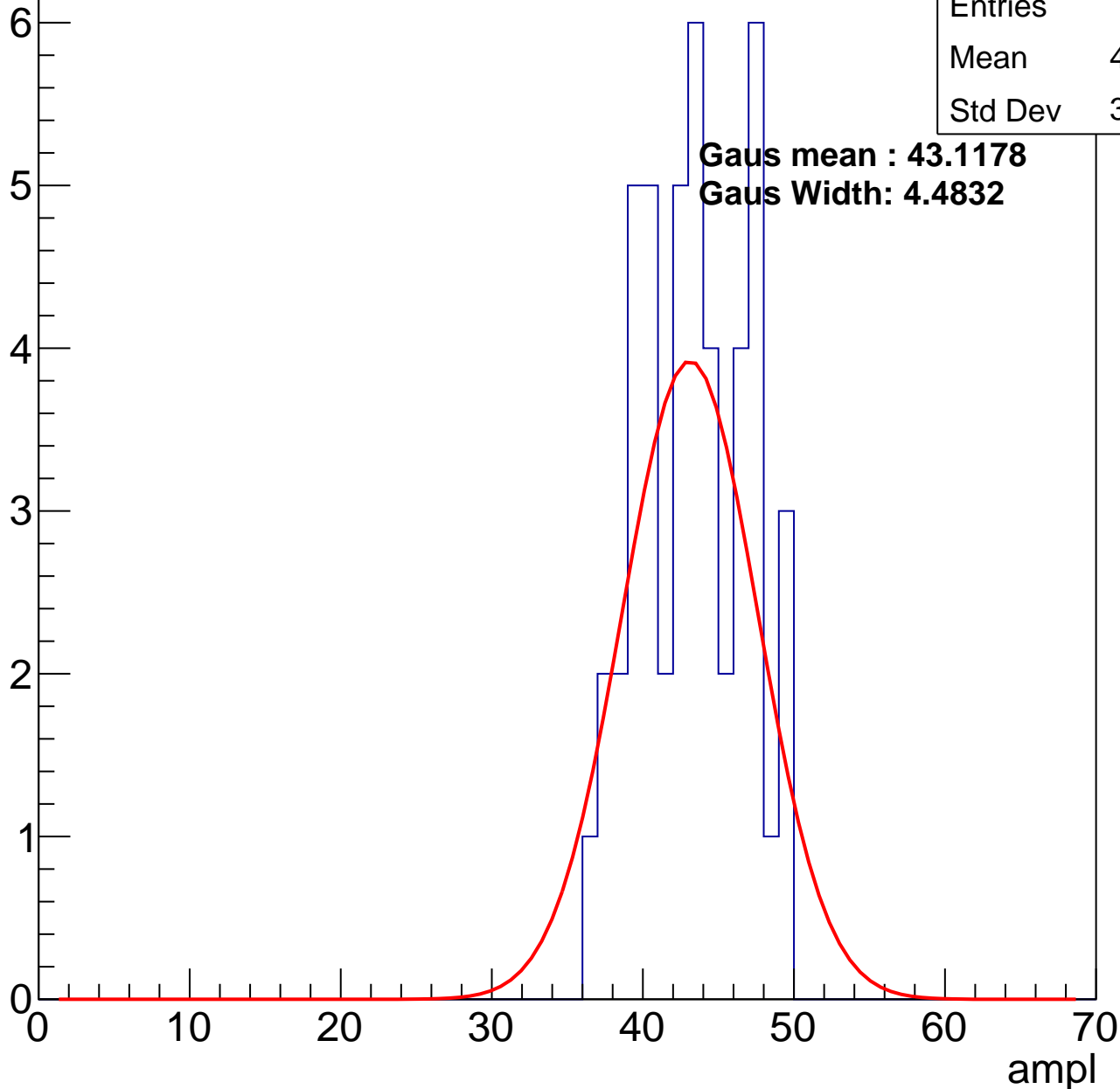
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	42.88
Std Dev	3.492

Gaus mean : 43.1178

Gaus Width: 4.4832

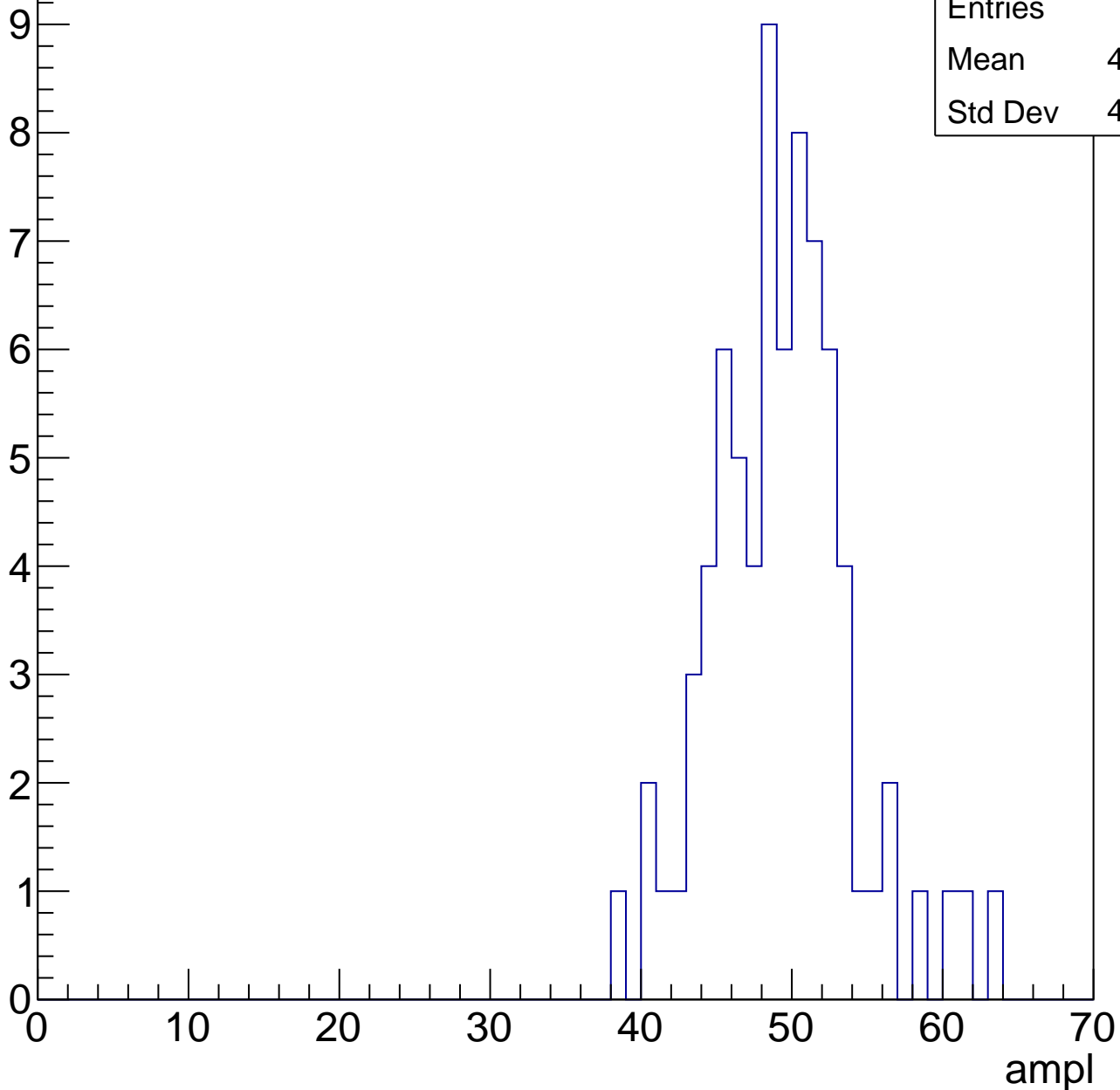


B1L103S, U20-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.85
Std Dev	4.652

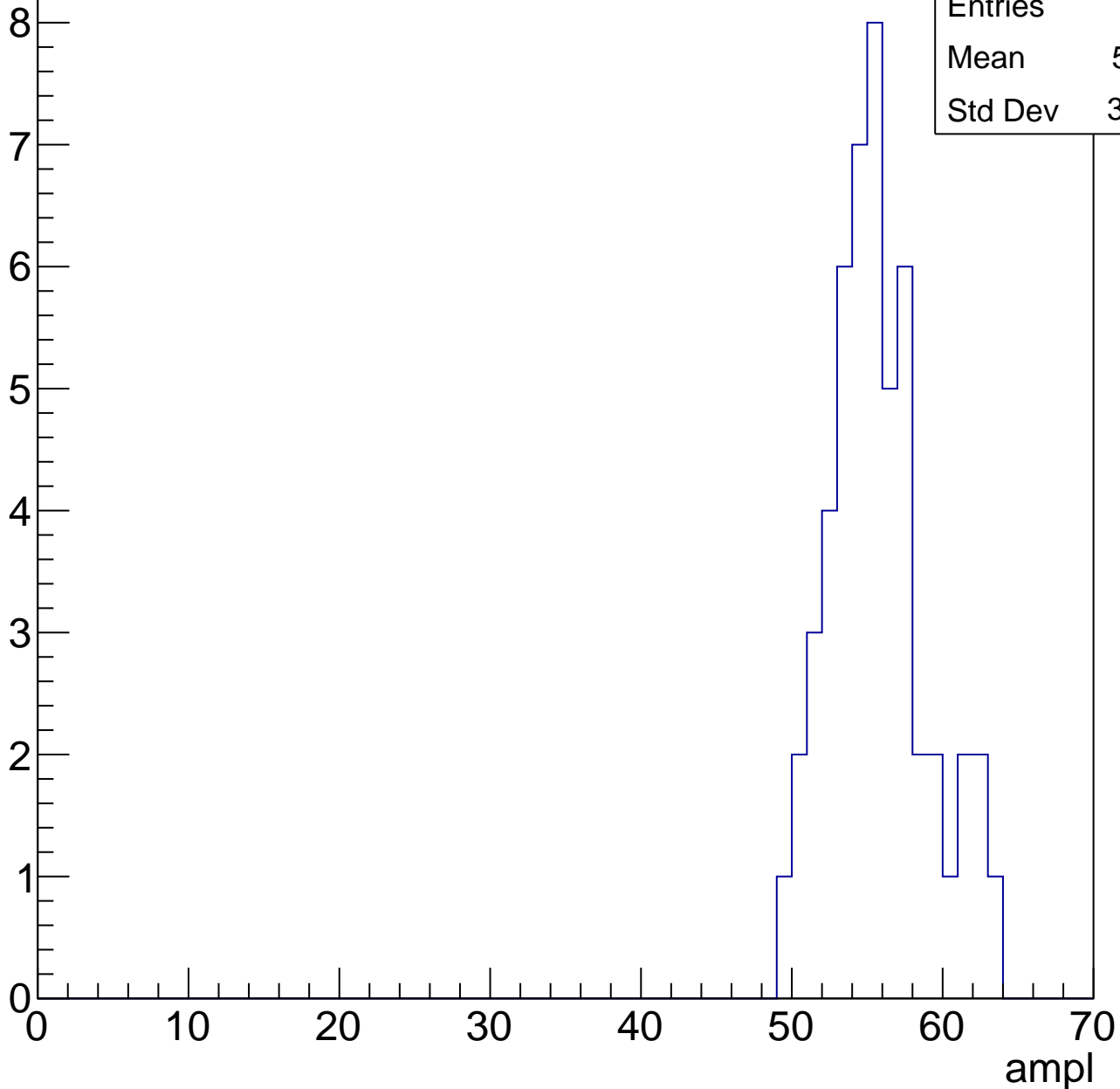


B1L103S, U20-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.21
Std Dev	3.213

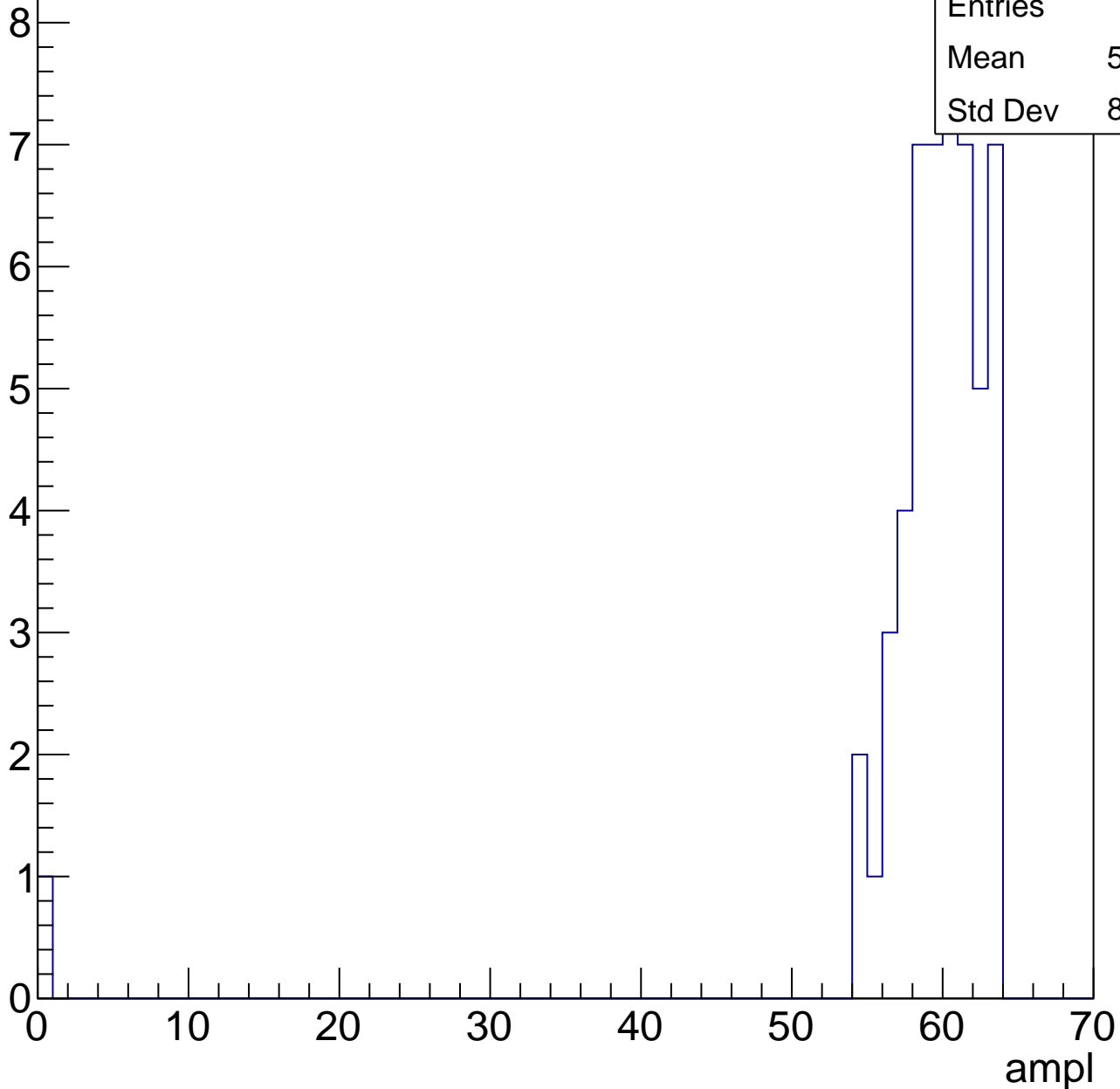


B1L103S, U20-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

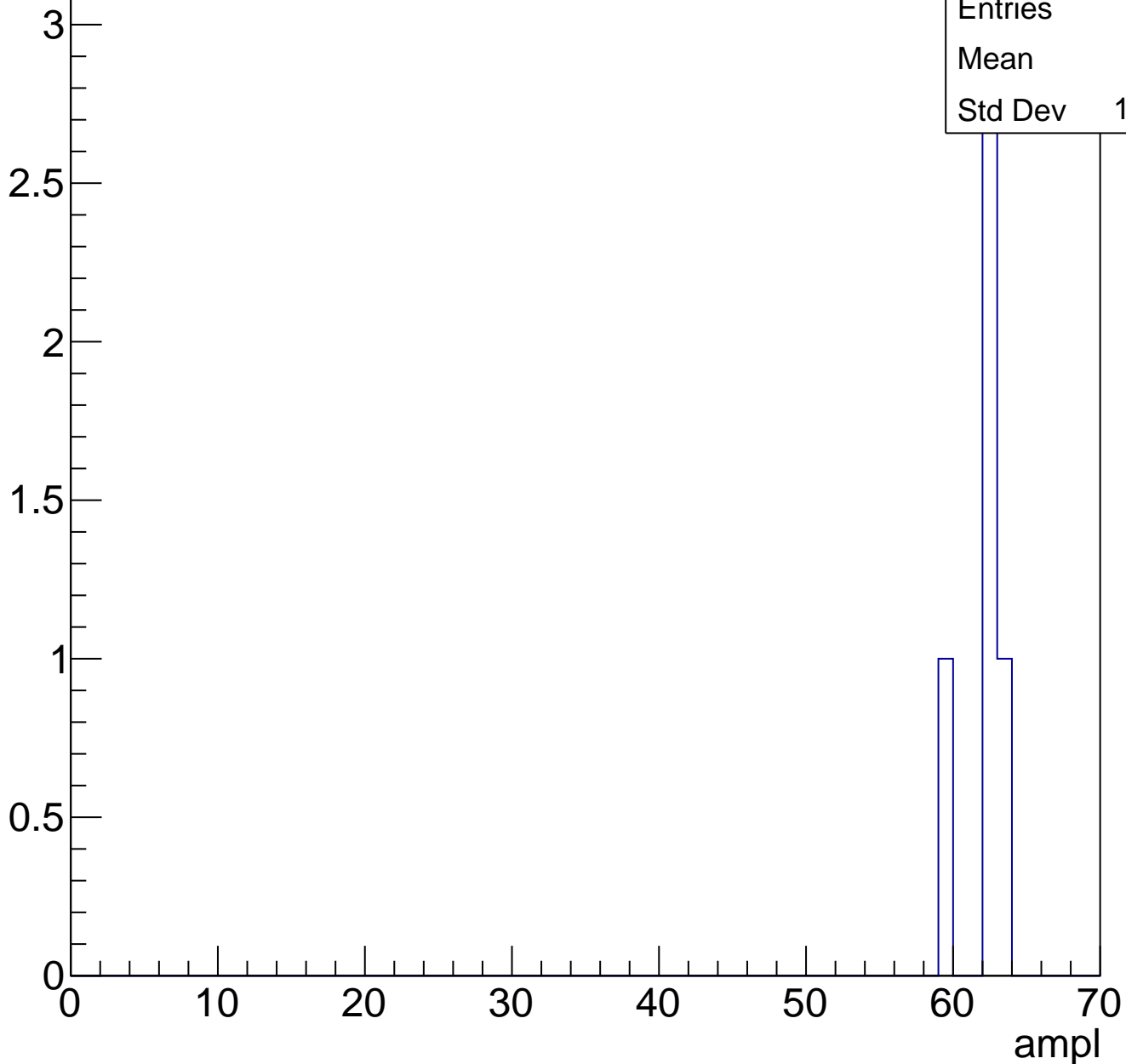
Entries	52
Mean	58.38
Std Dev	8.515



B1L103S, U20-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L103S, U20-ch120, adc0

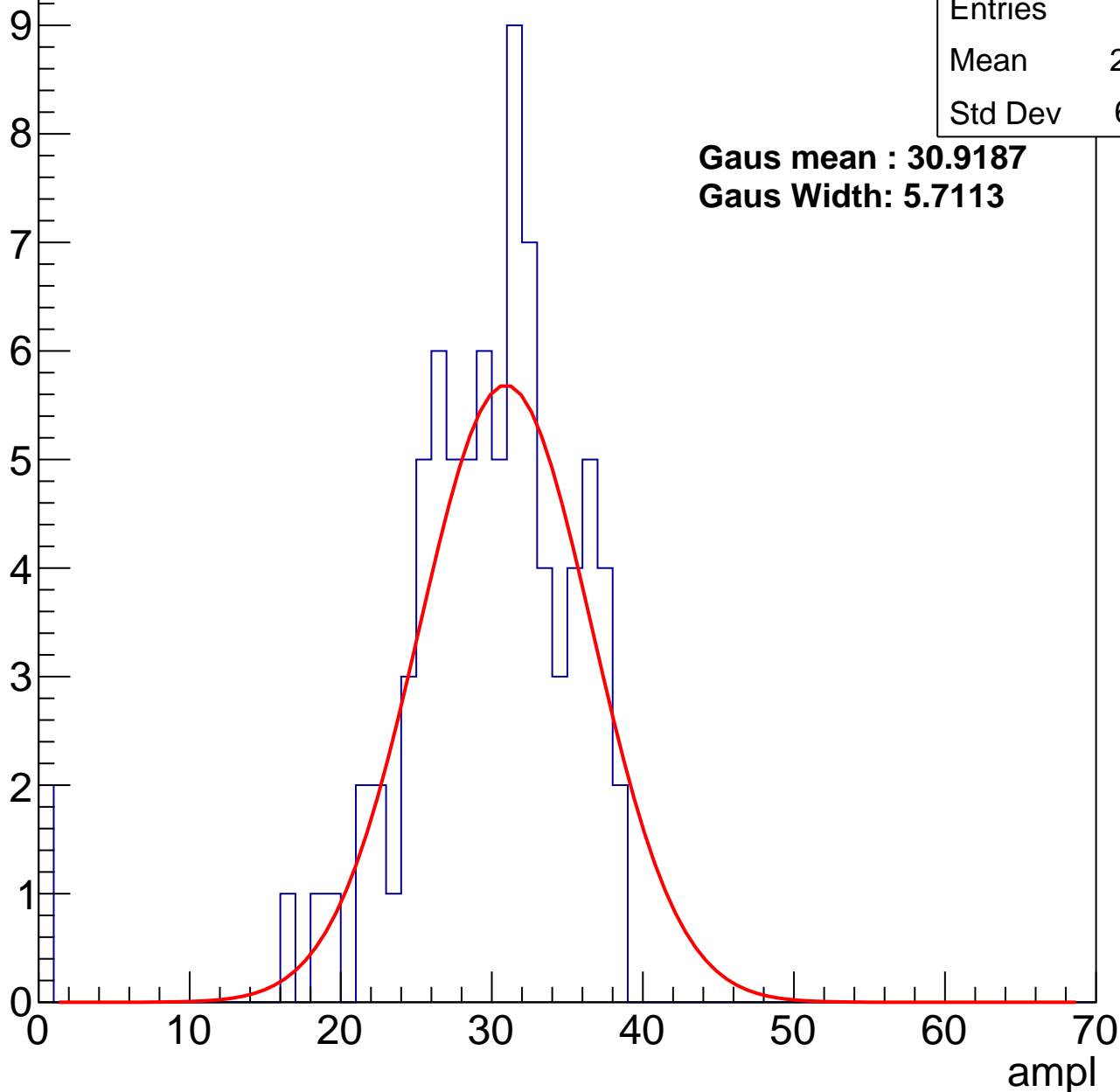
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	28.86
Std Dev	6.601

Gaus mean : 30.9187

Gaus Width: 5.7113



B1L103S, U20-ch120, adc1

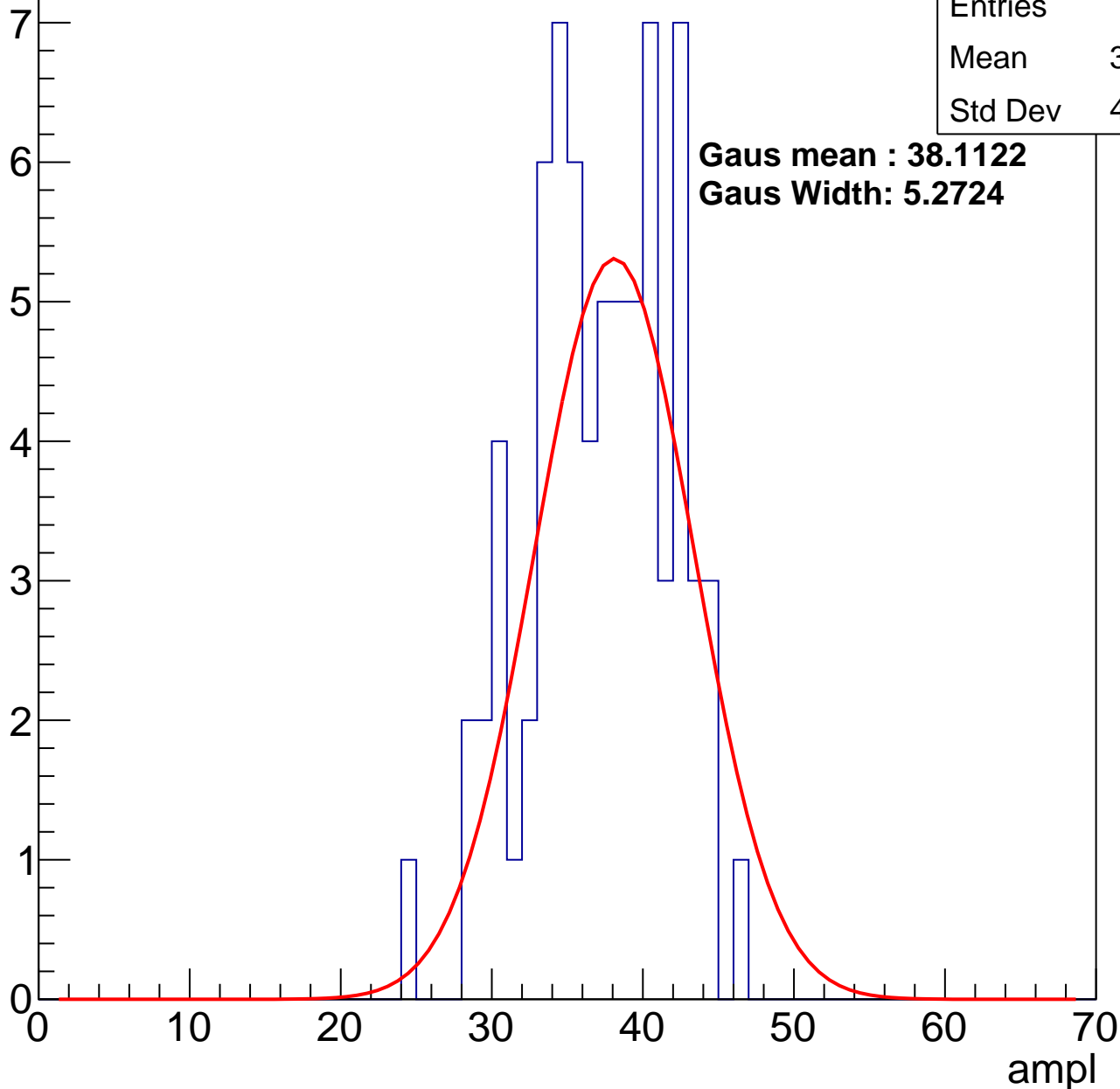
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.72
Std Dev	4.605

Gaus mean : 38.1122

Gaus Width: 5.2724



B1L103S, U20-ch120, adc2

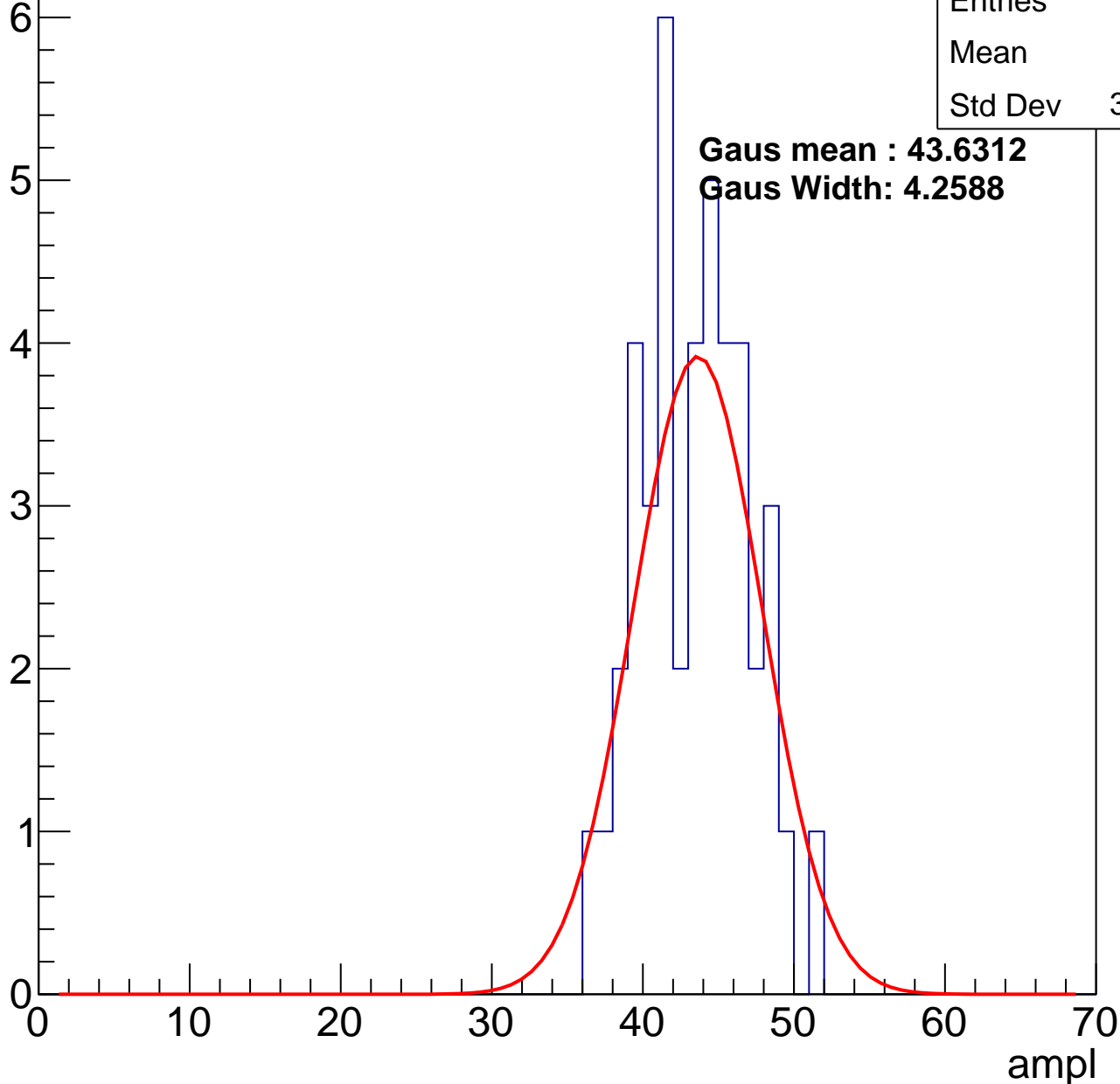
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	43
Std Dev	3.464

Gaus mean : 43.6312

Gaus Width: 4.2588

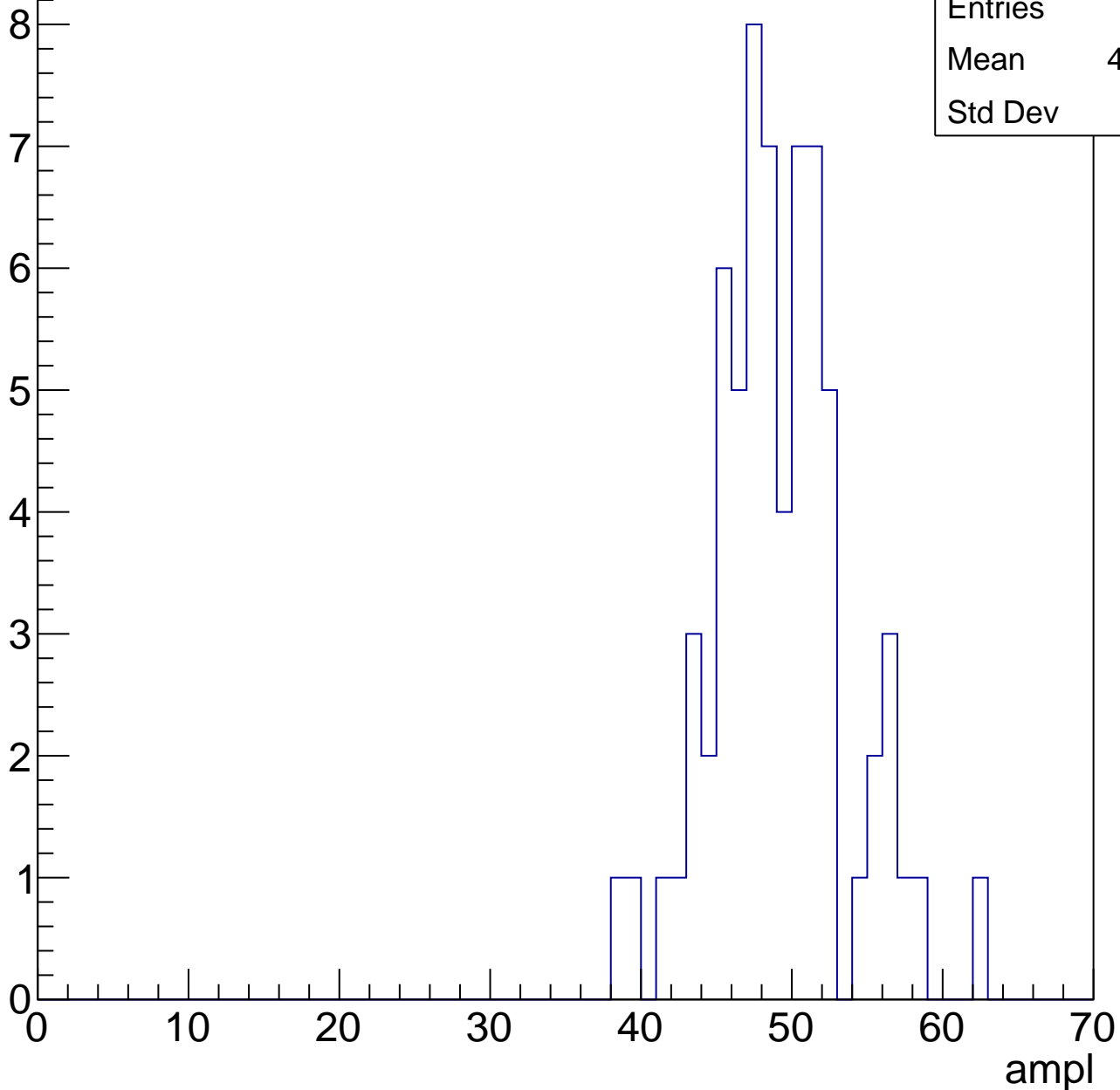


B1L103S, U20-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.67
Std Dev	4.41

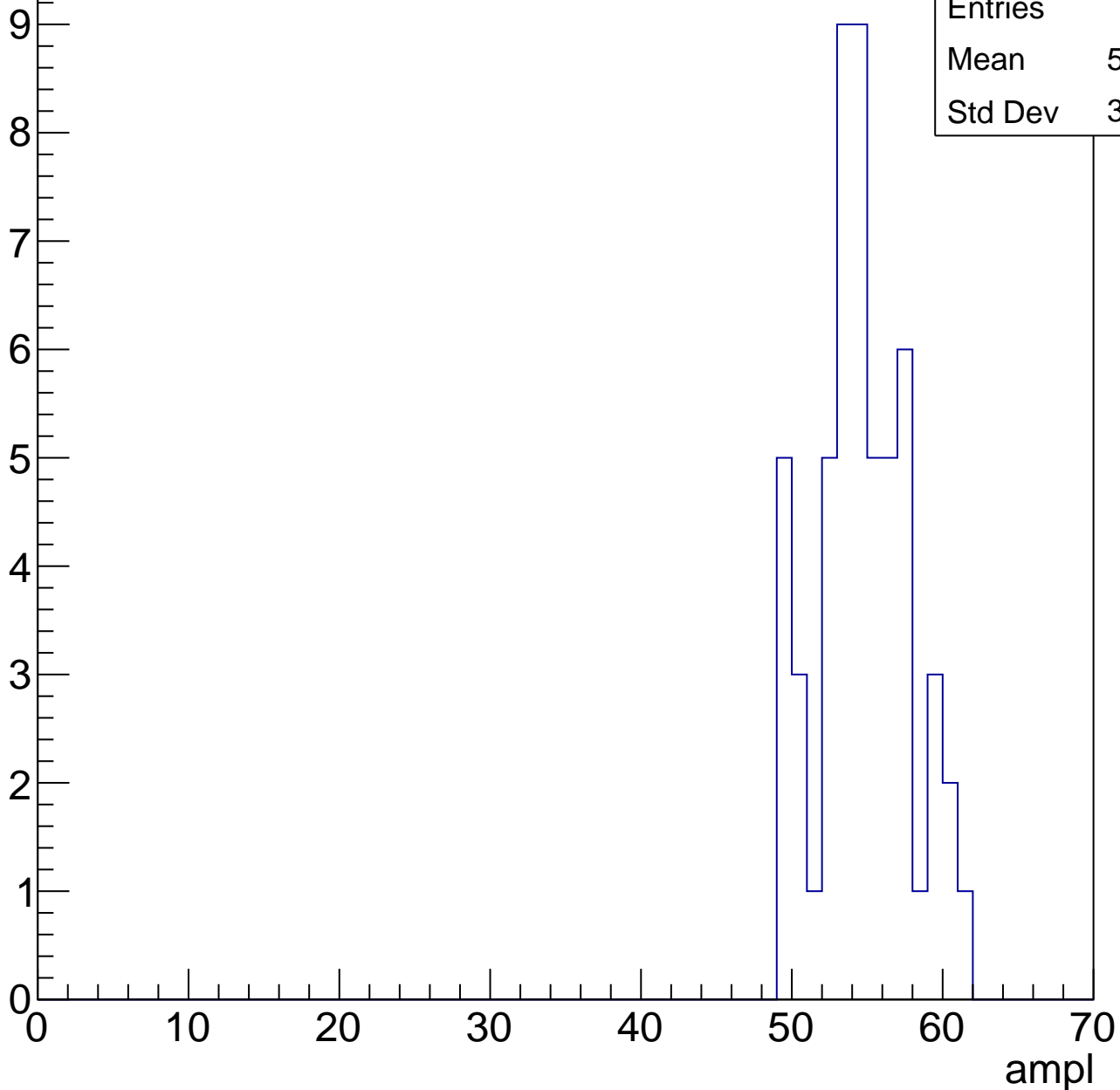


B1L103S, U20-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.22
Std Dev	3.013

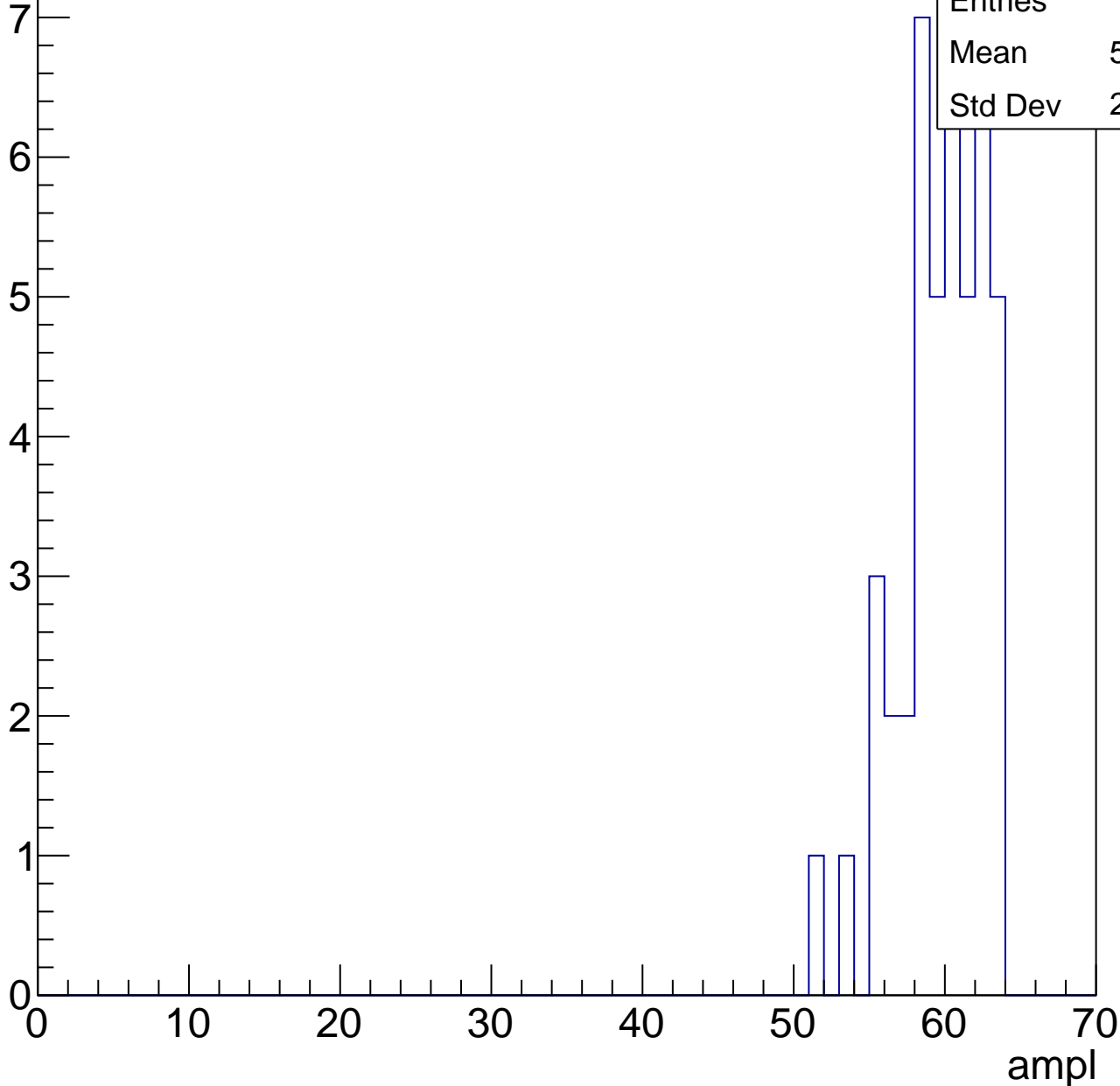


B1L103S, U20-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

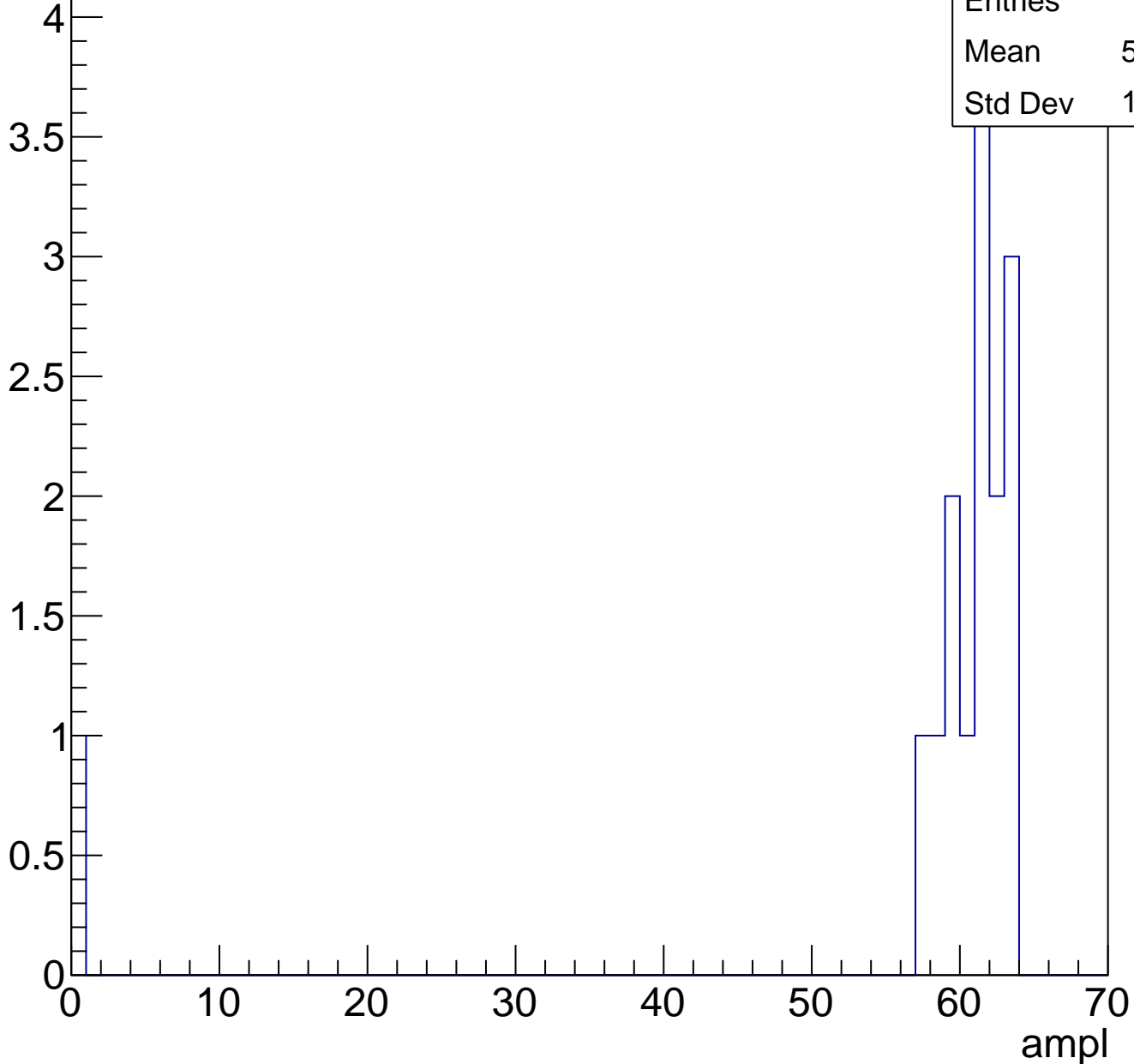
Entries	45
Mean	59.33
Std Dev	2.773



B1L103S, U20-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch121, adc0

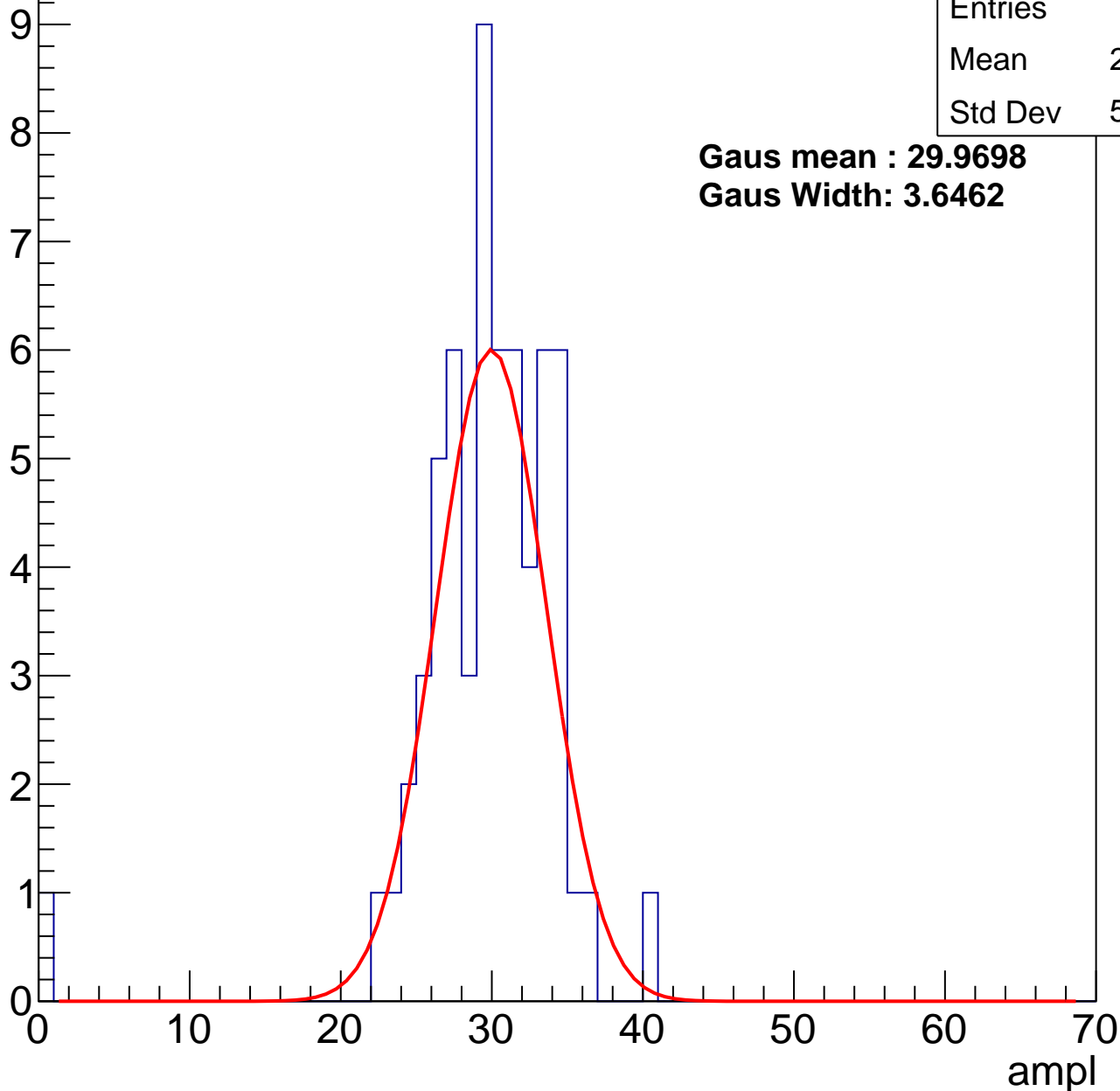
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.23
Std Dev	5.097

Gaus mean : 29.9698

Gaus Width: 3.6462



B1L103S, U20-ch121, adc1

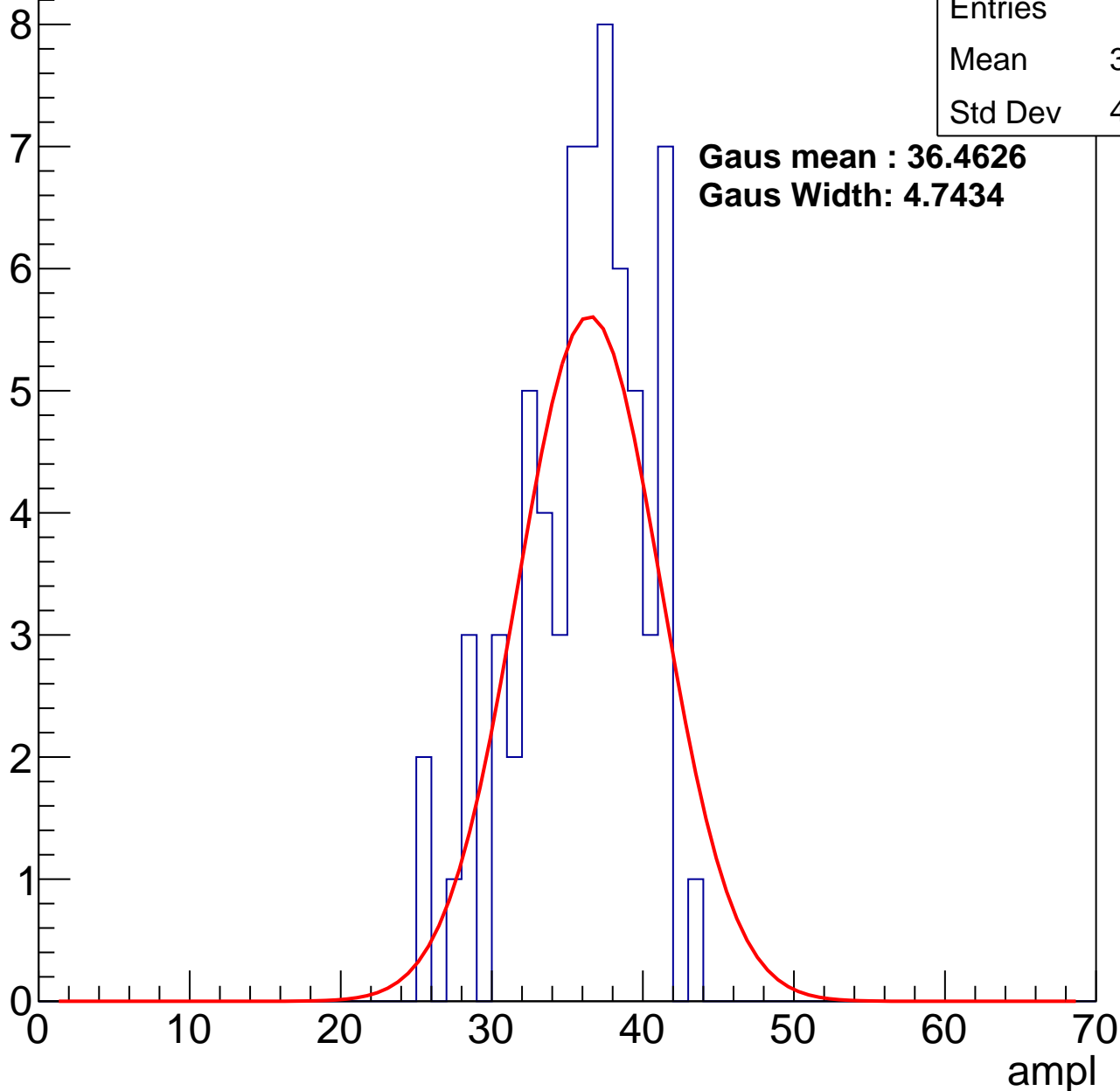
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.42
Std Dev	4.115

Gaus mean : 36.4626

Gaus Width: 4.7434



B1L103S, U20-ch121, adc2

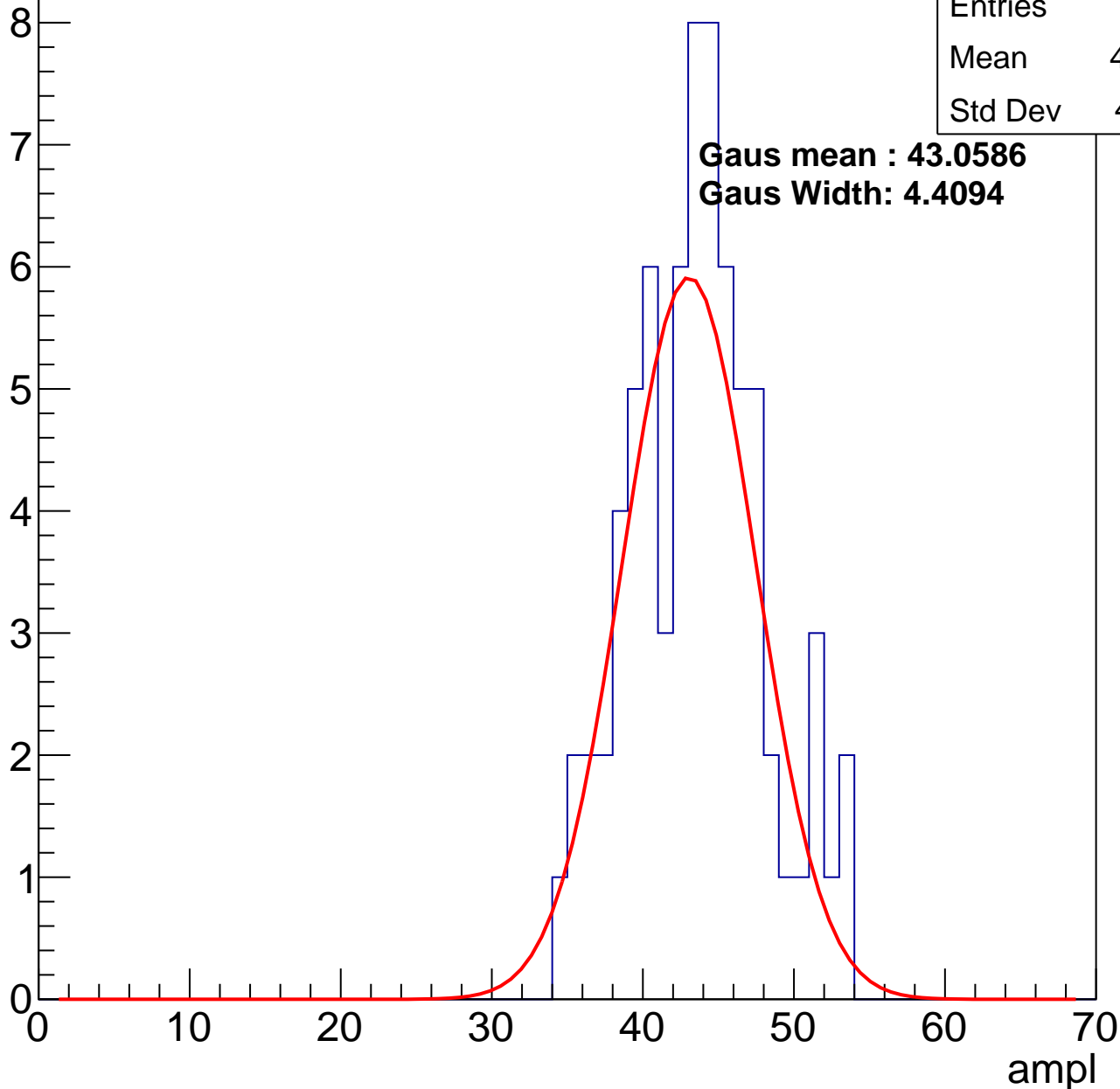
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.14
Std Dev	4.371

Gaus mean : 43.0586

Gaus Width: 4.4094

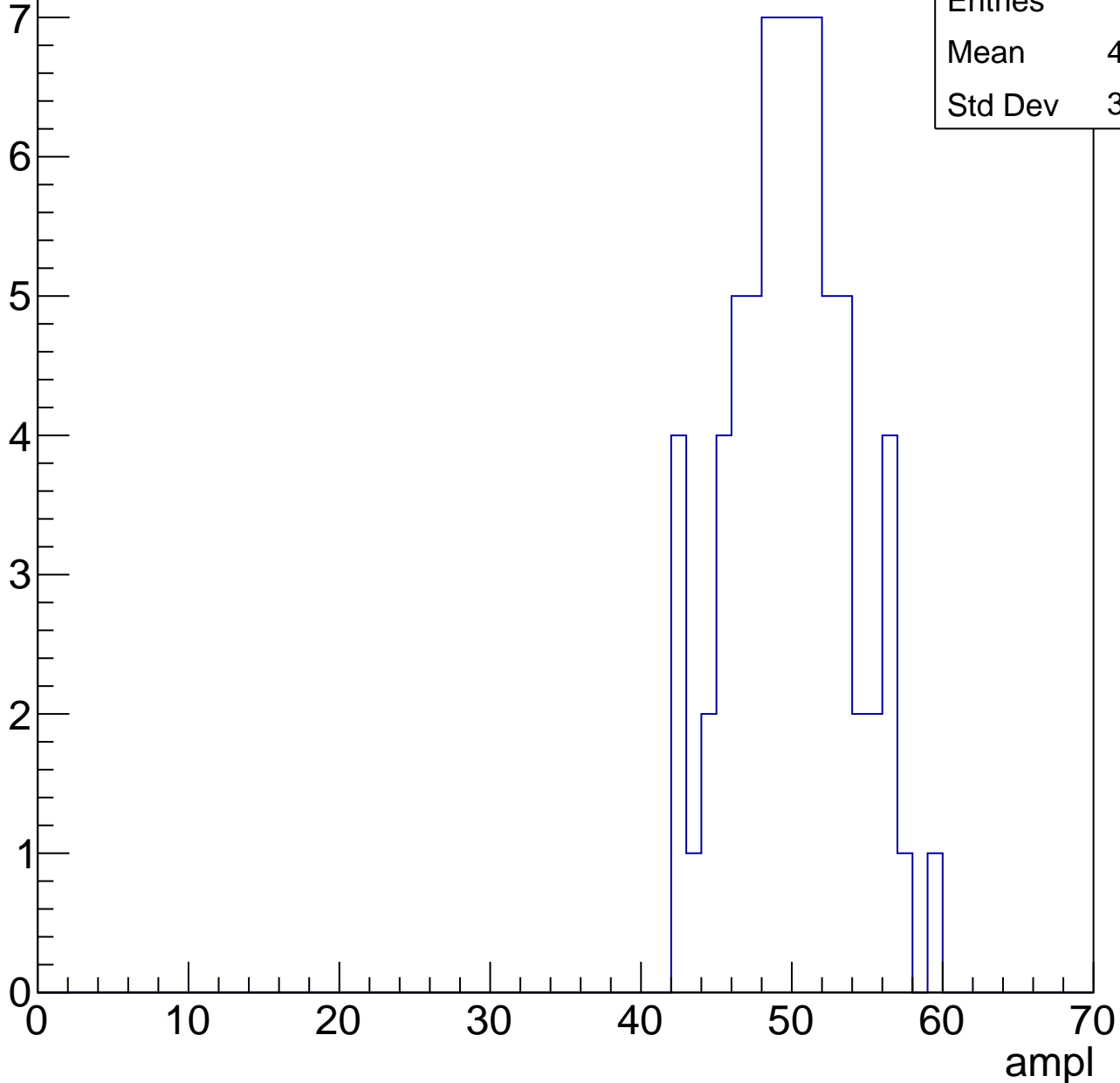


B1L103S, U20-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.46
Std Dev	3.892

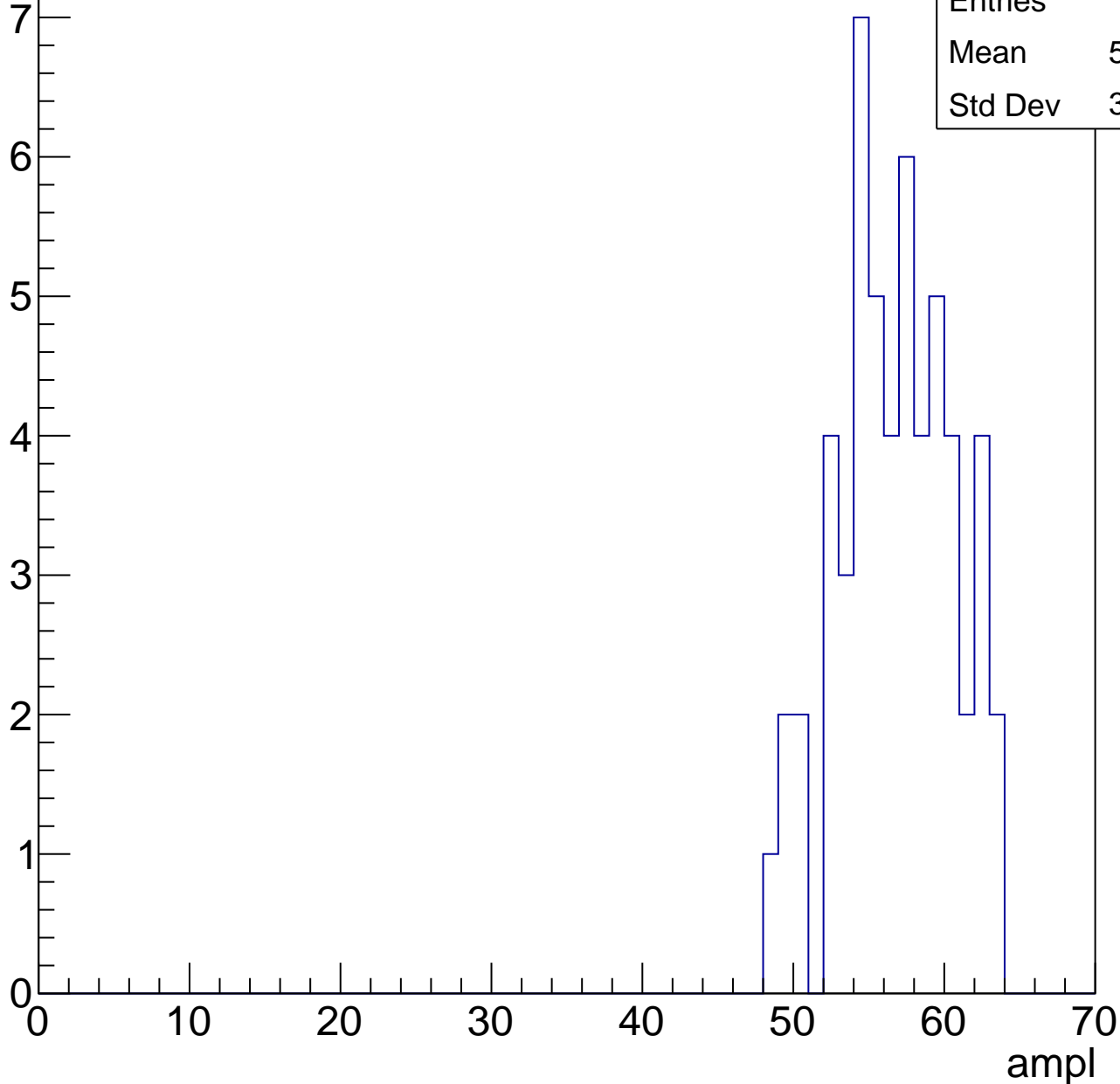


B1L103S, U20-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.27
Std Dev	3.758

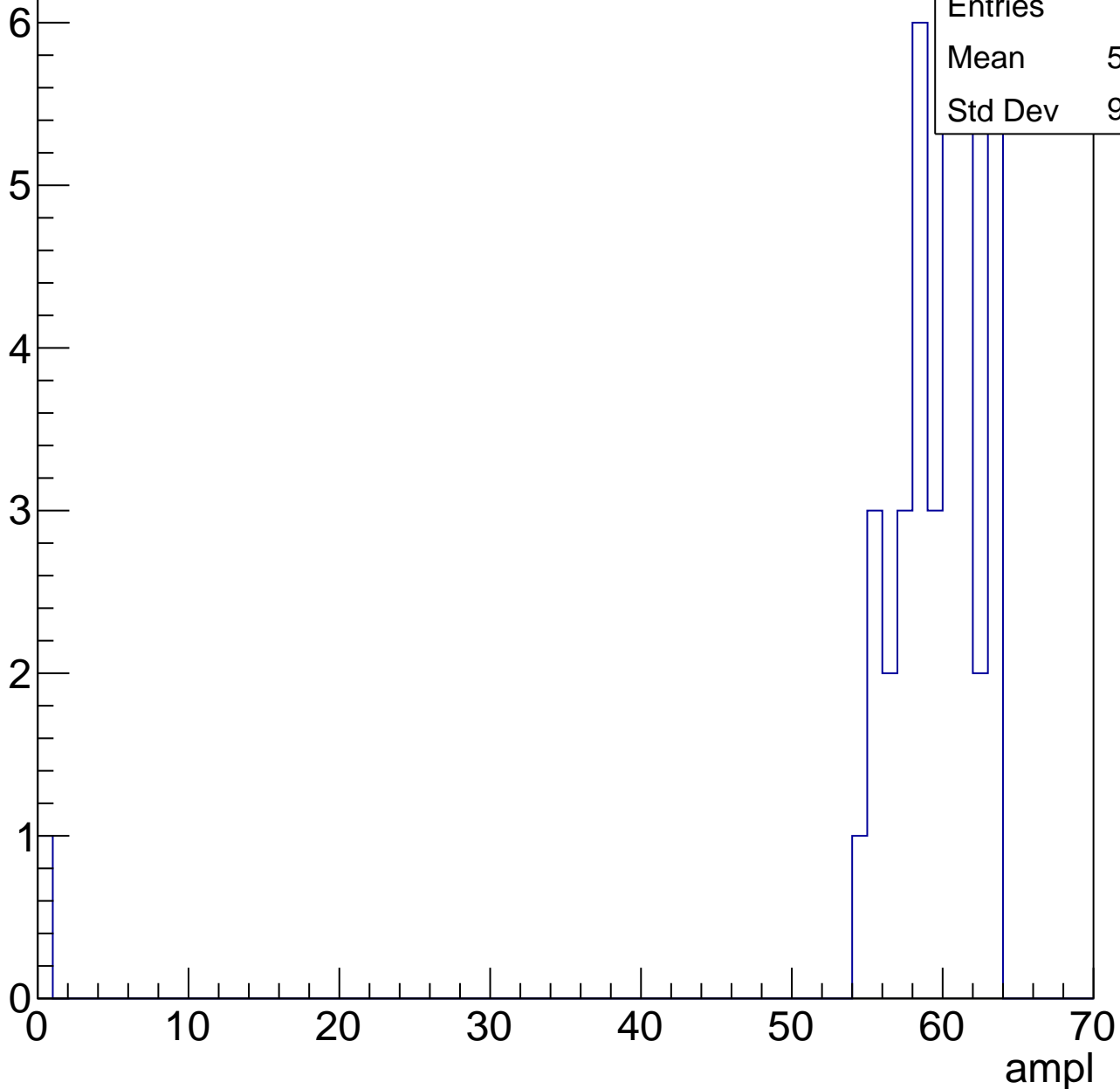


B1L103S, U20-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

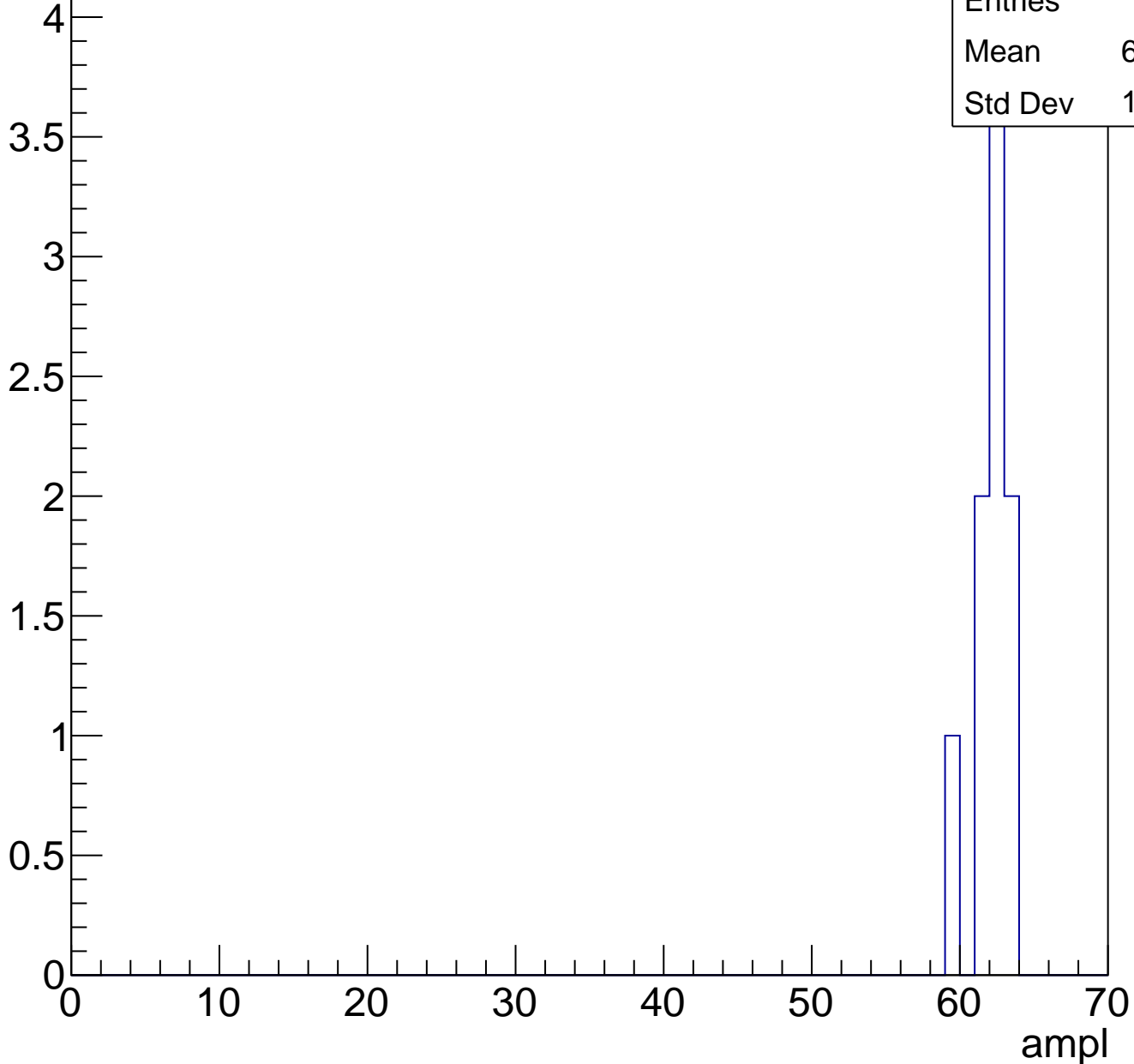
Entries	39
Mean	57.82
Std Dev	9.714



B1L103S, U20-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U20-ch122, adc0

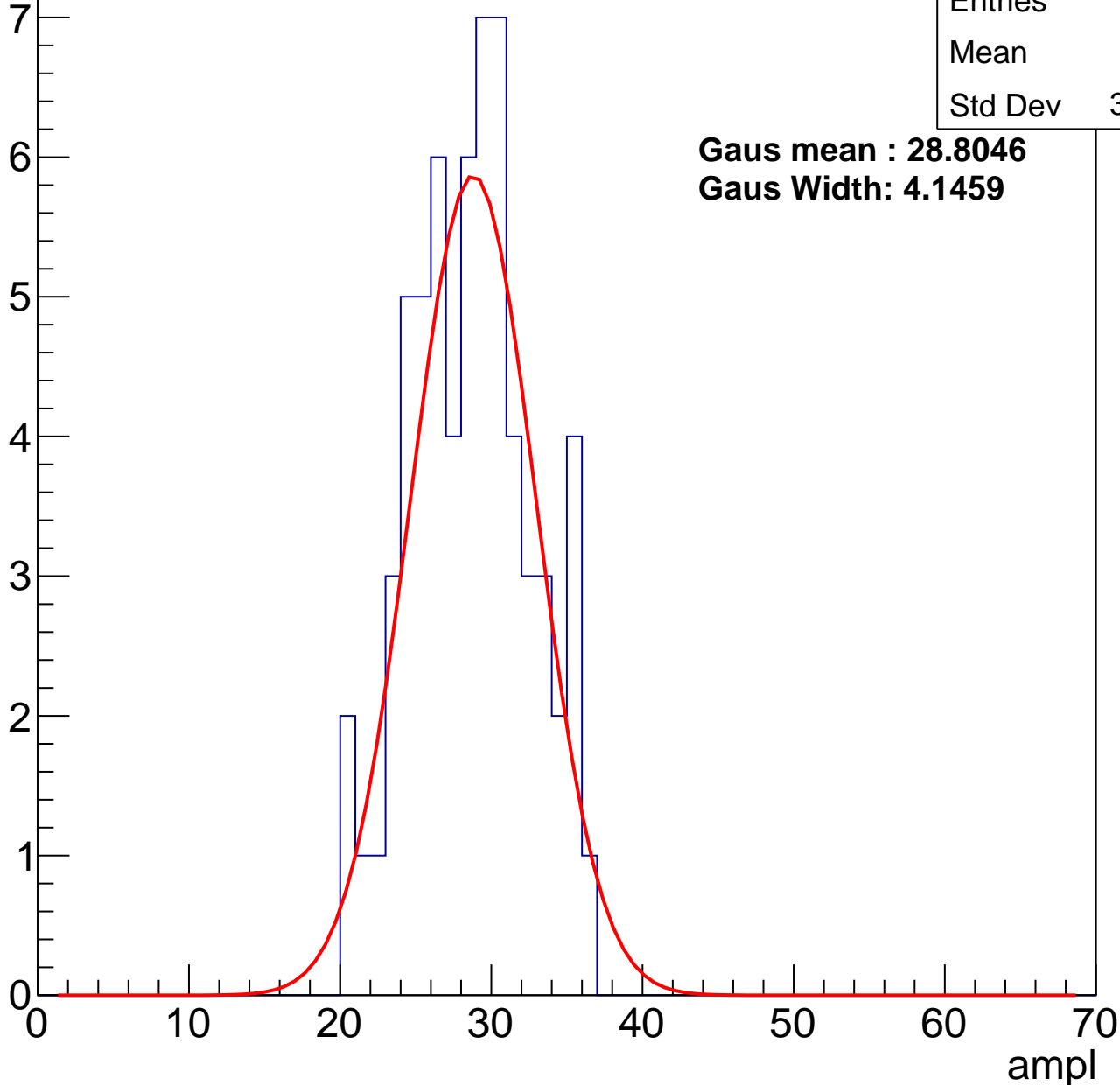
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.2
Std Dev	3.882

Gaus mean : 28.8046

Gaus Width: 4.1459



B1L103S, U20-ch122, adc1

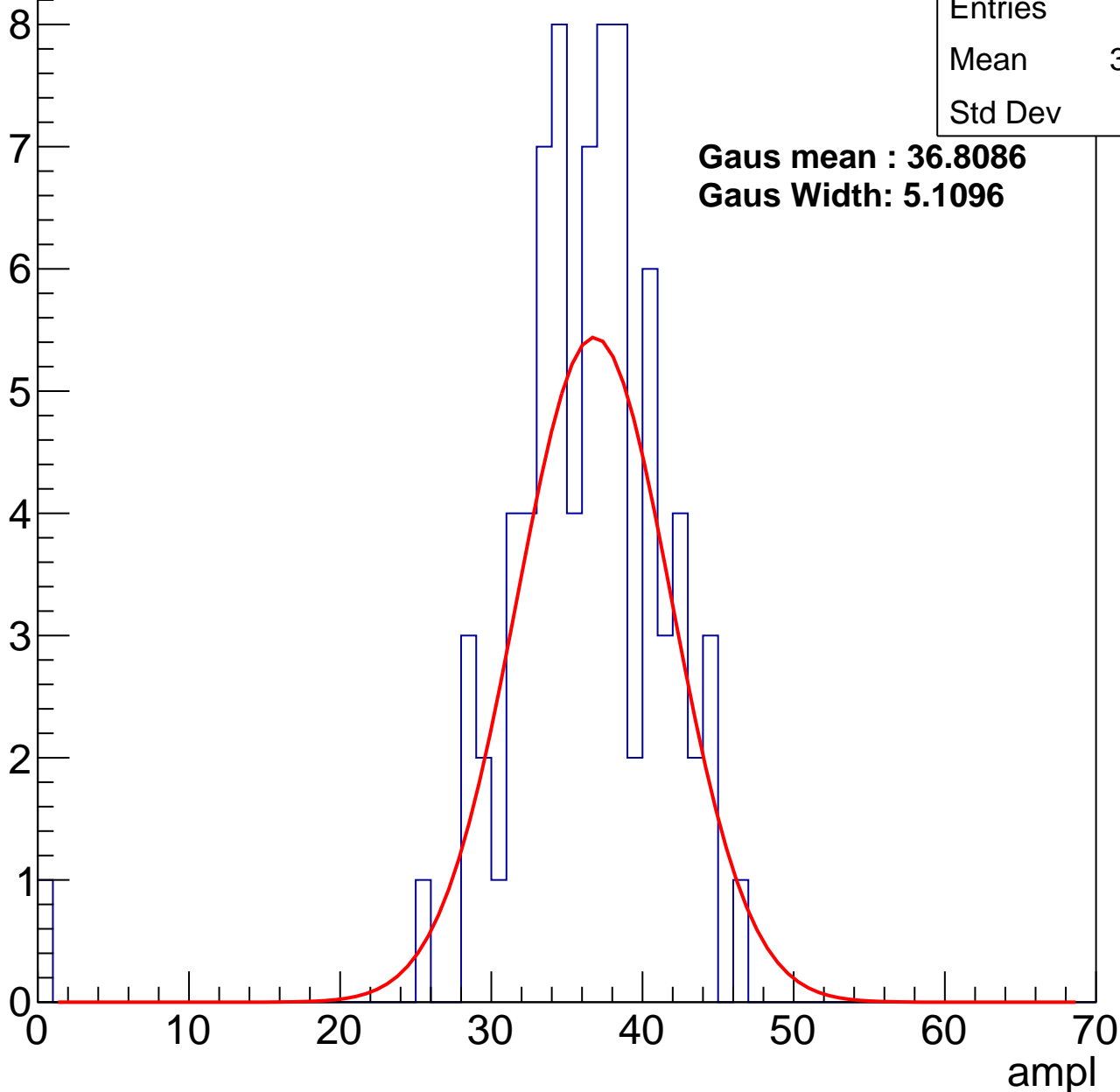
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.66
Std Dev	5.91

Gaus mean : 36.8086

Gaus Width: 5.1096



B1L103S, U20-ch122, adc2

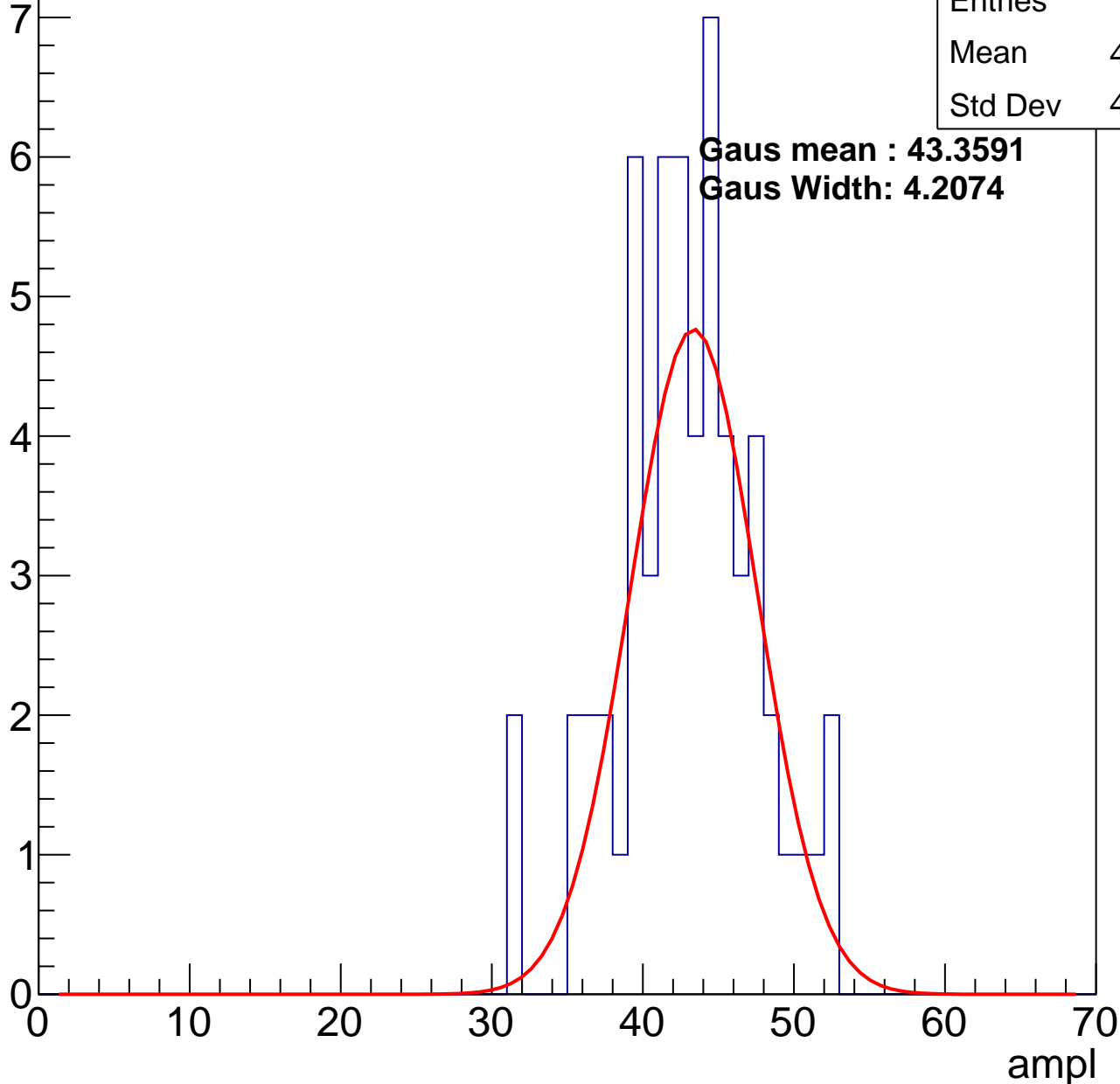
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.44
Std Dev	4.548

Gaus mean : 43.3591

Gaus Width: 4.2074

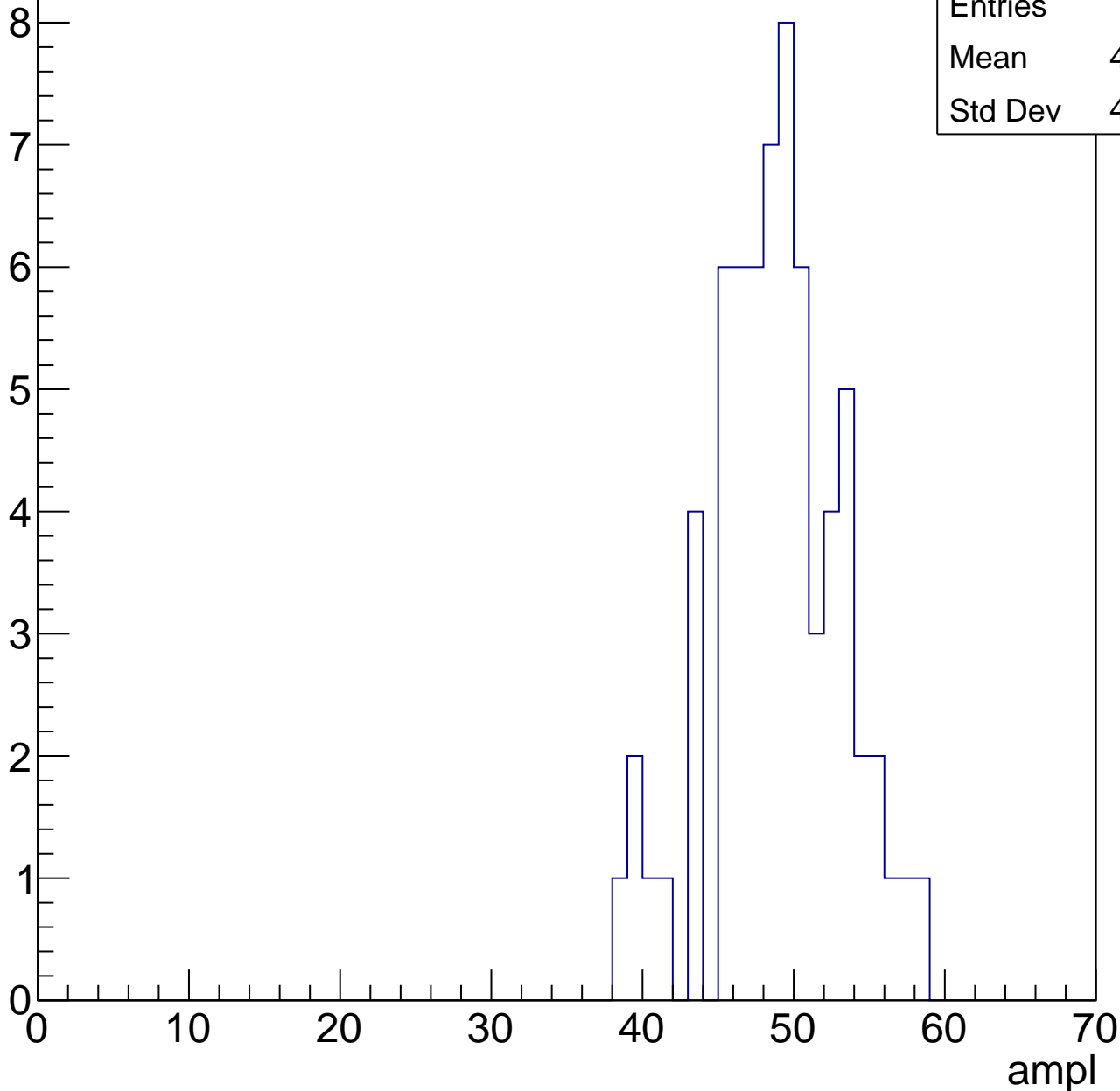


B1L103S, U20-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.36
Std Dev	4.256

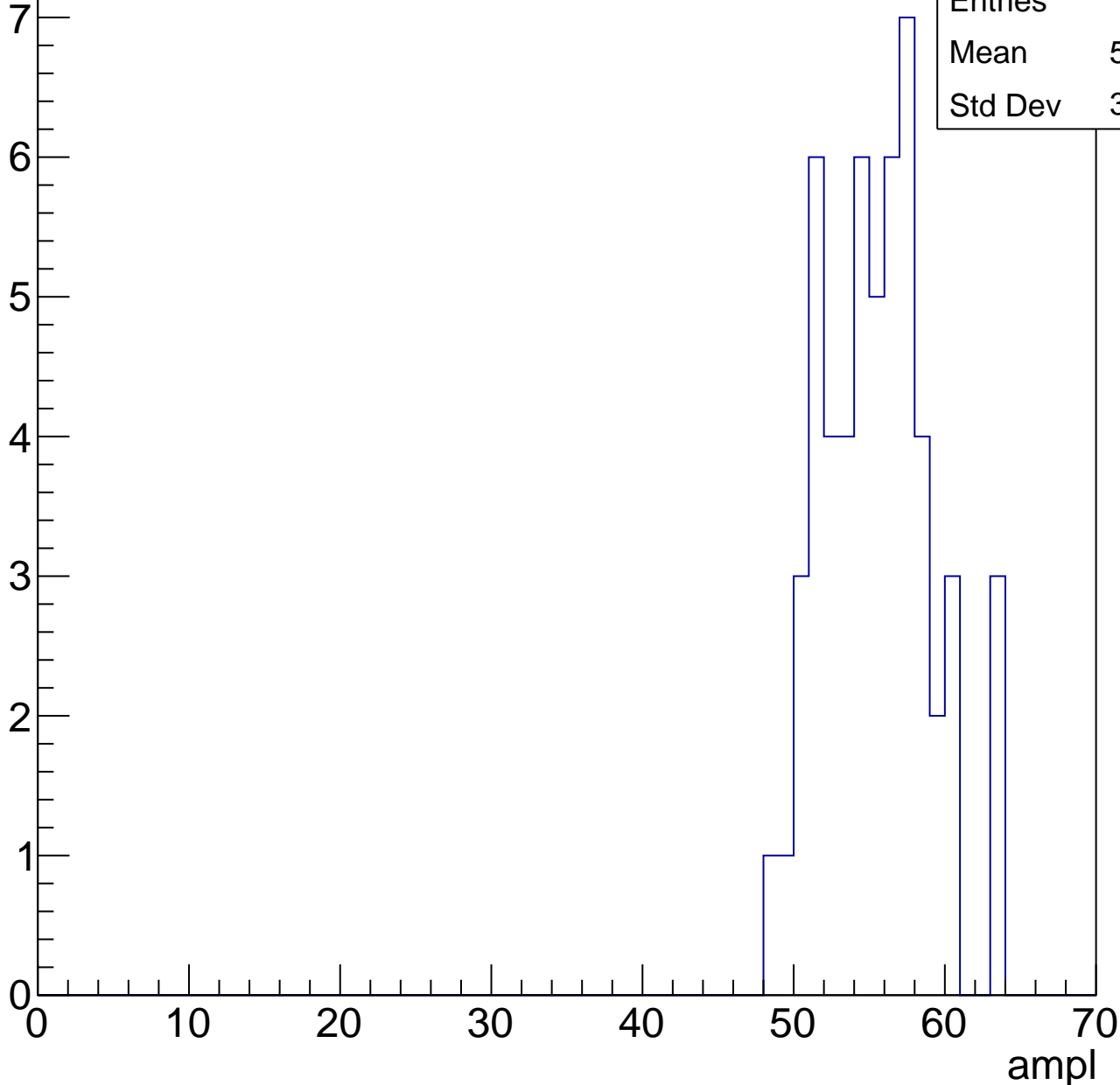


B1L103S, U20-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

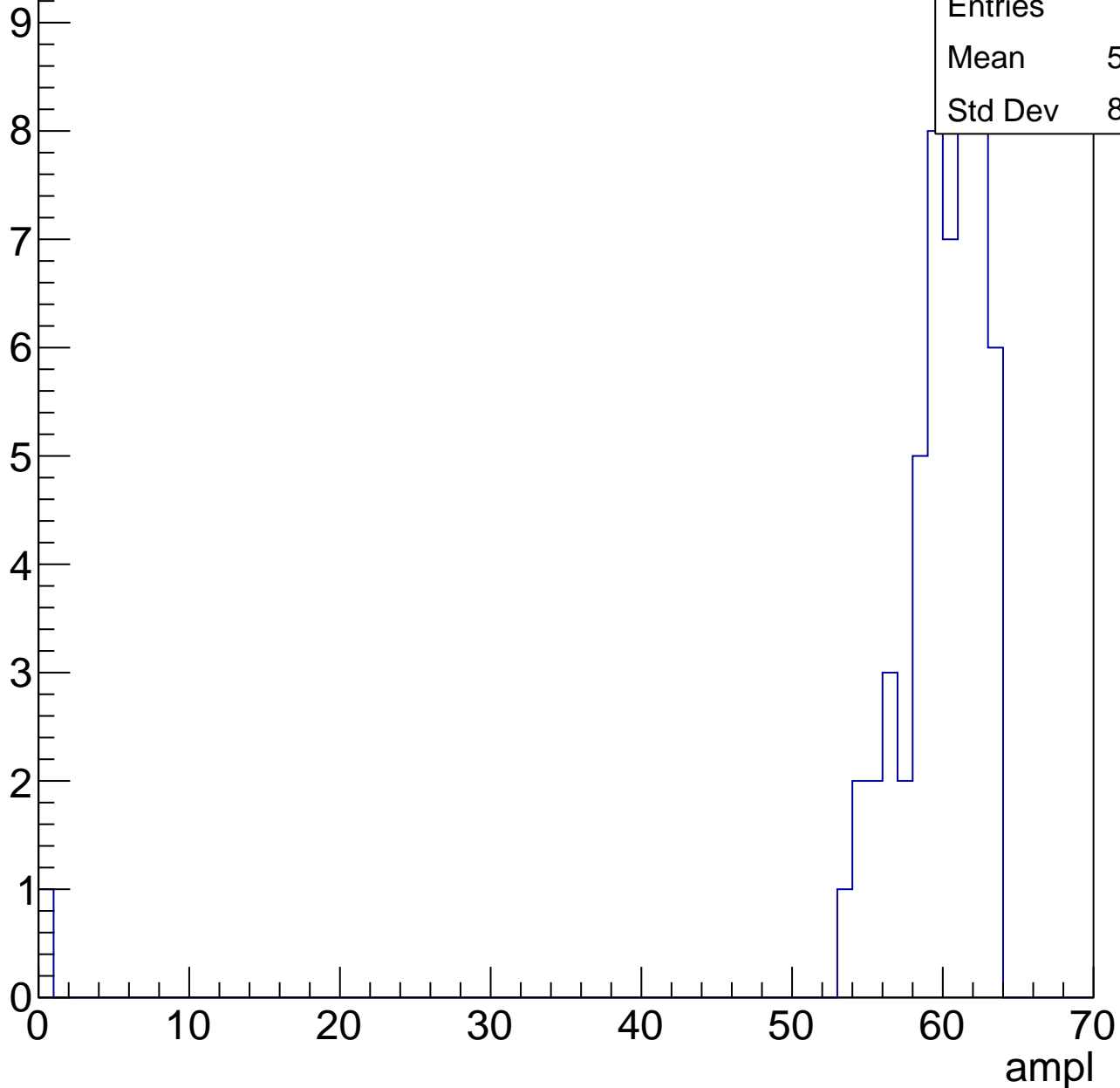
Entries	55
Mean	55.02
Std Dev	3.524



B1L103S, U20-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

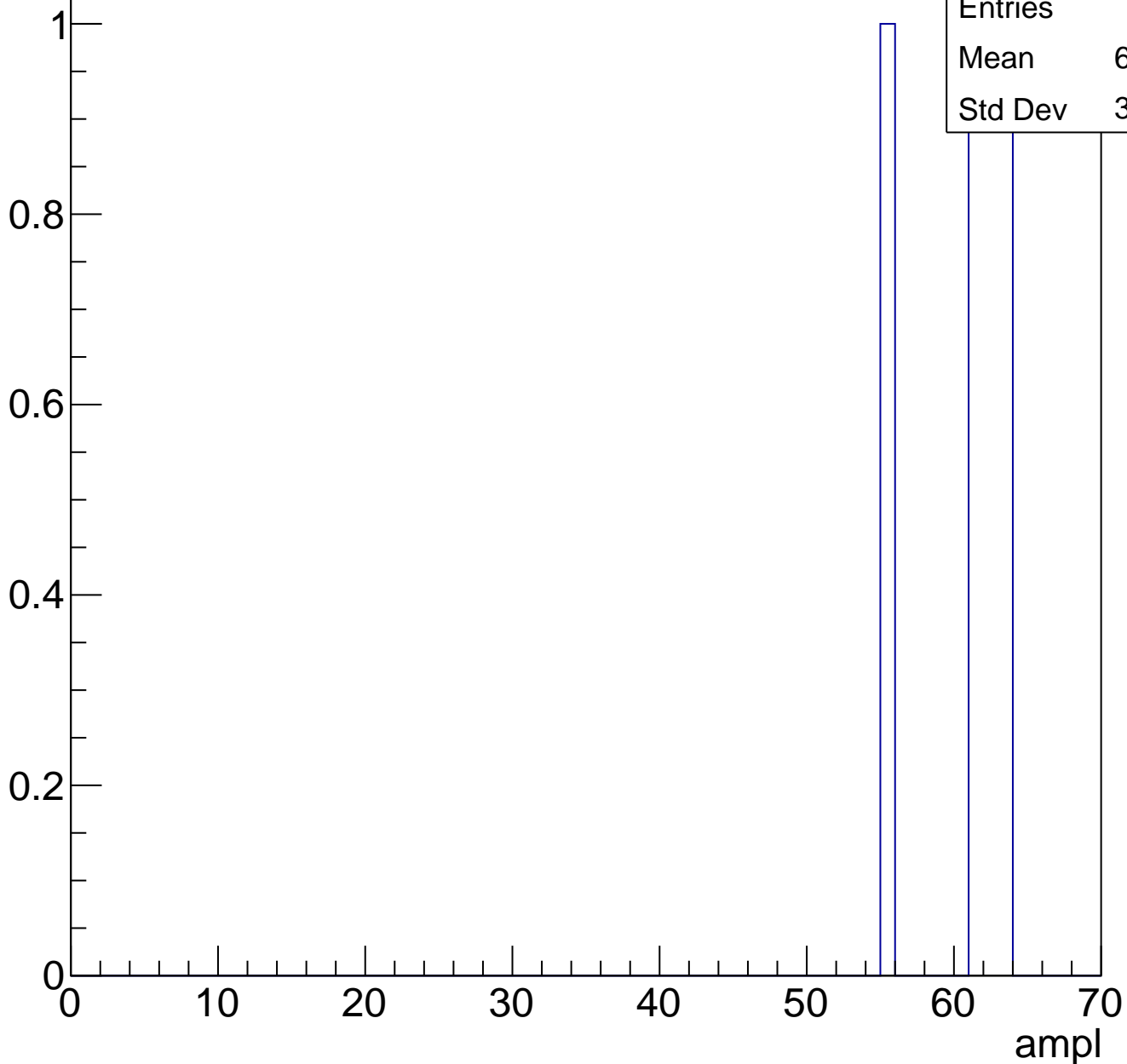
Entry



B1L103S, U20-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch123, adc0

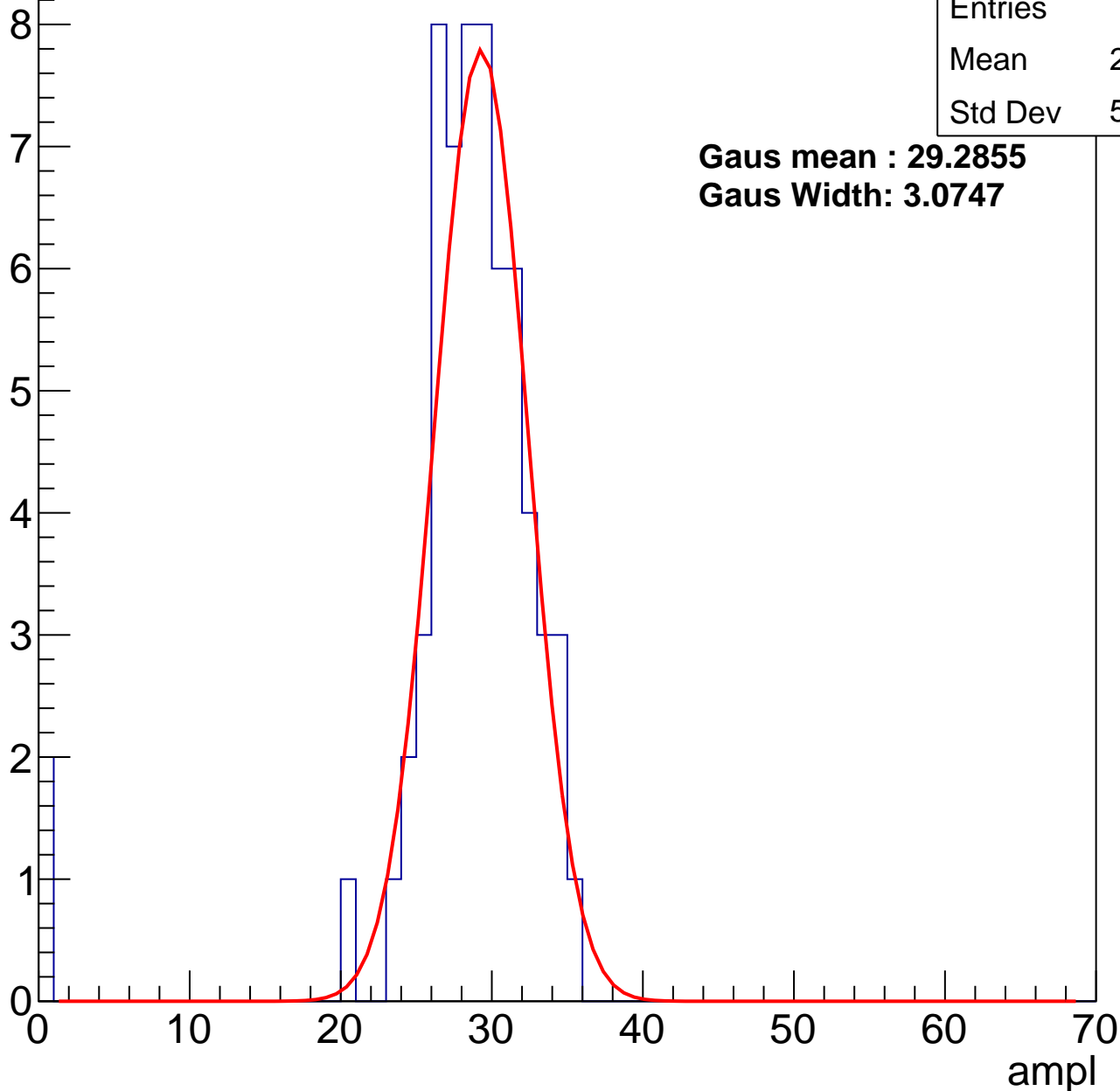
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.76
Std Dev	5.819

Gaus mean : 29.2855

Gaus Width: 3.0747



B1L103S, U20-ch123, adc1

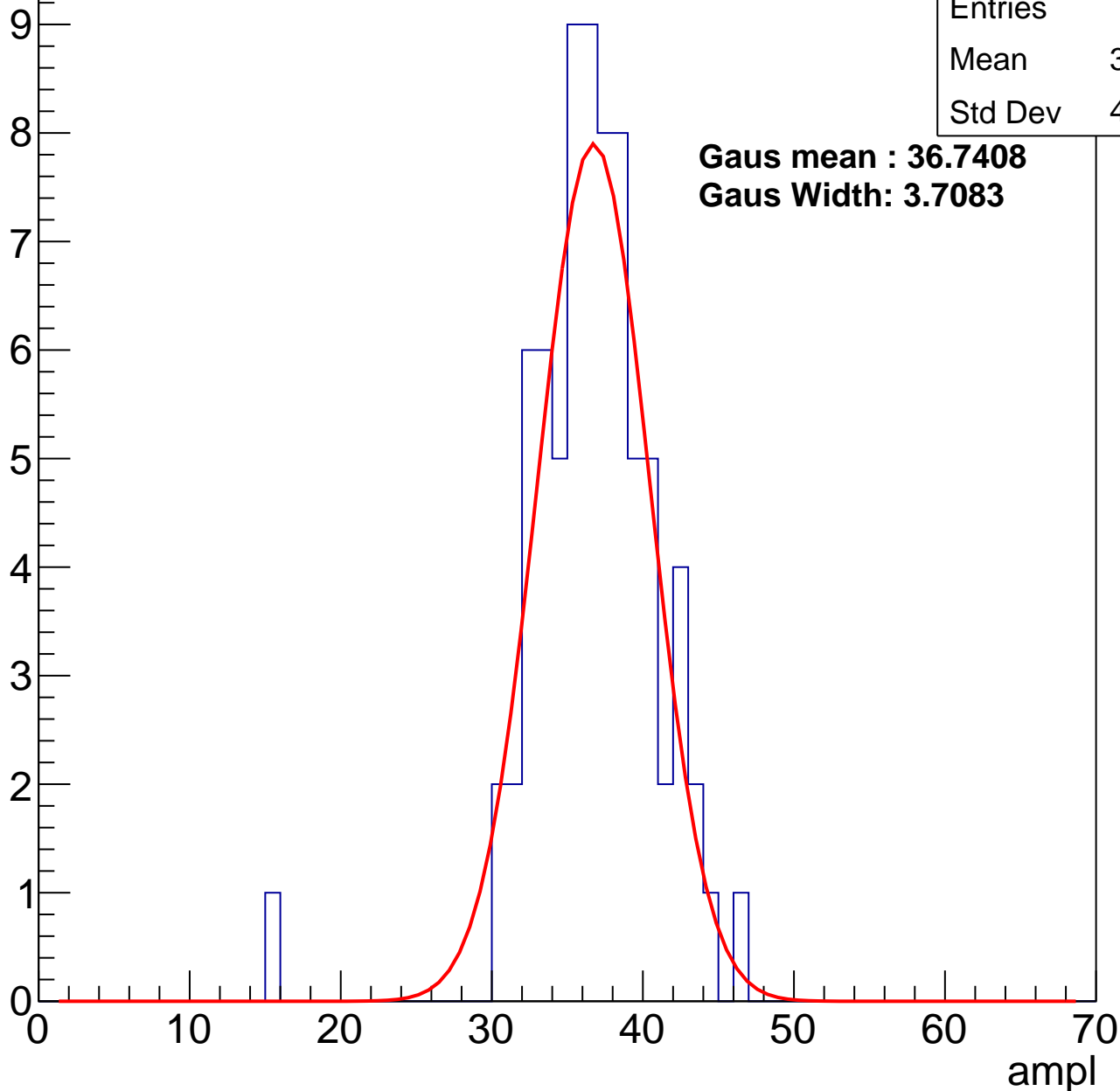
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.28
Std Dev	4.226

Gaus mean : 36.7408

Gaus Width: 3.7083



B1L103S, U20-ch123, adc2

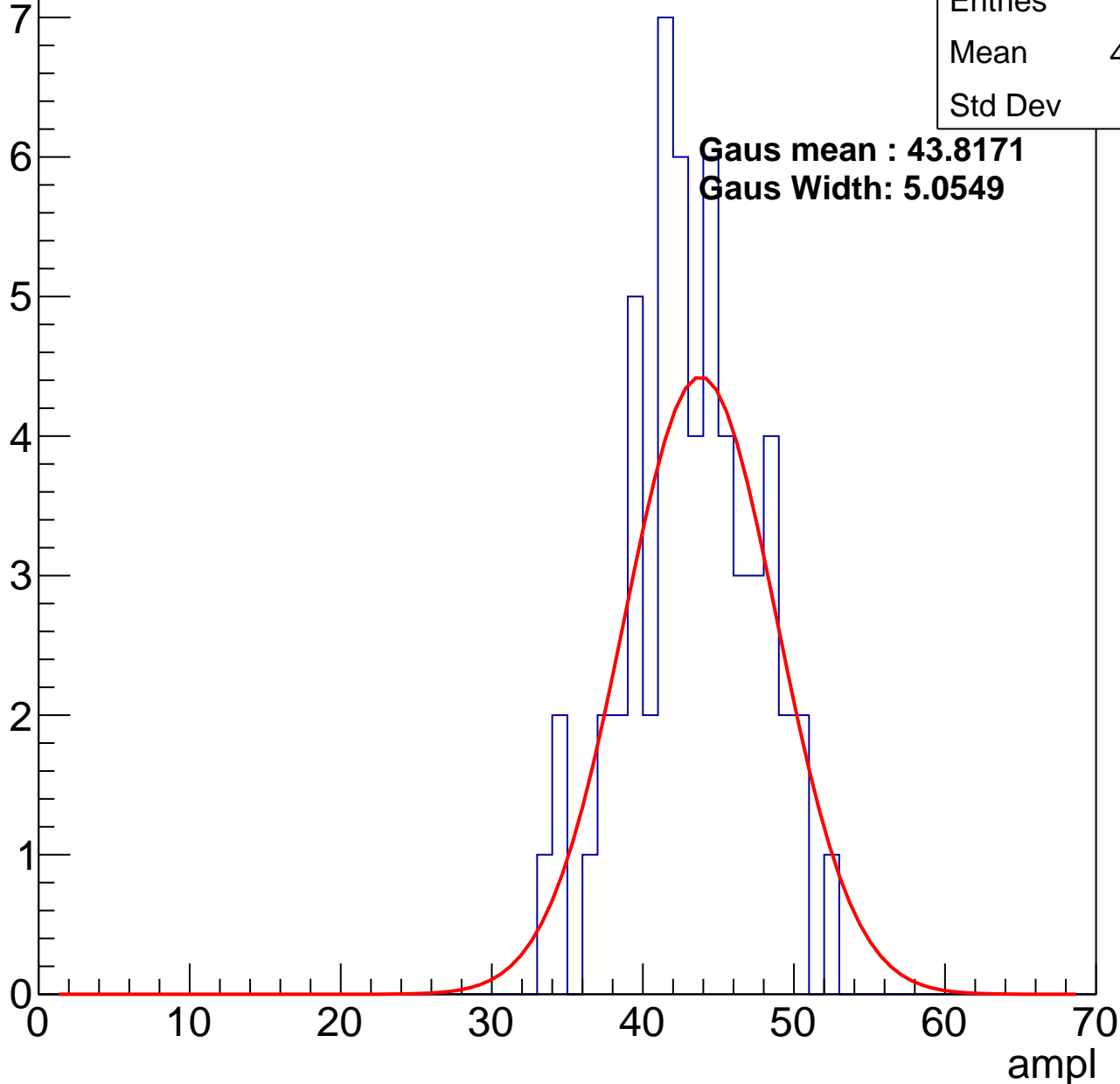
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.77
Std Dev	4.21

Gaus mean : 43.8171

Gaus Width: 5.0549

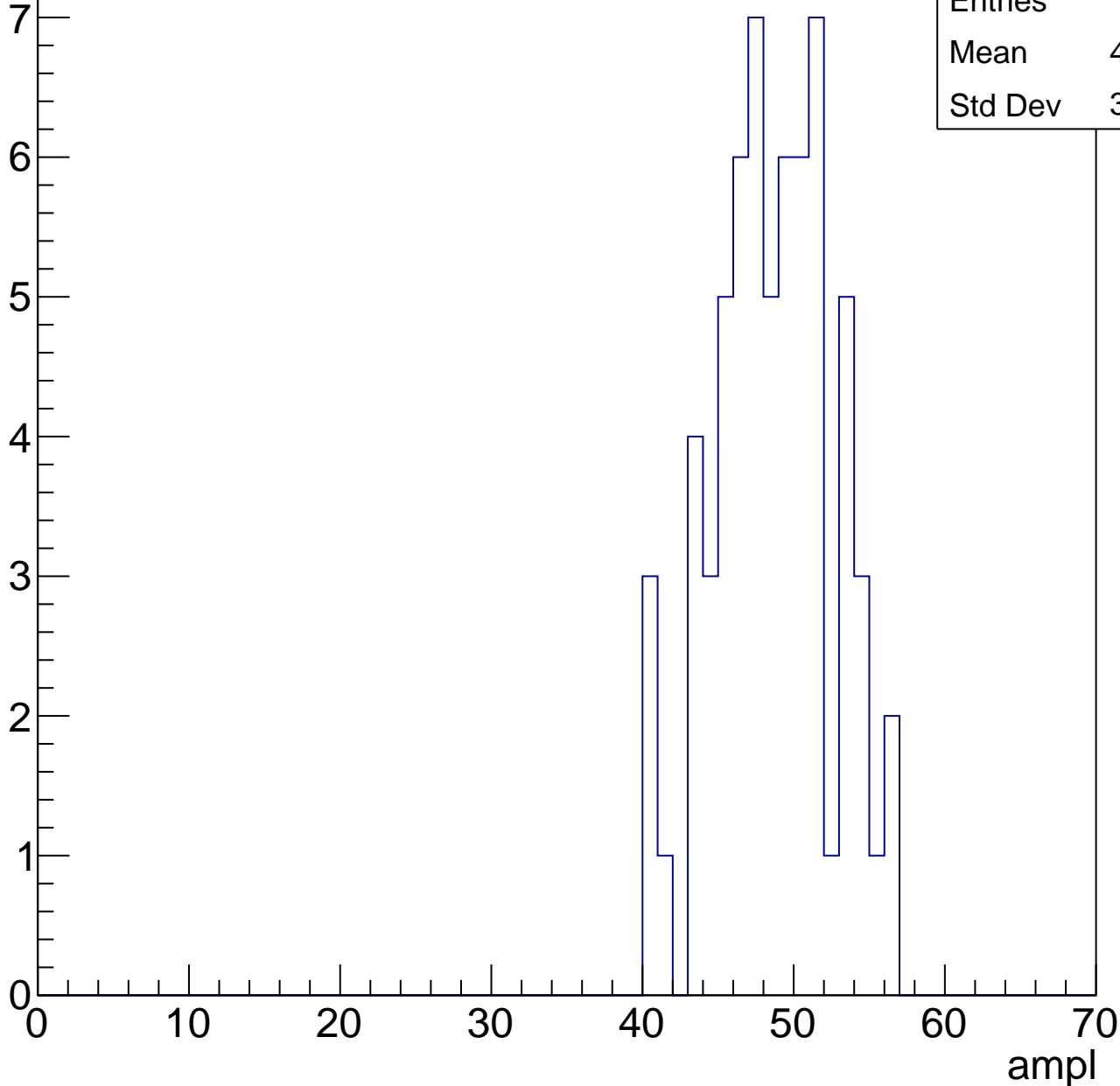


B1L103S, U20-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.18
Std Dev	3.886

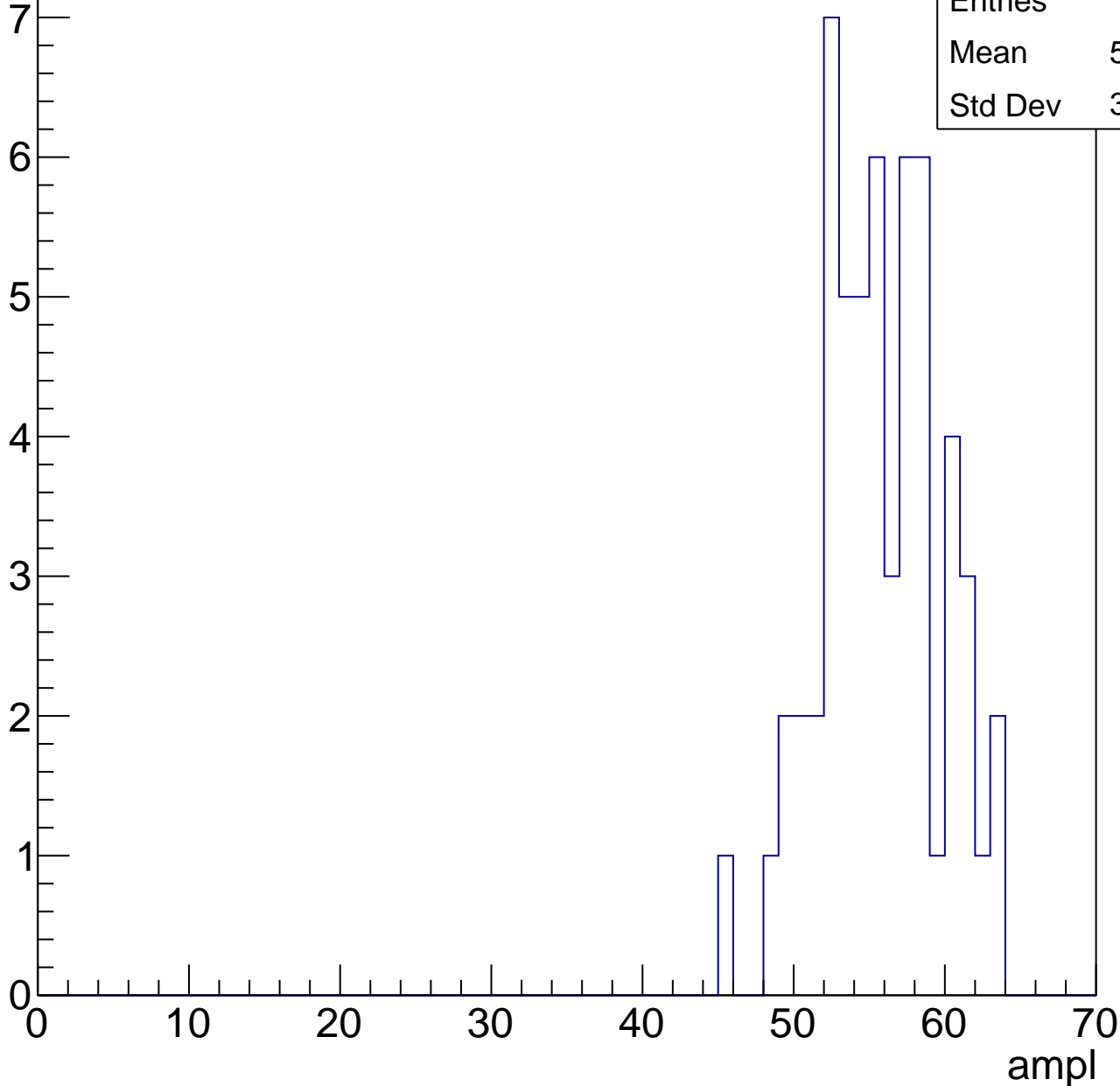


B1L103S, U20-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.26
Std Dev	3.905

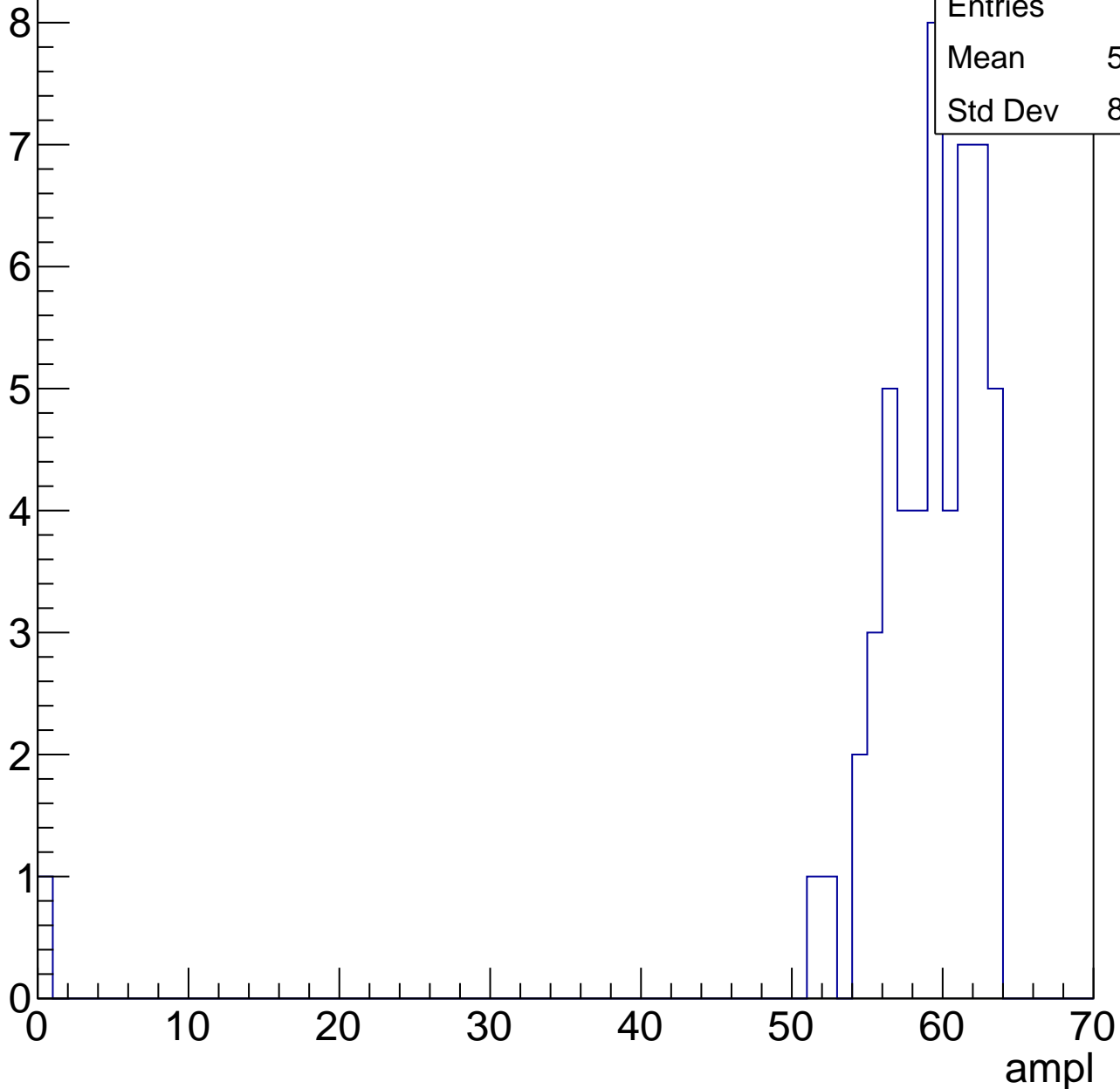


B1L103S, U20-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

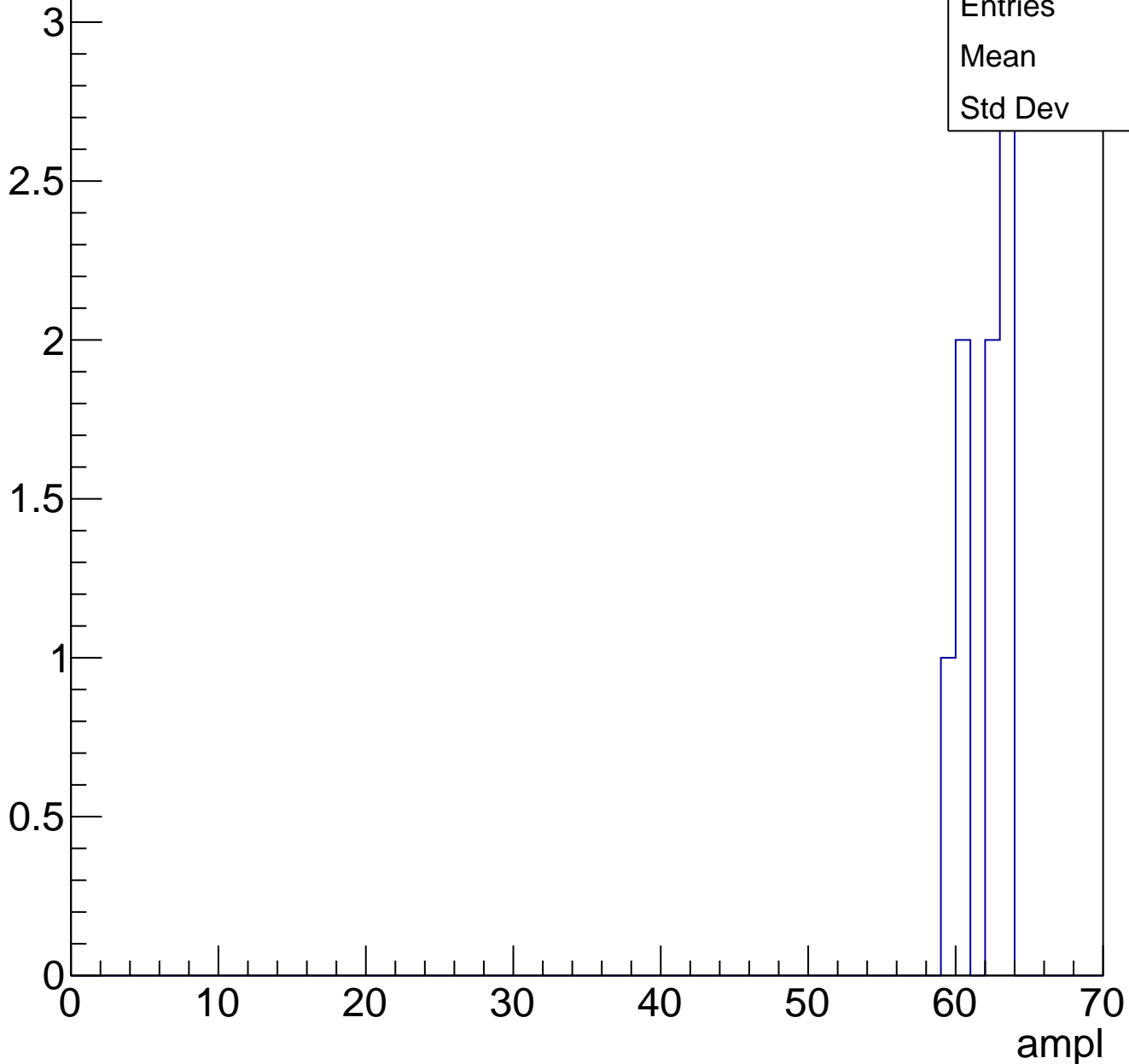
Entries	52
Mean	57.77
Std Dev	8.608



B1L103S, U20-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	8
Mean	61.5
Std Dev	1.5

B1L103S, U20-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U20-ch124, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	27.91
Std Dev	5.99

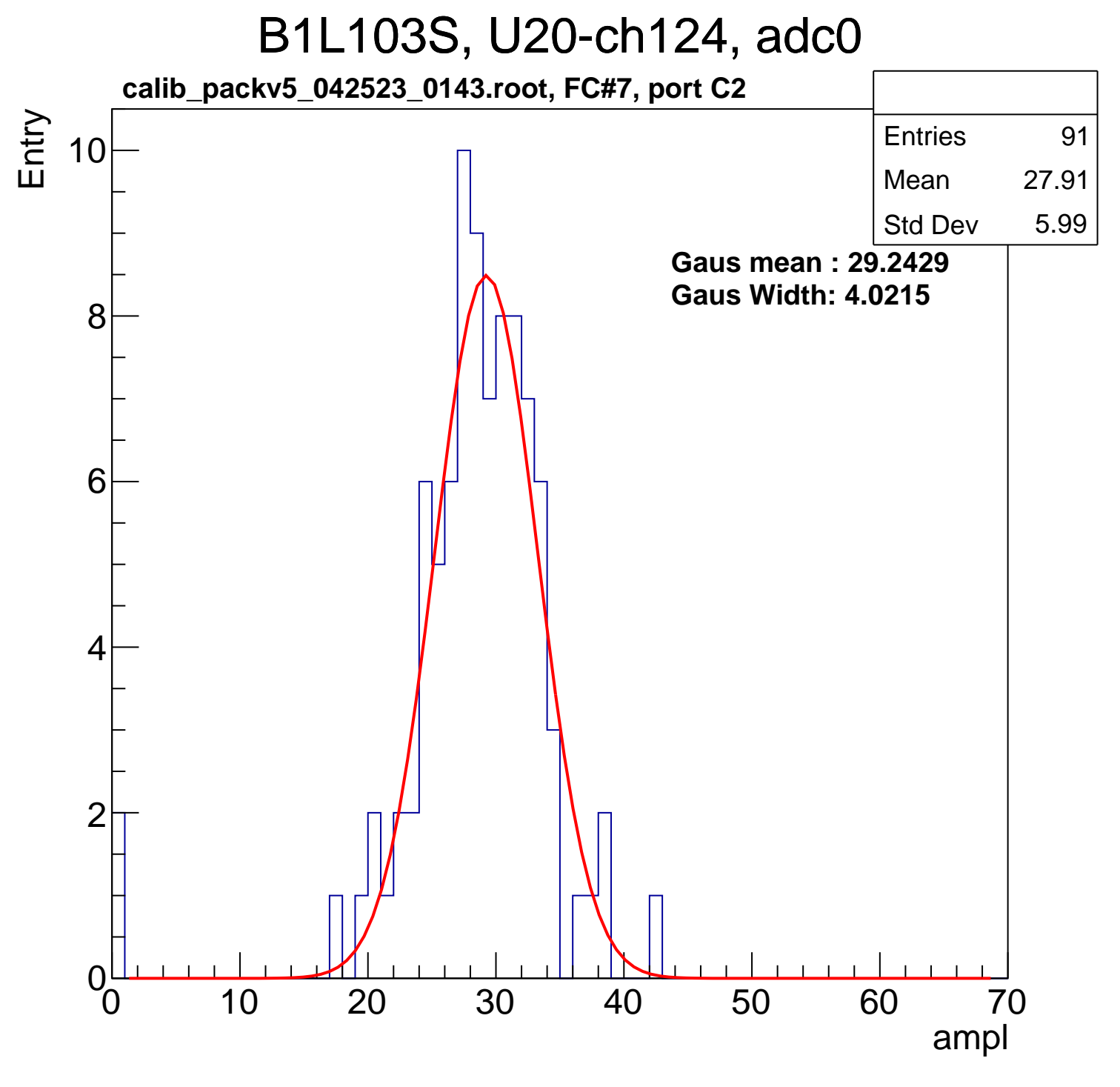
Gaus mean : 29.2429
Gaus Width: 4.0215

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U20-ch124, adc1

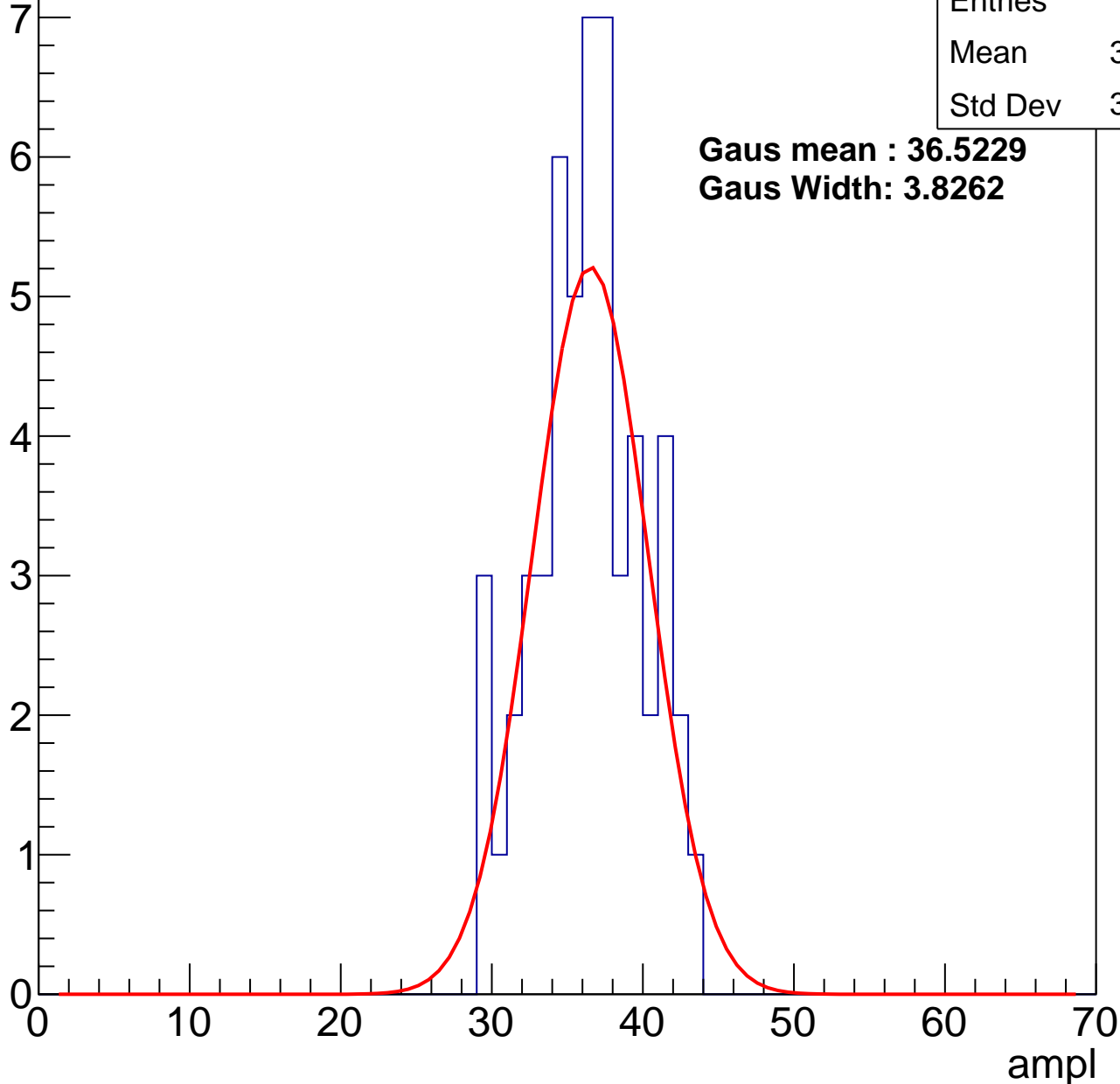
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	35.94
Std Dev	3.488

Gaus mean : 36.5229

Gaus Width: 3.8262



B1L103S, U20-ch124, adc2

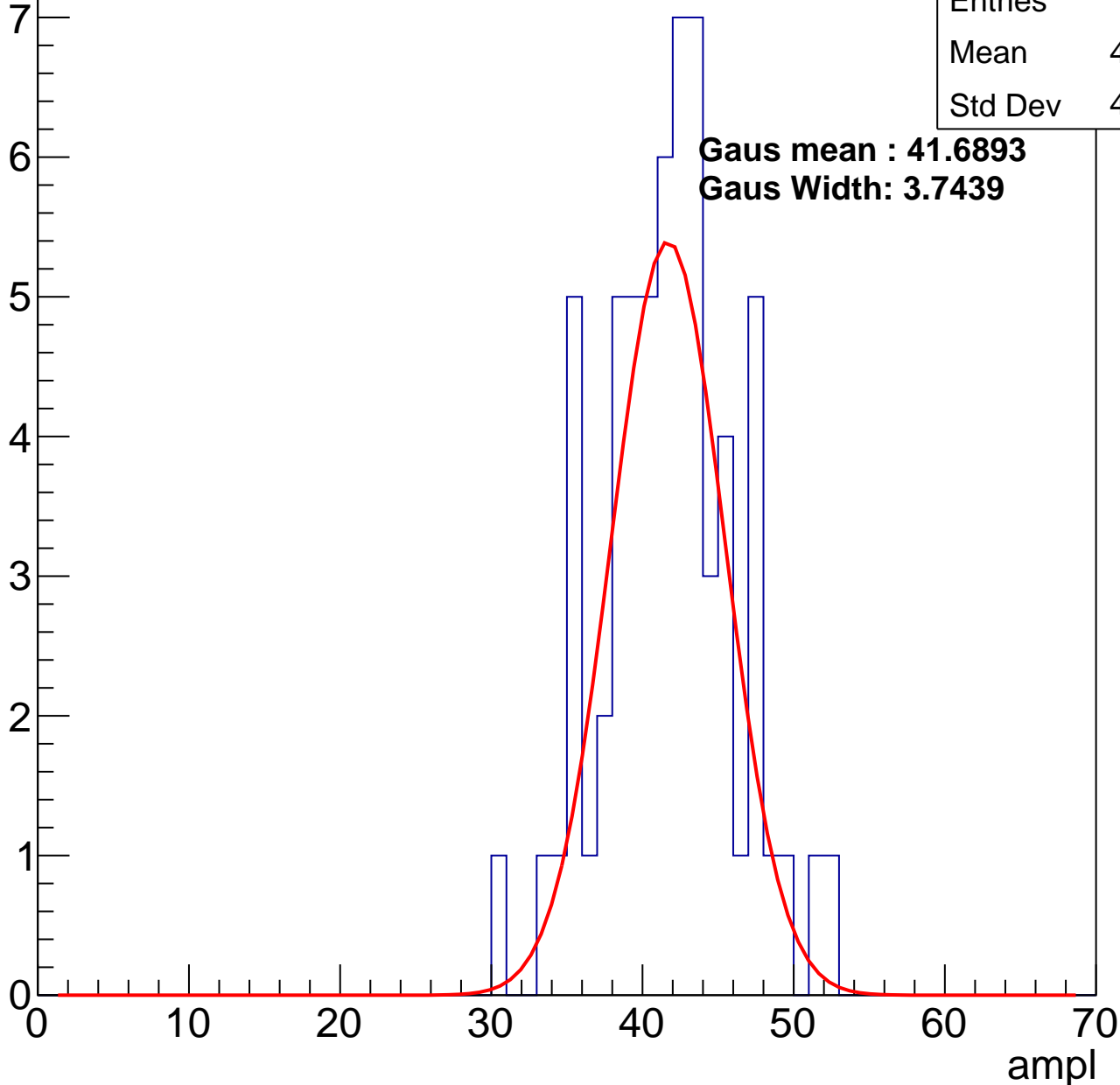
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.29
Std Dev	4.366

Gaus mean : 41.6893

Gaus Width: 3.7439

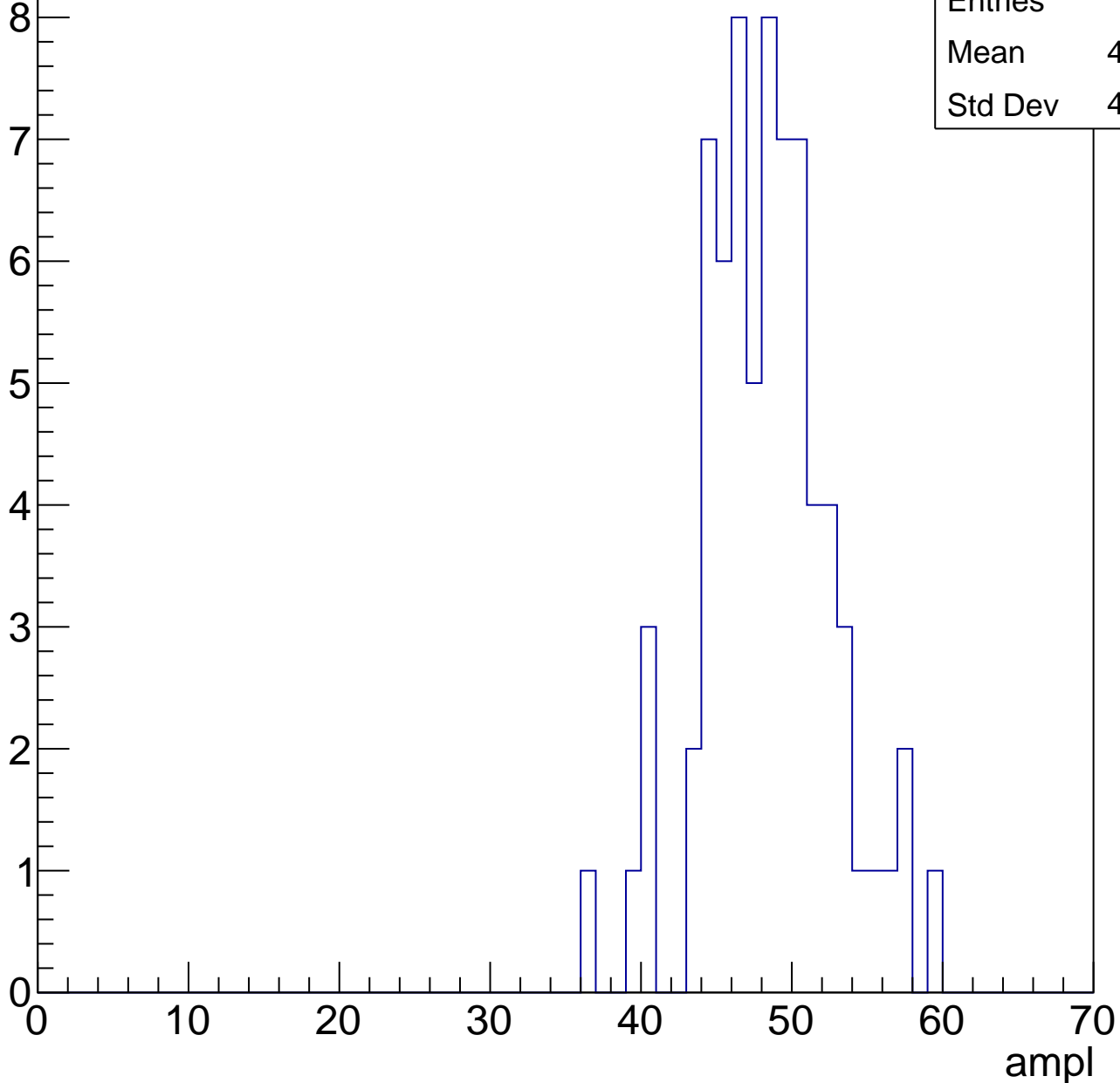


B1L103S, U20-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	47.89
Std Dev	4.267

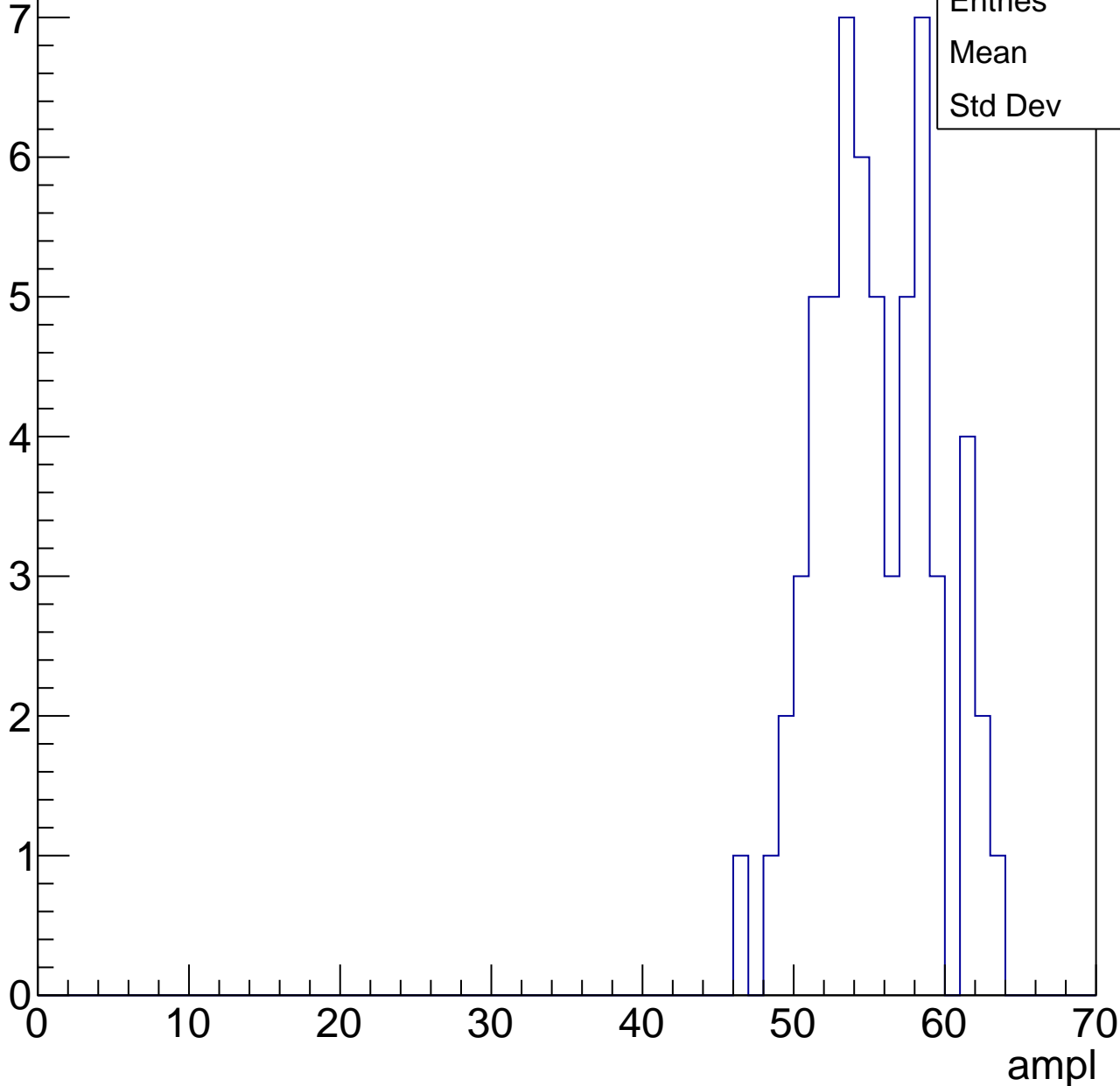


B1L103S, U20-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.9
Std Dev	3.82



B1L103S, U20-ch124, adc5

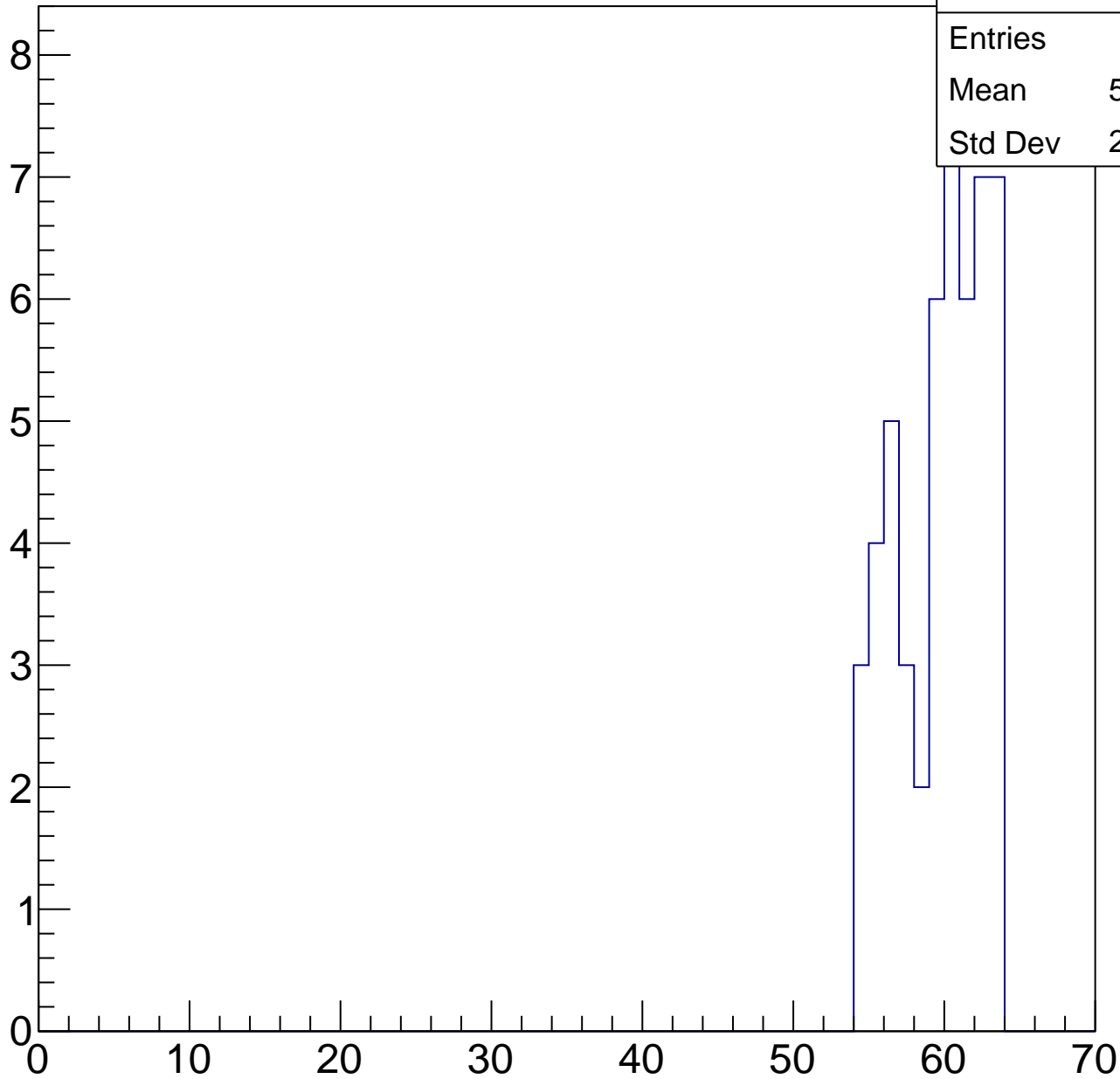
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.29
Std Dev	2.803

ampl



B1L103S, U20-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

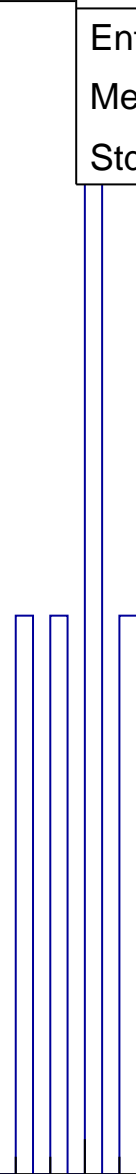
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	59.83
Std Dev	2.339

0 10 20 30 40 50 60 70

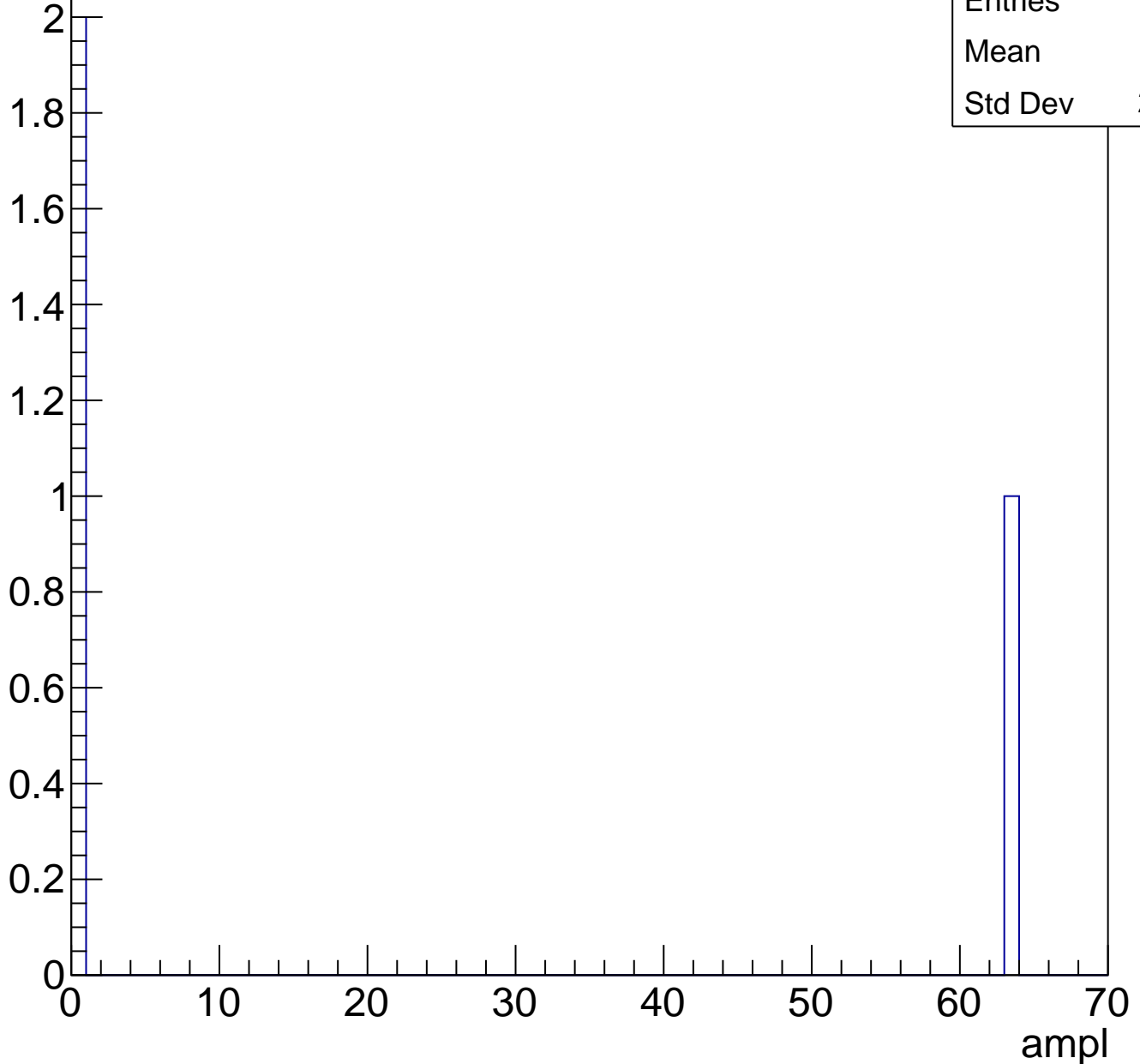
ampl



B1L103S, U20-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch125, adc0

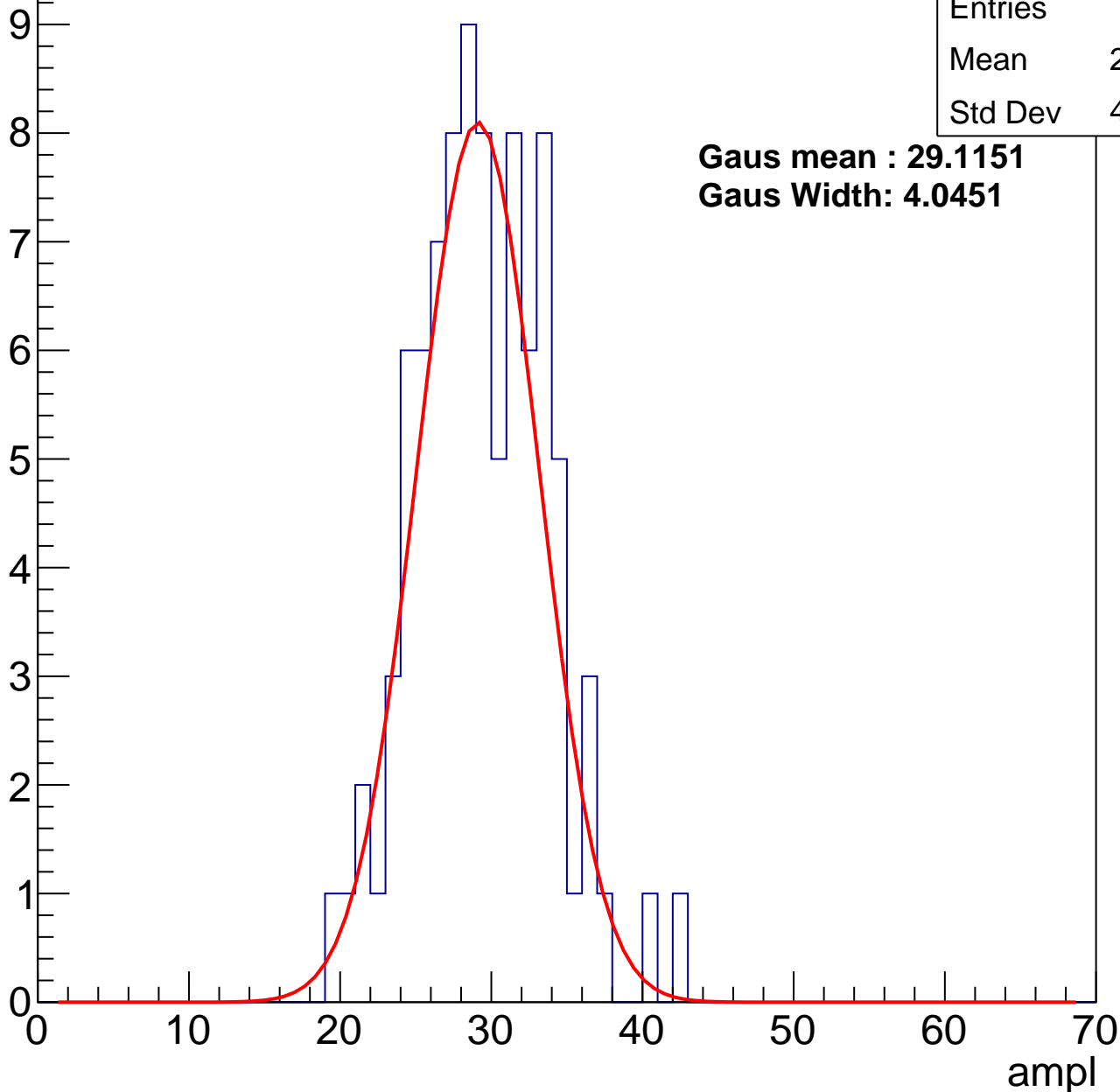
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	91
Mean	28.95
Std Dev	4.318

Gaus mean : 29.1151

Gaus Width: 4.0451



B1L103S, U20-ch125, adc1

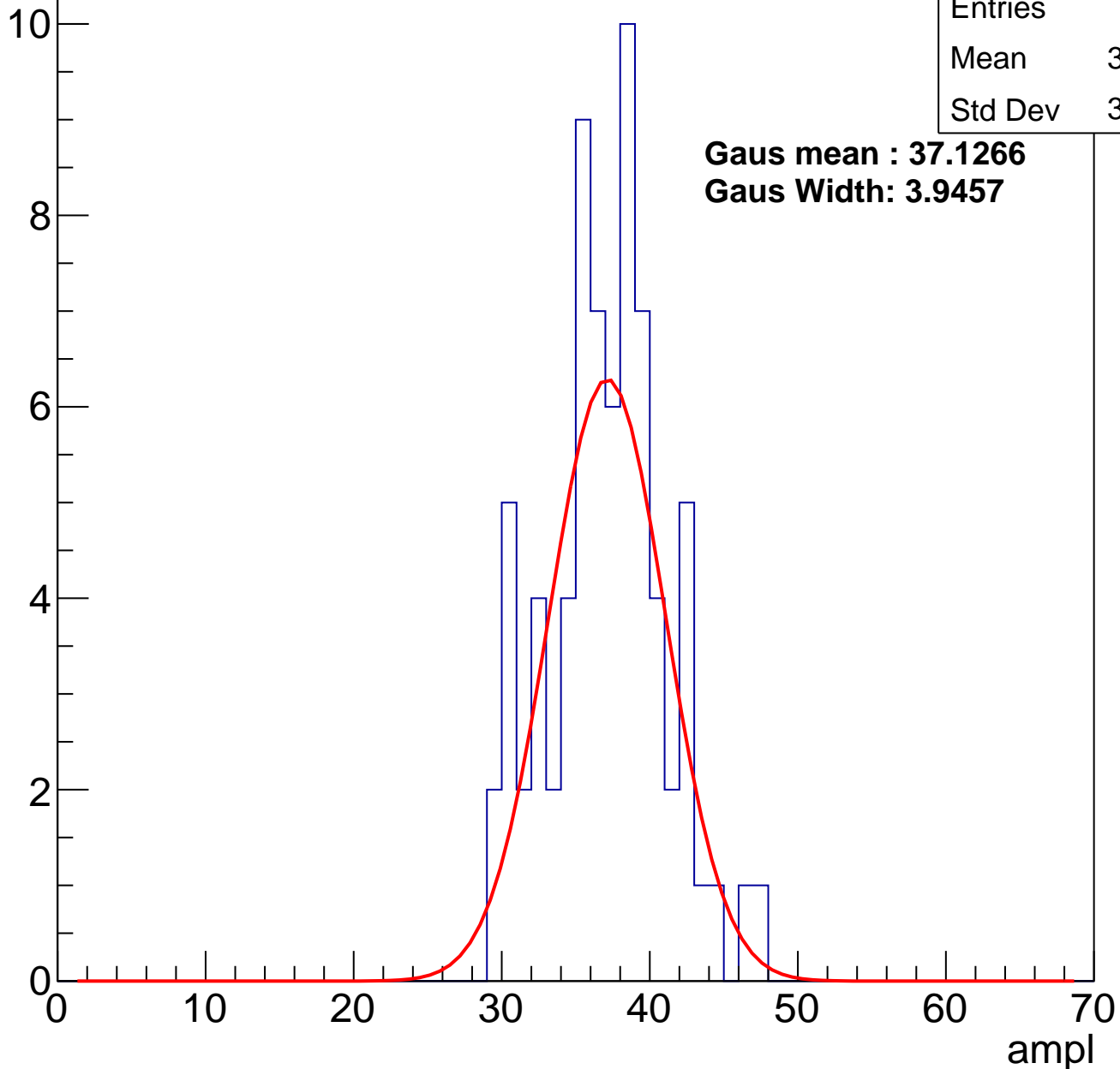
calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	36.63
Std Dev	3.957

Gaus mean : 37.1266

Gaus Width: 3.9457

Entry



B1L103S, U20-ch125, adc2

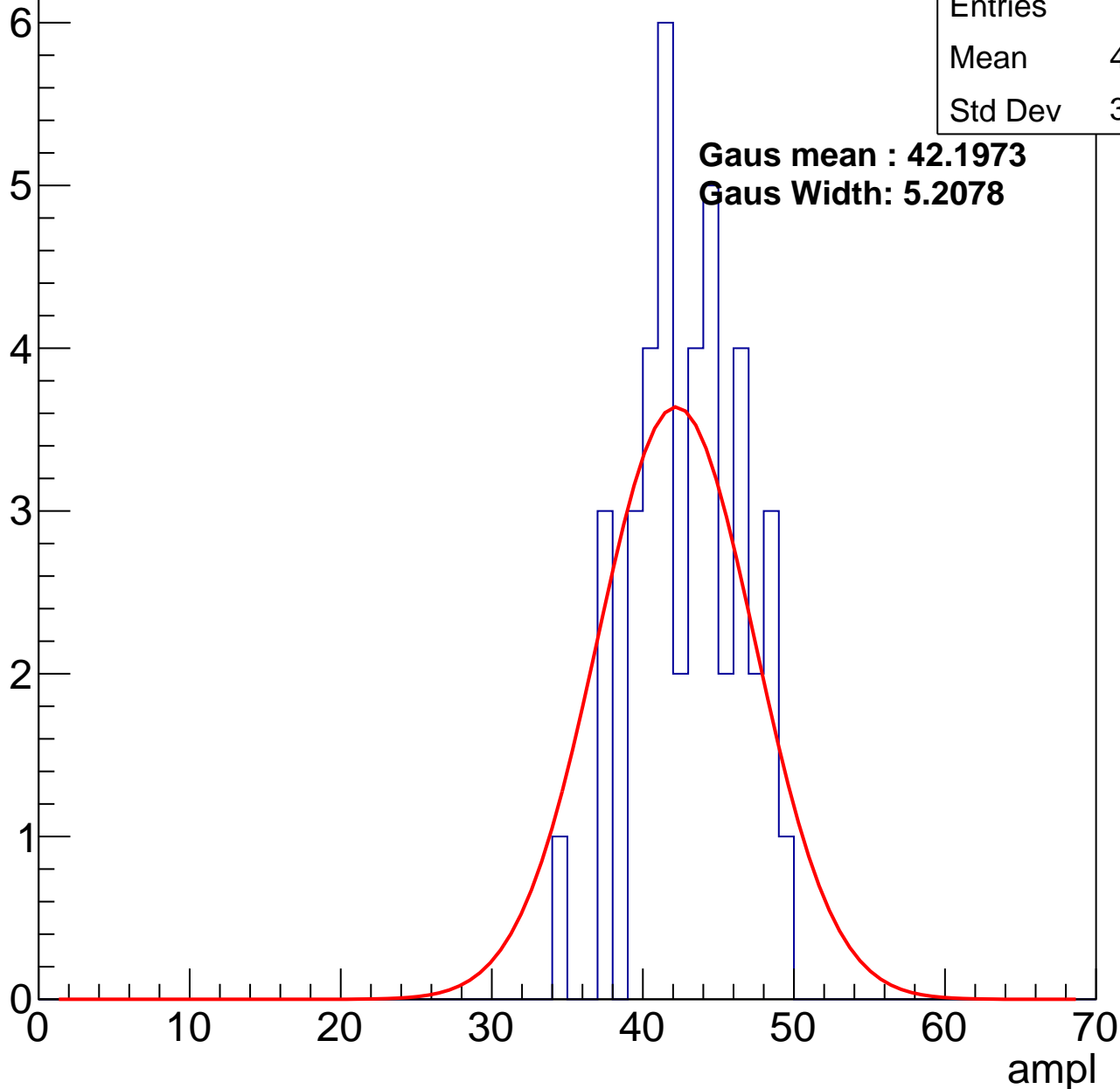
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	42.62
Std Dev	3.483

Gaus mean : 42.1973

Gaus Width: 5.2078

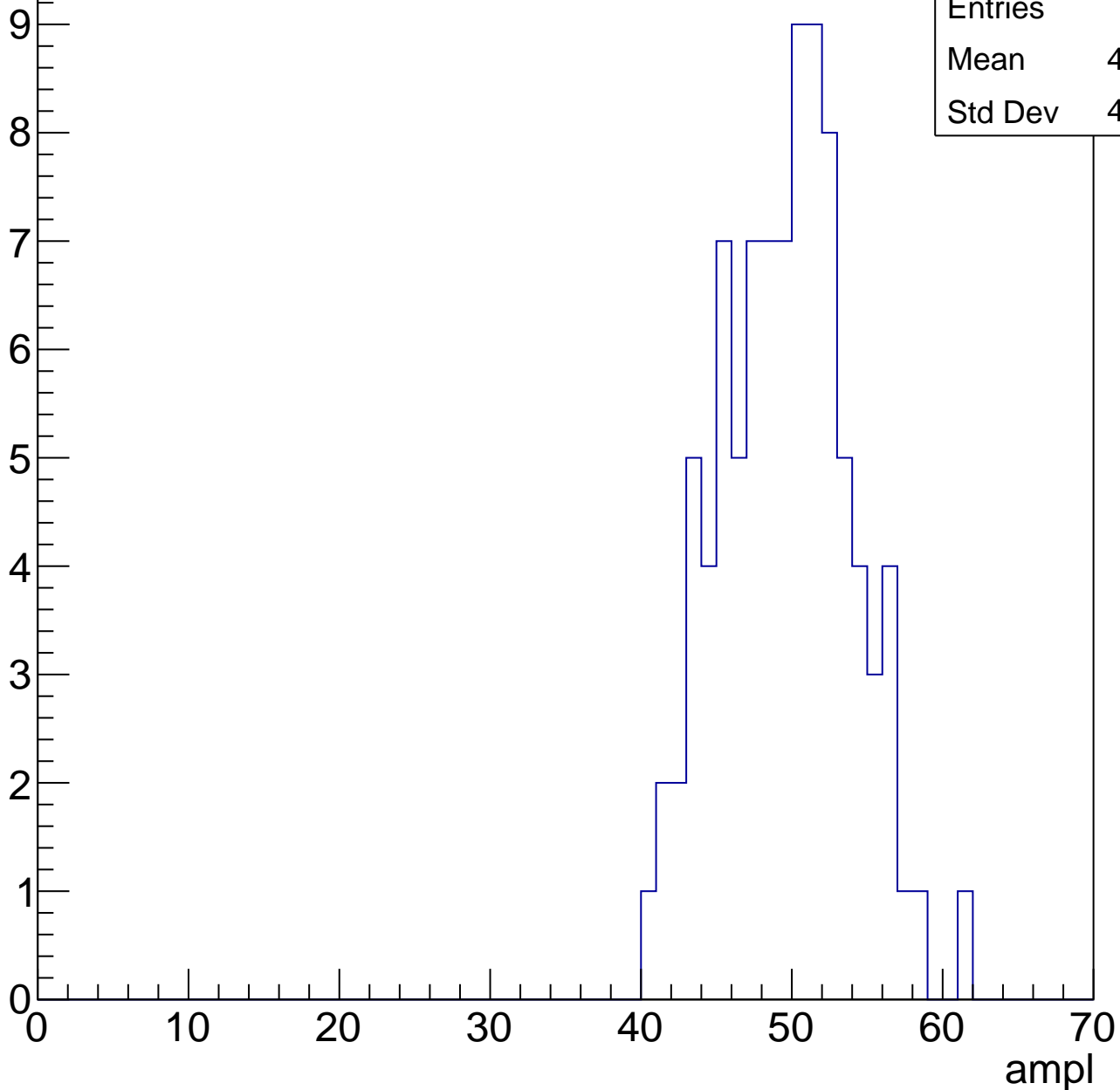


B1L103S, U20-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	92
Mean	49.14
Std Dev	4.257

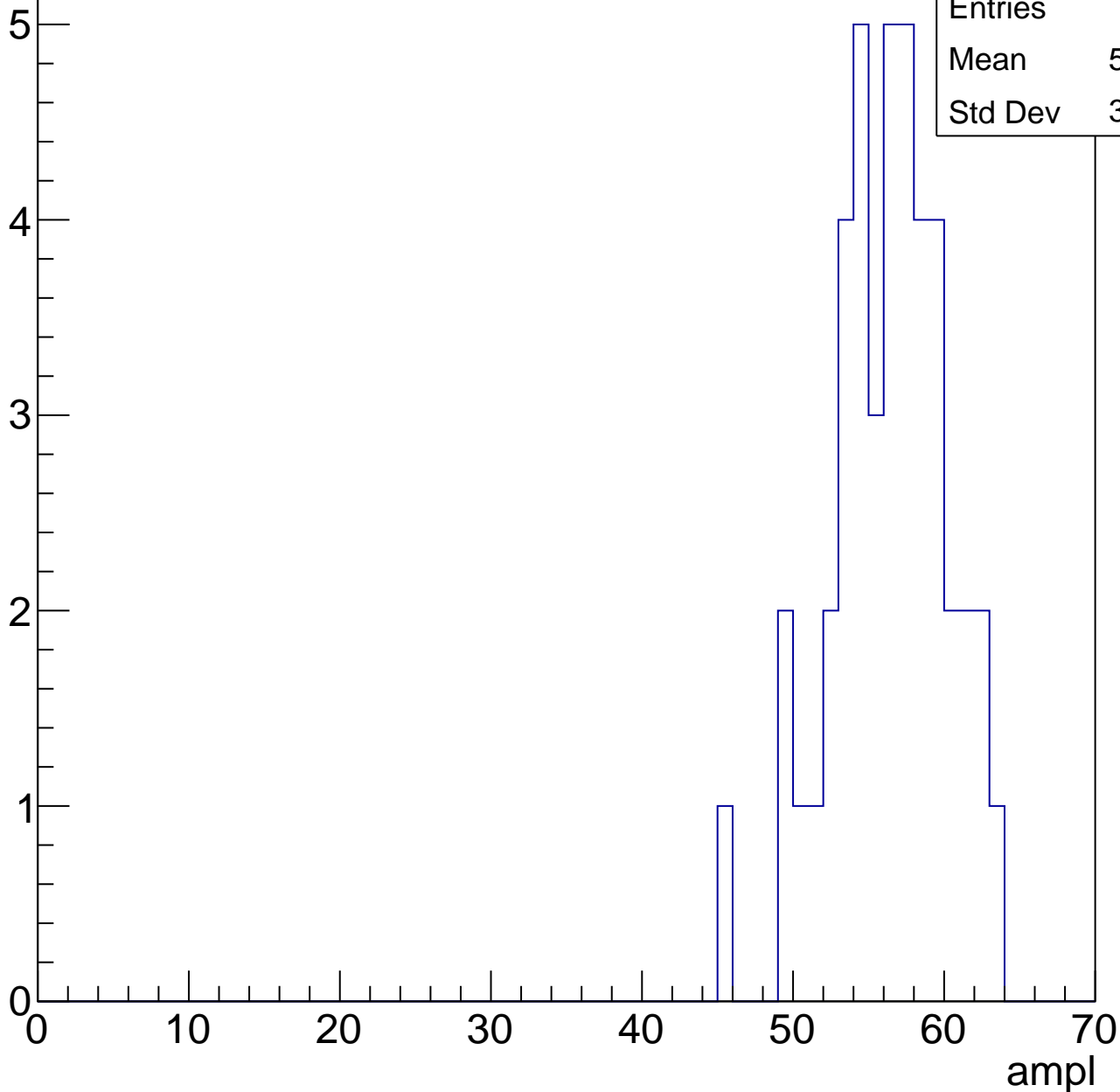


B1L103S, U20-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.84
Std Dev	3.784

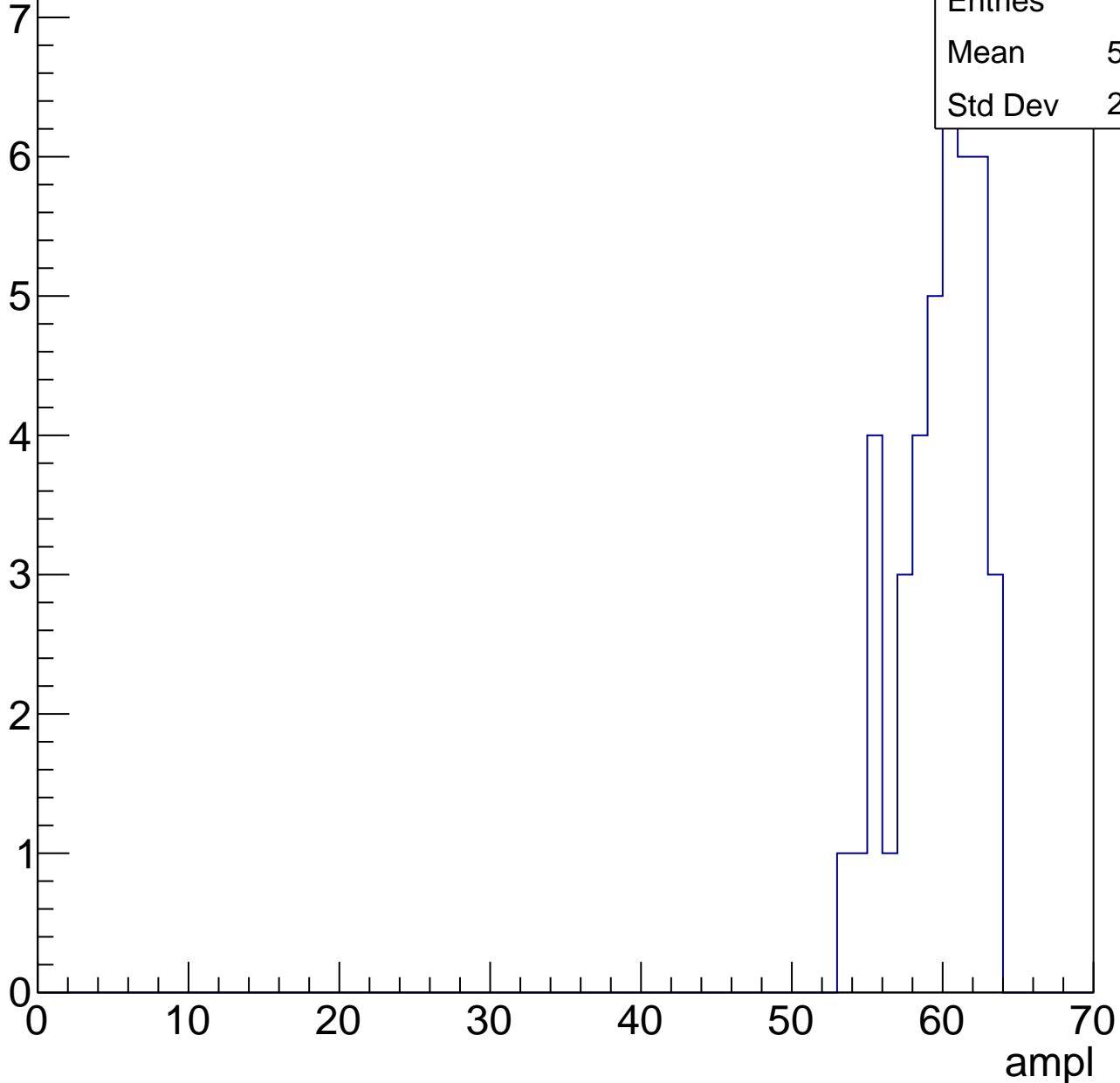


B1L103S, U20-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	59.22
Std Dev	2.618

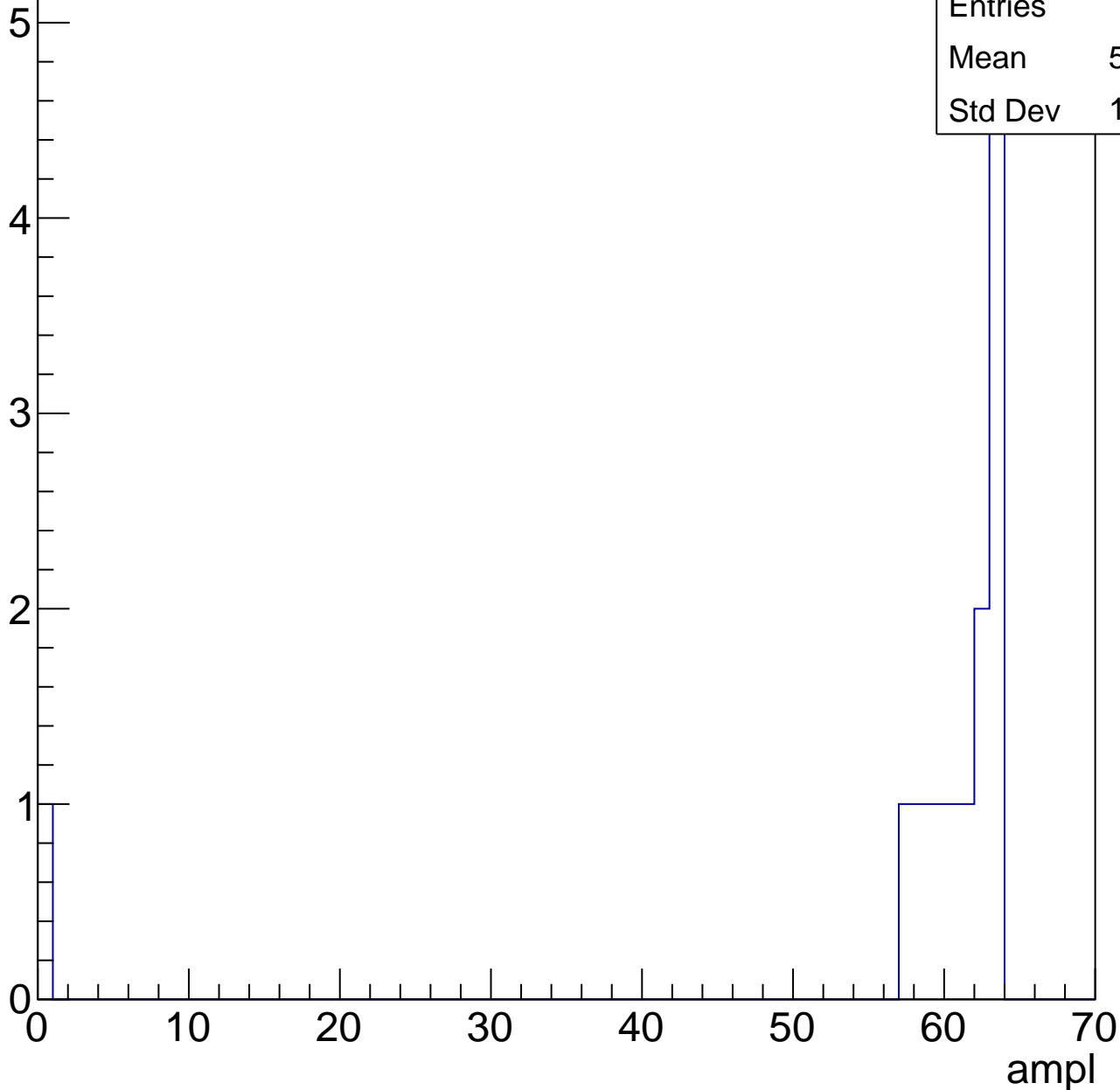


B1L103S, U20-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	56.46
Std Dev	16.42



B1L103S, U20-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch126, adc0

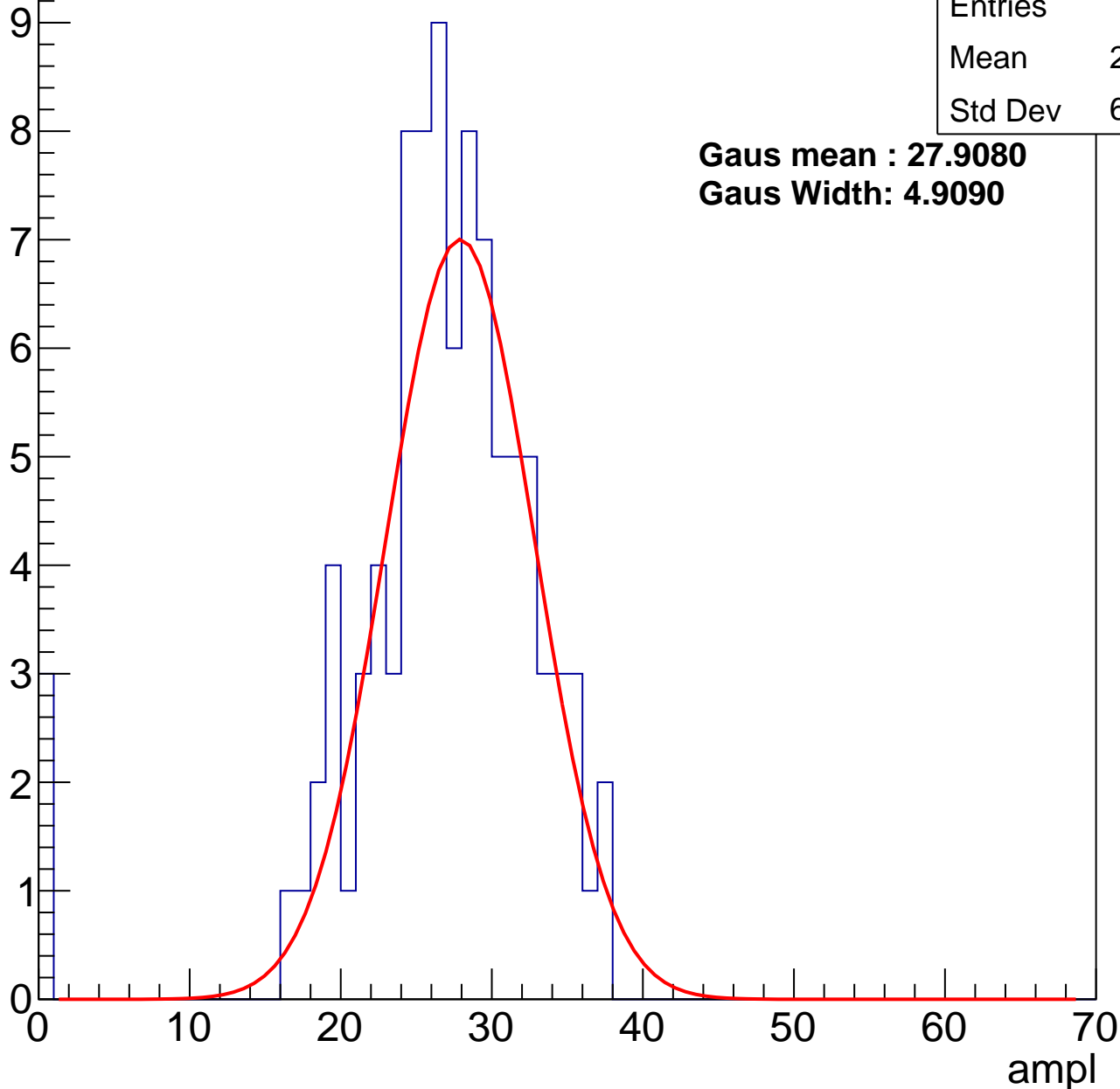
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	95
Mean	26.12
Std Dev	6.636

Gaus mean : 27.9080

Gaus Width: 4.9090



B1L103S, U20-ch126, adc1

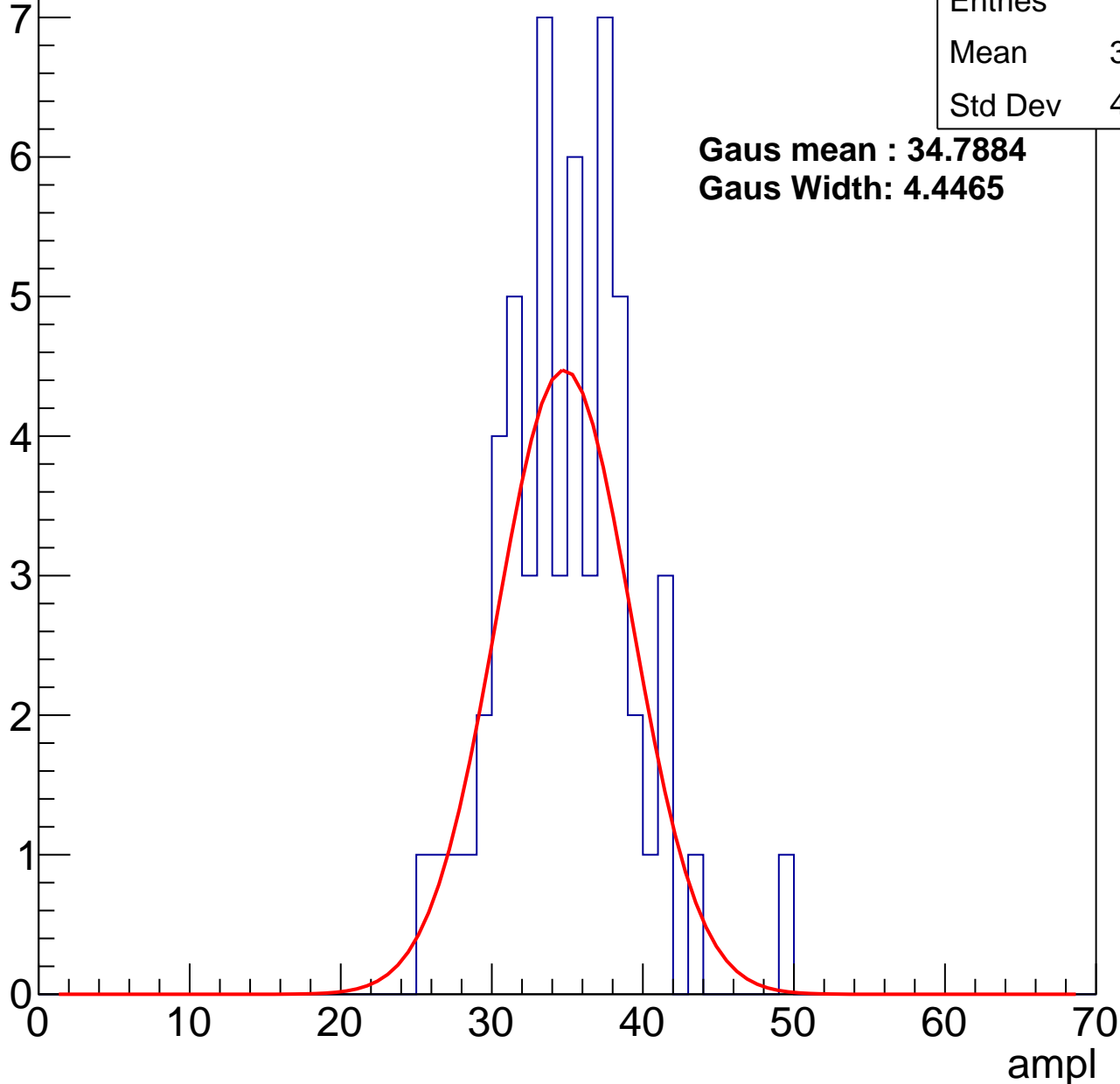
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	34.53
Std Dev	4.377

Gaus mean : 34.7884

Gaus Width: 4.4465



B1L103S, U20-ch126, adc2

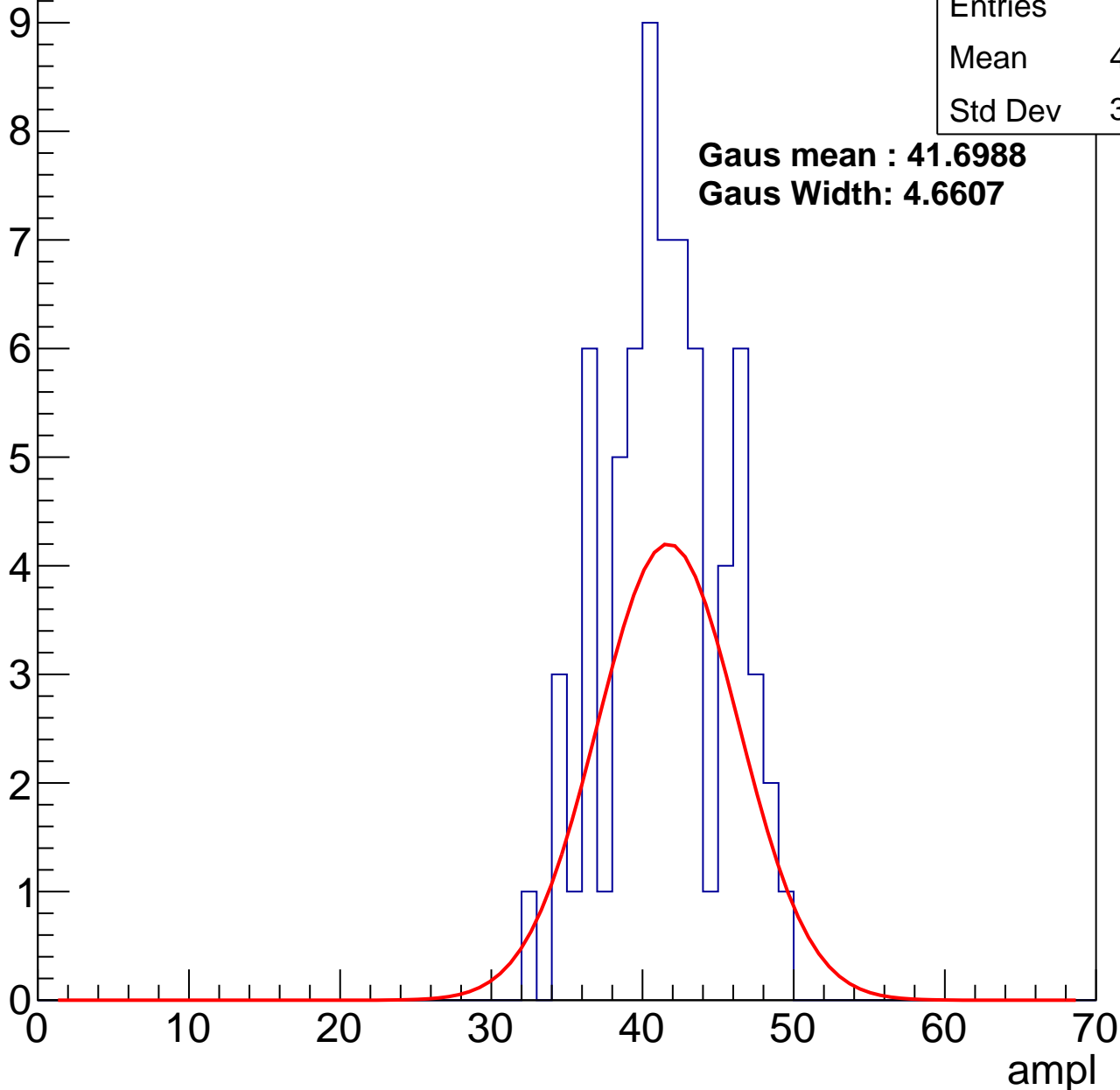
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.03
Std Dev	3.867

Gaus mean : 41.6988

Gaus Width: 4.6607

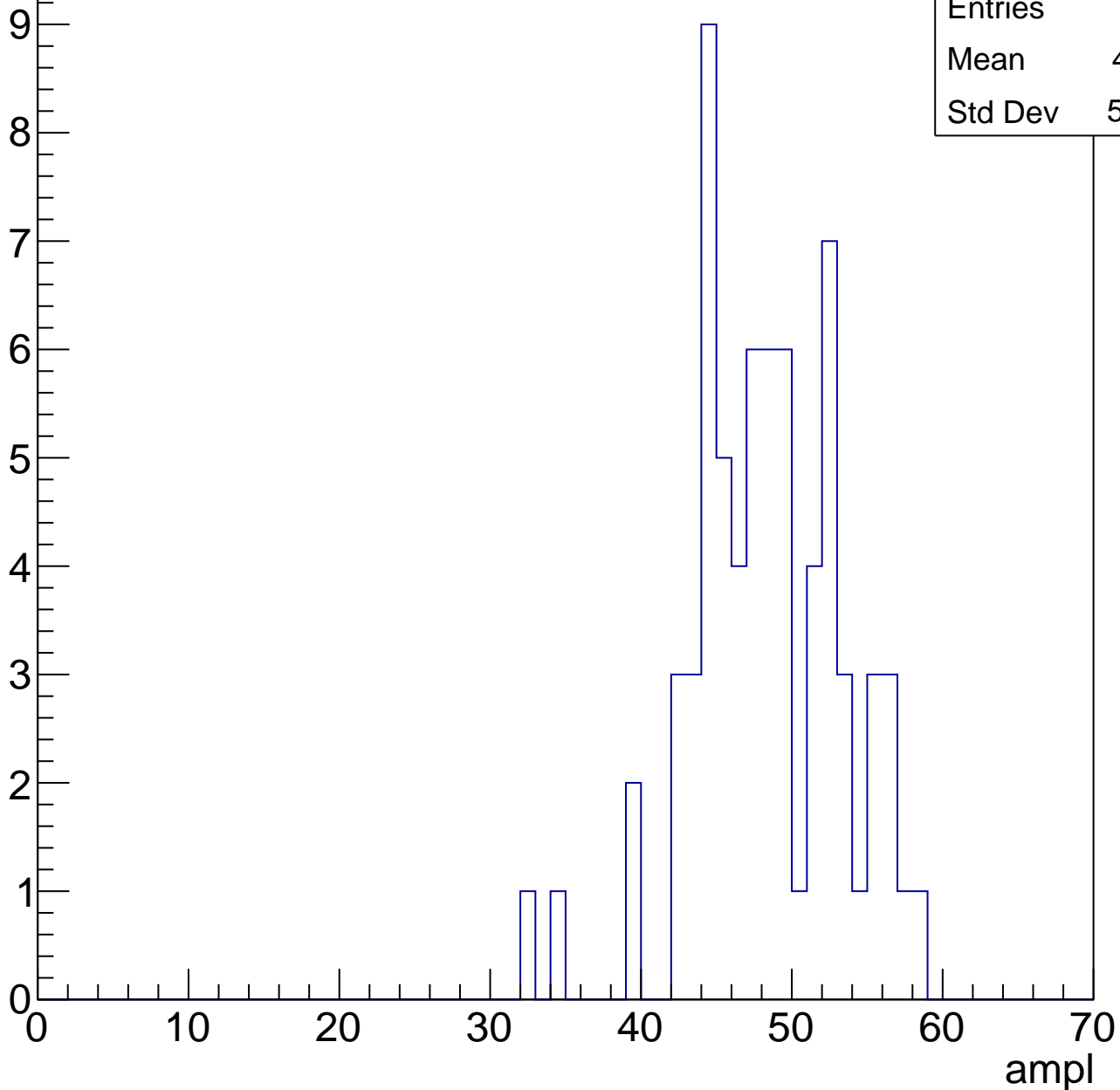


B1L103S, U20-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	47.81
Std Dev	5.063

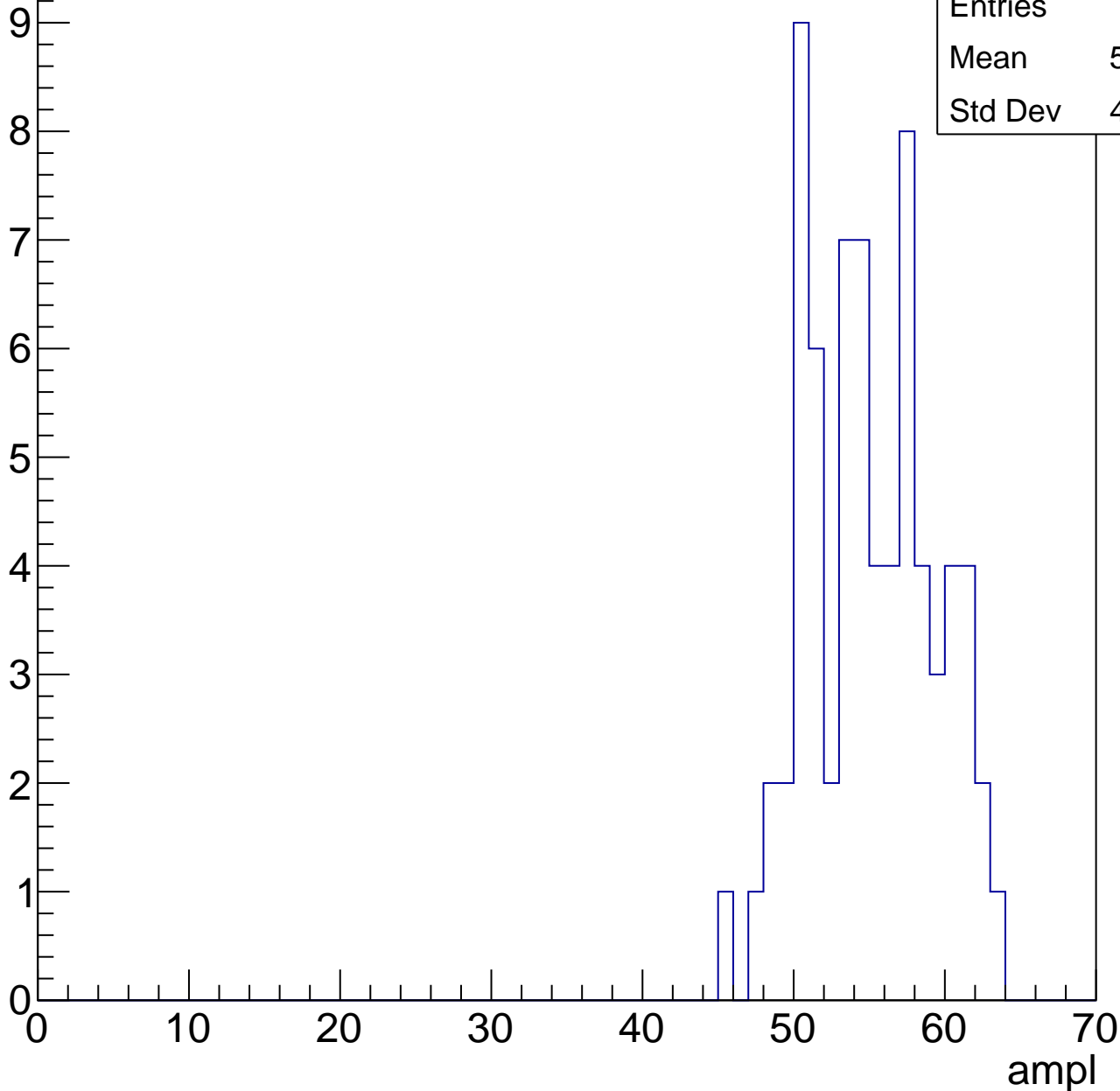


B1L103S, U20-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	54.58
Std Dev	4.147

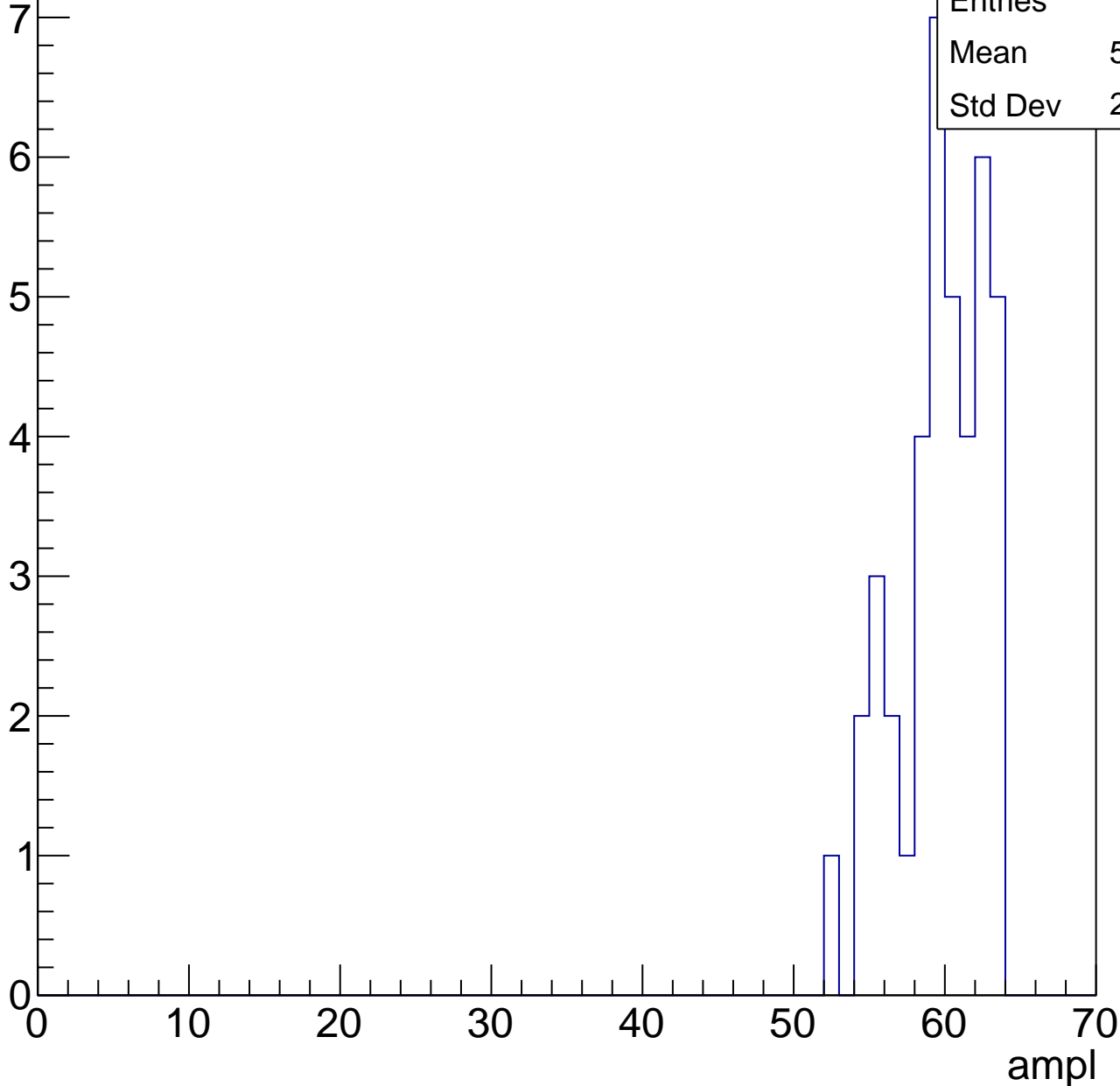


B1L103S, U20-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

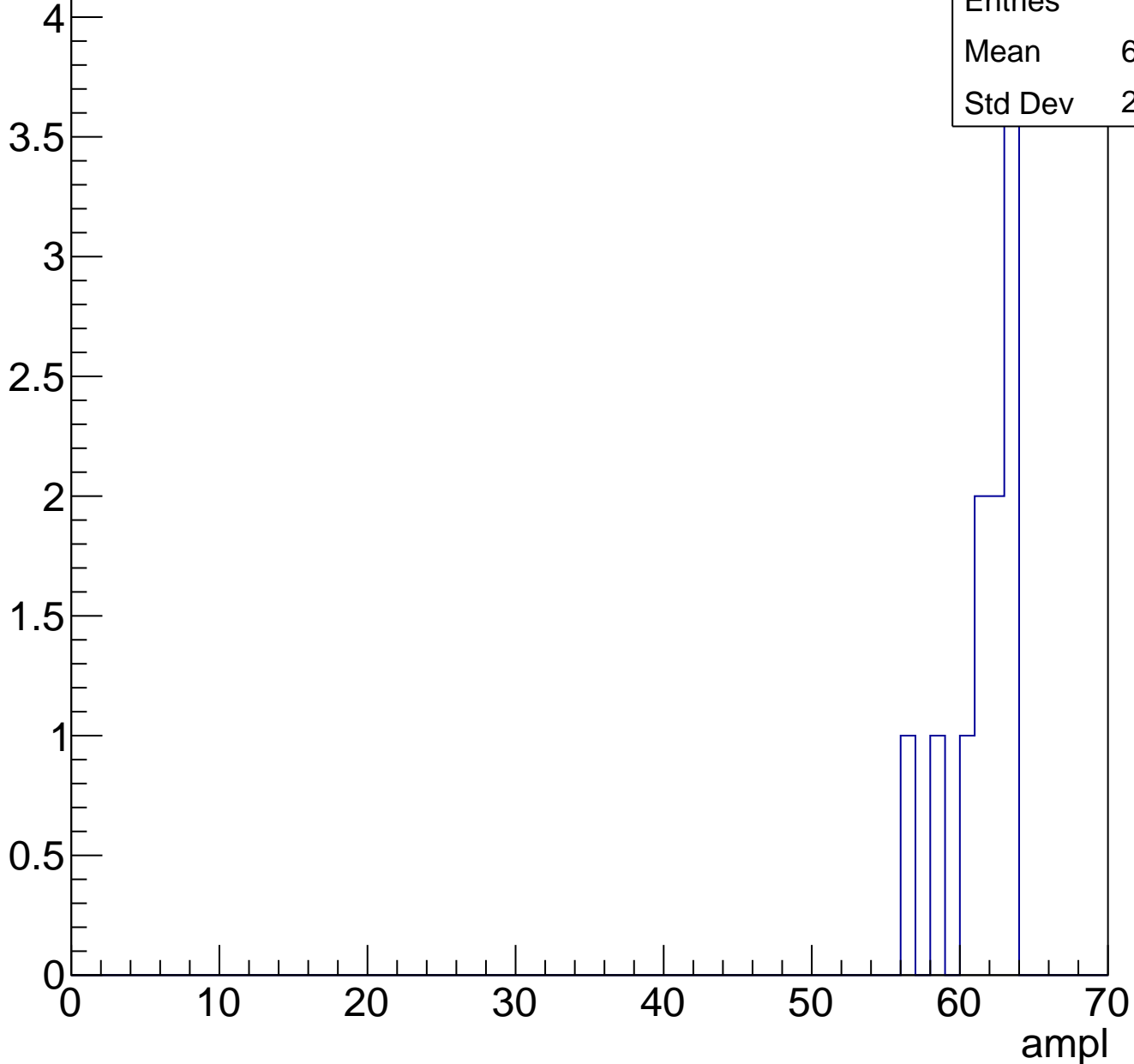
Entries	40
Mean	59.25
Std Dev	2.853



B1L103S, U20-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U20-ch127, adc0

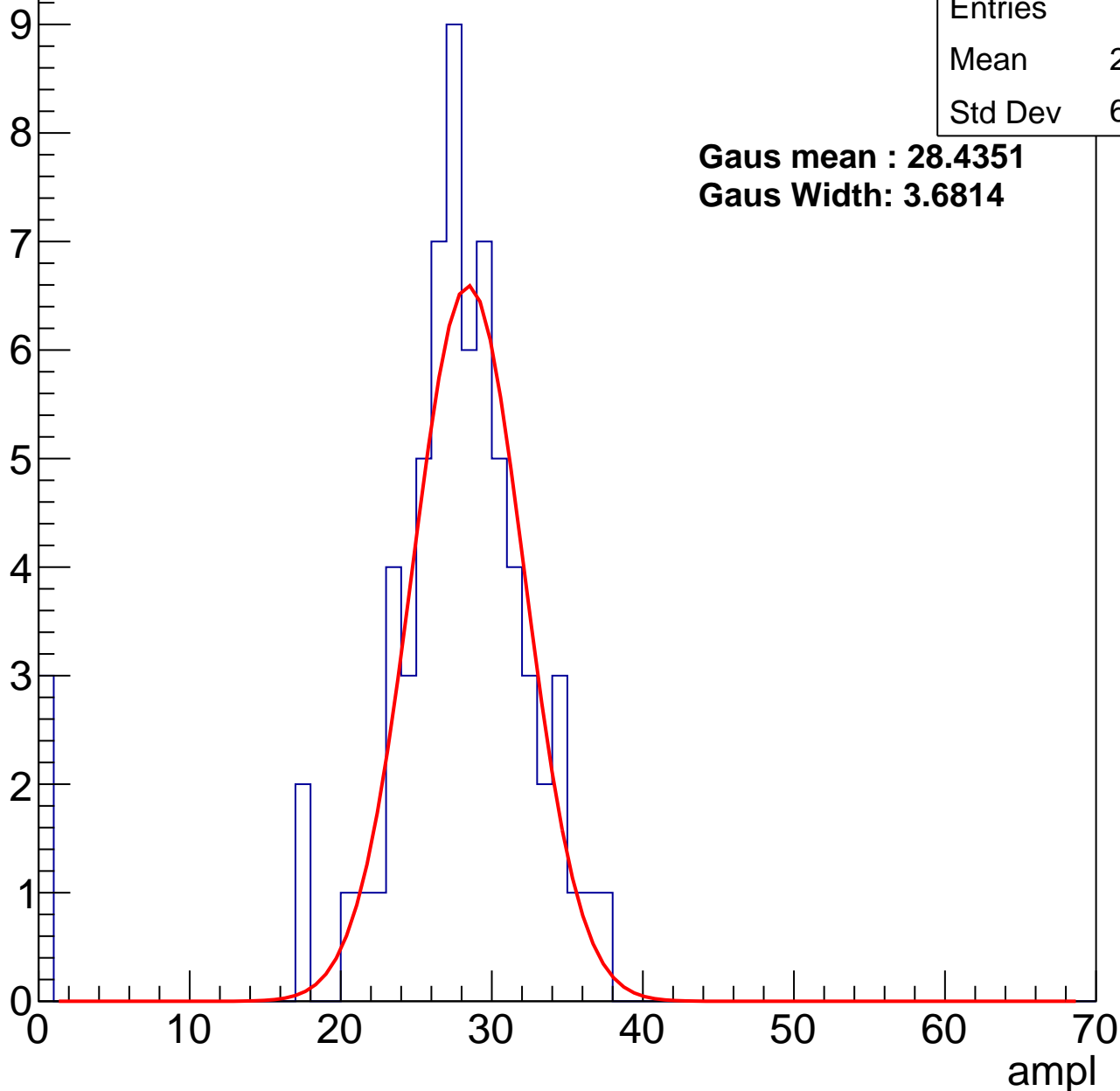
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.49
Std Dev	6.894

Gaus mean : 28.4351

Gaus Width: 3.6814



B1L103S, U20-ch127, adc1

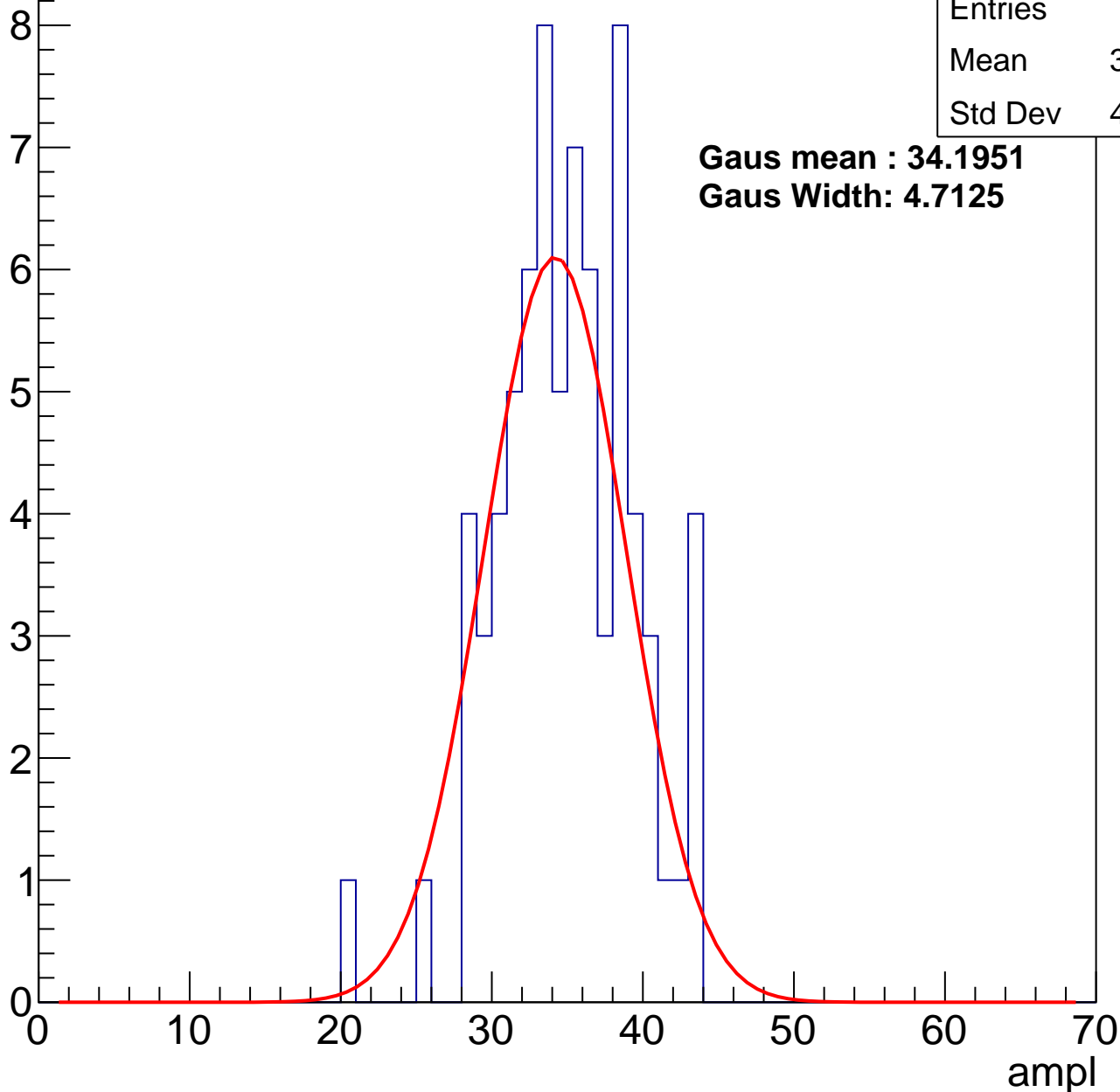
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	34.49
Std Dev	4.427

Gaus mean : 34.1951

Gaus Width: 4.7125



B1L103S, U20-ch127, adc2

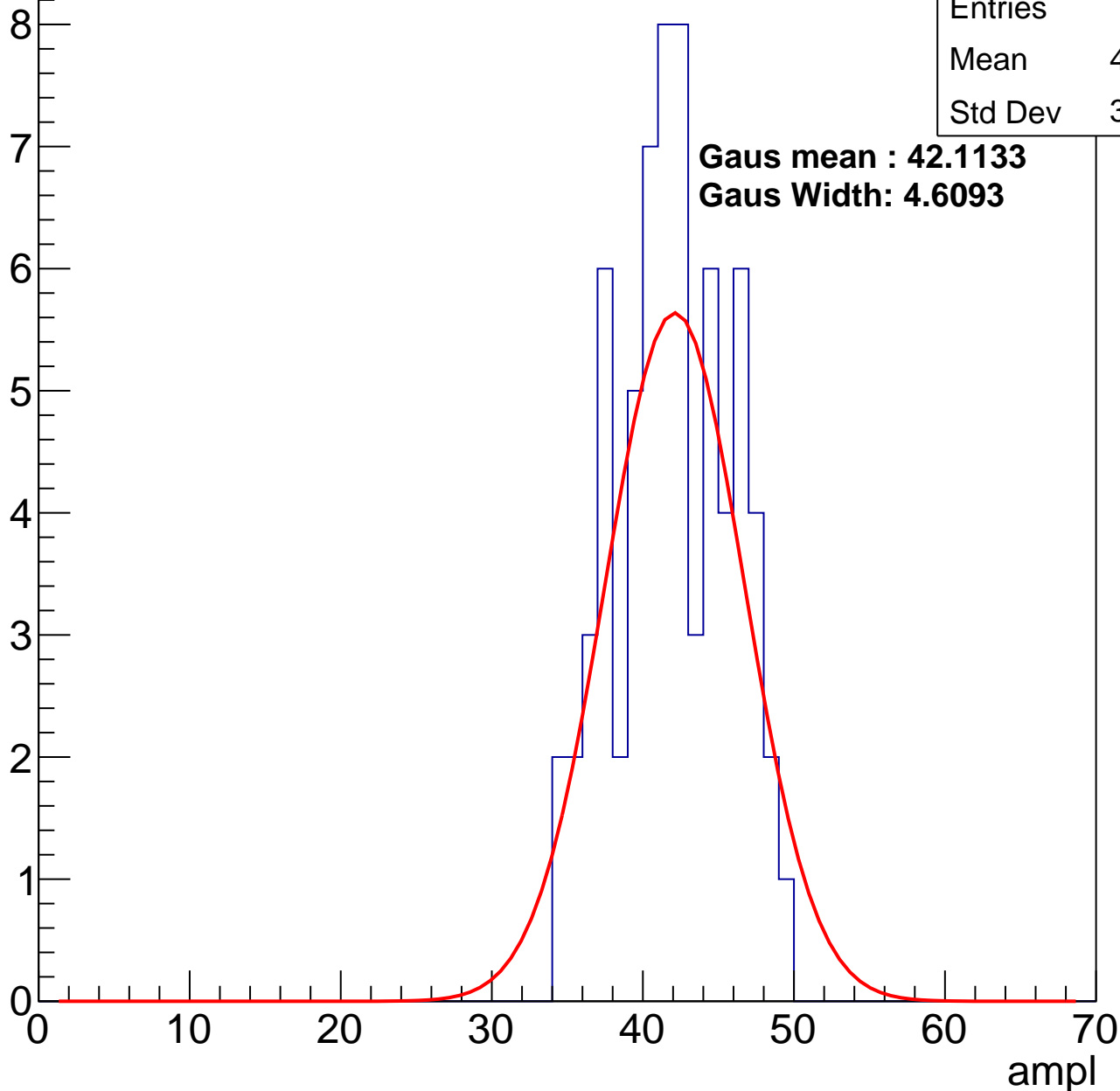
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.52
Std Dev	3.732

Gaus mean : 42.1133

Gaus Width: 4.6093

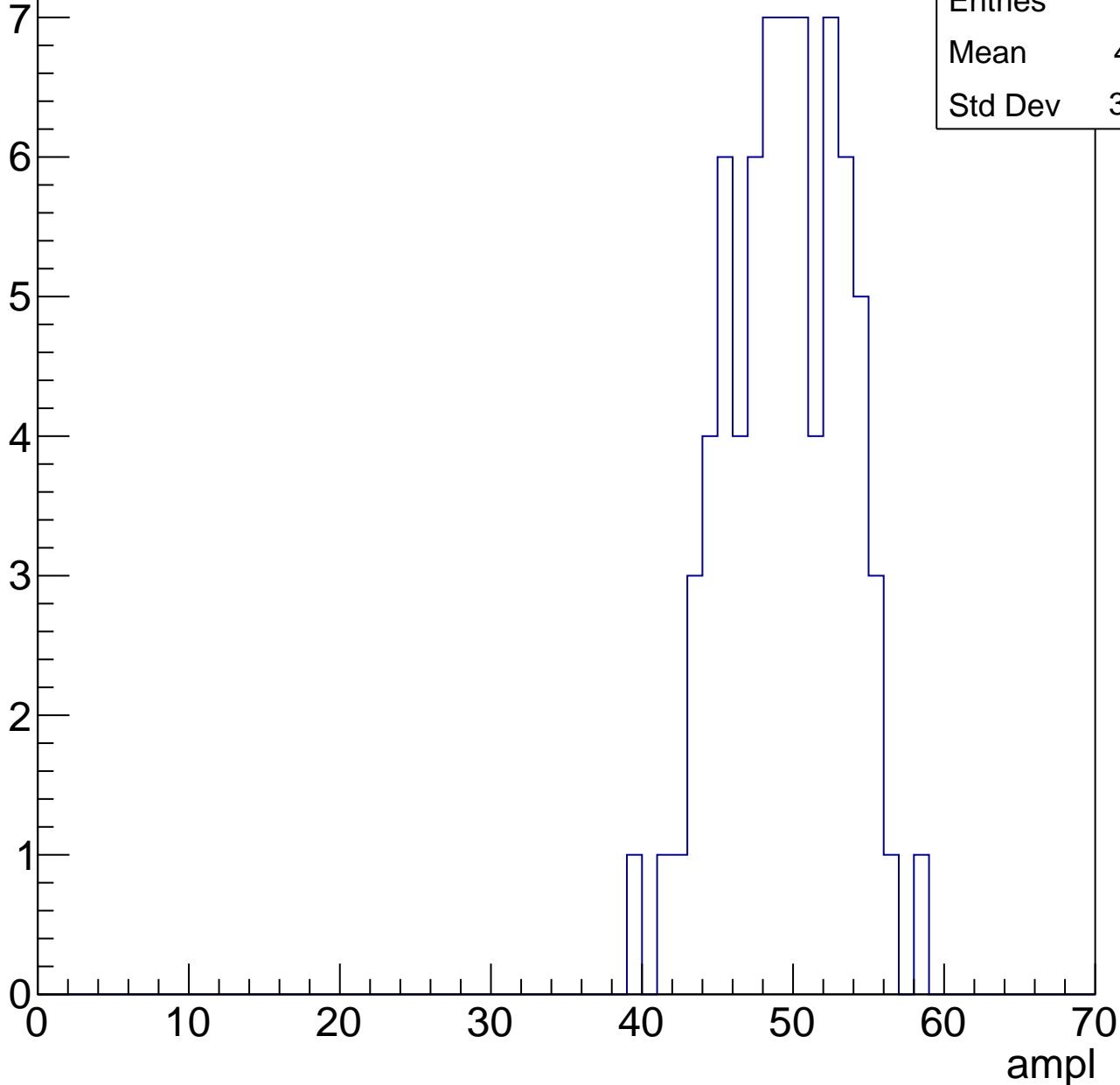


B1L103S, U20-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.01
Std Dev	3.906

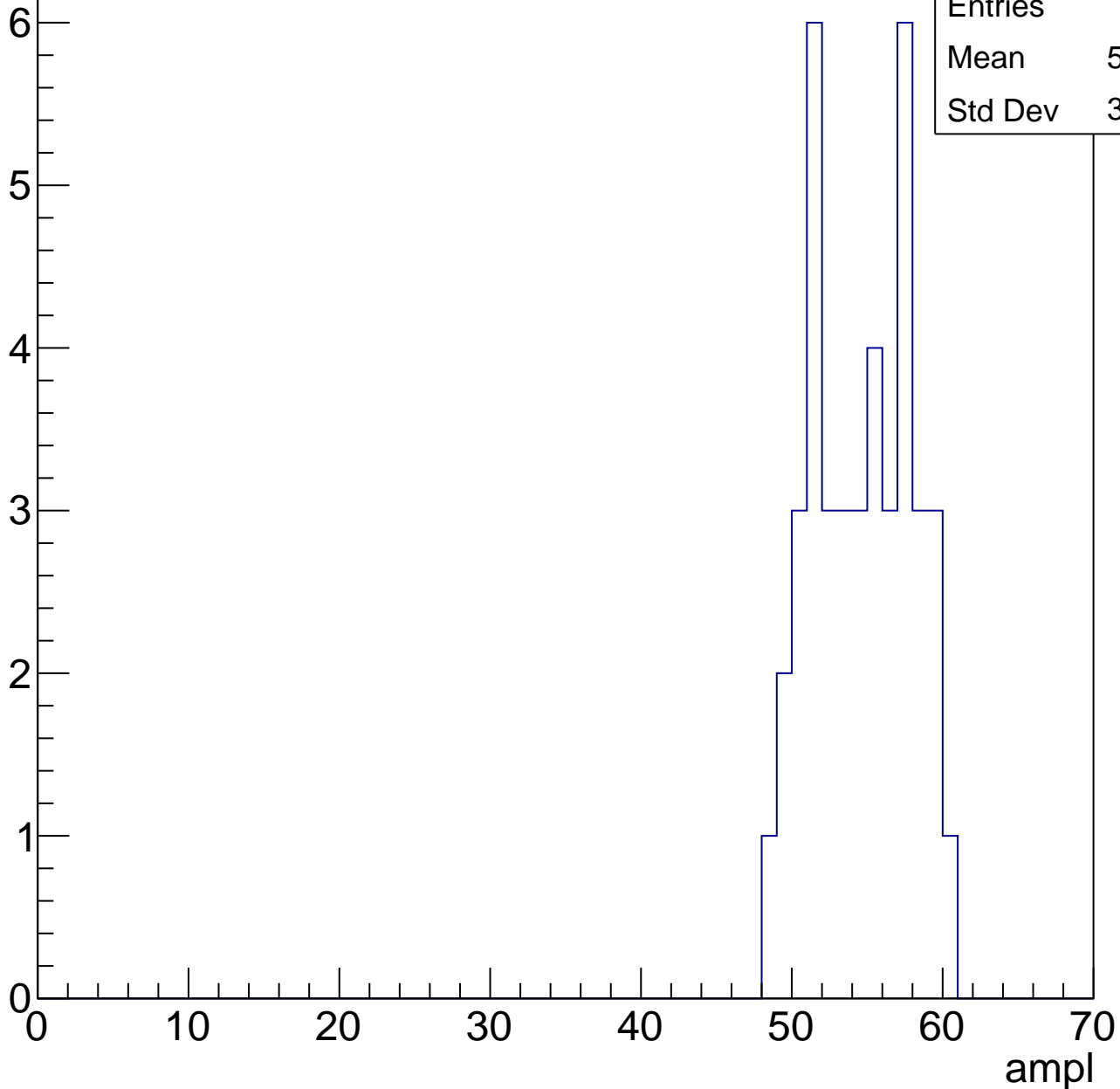


B1L103S, U20-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	54.15
Std Dev	3.243

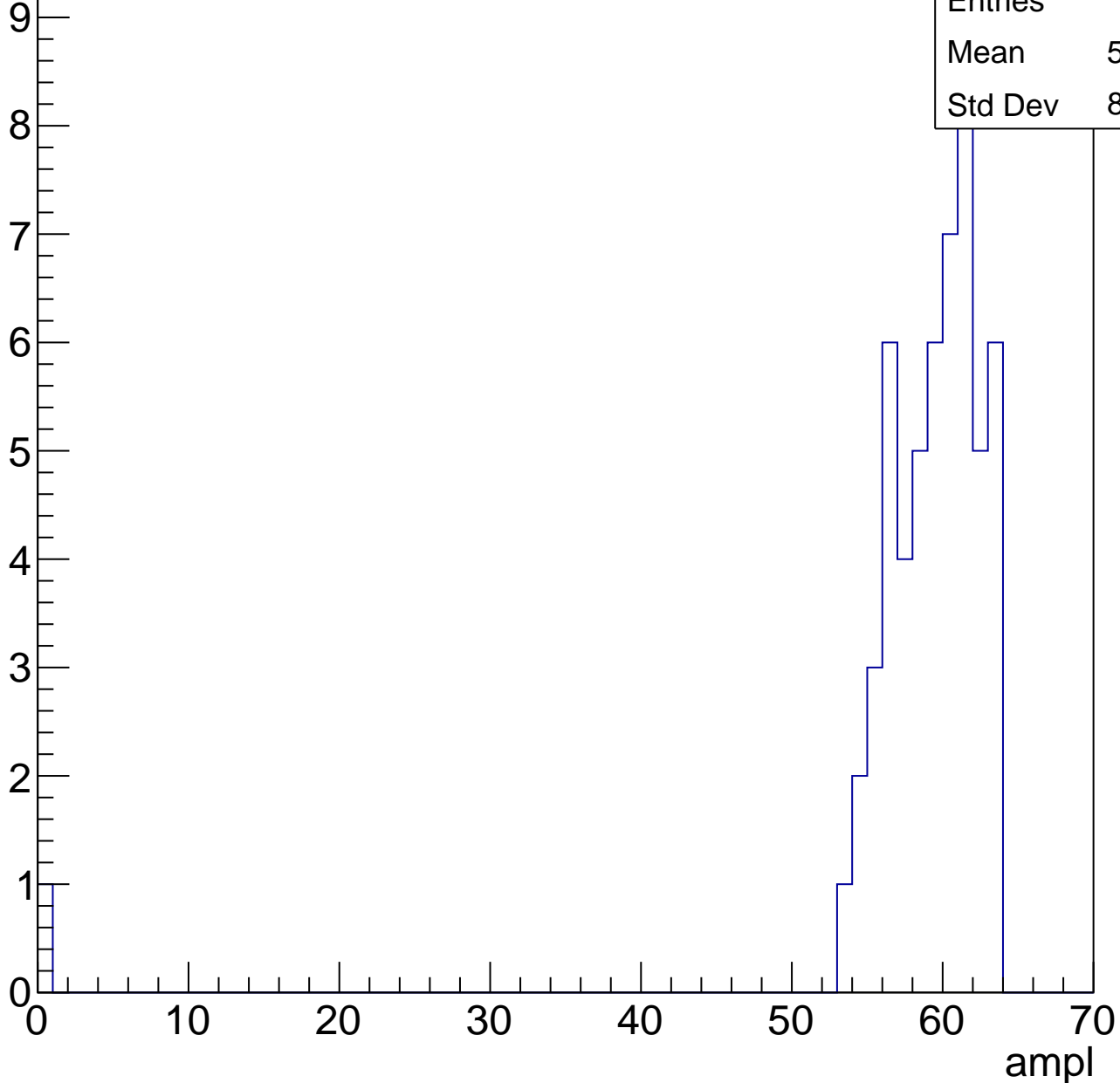


B1L103S, U20-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.02
Std Dev	8.335

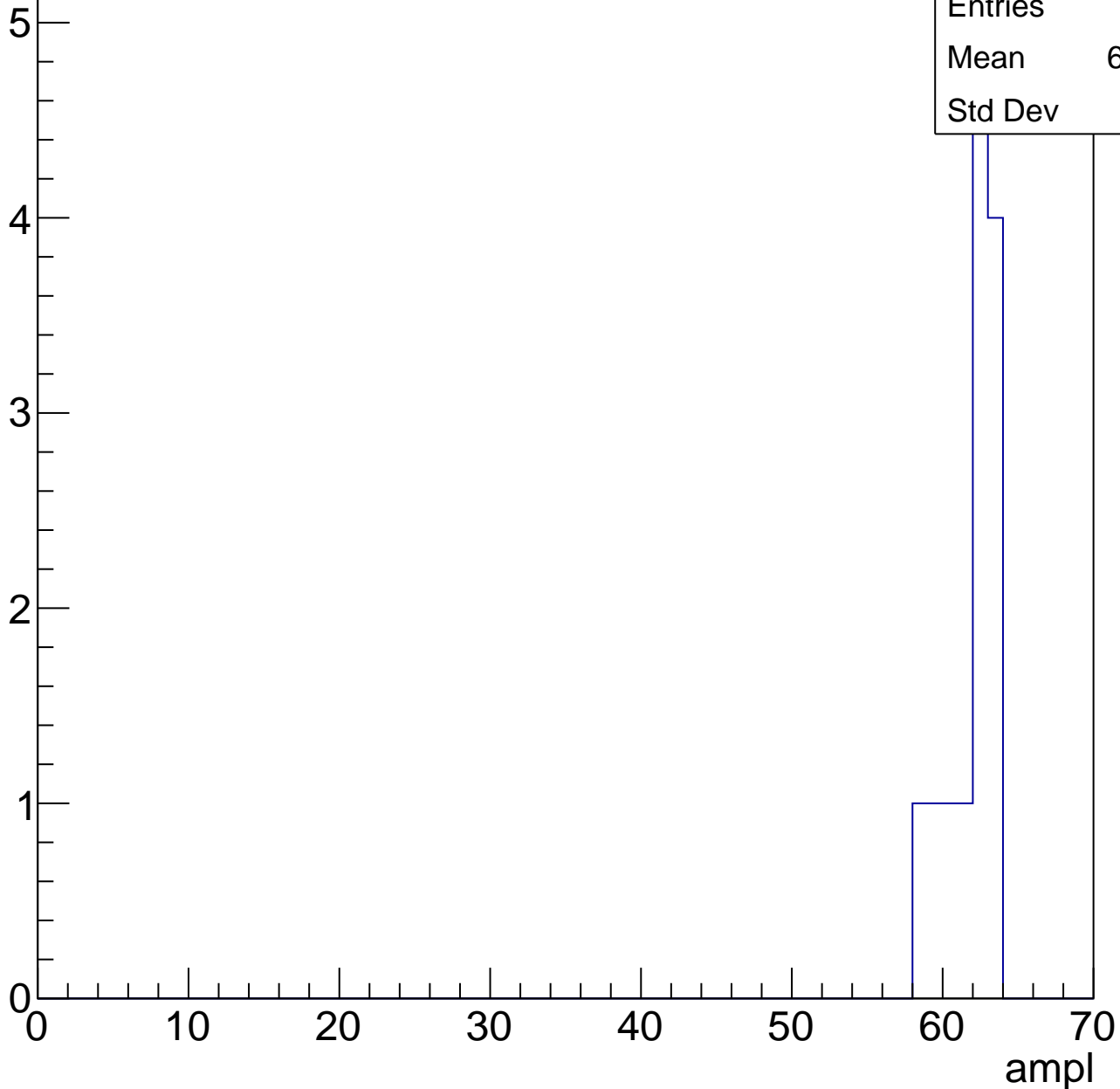


B1L103S, U20-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

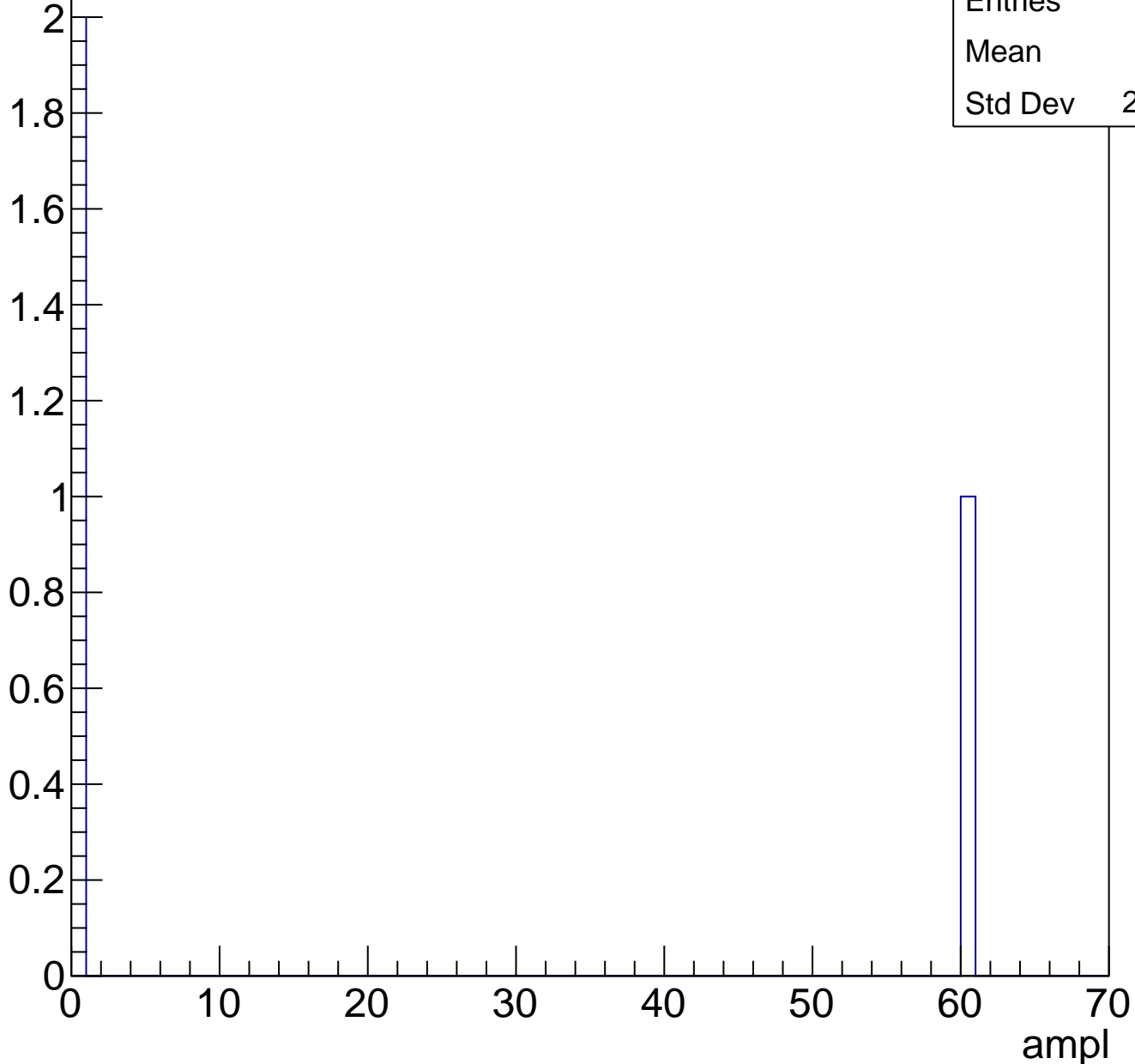
Entries	13
Mean	61.54
Std Dev	1.55



B1L103S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

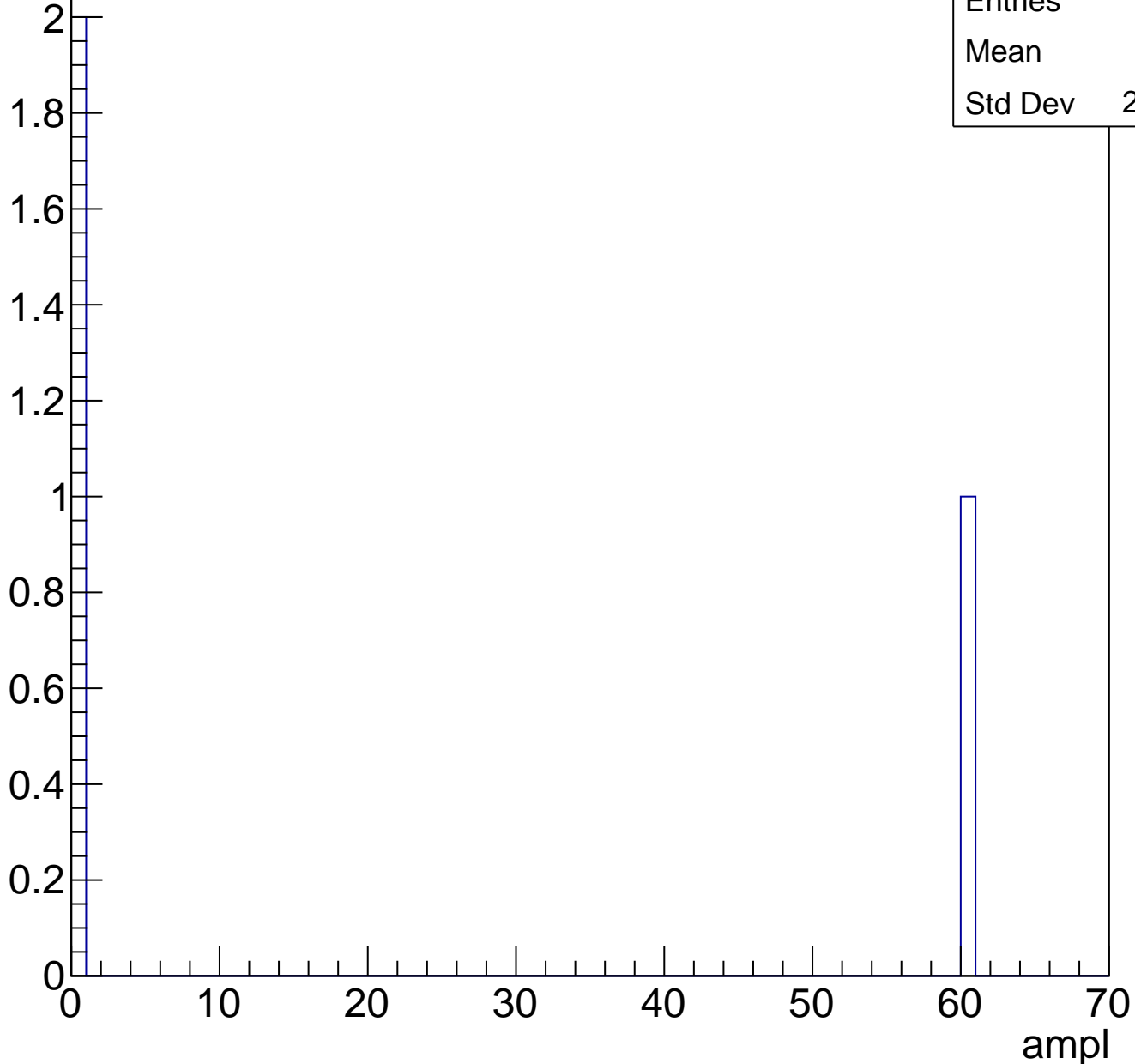


Entries	3
Mean	20
Std Dev	28.28

B1L103S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20
Std Dev	28.28