

B0L002S, U7-ch0, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	31.11
Std Dev	5.104

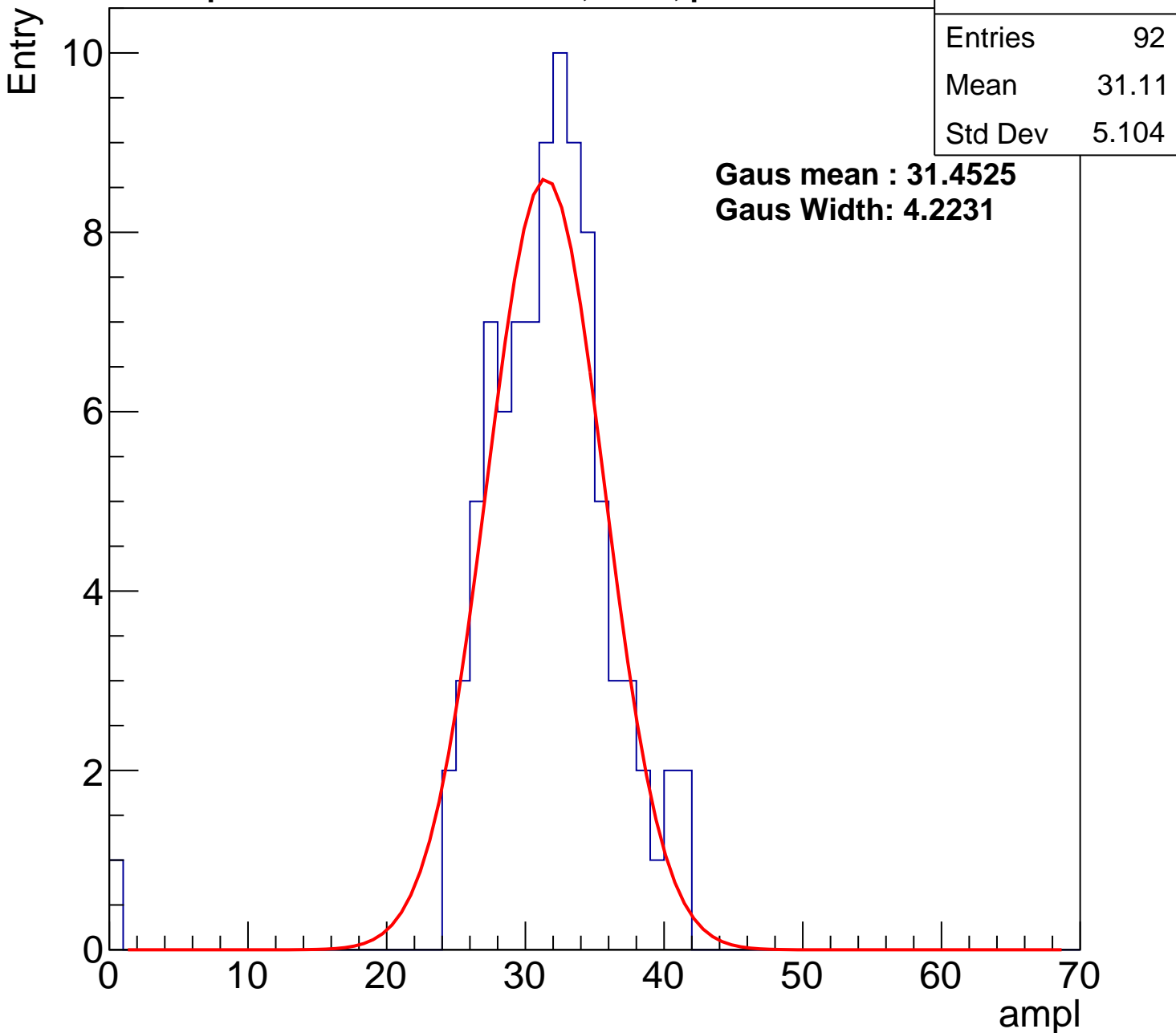
Gaus mean : 31.4525
Gaus Width: 4.2231

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch0, adc1

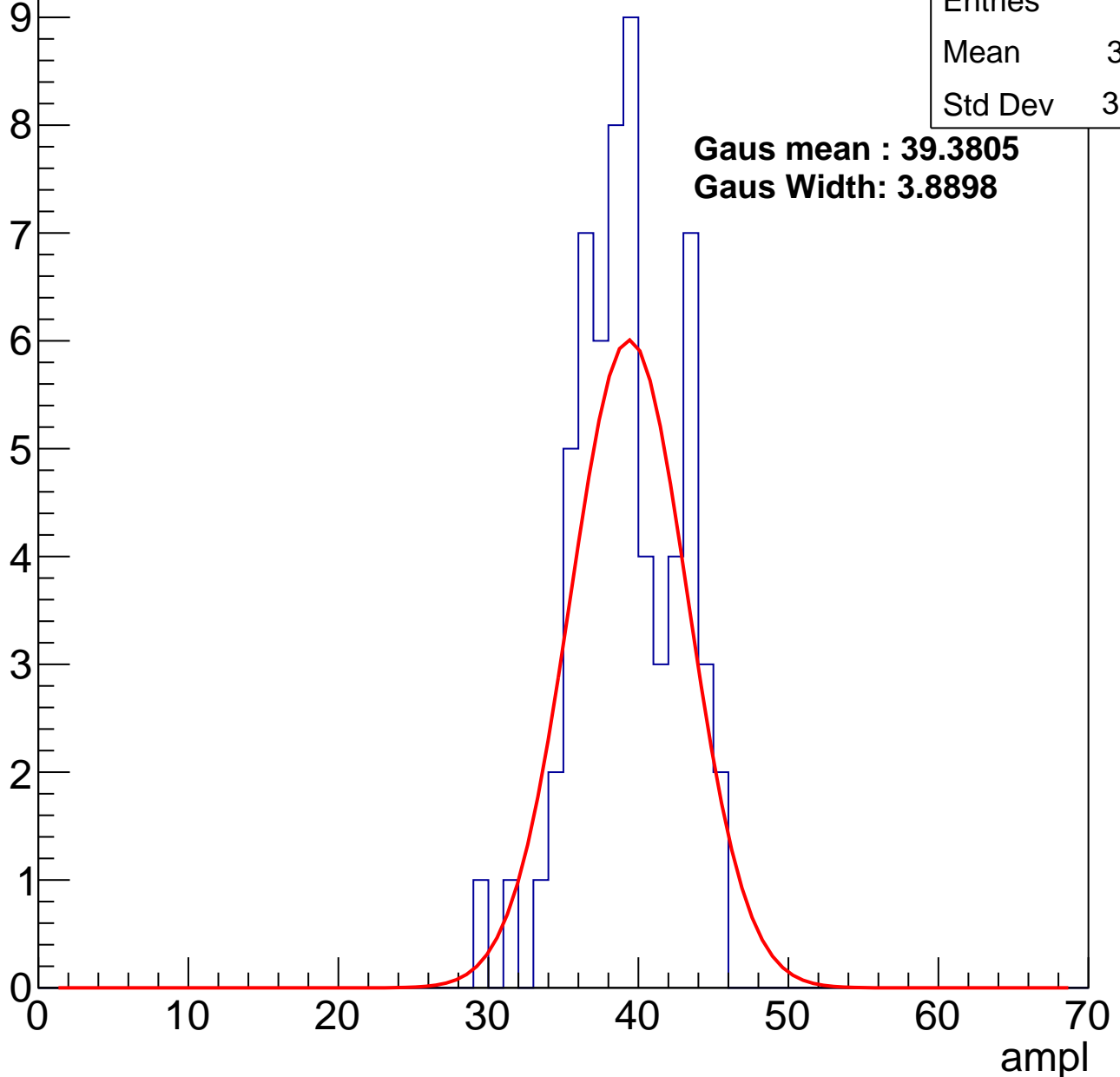
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.71
Std Dev	3.406

Gaus mean : 39.3805

Gaus Width: 3.8898



B0L002S, U7-ch0, adc2

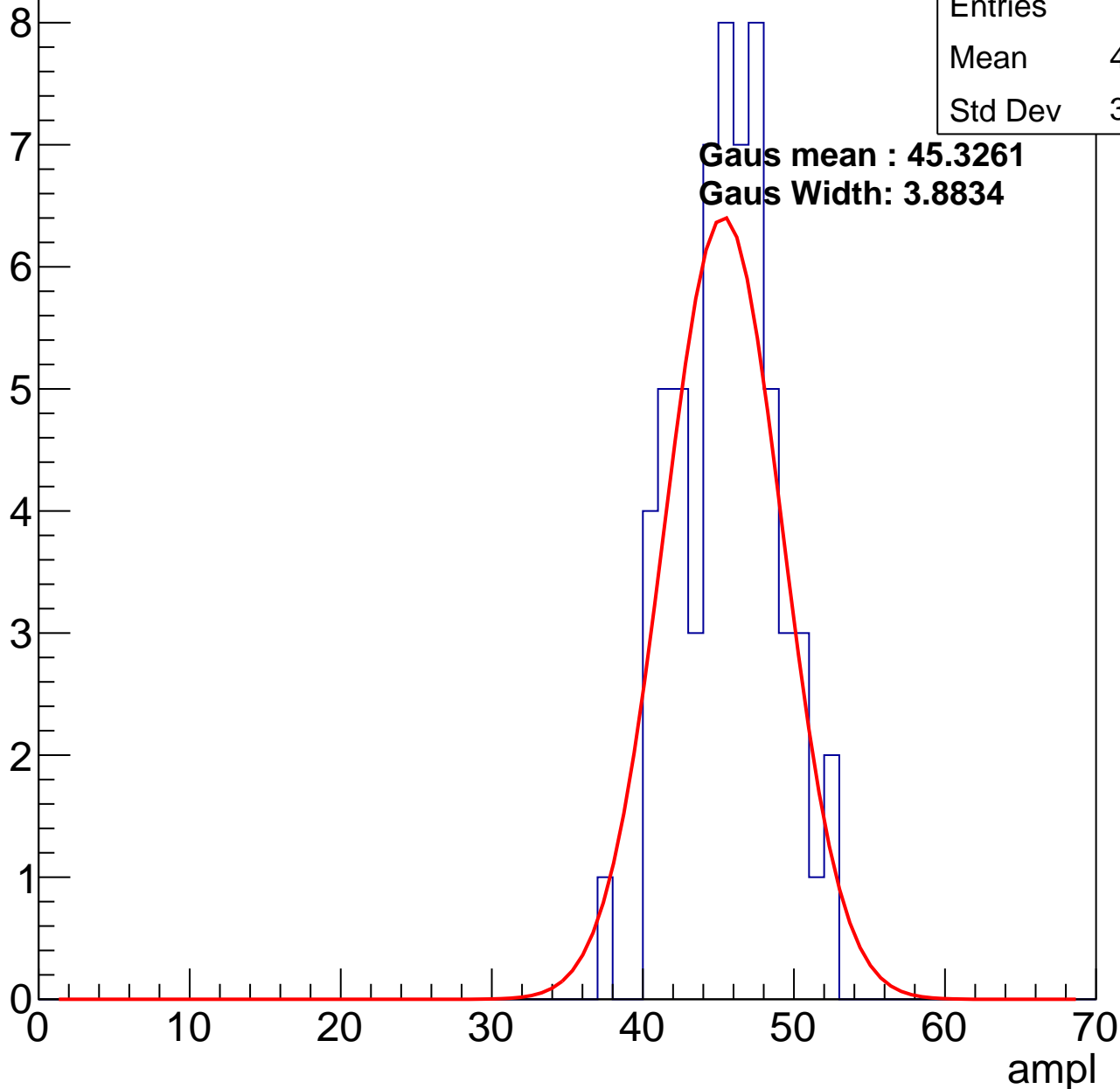
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.15
Std Dev	3.232

Gaus mean : 45.3261

Gaus Width: 3.8834

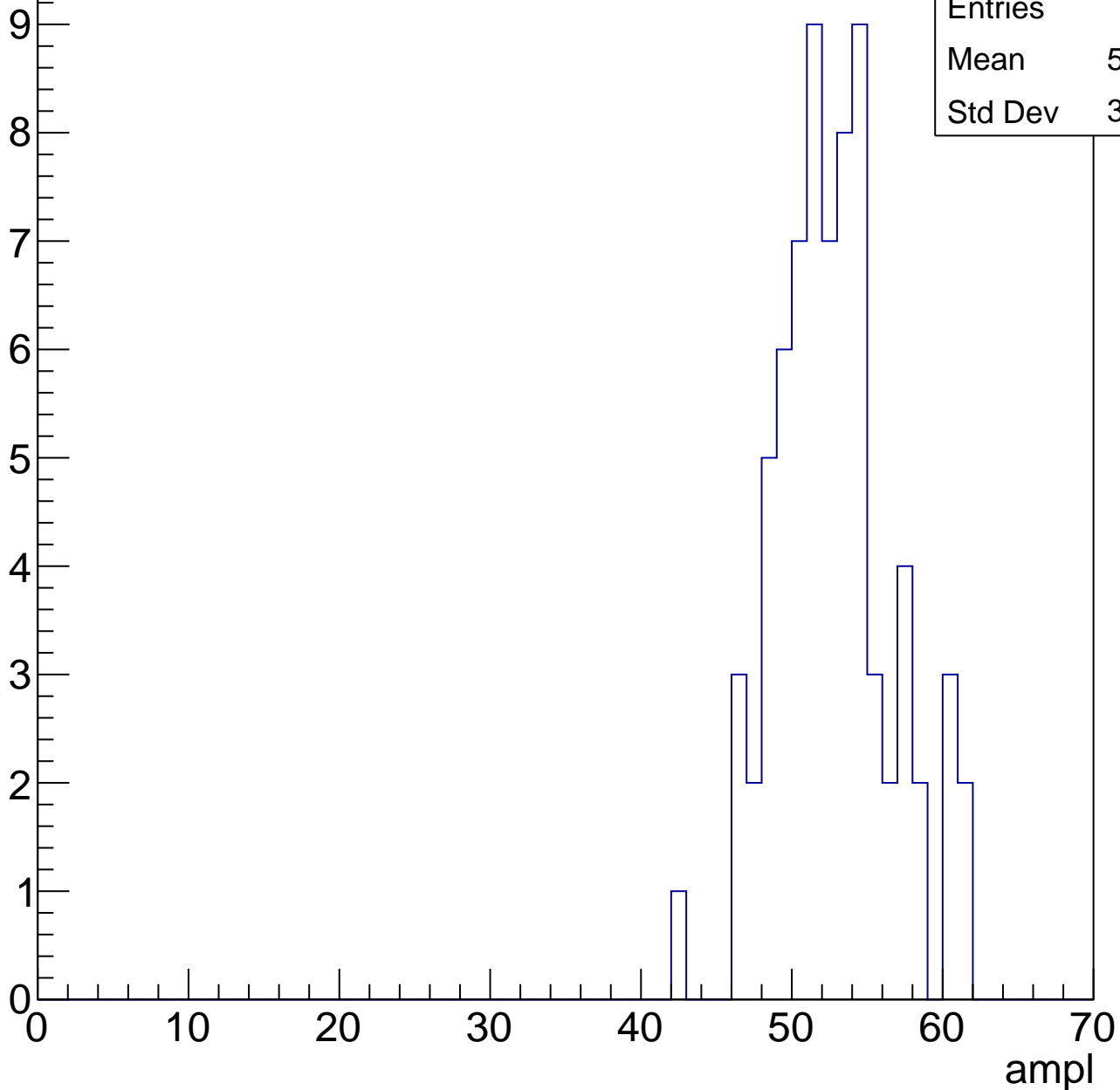


B0L002S, U7-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

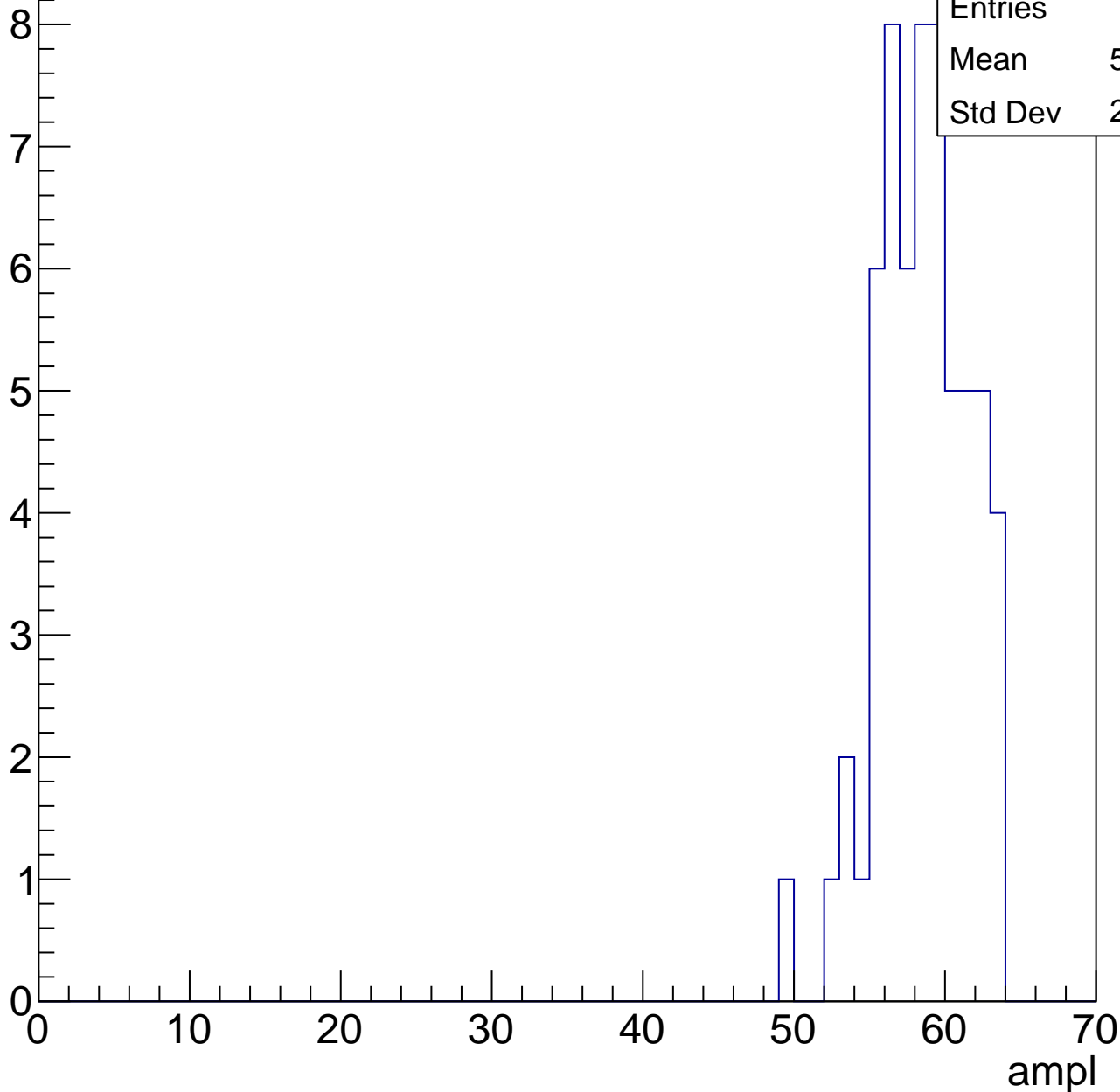
Entries	73
Mean	52.25
Std Dev	3.799



B0L002S, U7-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

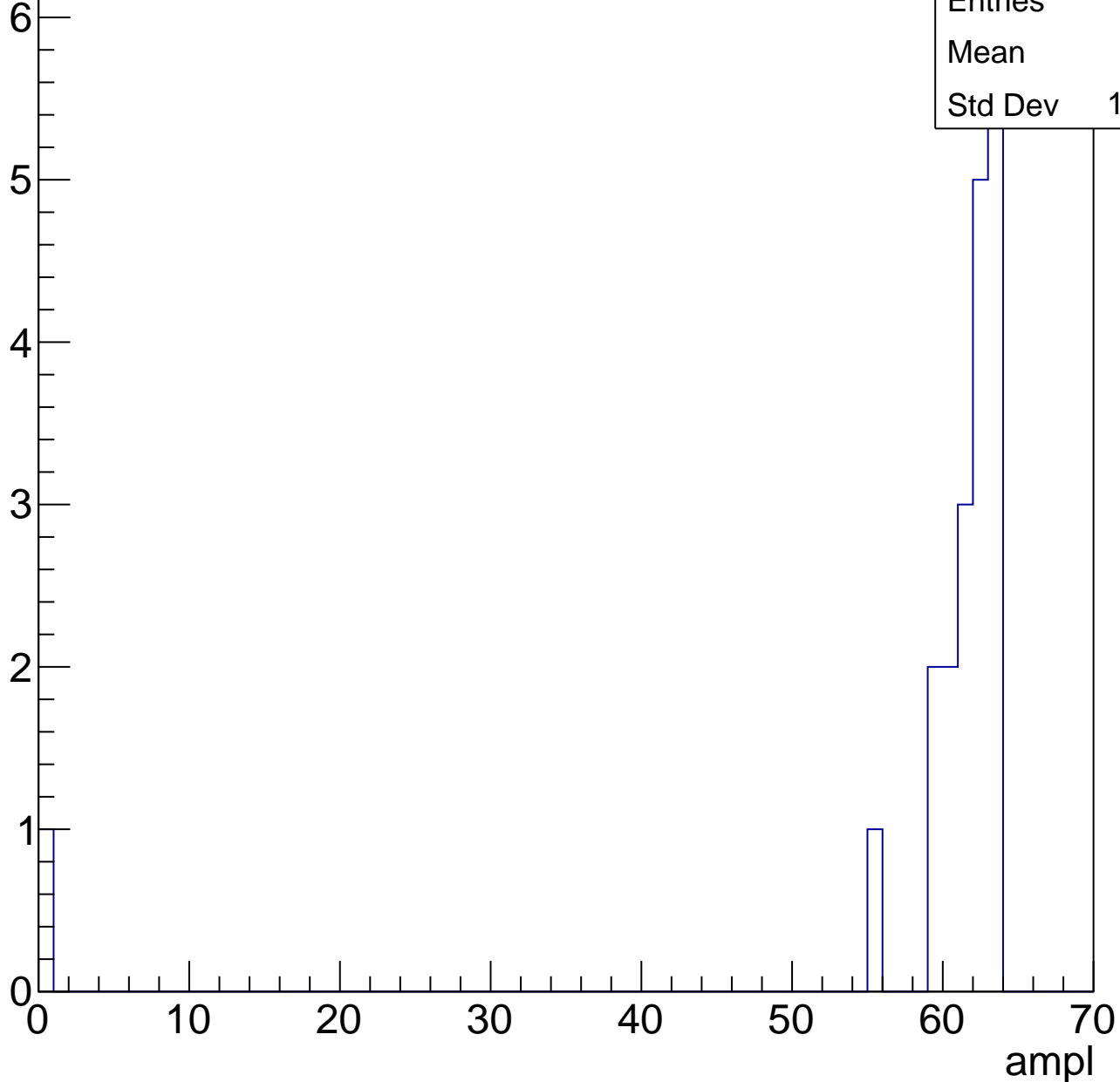


B0L002S, U7-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.2
Std Dev	13.49



B0L002S, U7-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch1, adc0

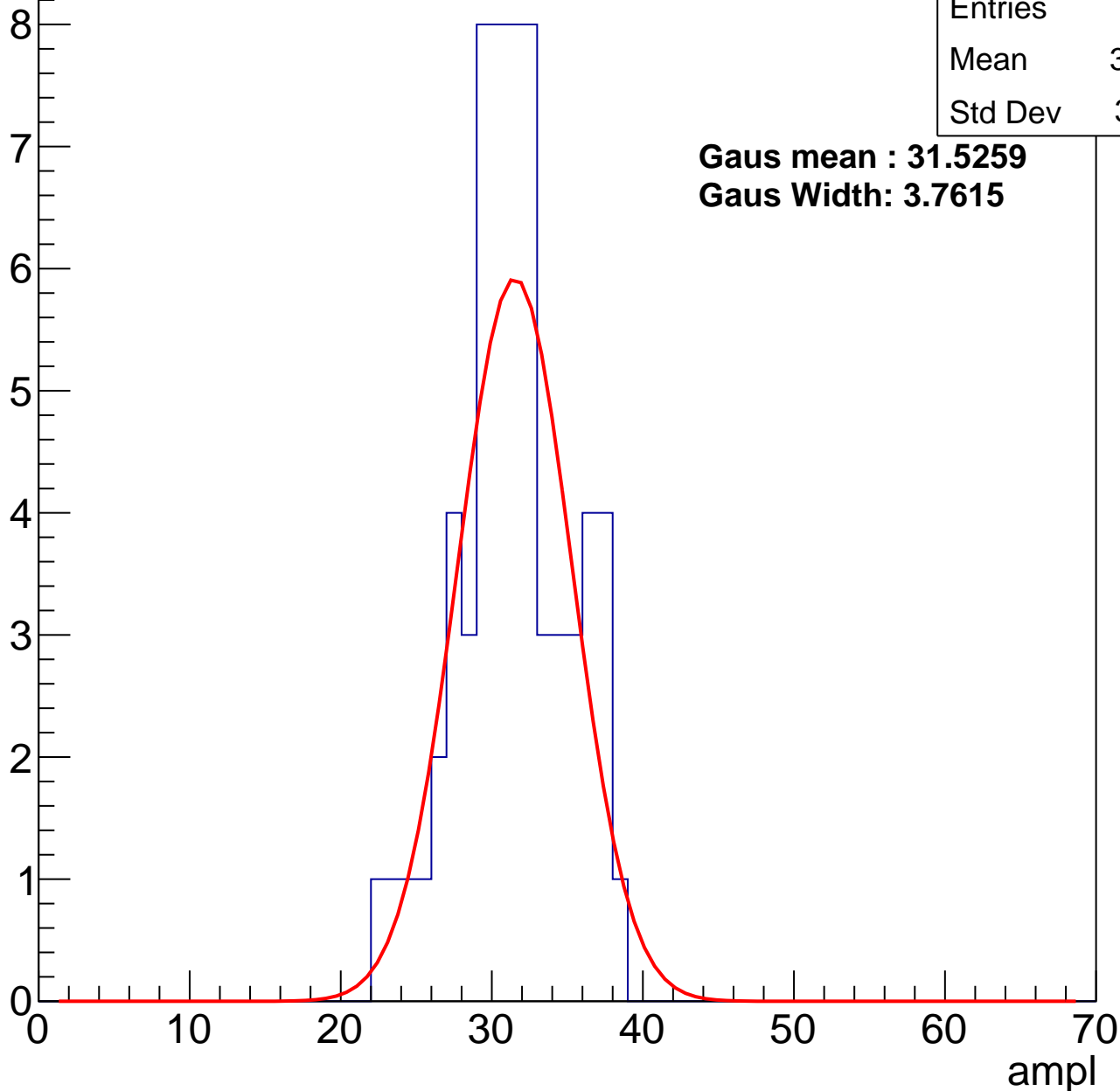
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.95
Std Dev	3.561

Gaus mean : 31.5259

Gaus Width: 3.7615



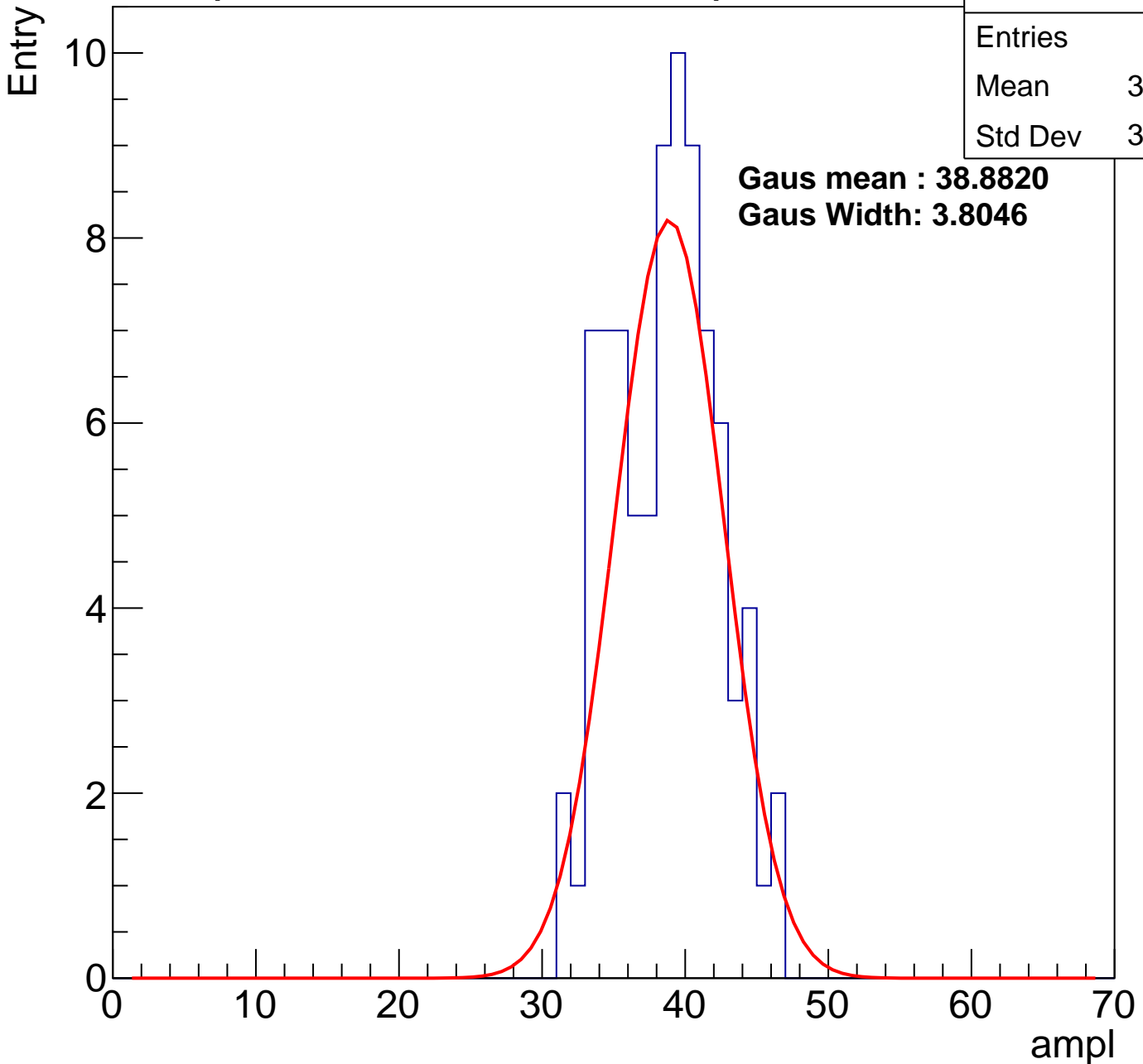
B0L002S, U7-ch1, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	38.19
Std Dev	3.609

Gaus mean : 38.8820

Gaus Width: 3.8046



B0L002S, U7-ch1, adc2

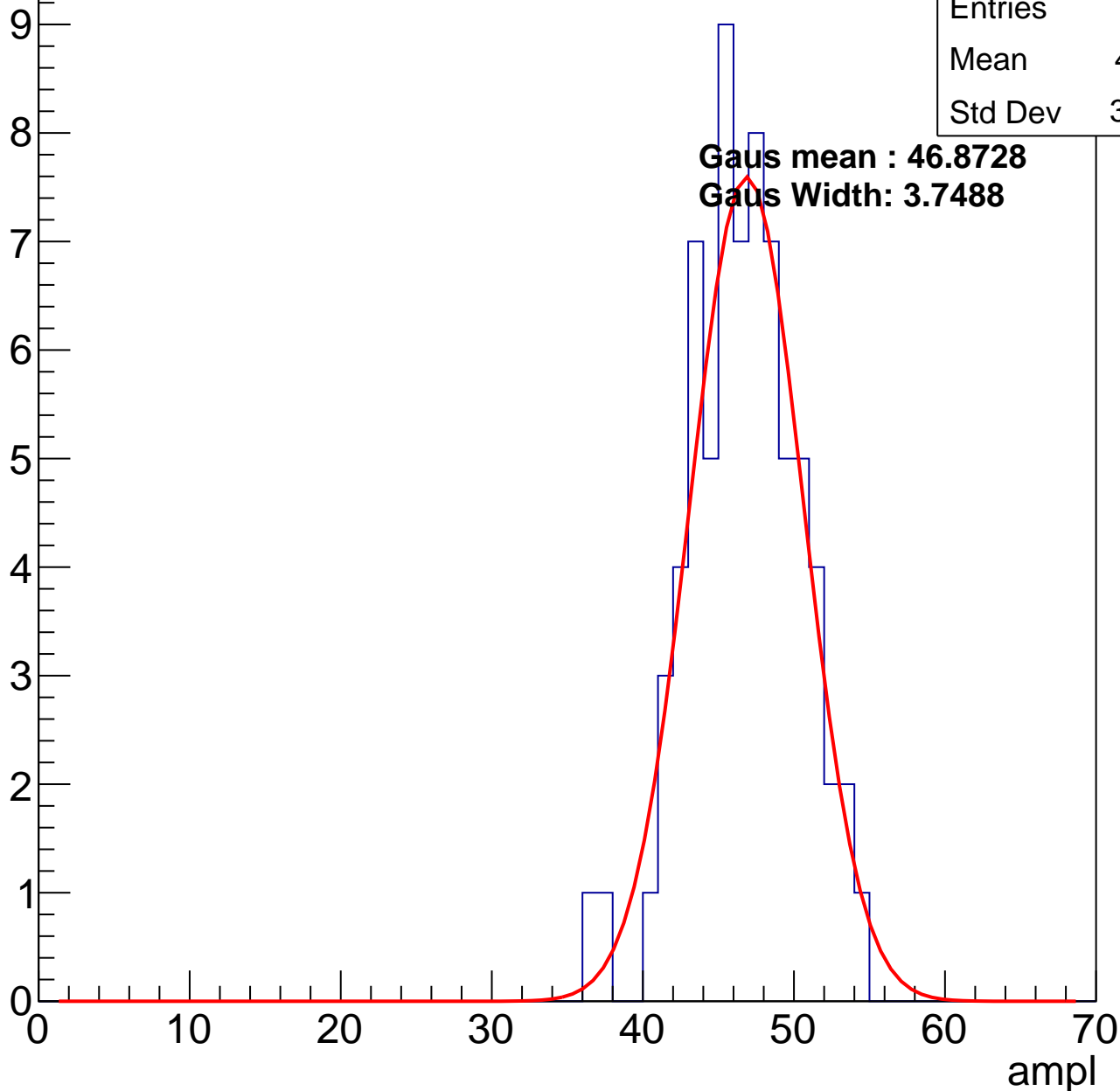
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	46.21
Std Dev	3.613

Gaus mean : 46.8728

Gaus Width: 3.7488

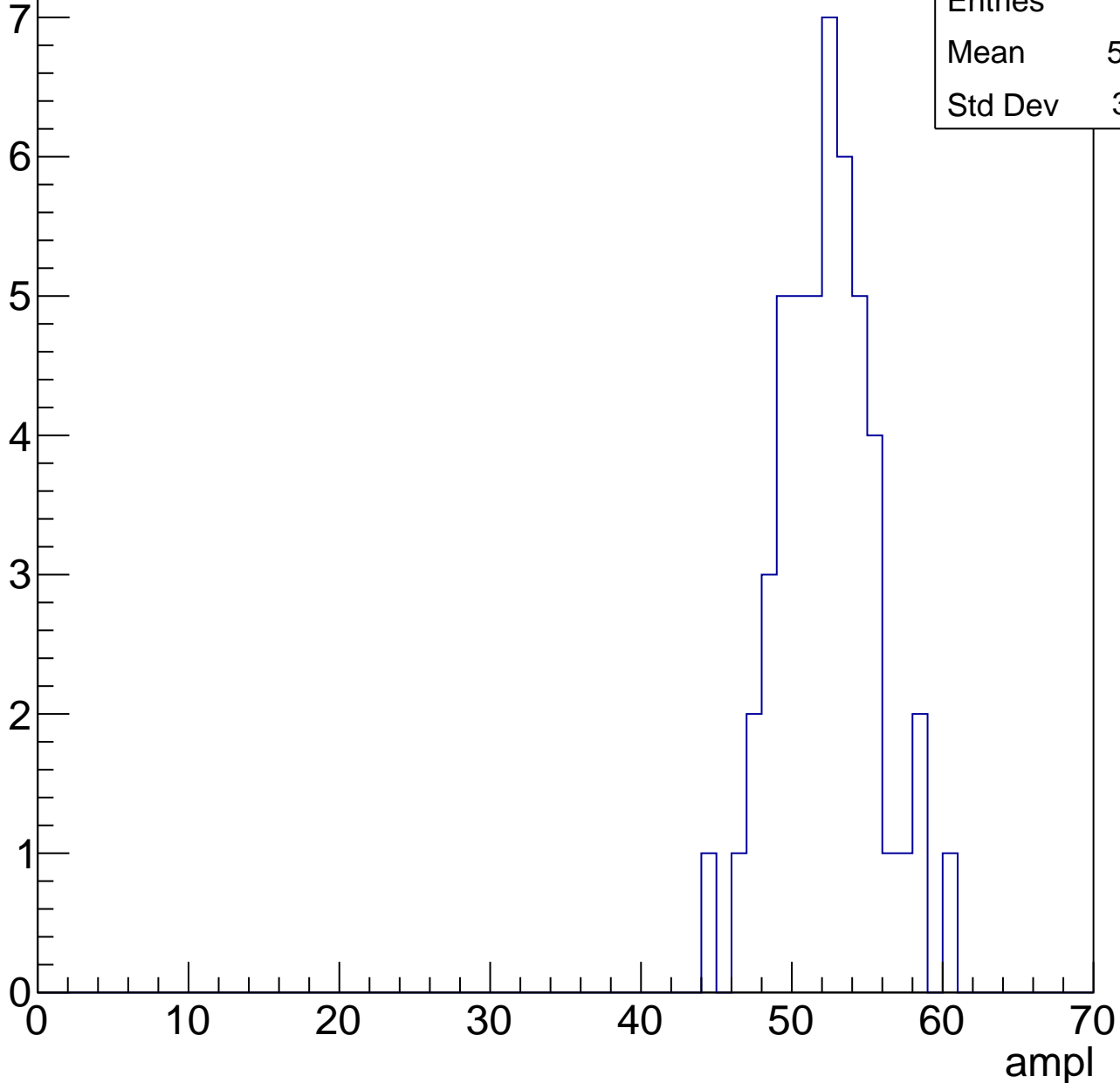


B0L002S, U7-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

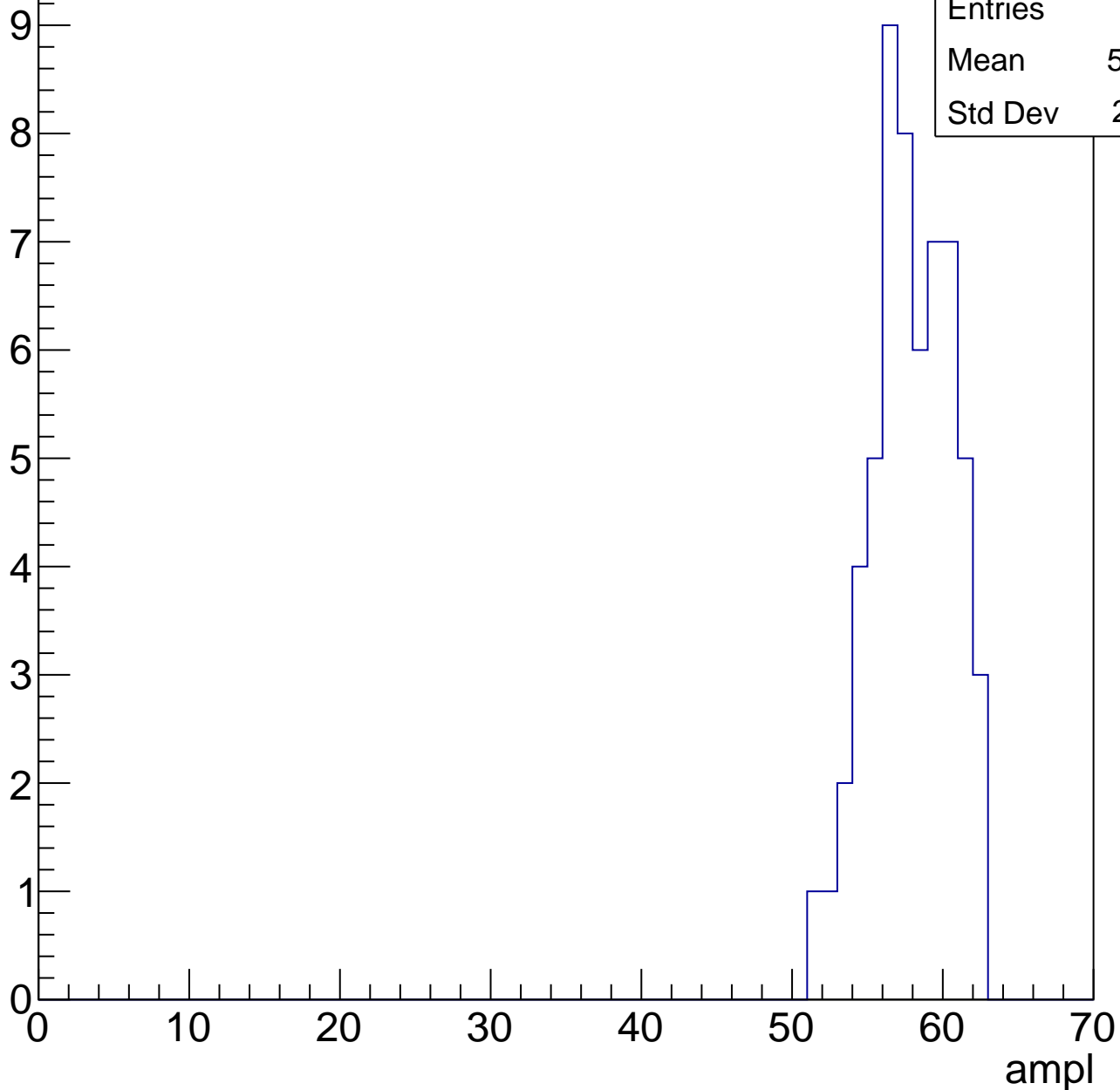
Entries	49
Mean	51.82
Std Dev	3.211



B0L002S, U7-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

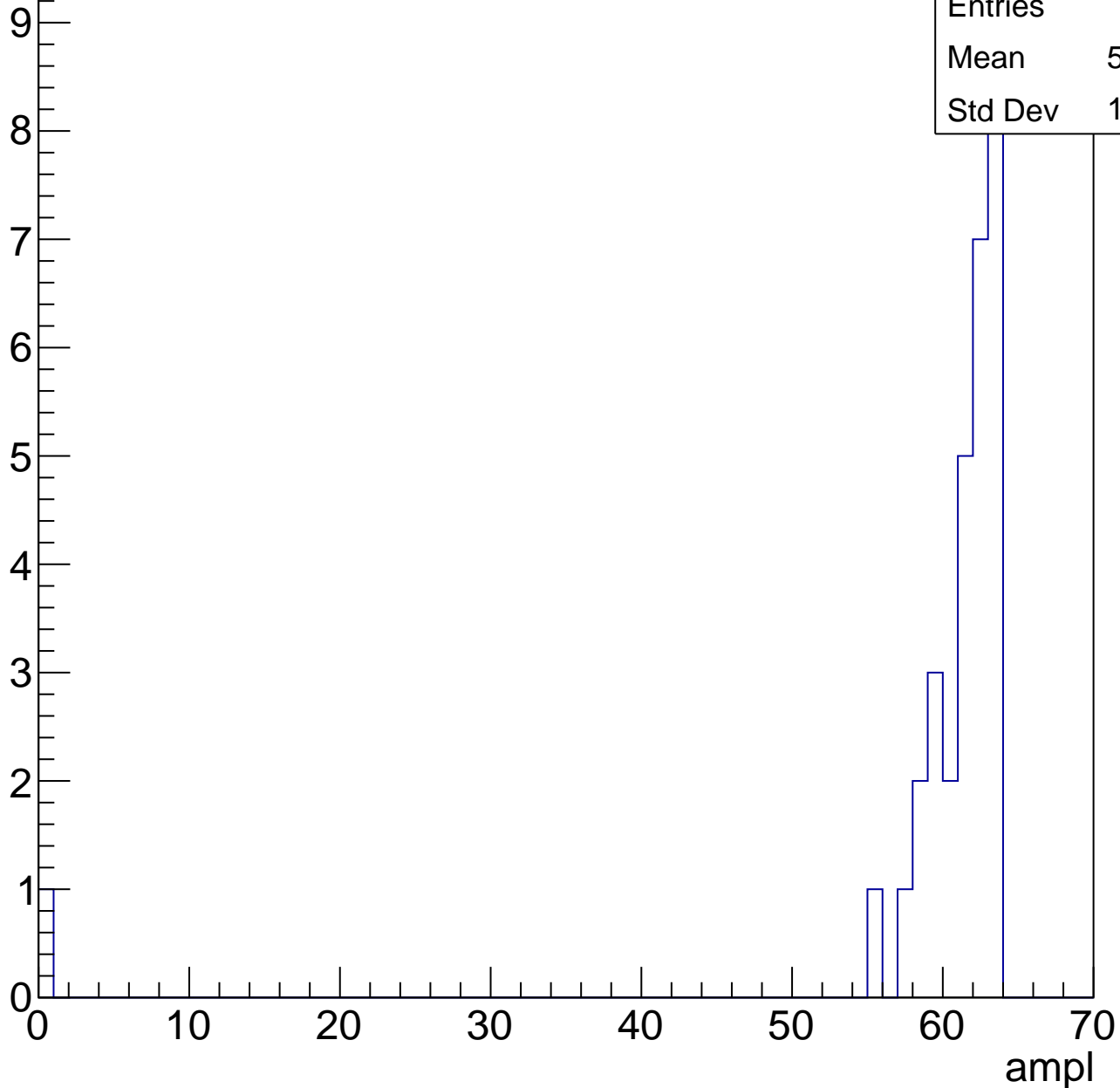


B0L002S, U7-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.06
Std Dev	10.97



B0L002S, U7-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

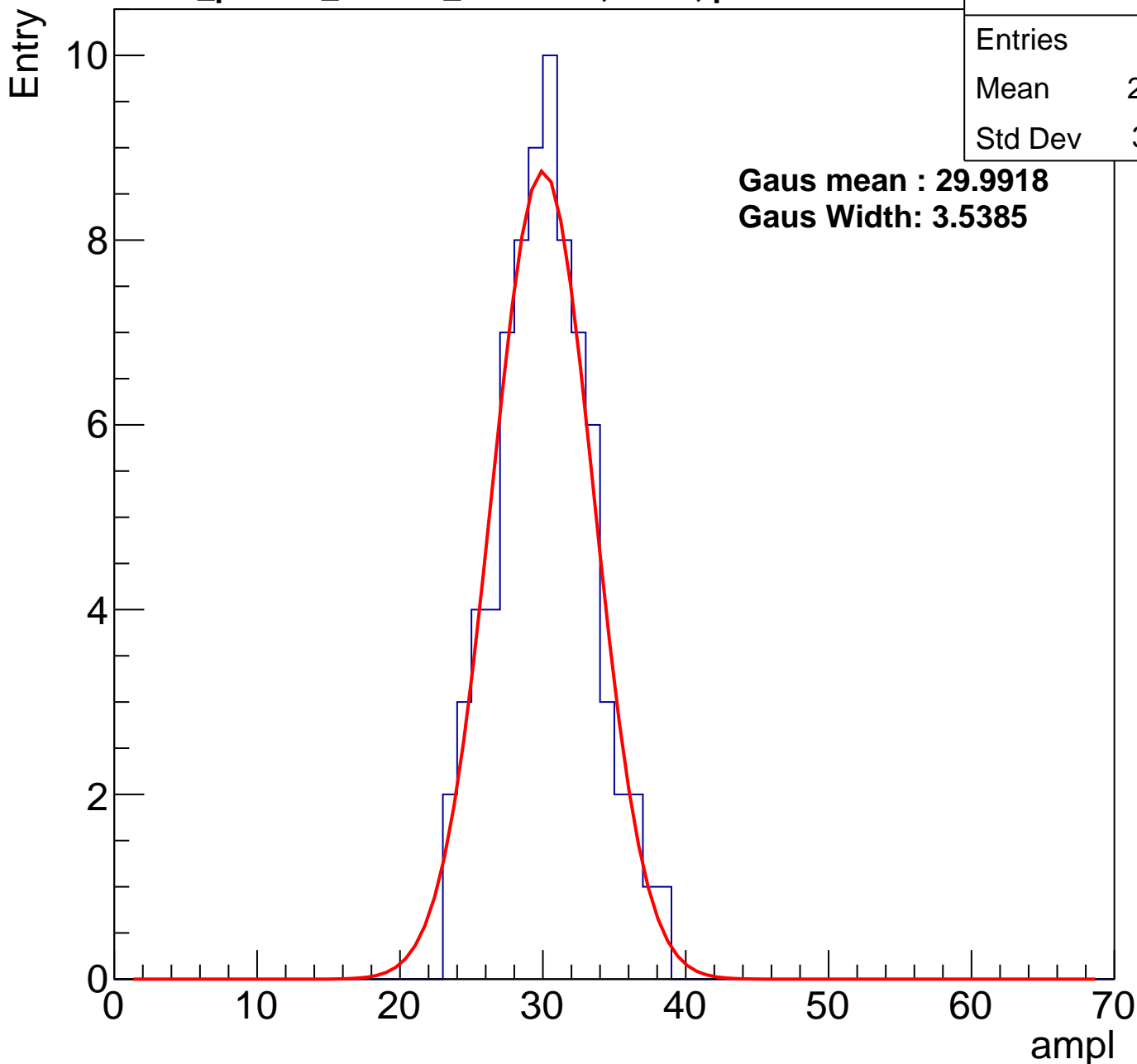
B0L002S, U7-ch2, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	29.68
Std Dev	3.301

Gaus mean : 29.9918

Gaus Width: 3.5385



B0L002S, U7-ch2, adc1

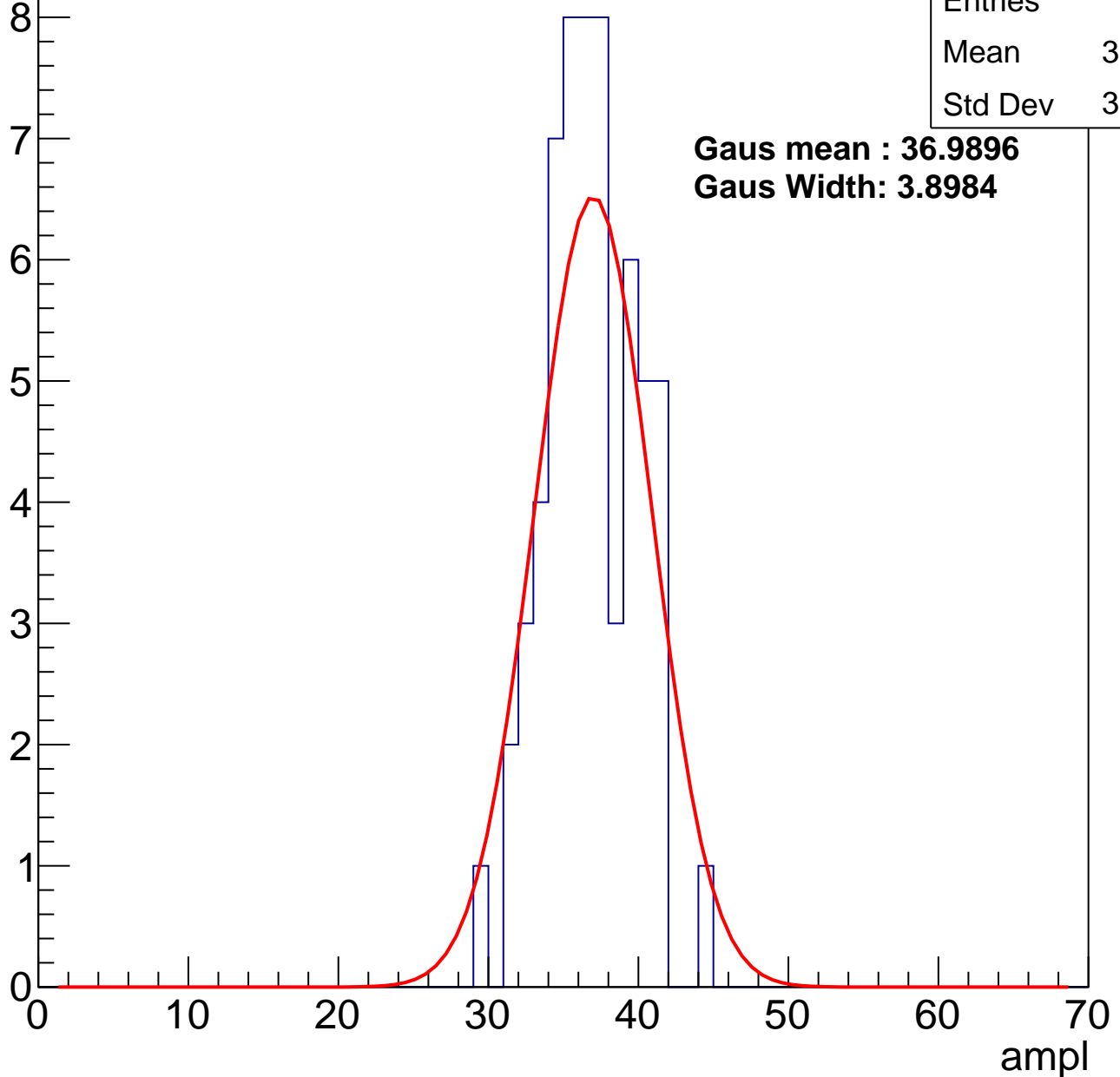
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.36
Std Dev	3.014

Gaus mean : 36.9896

Gaus Width: 3.8984



B0L002S, U7-ch2, adc2

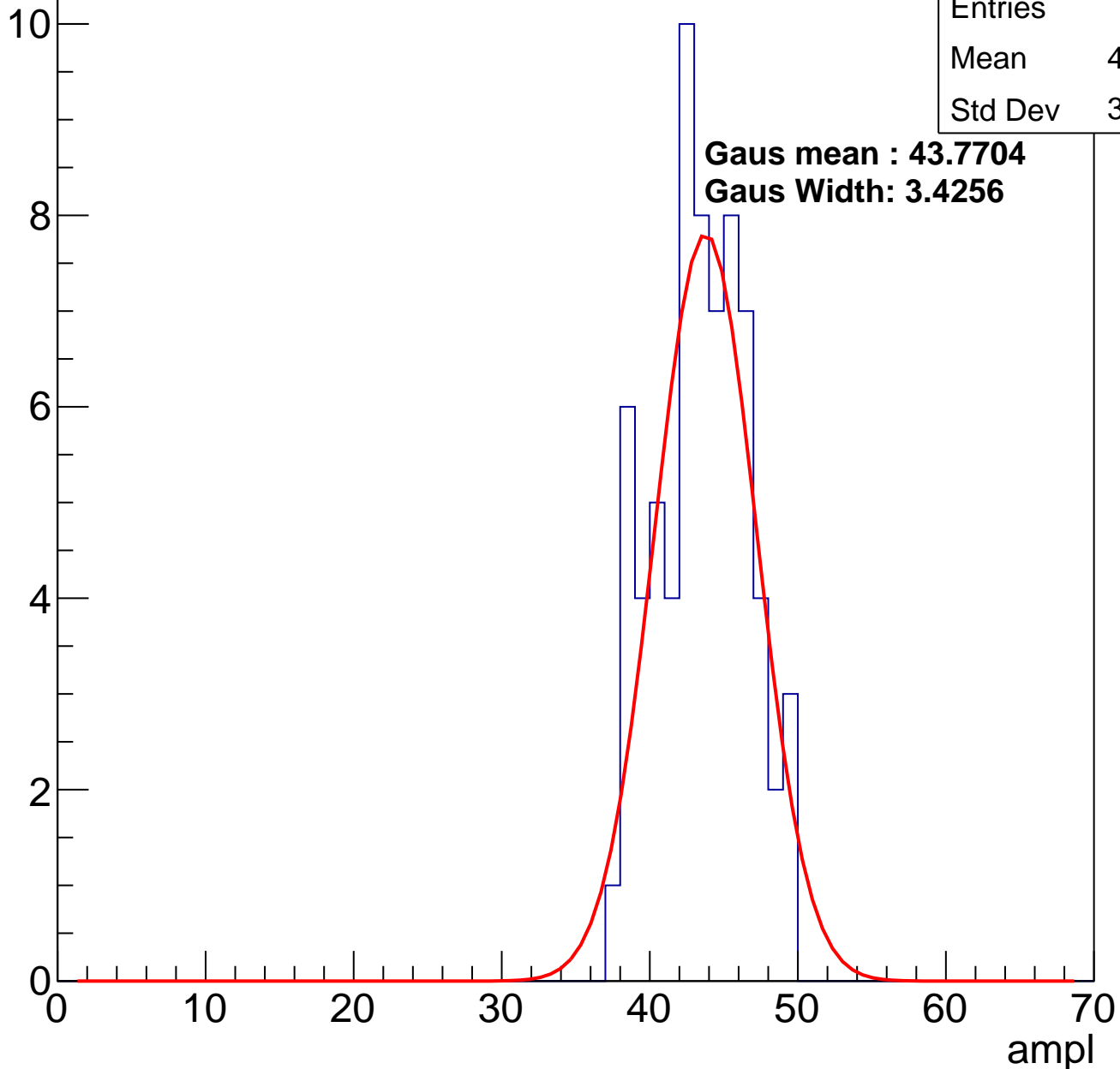
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	43.04
Std Dev	3.057

Gaus mean : 43.7704

Gaus Width: 3.4256

Entry



B0L002S, U7-ch2, adc3

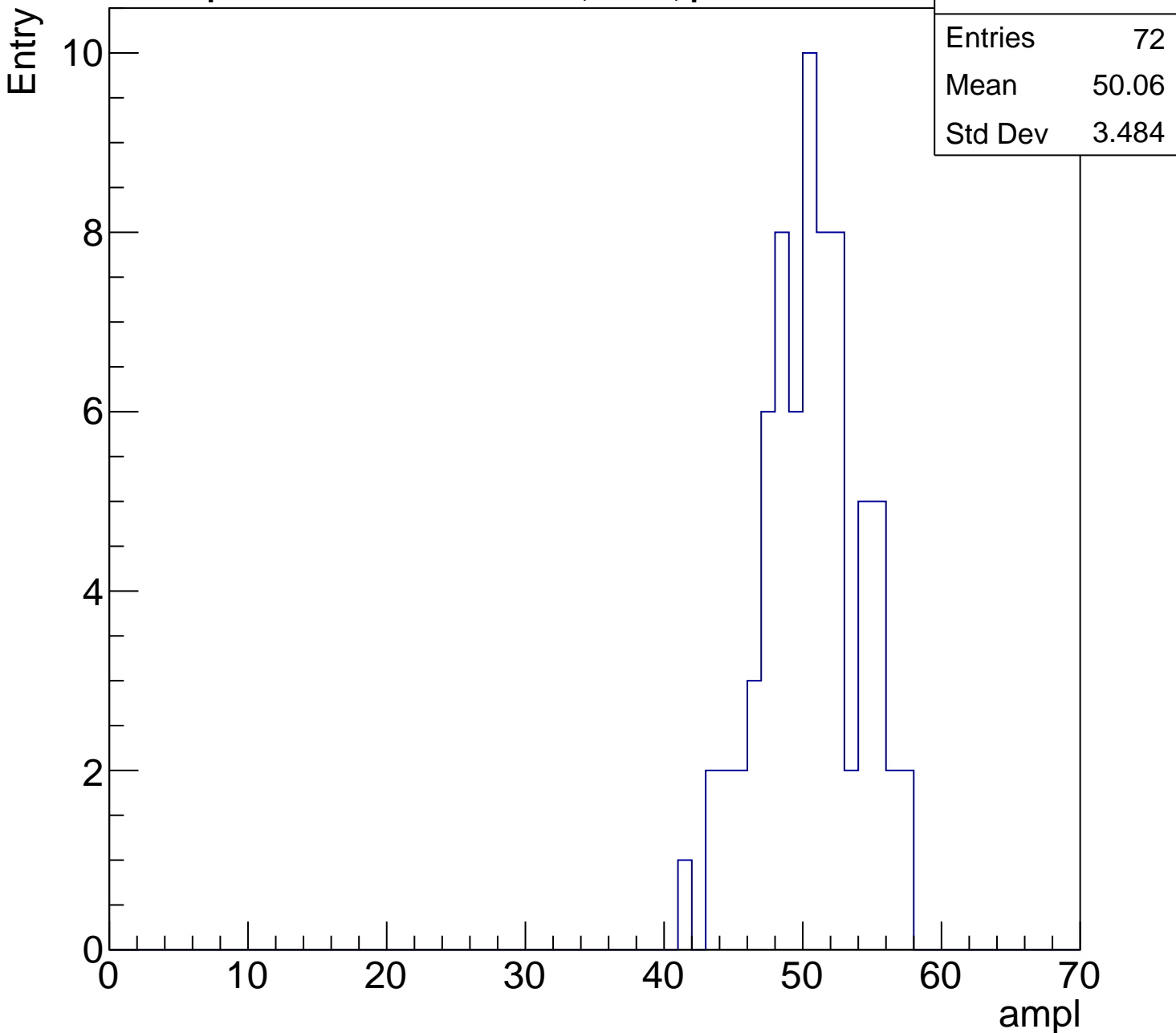
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	50.06
Std Dev	3.484

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

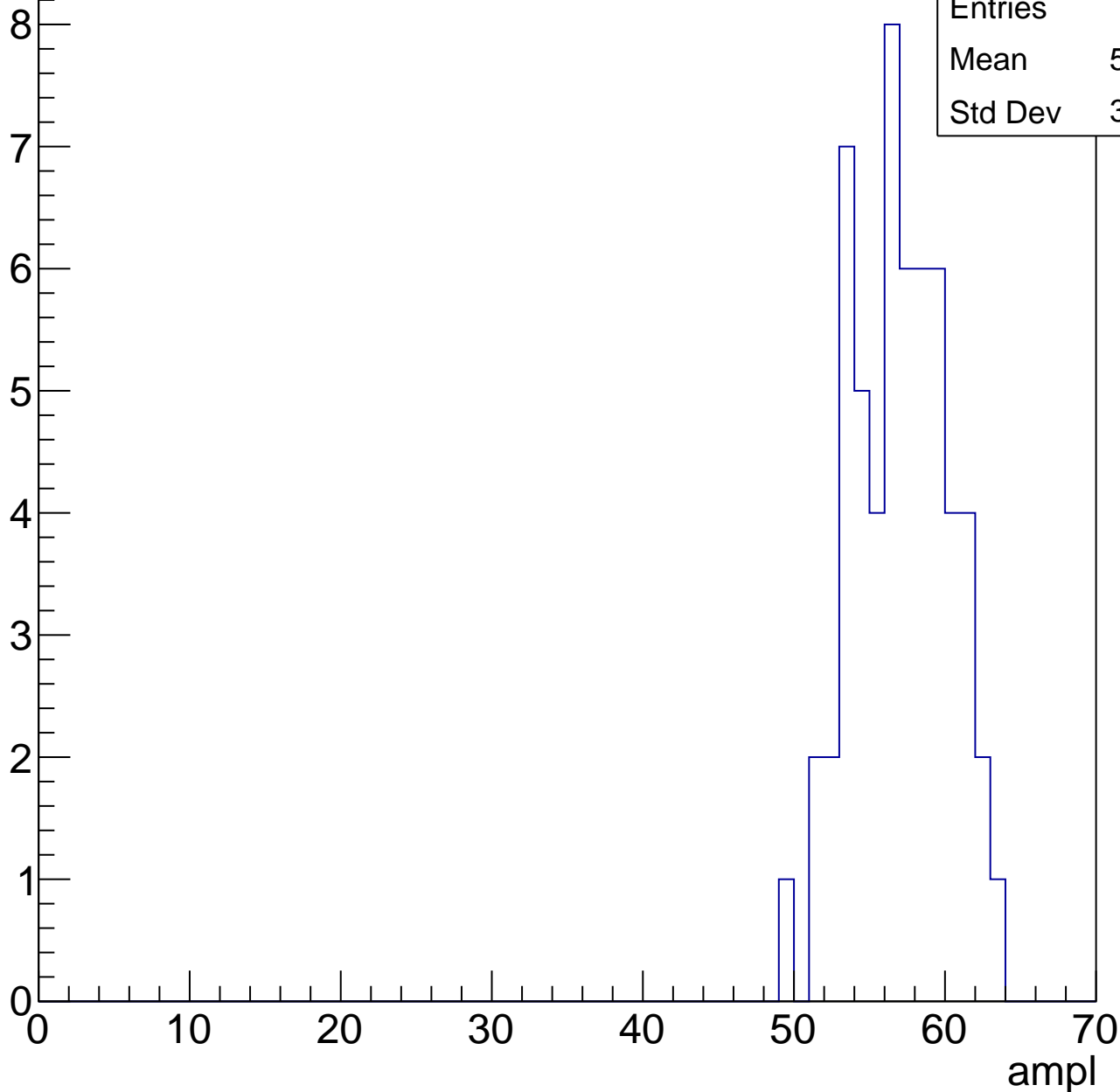


B0L002S, U7-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

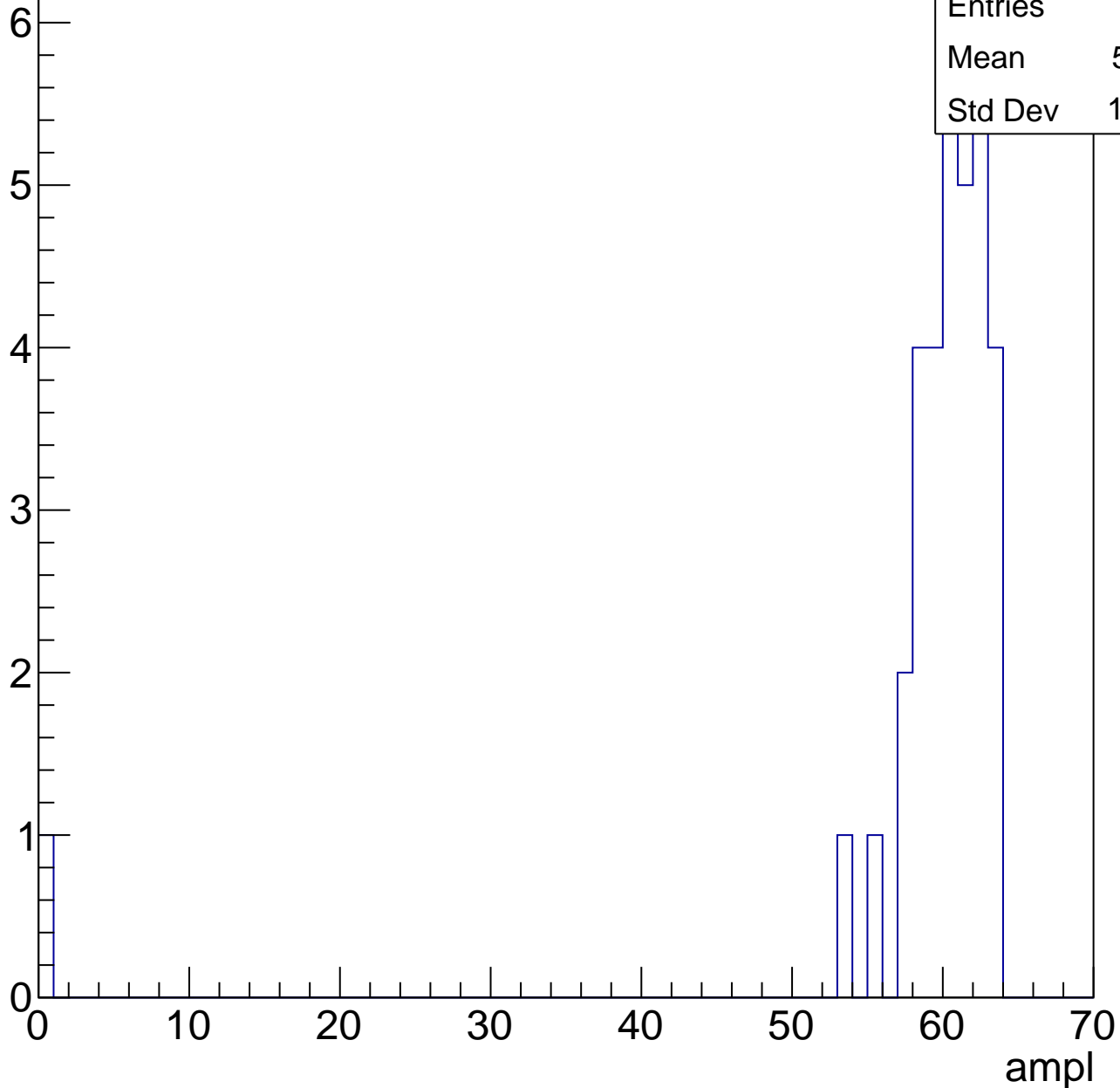
Entries	58
Mean	56.53
Std Dev	3.136



B0L002S, U7-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

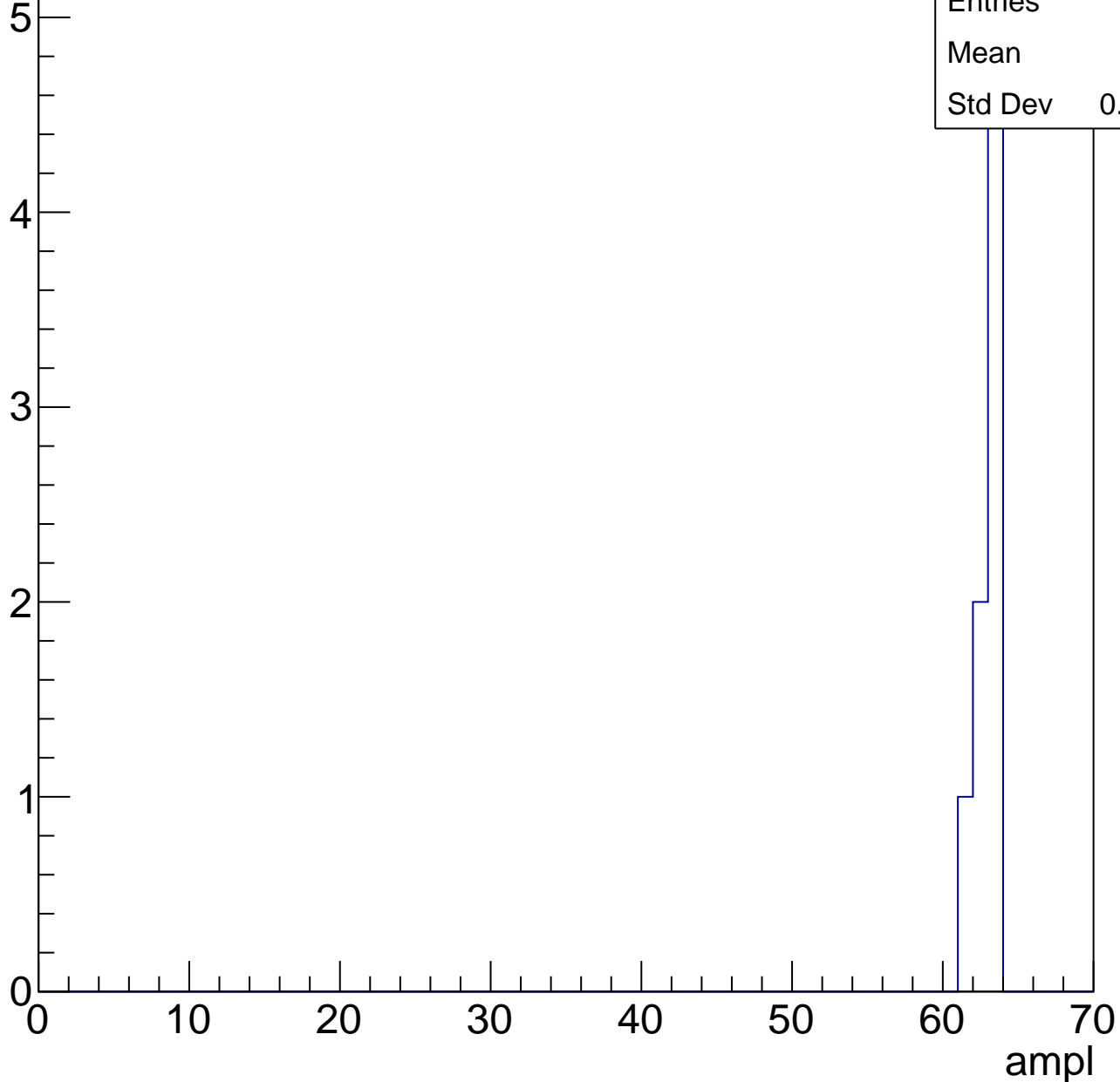


B0L002S, U7-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B0L002S, U7-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch3, adc0

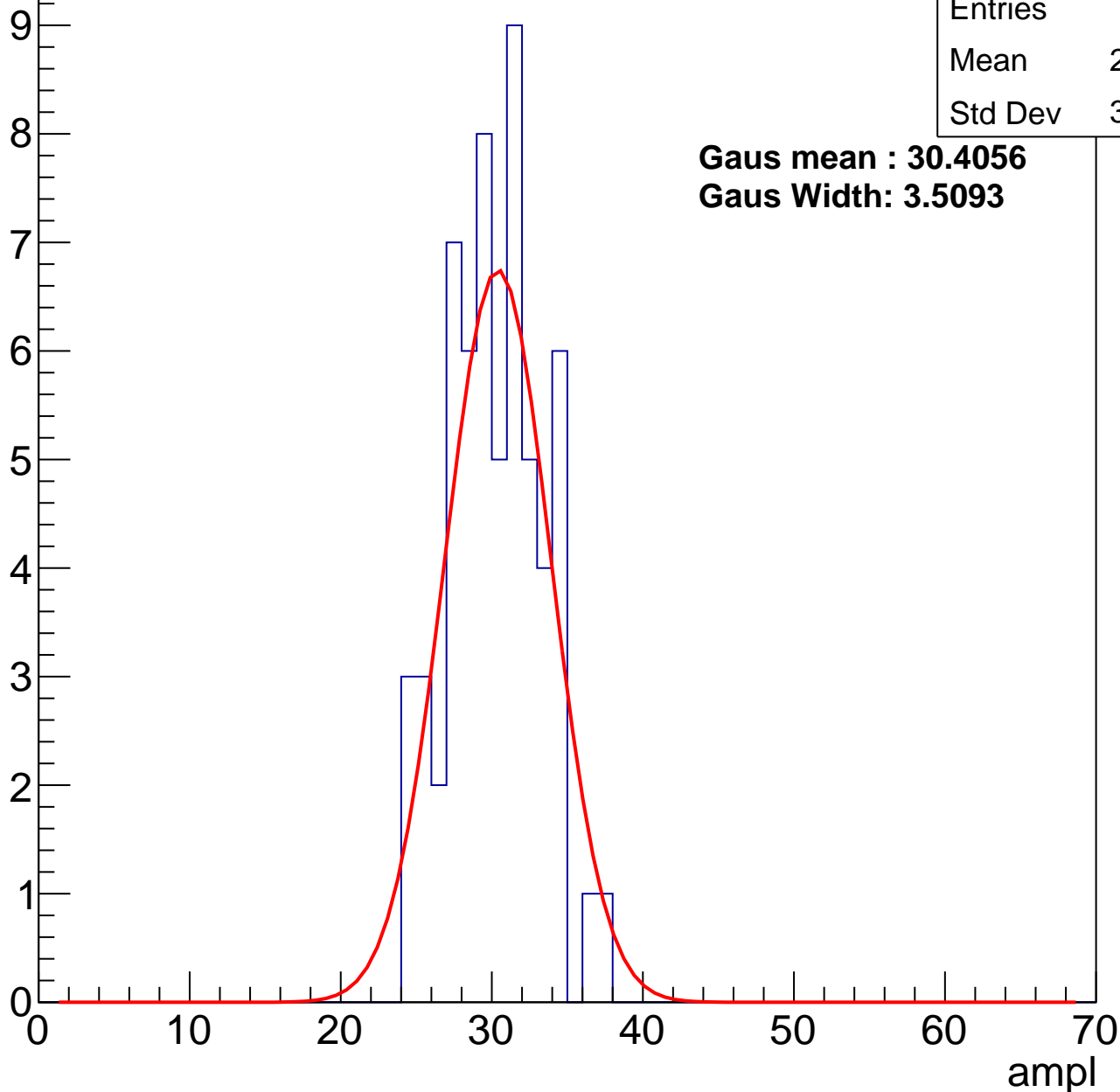
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.77
Std Dev	3.035

Gaus mean : 30.4056

Gaus Width: 3.5093



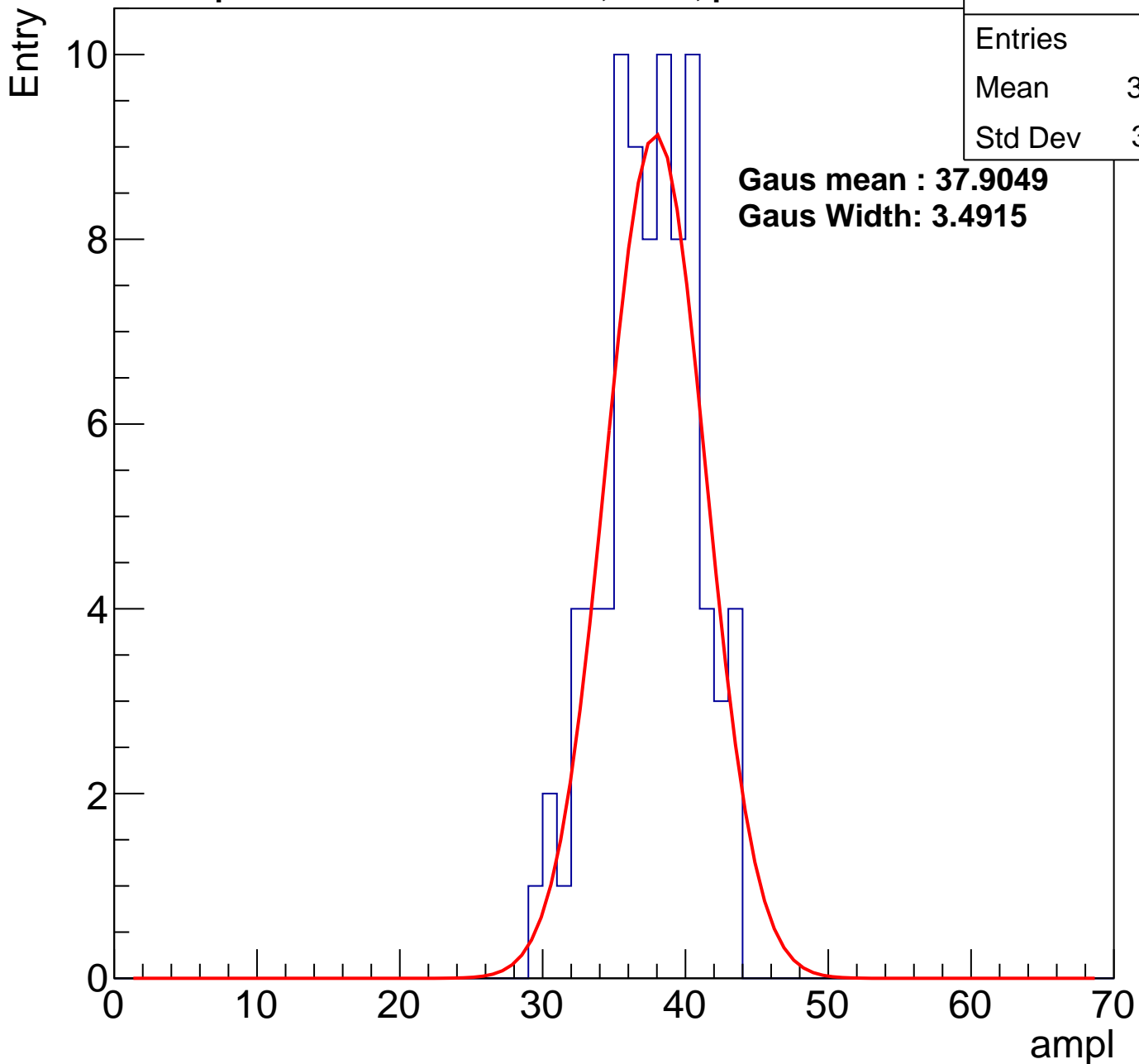
B0L002S, U7-ch3, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	37.07
Std Dev	3.241

Gaus mean : 37.9049

Gaus Width: 3.4915



B0L002S, U7-ch3, adc2

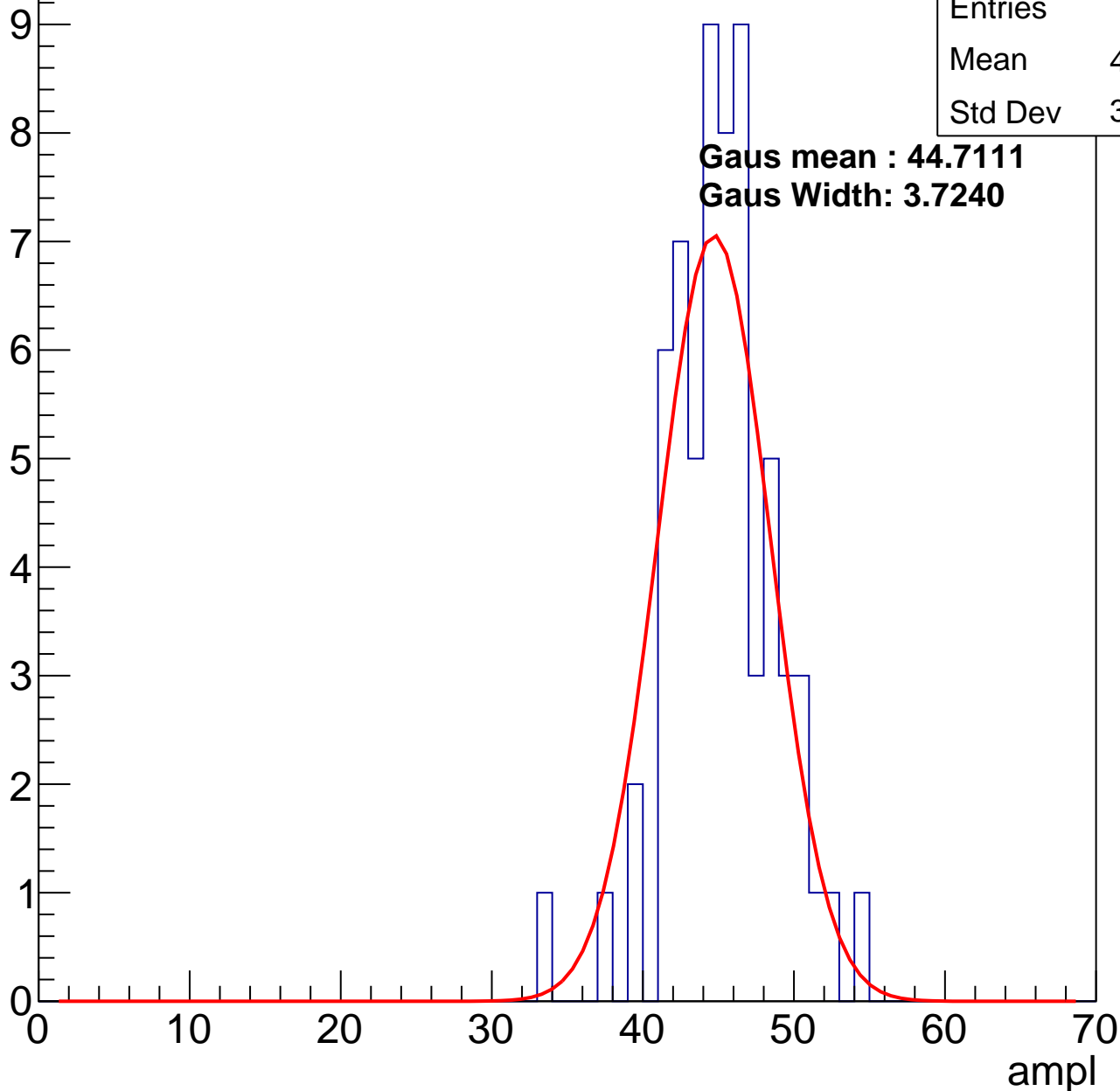
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.74
Std Dev	3.549

Gaus mean : 44.7111

Gaus Width: 3.7240

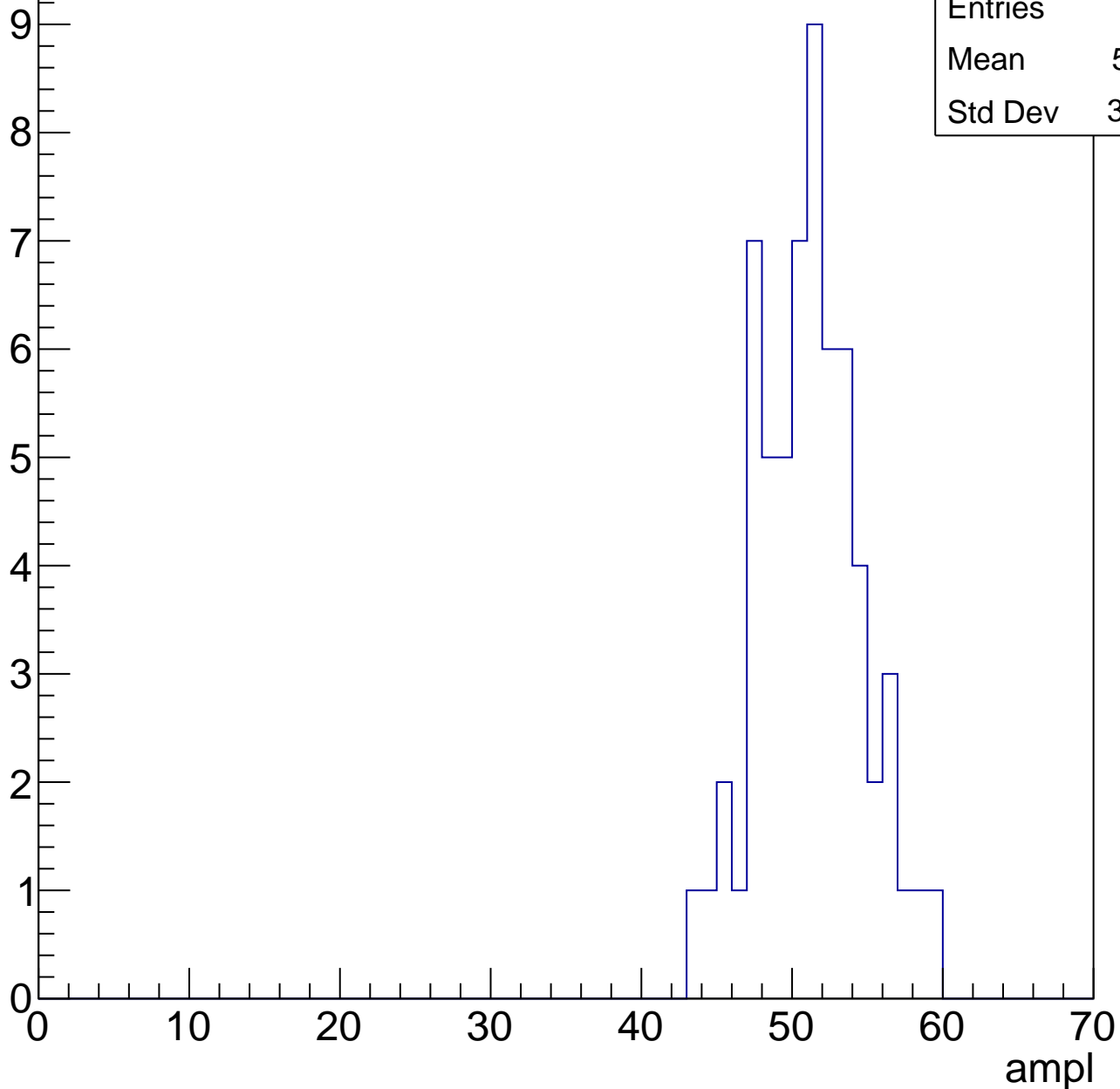


B0L002S, U7-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

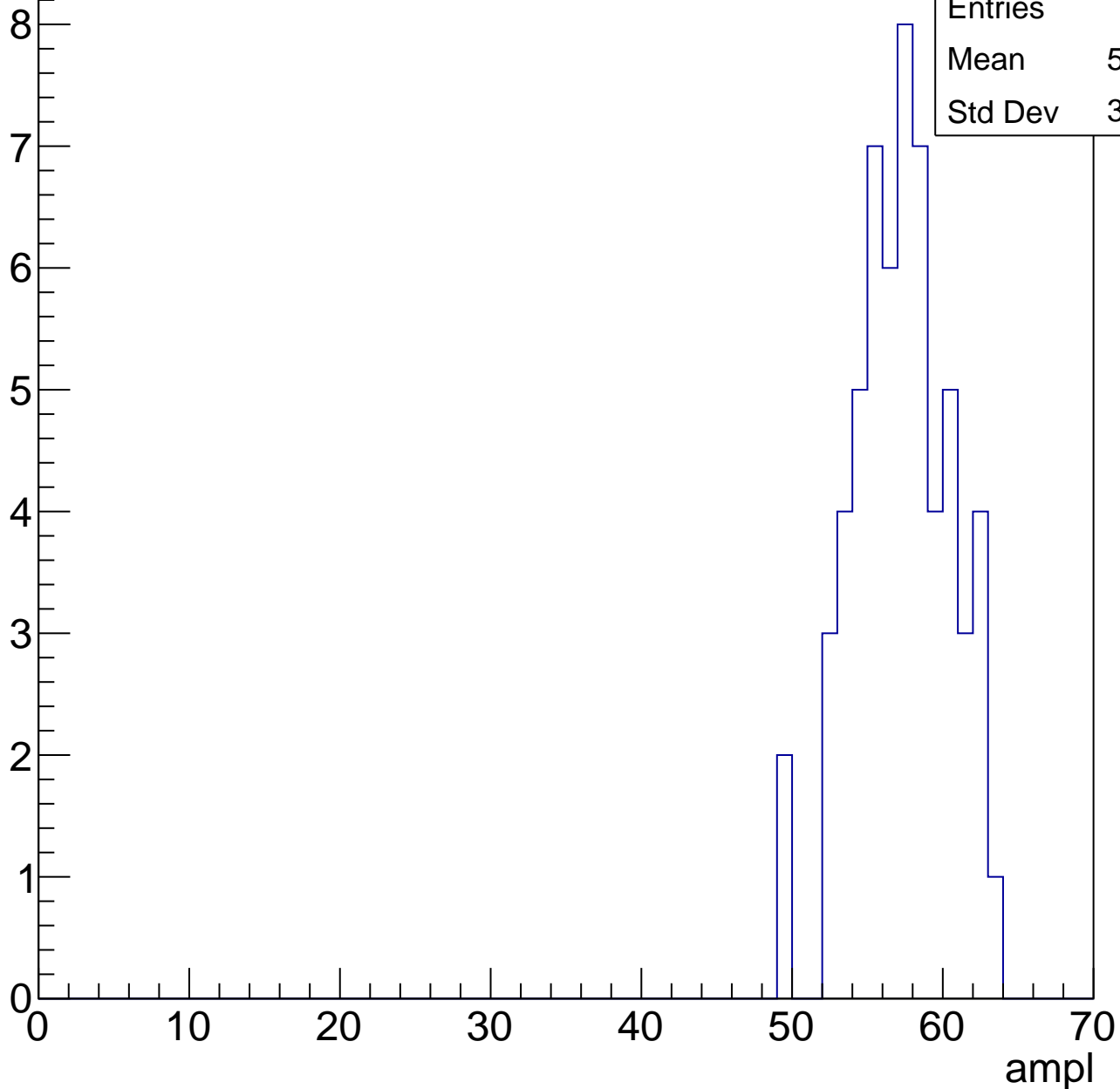
Entries	62
Mean	50.71
Std Dev	3.386



B0L002S, U7-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

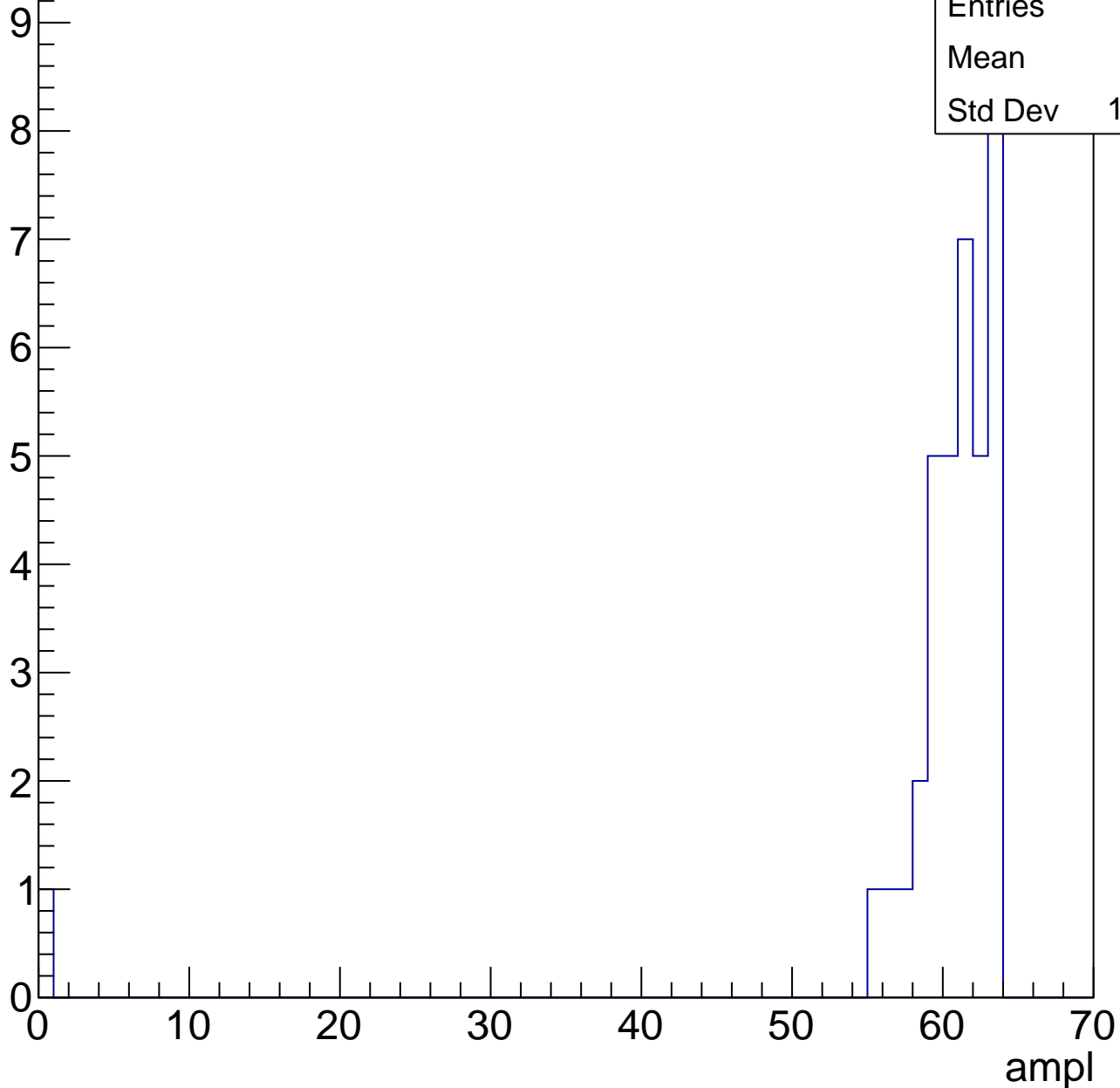
Entry



B0L002S, U7-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

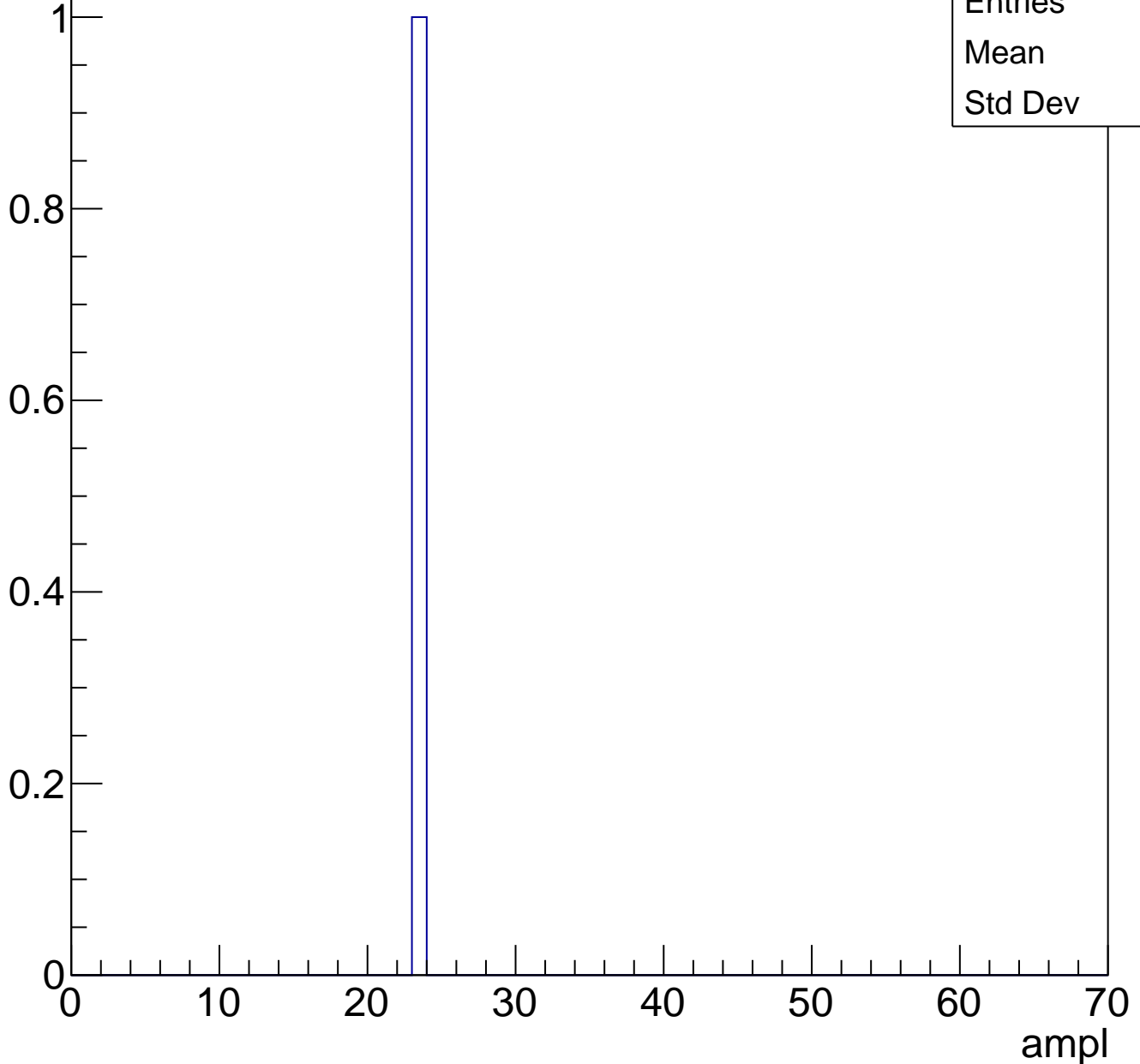
Entry



B0L002S, U7-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U7-ch4, adc0

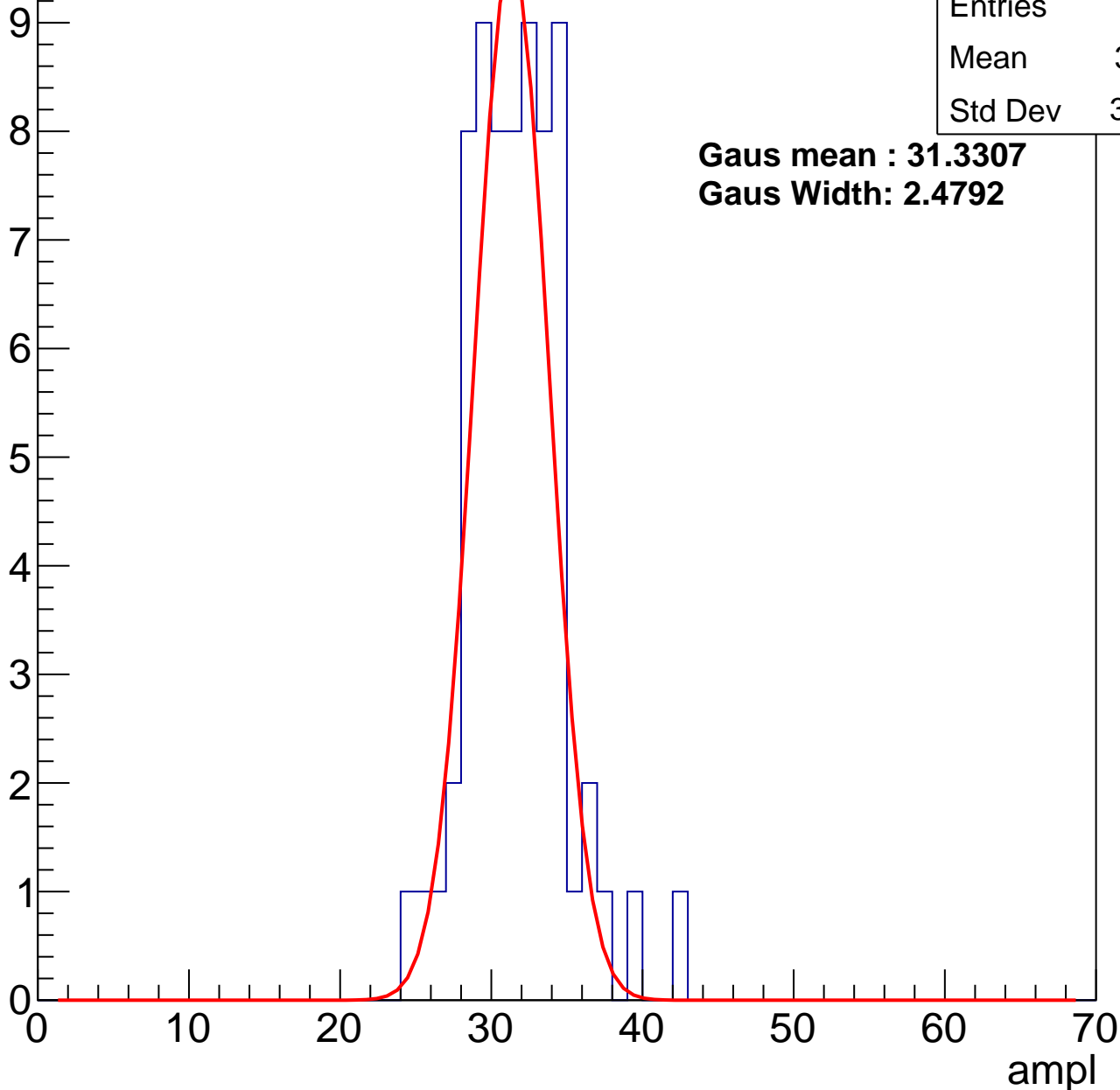
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.21
Std Dev	3.079

Gaus mean : 31.3307

Gaus Width: 2.4792

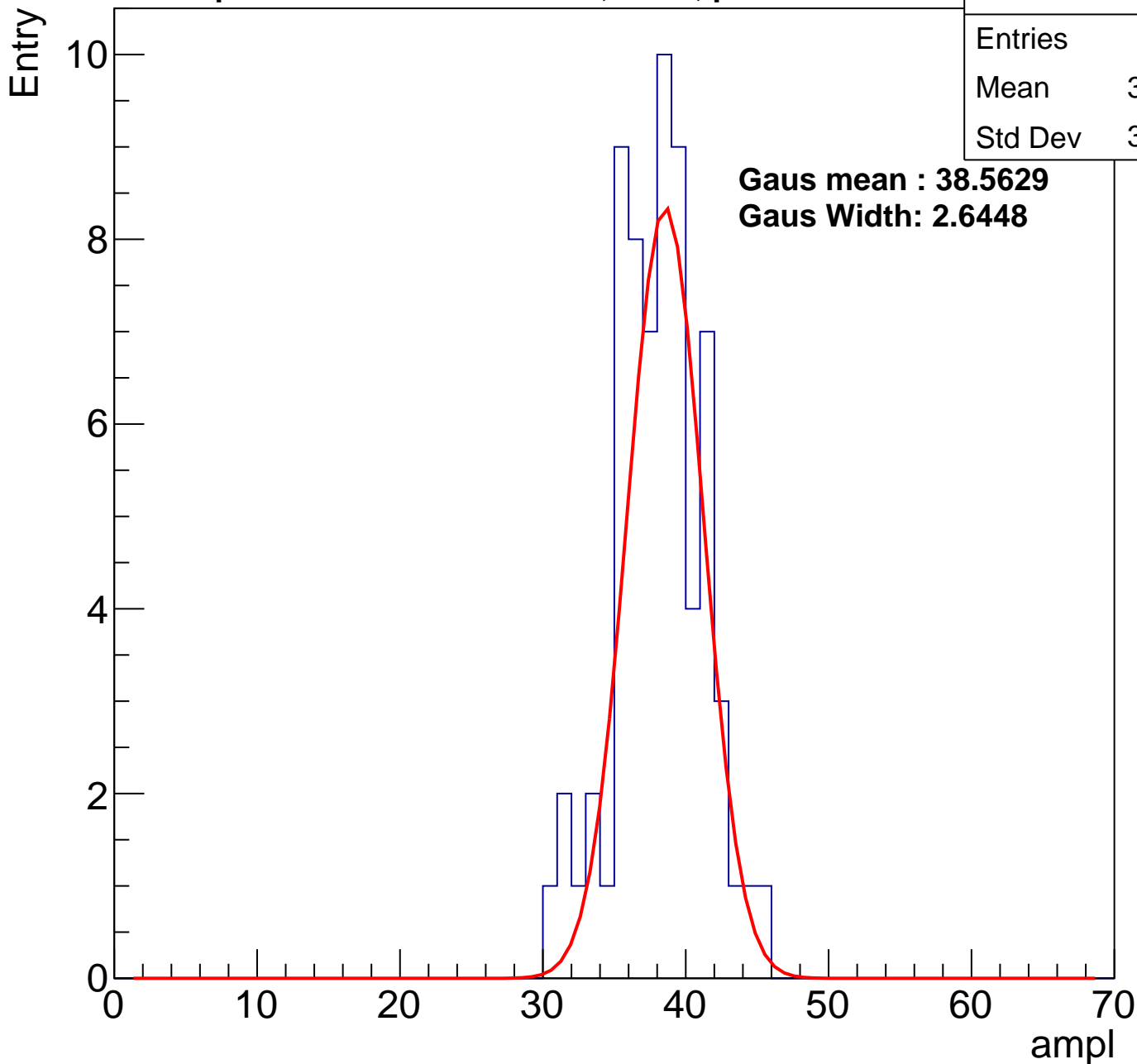


B0L002S, U7-ch4, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	37.64
Std Dev	3.045

Gaus mean : 38.5629
Gaus Width: 2.6448



B0L002S, U7-ch4, adc2

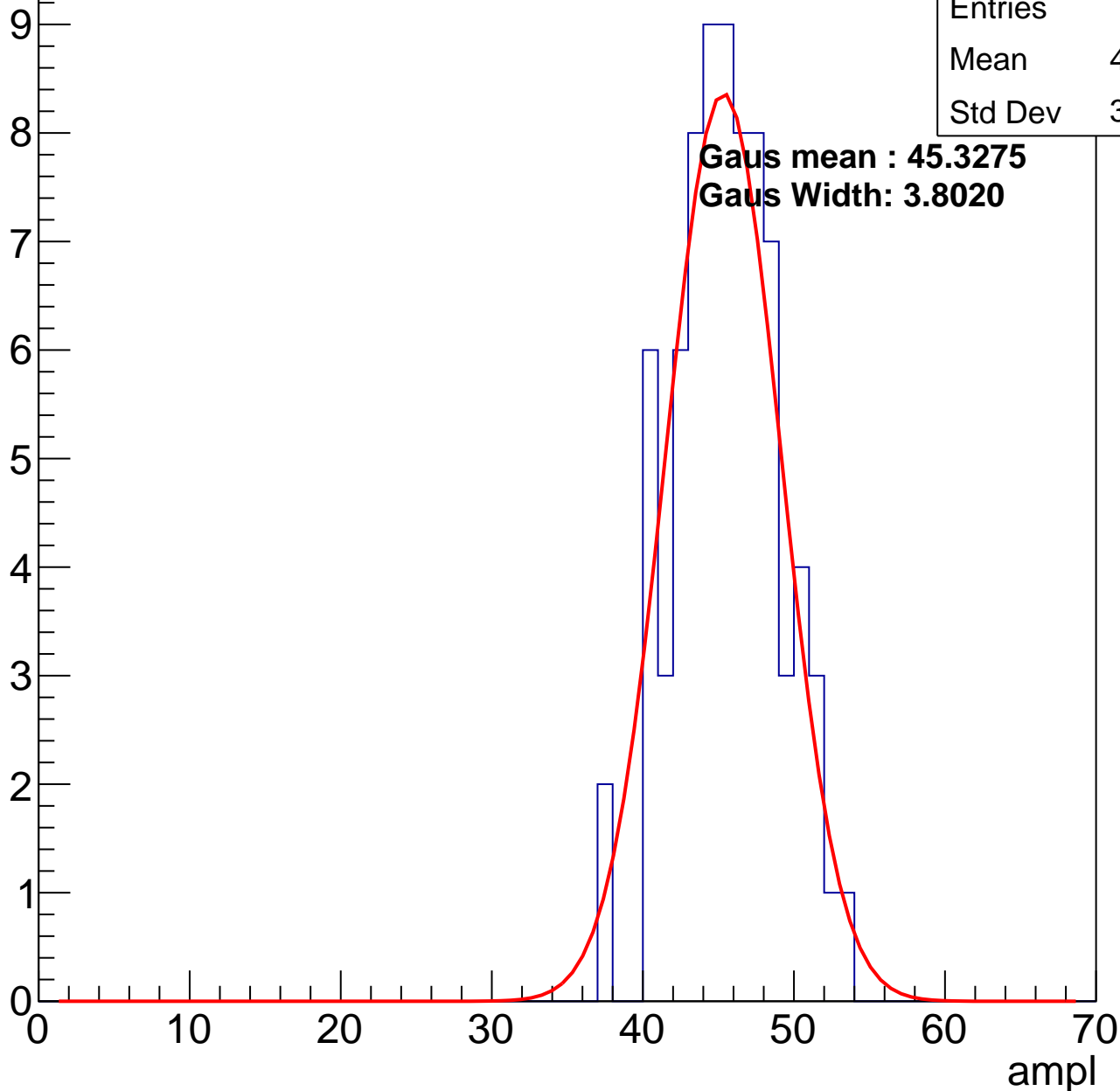
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	45.12
Std Dev	3.382

Gaus mean : 45.3275

Gaus Width: 3.8020

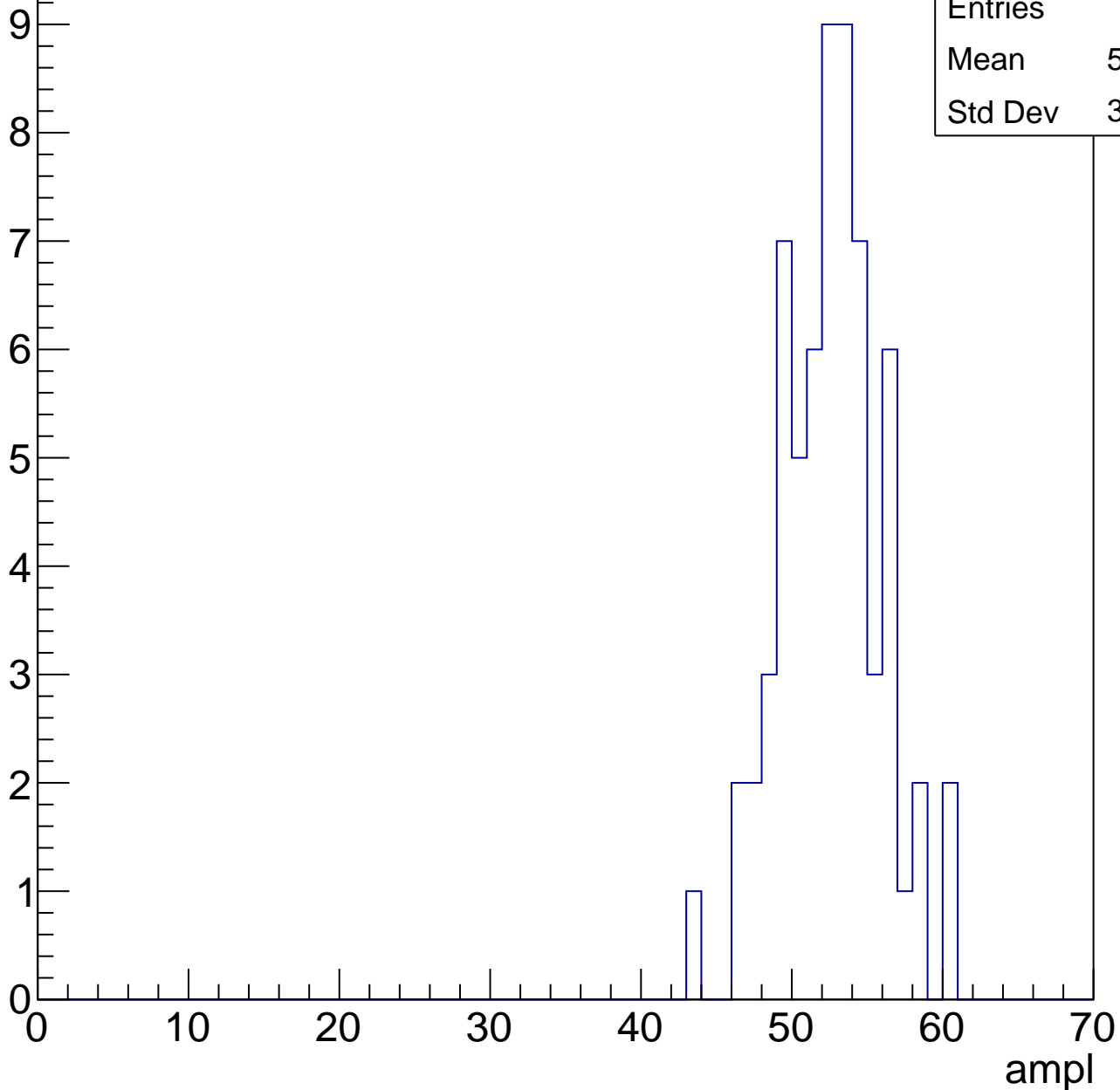


B0L002S, U7-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

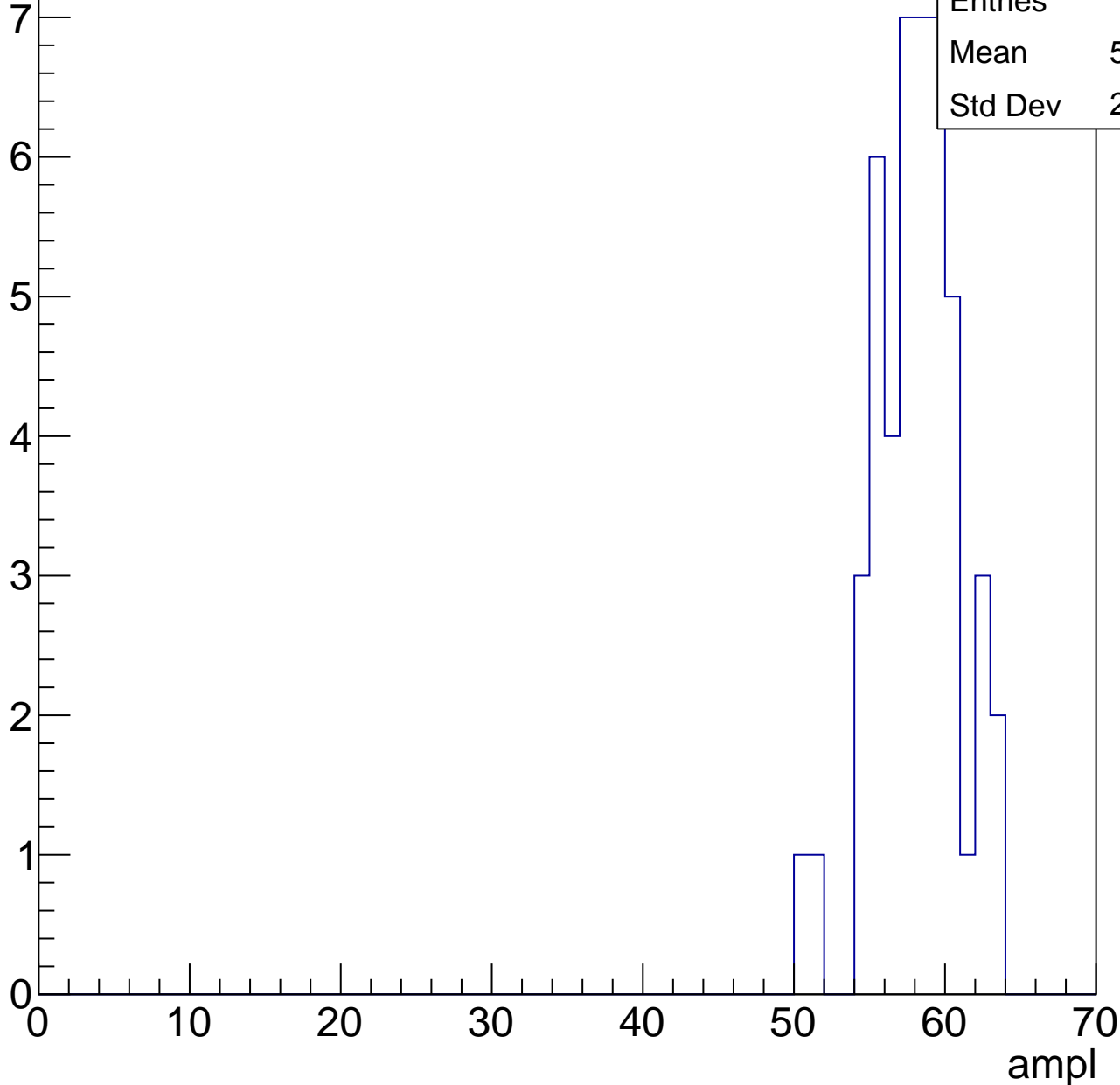
Entries	65
Mean	52.14
Std Dev	3.337



B0L002S, U7-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

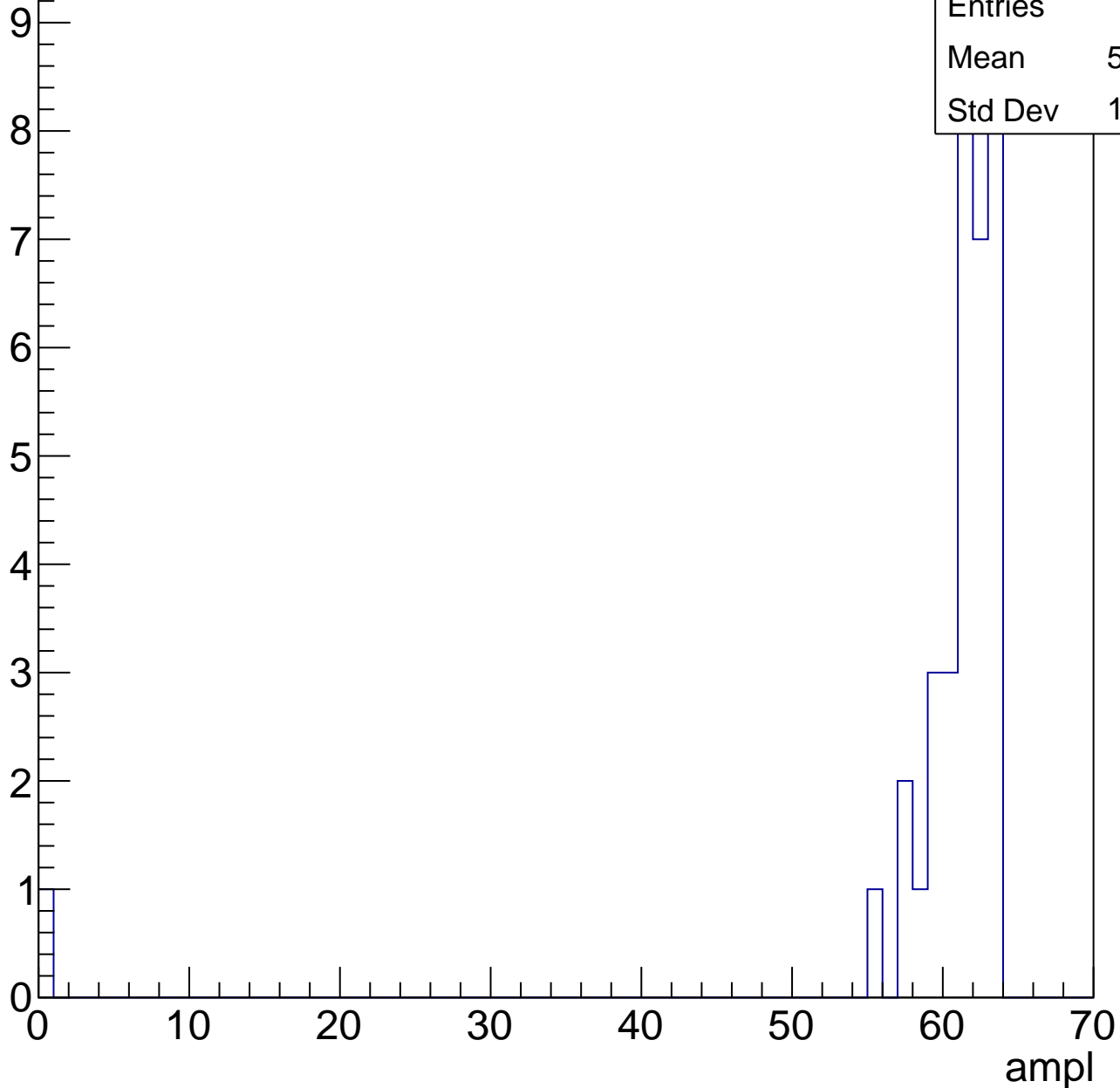
Entry



B0L002S, U7-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

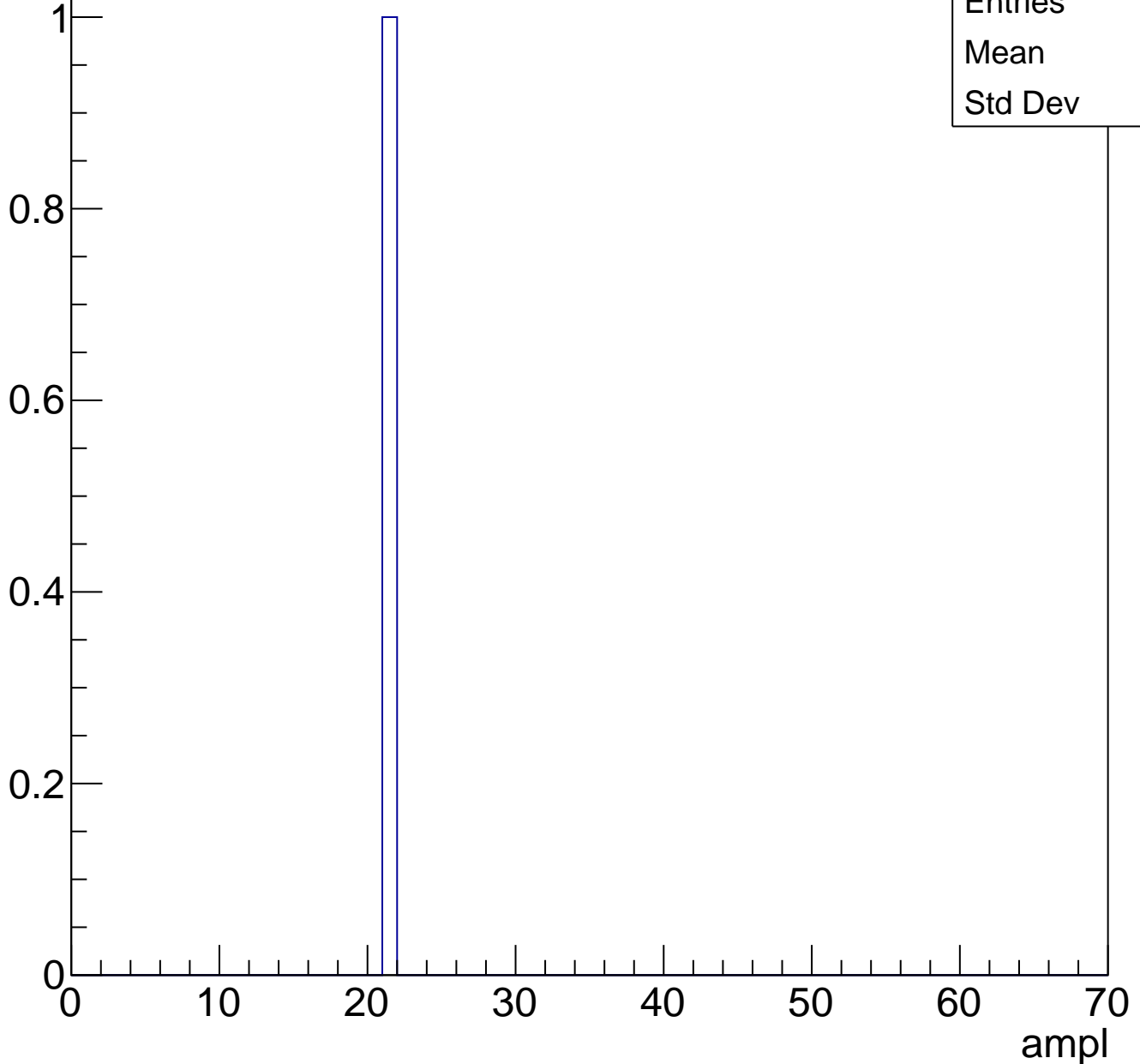


Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch5, adc0

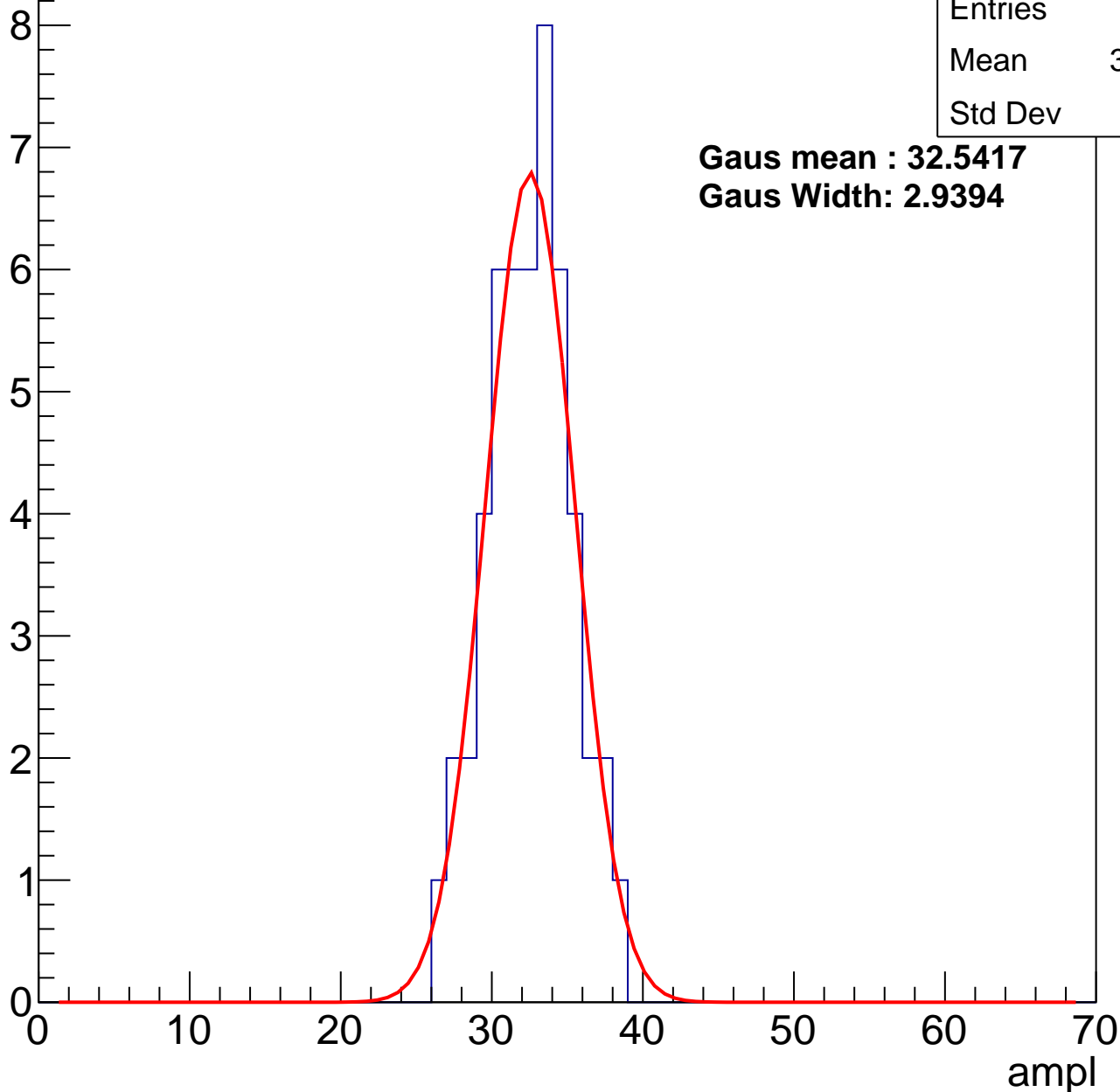
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	32.04
Std Dev	2.72

Gaus mean : 32.5417

Gaus Width: 2.9394



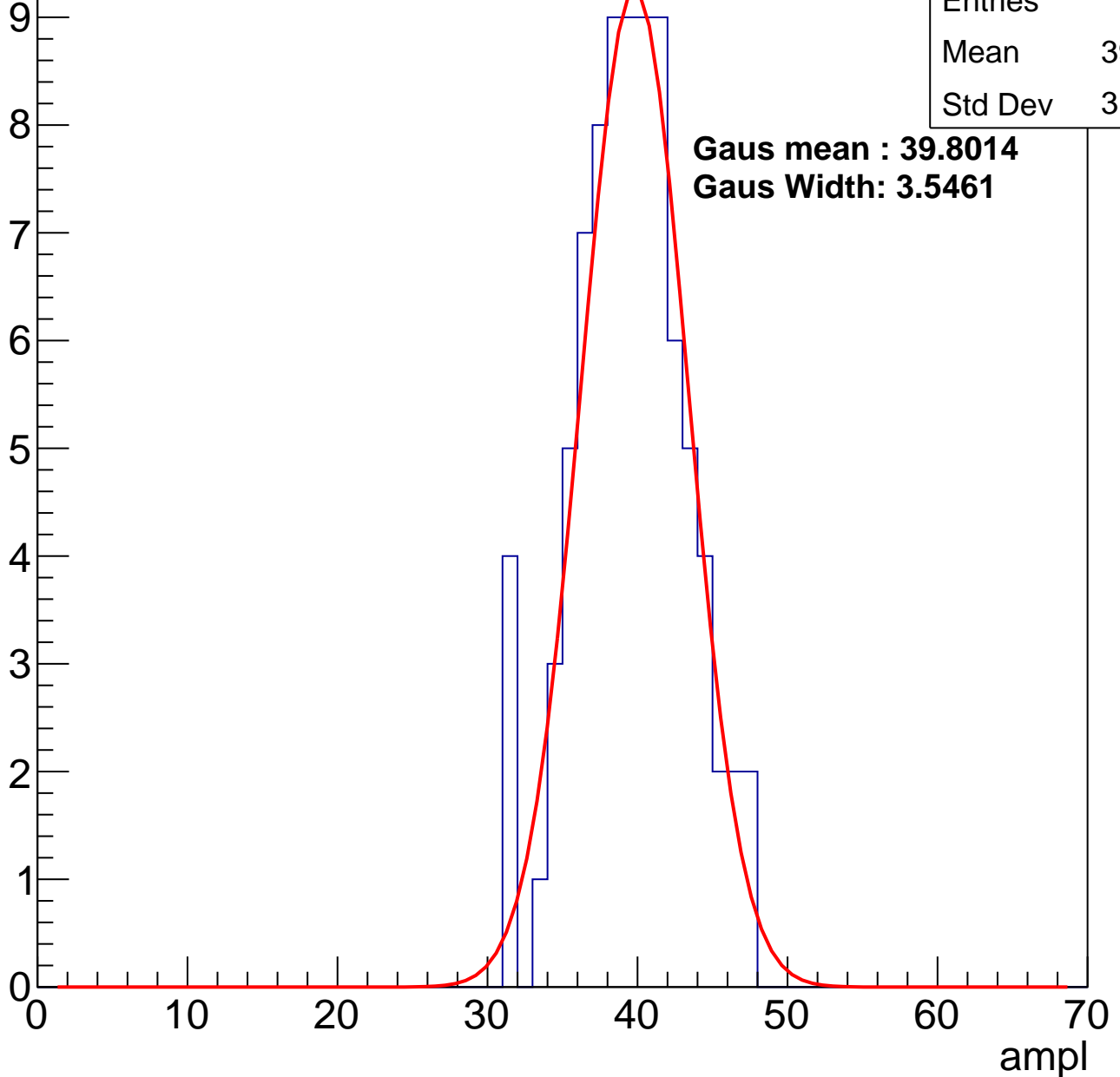
B0L002S, U7-ch5, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	39.09
Std Dev	3.642

Gaus mean : 39.8014
Gaus Width: 3.5461



B0L002S, U7-ch5, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	46.75
Std Dev	3.135

Gaus mean : 47.4408

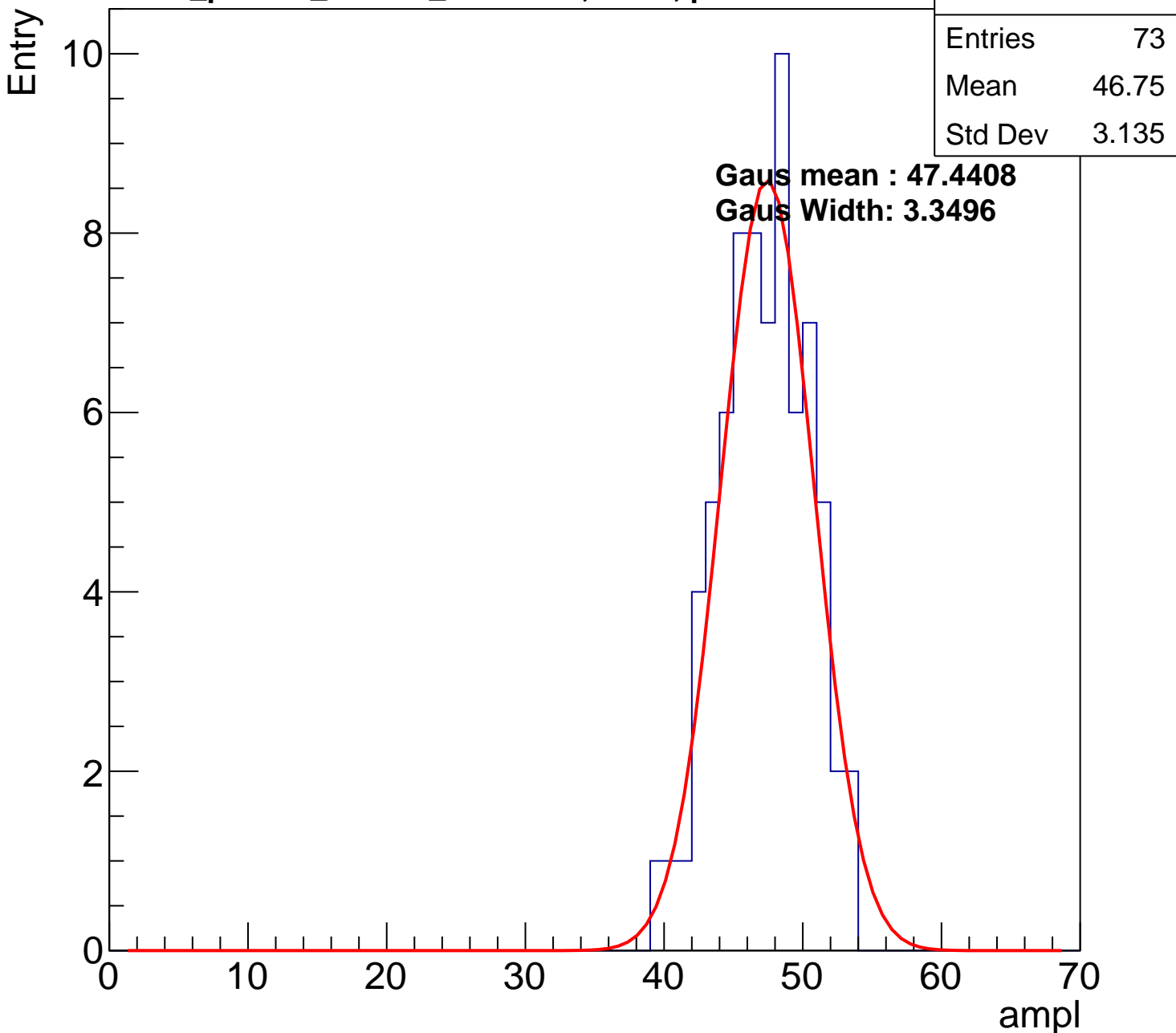
Gaus Width: 3.3496

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

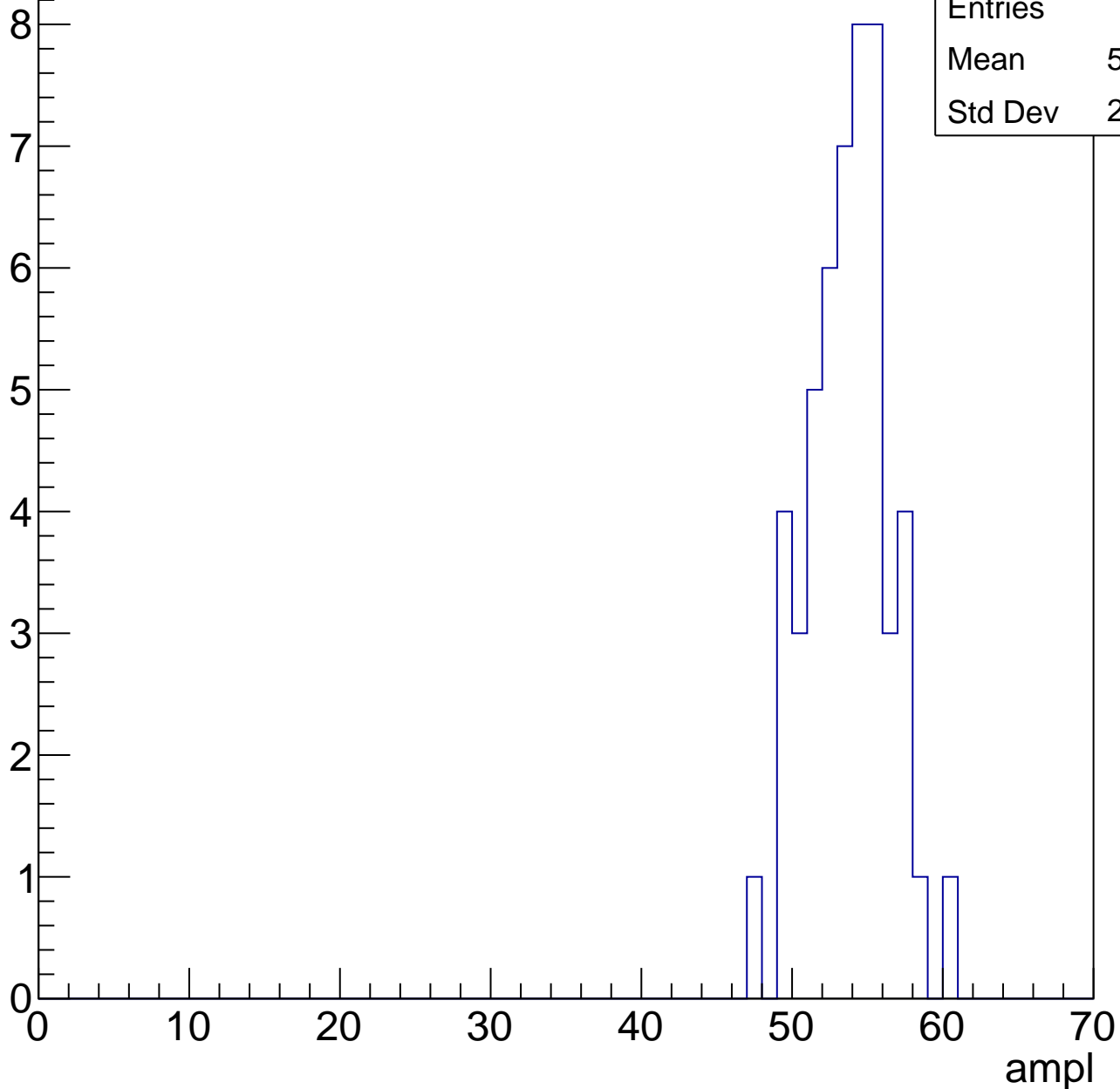


B0L002S, U7-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

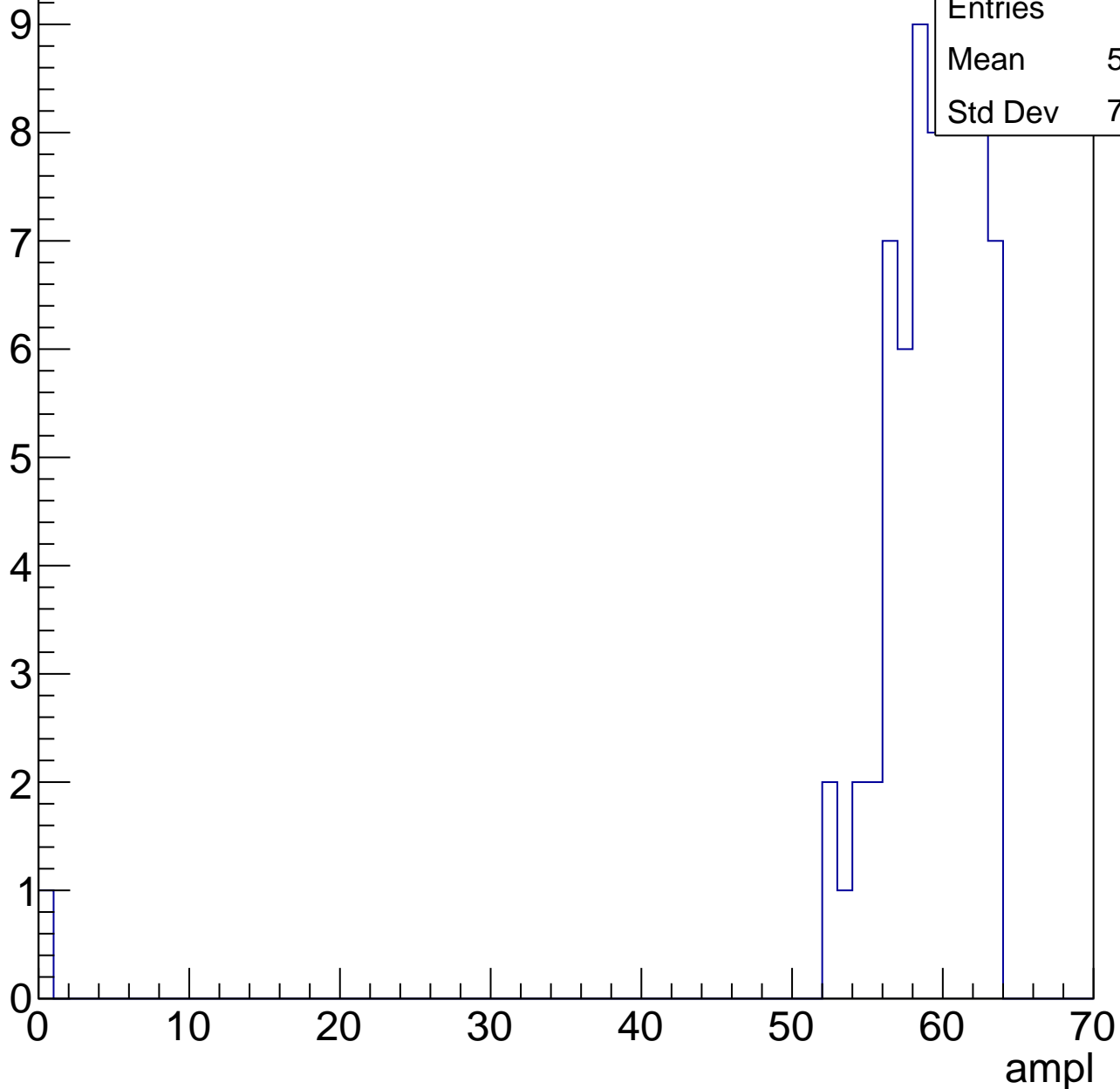
Entries	51
Mean	53.27
Std Dev	2.635



B0L002S, U7-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

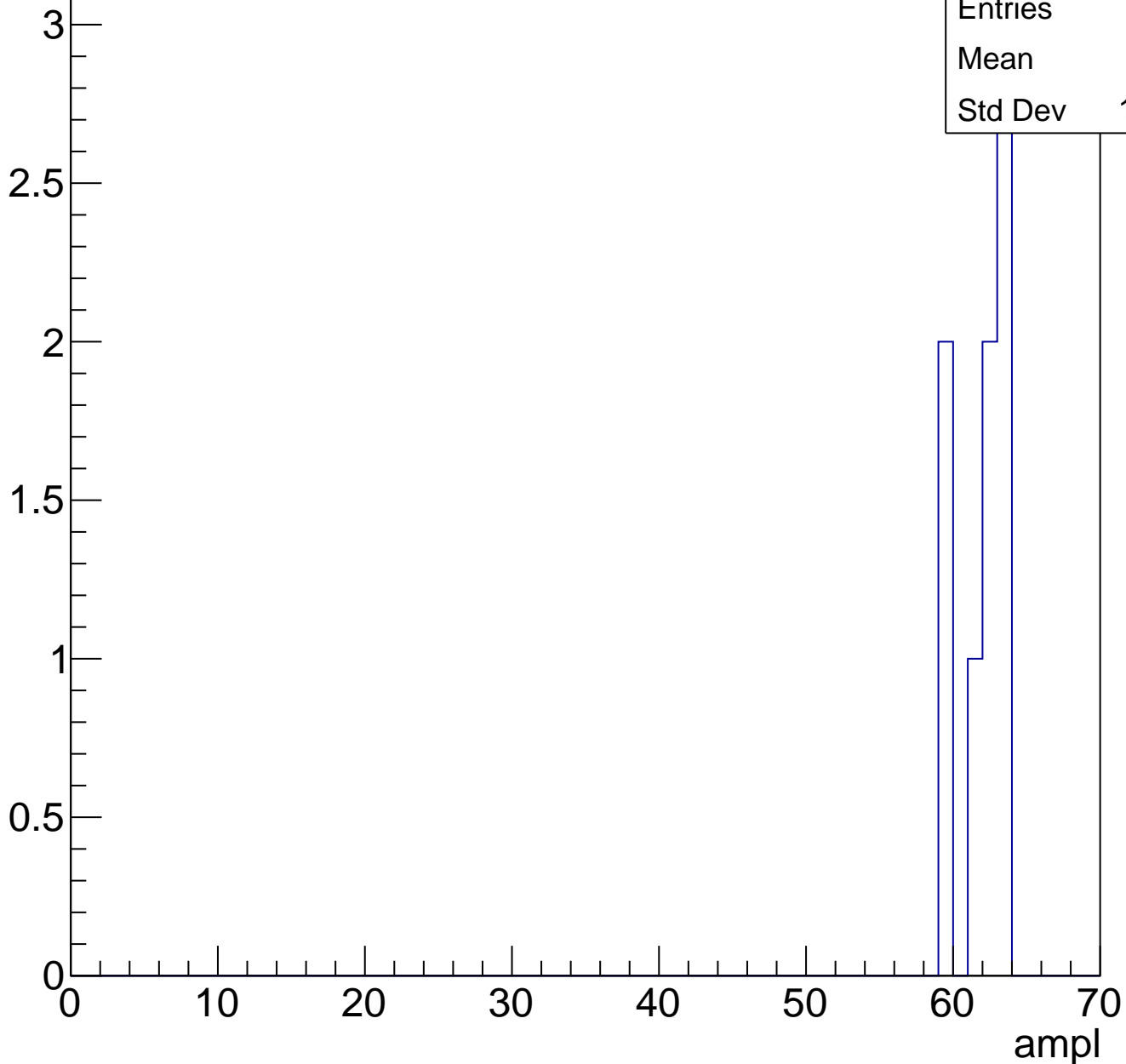
Entry



B0L002S, U7-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch6, adc0

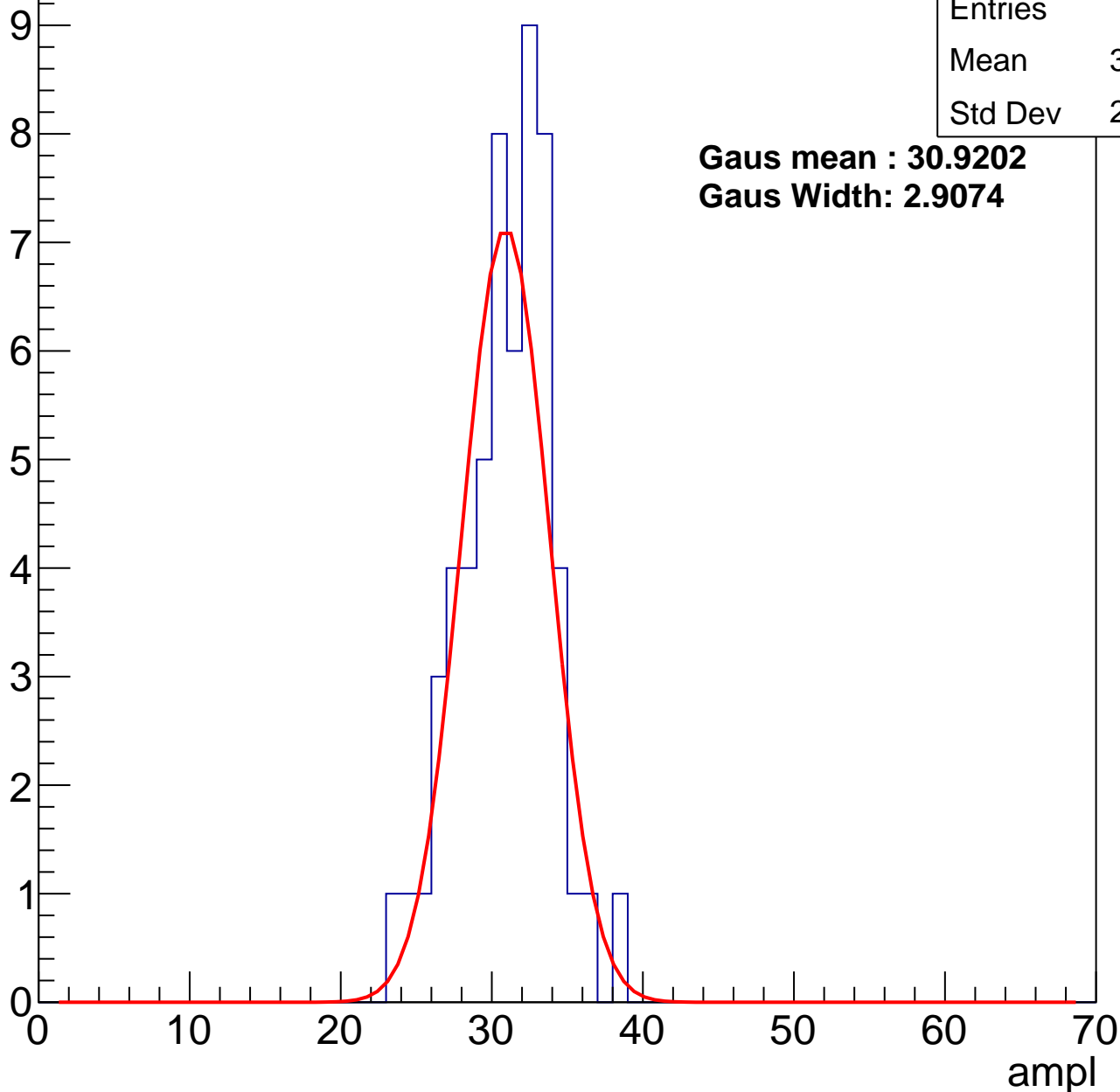
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	30.49
Std Dev	2.974

Gaus mean : 30.9202

Gaus Width: 2.9074



B0L002S, U7-ch6, adc1

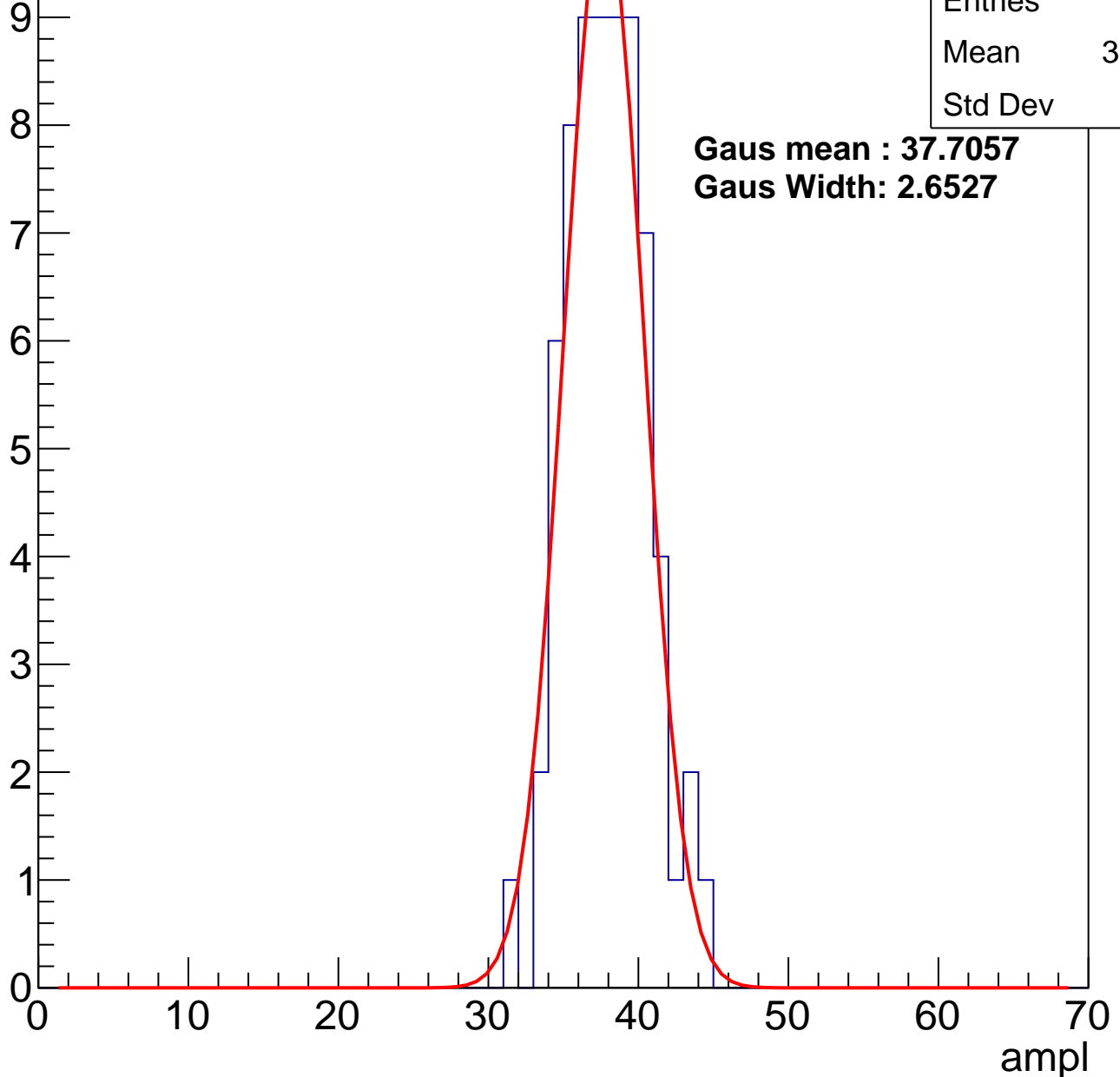
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.46
Std Dev	2.62

Gaus mean : 37.7057

Gaus Width: 2.6527



B0L002S, U7-ch6, adc2

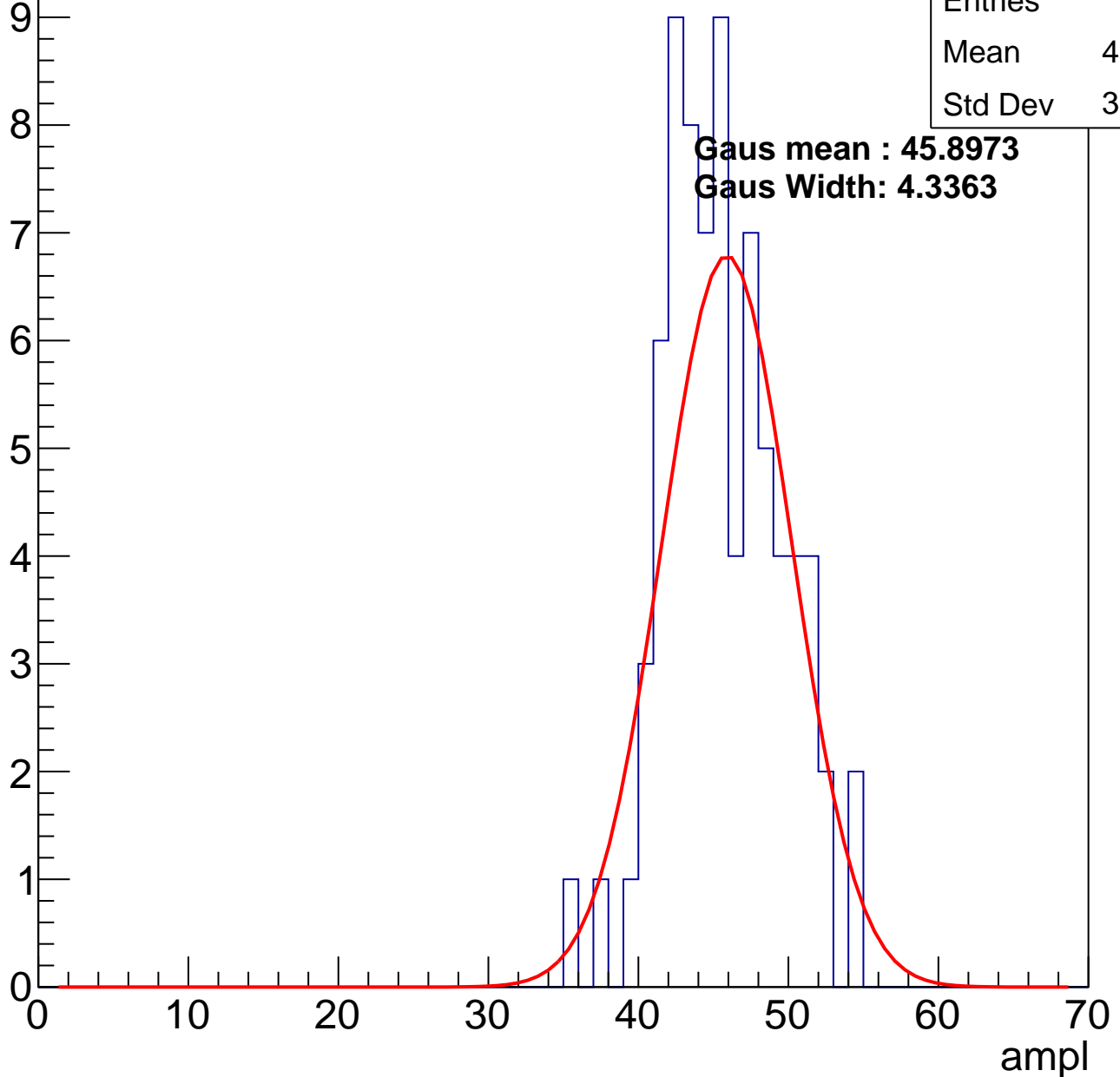
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	45.16
Std Dev	3.838

Gaus mean : 45.8973

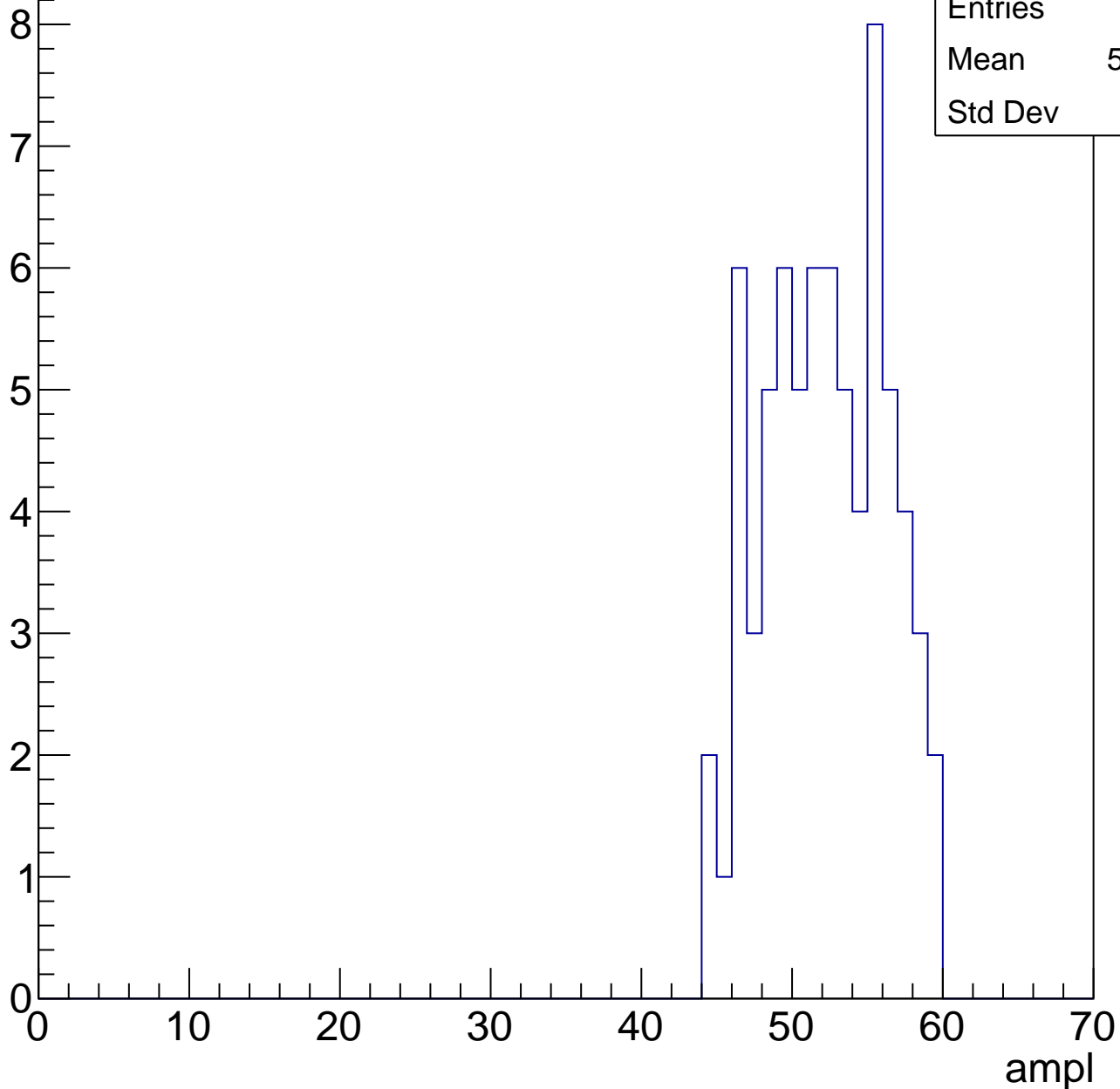
Gaus Width: 4.3363



B0L002S, U7-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

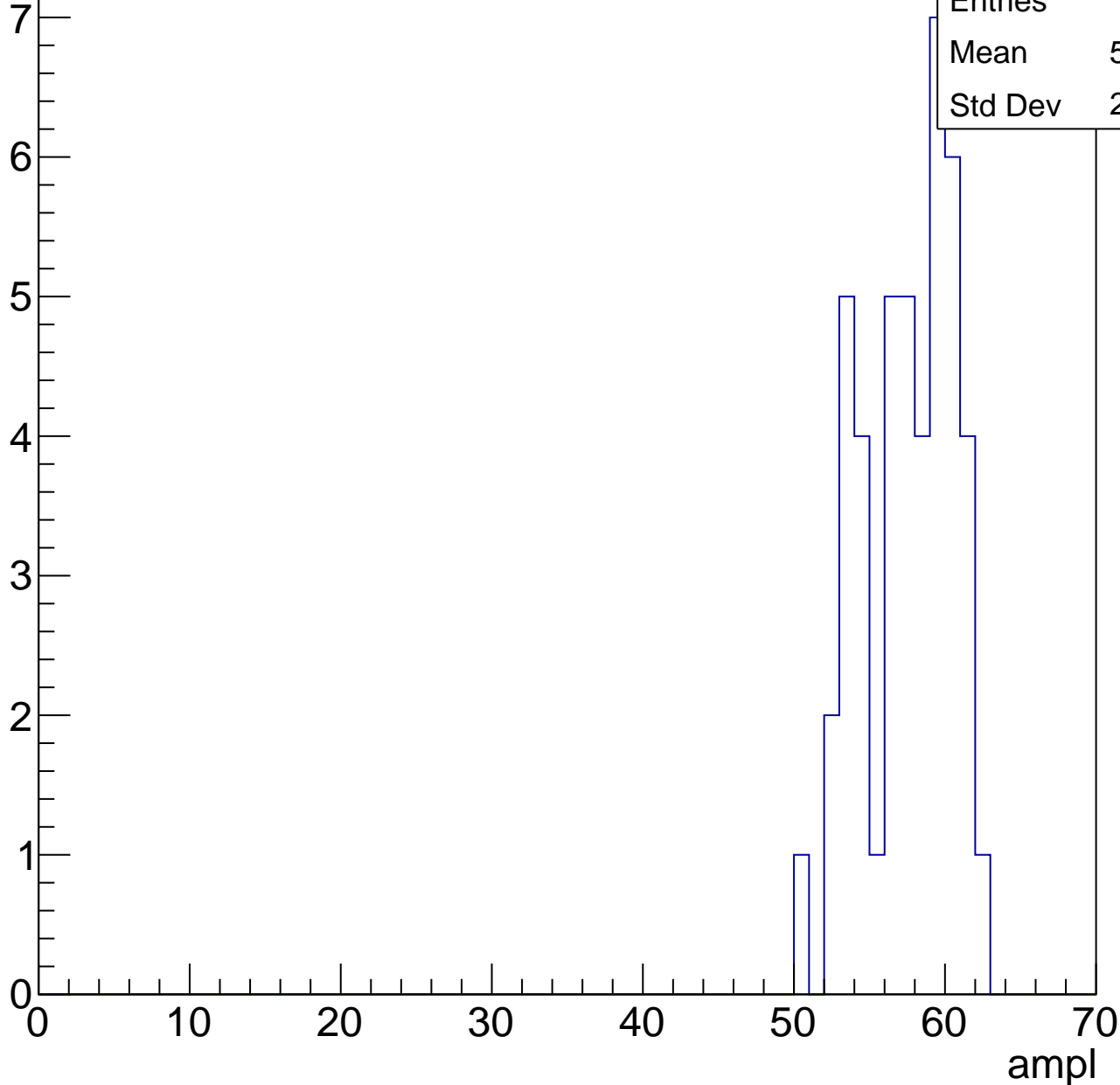


B0L002S, U7-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	57.02
Std Dev	2.978



B0L002S, U7-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	36
Mean	59.42
Std Dev	10.23

Entry

10

8

6

4

2

0

0

10

20

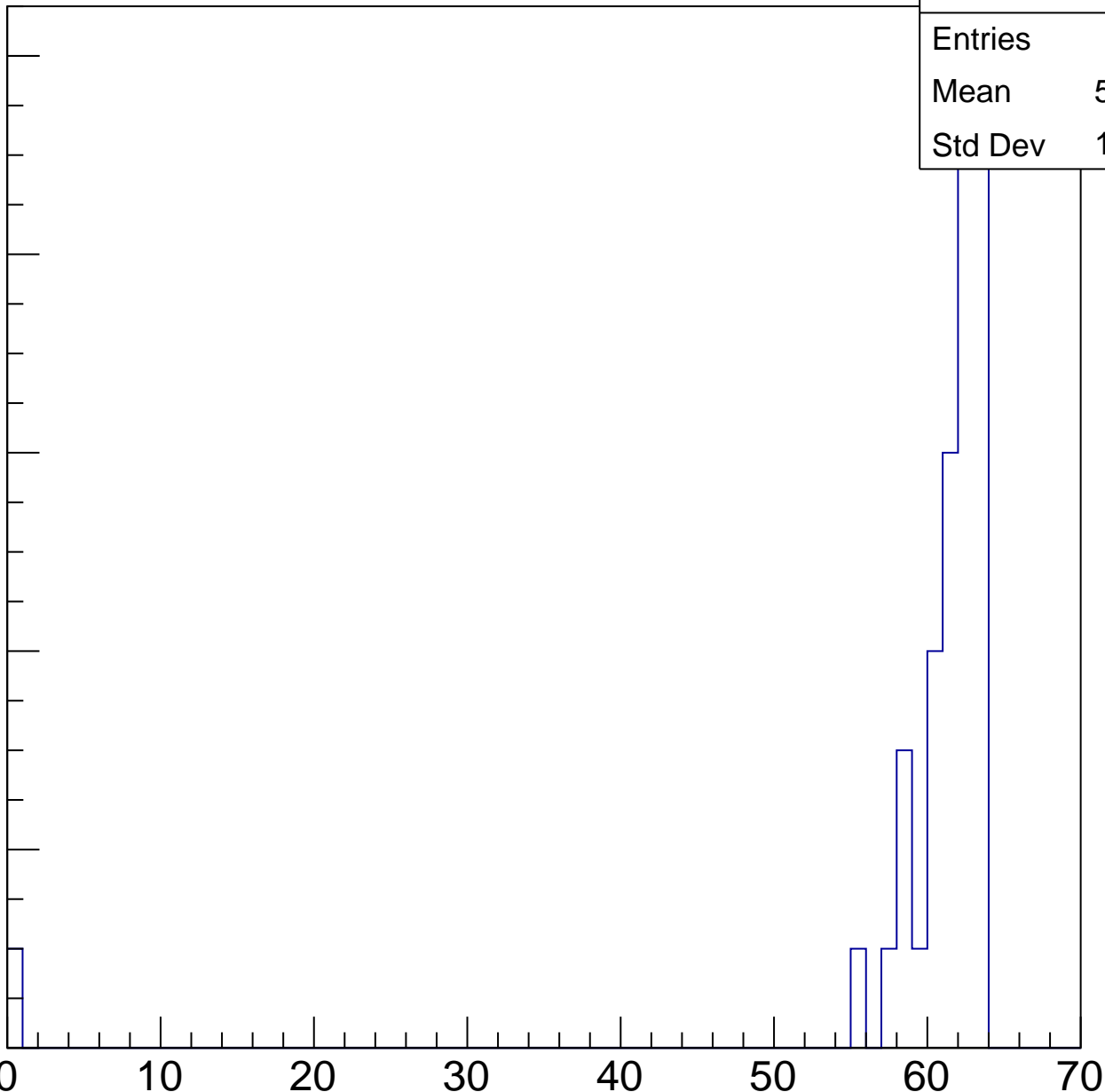
30

40

50

60

ampl



B0L002S, U7-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L002S, U7-ch7, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	29.31
Std Dev	3.105

Gaus mean : 29.5704

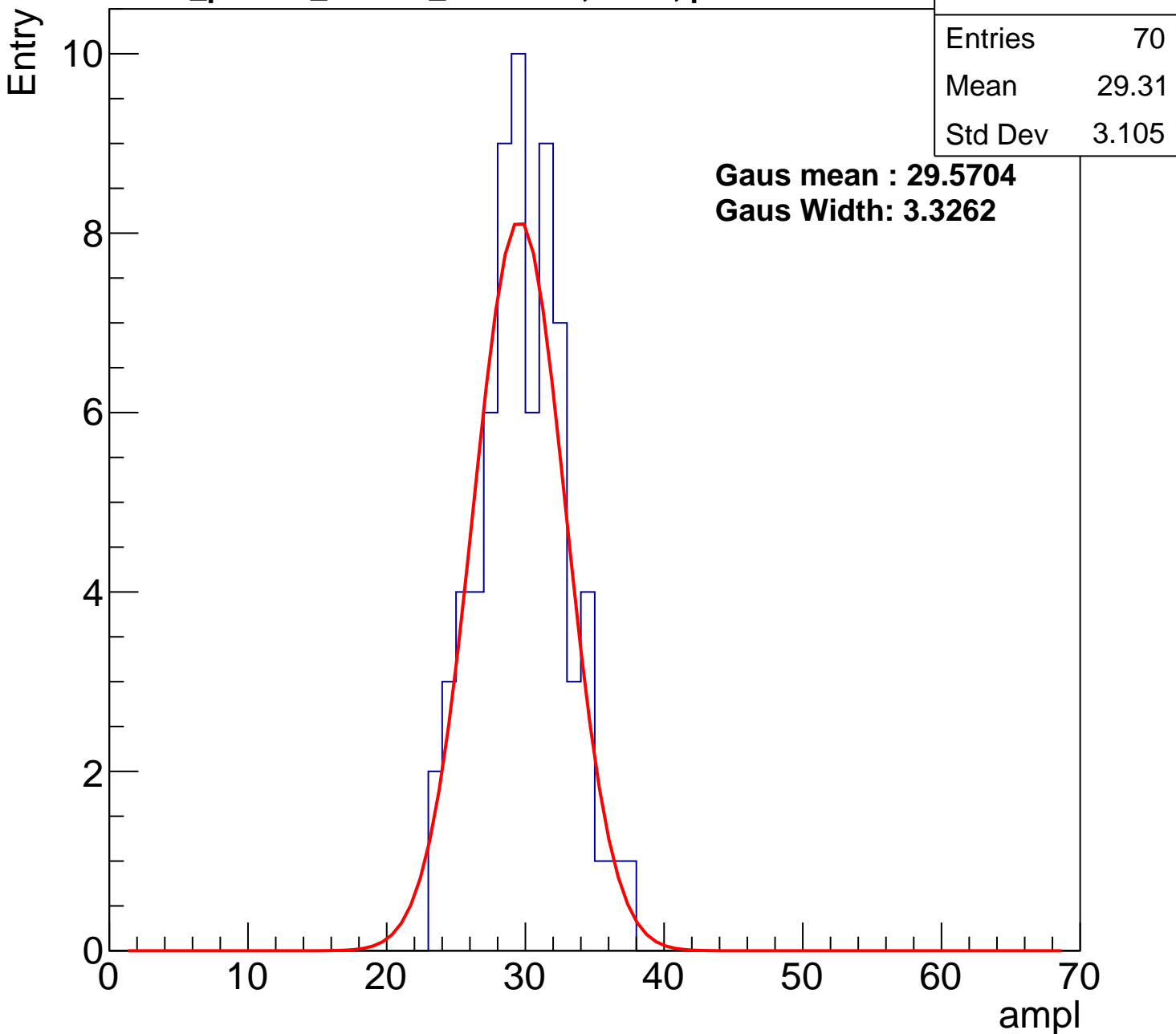
Gaus Width: 3.3262

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch7, adc1

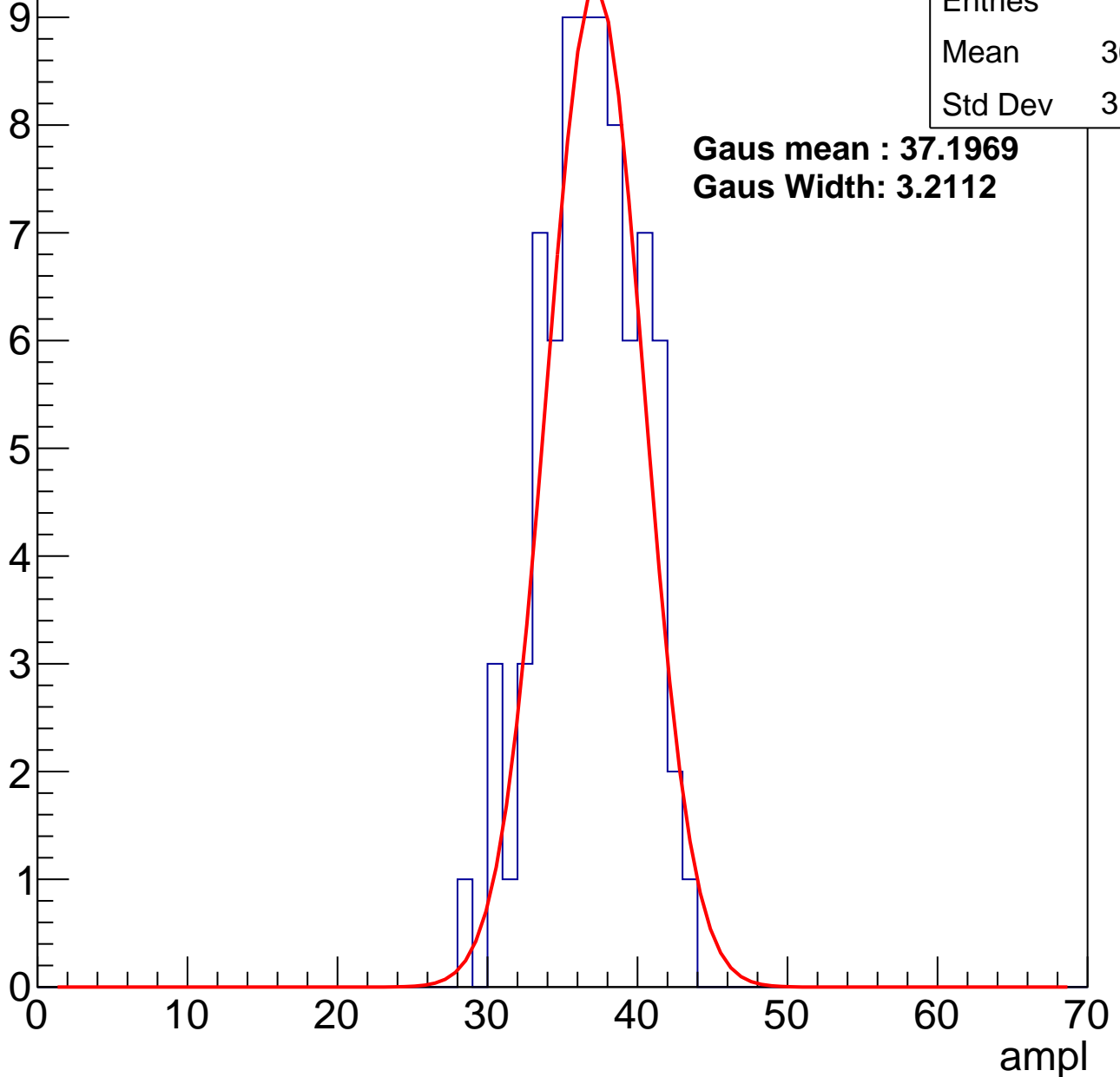
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	36.45
Std Dev	3.209

Gaus mean : 37.1669

Gaus Width: 3.2112



B0L002S, U7-ch7, adc2

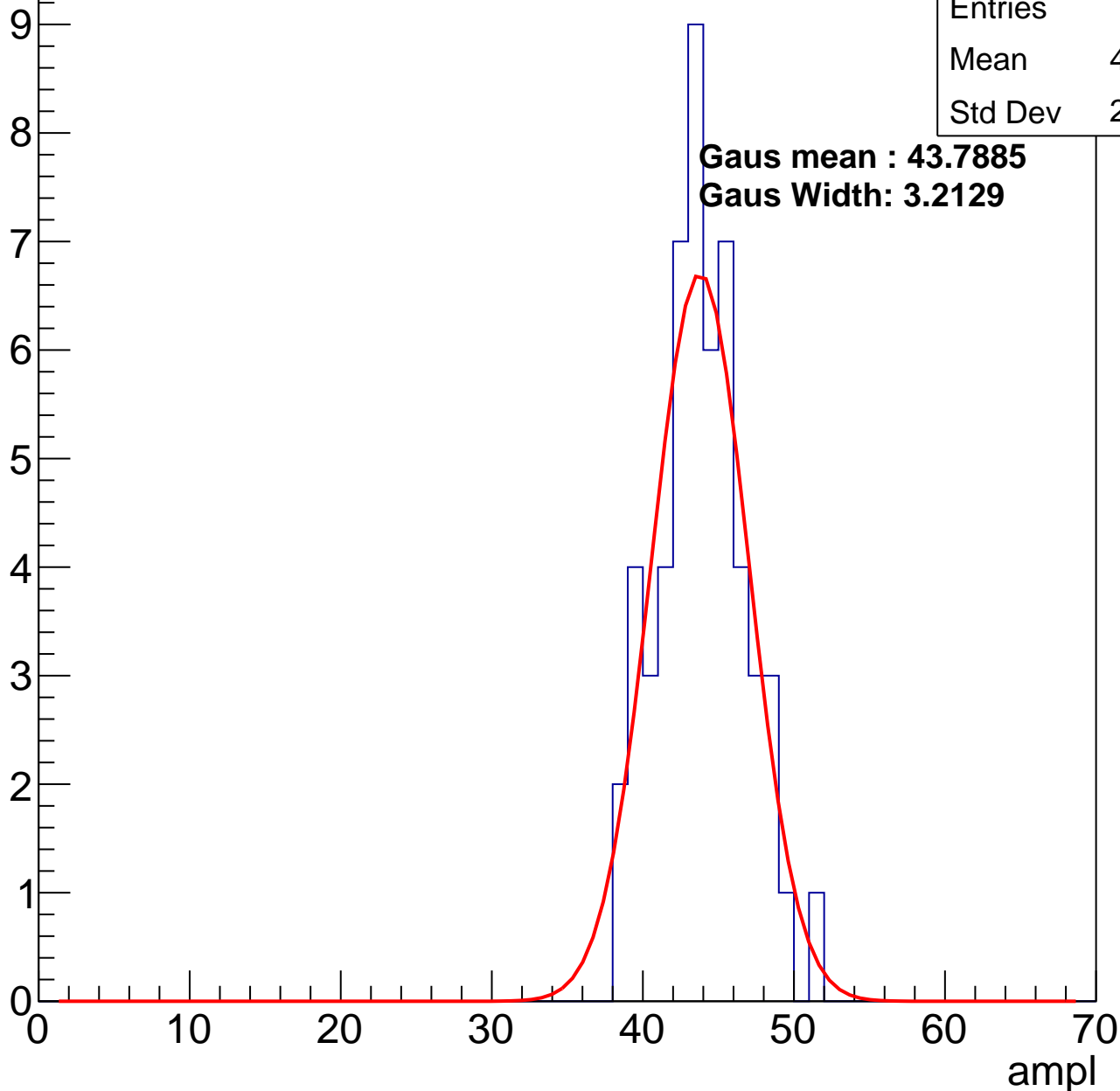
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.43
Std Dev	2.878

Gaus mean : 43.7885

Gaus Width: 3.2129



B0L002S, U7-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

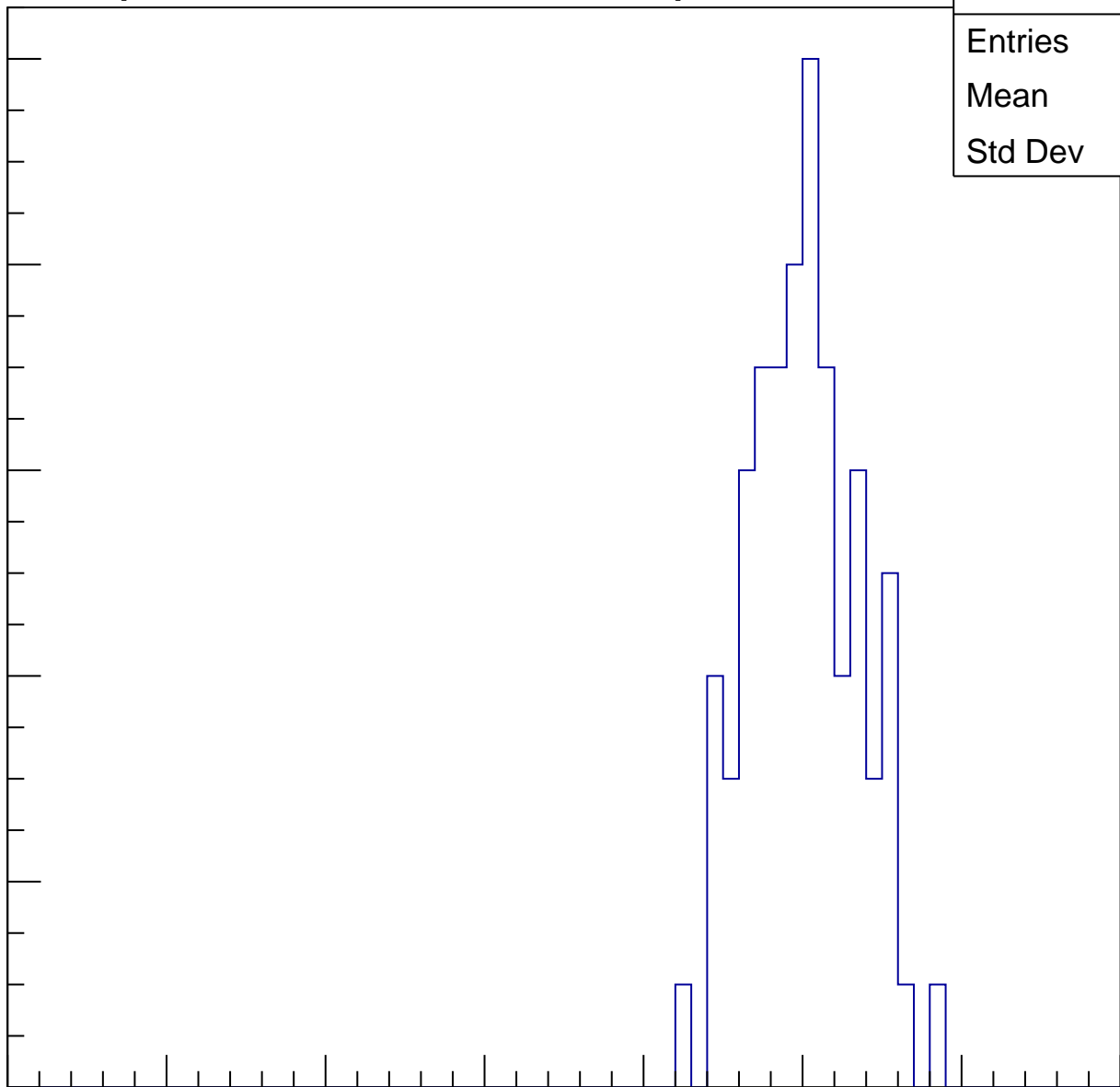
Entries	73
Mean	49.59
Std Dev	3.343

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

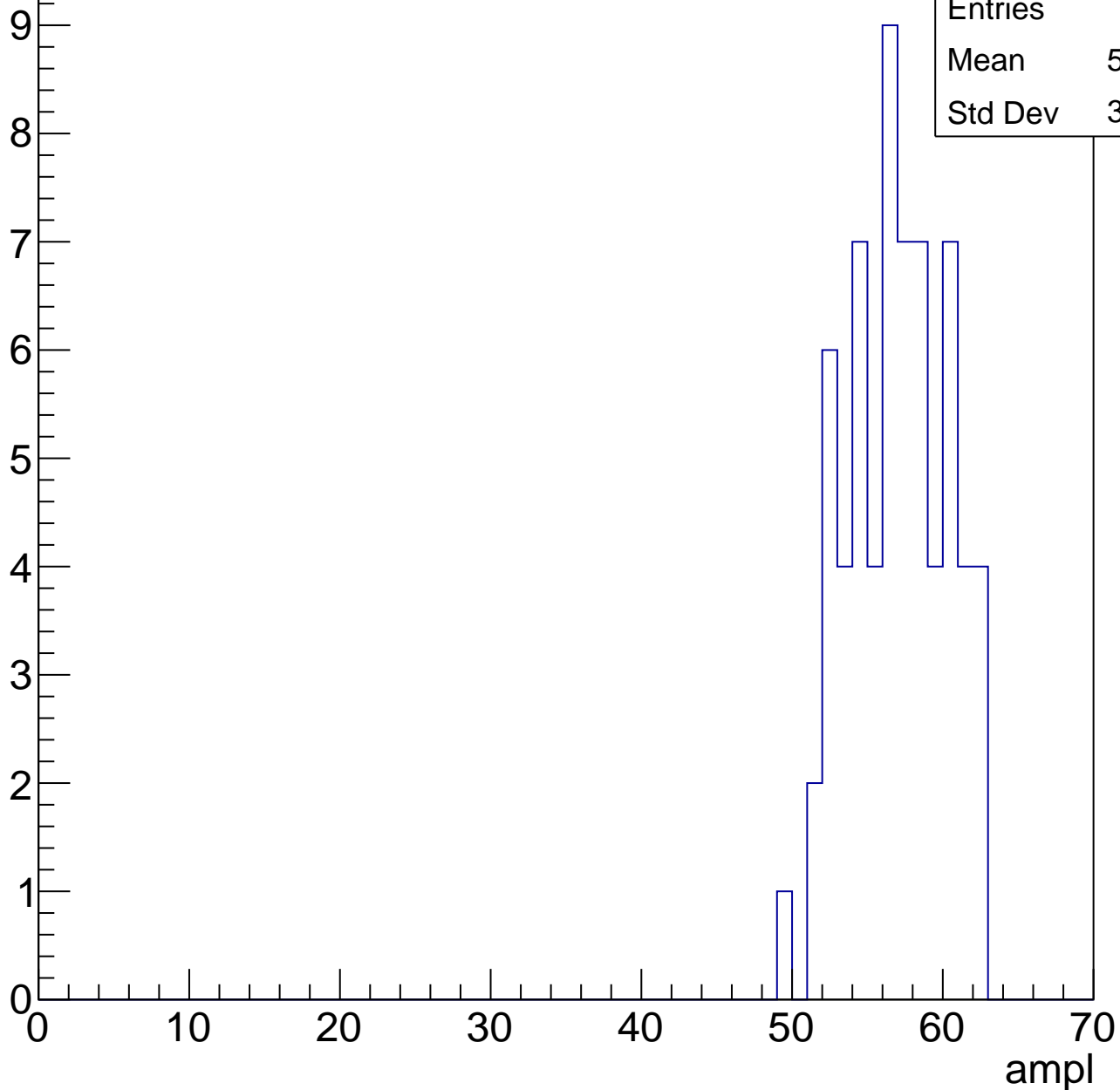
ampl



B0L002S, U7-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



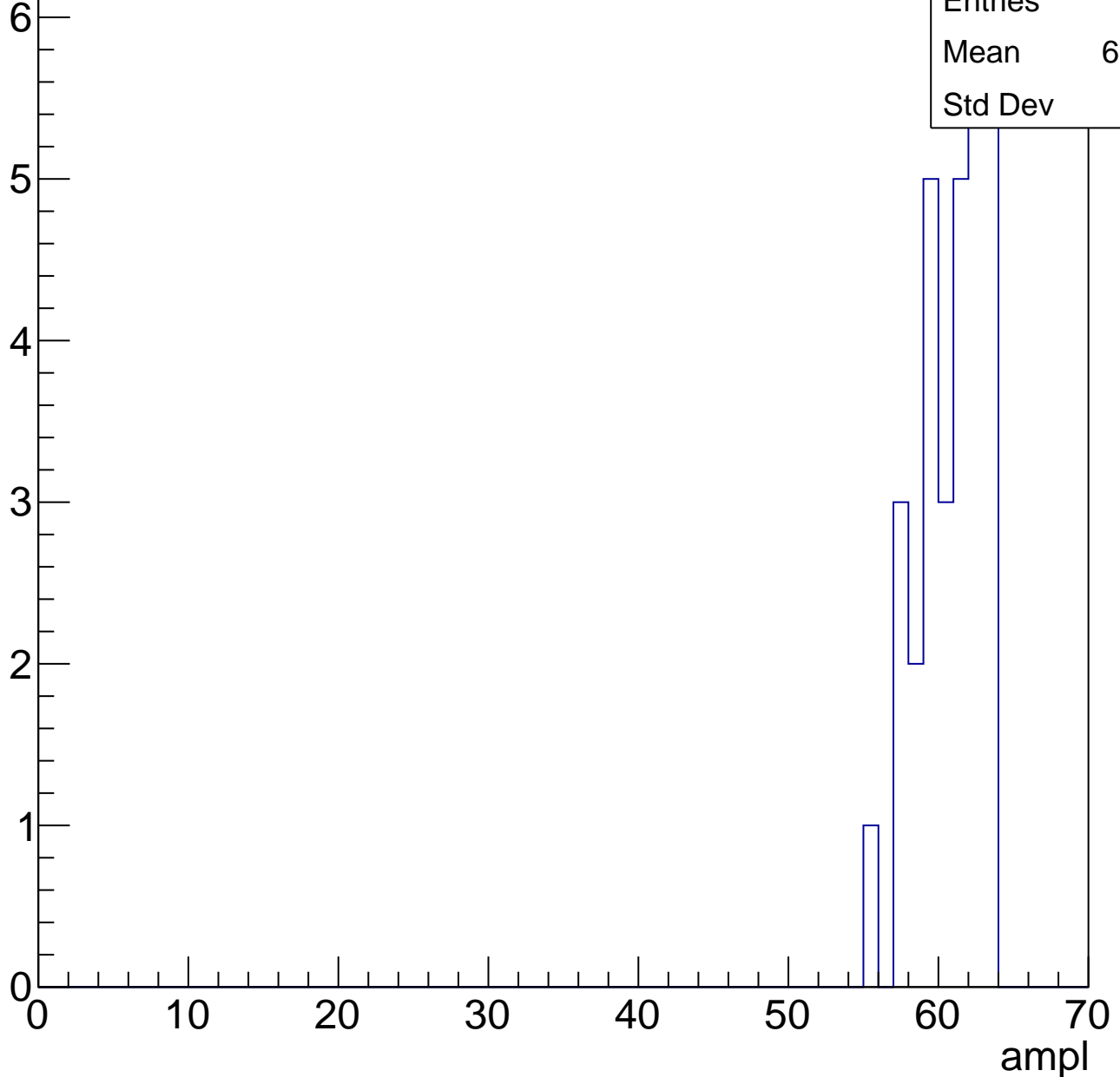
Entries	66
Mean	56.52
Std Dev	3.192

B0L002S, U7-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	60.39
Std Dev	2.15



B0L002S, U7-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

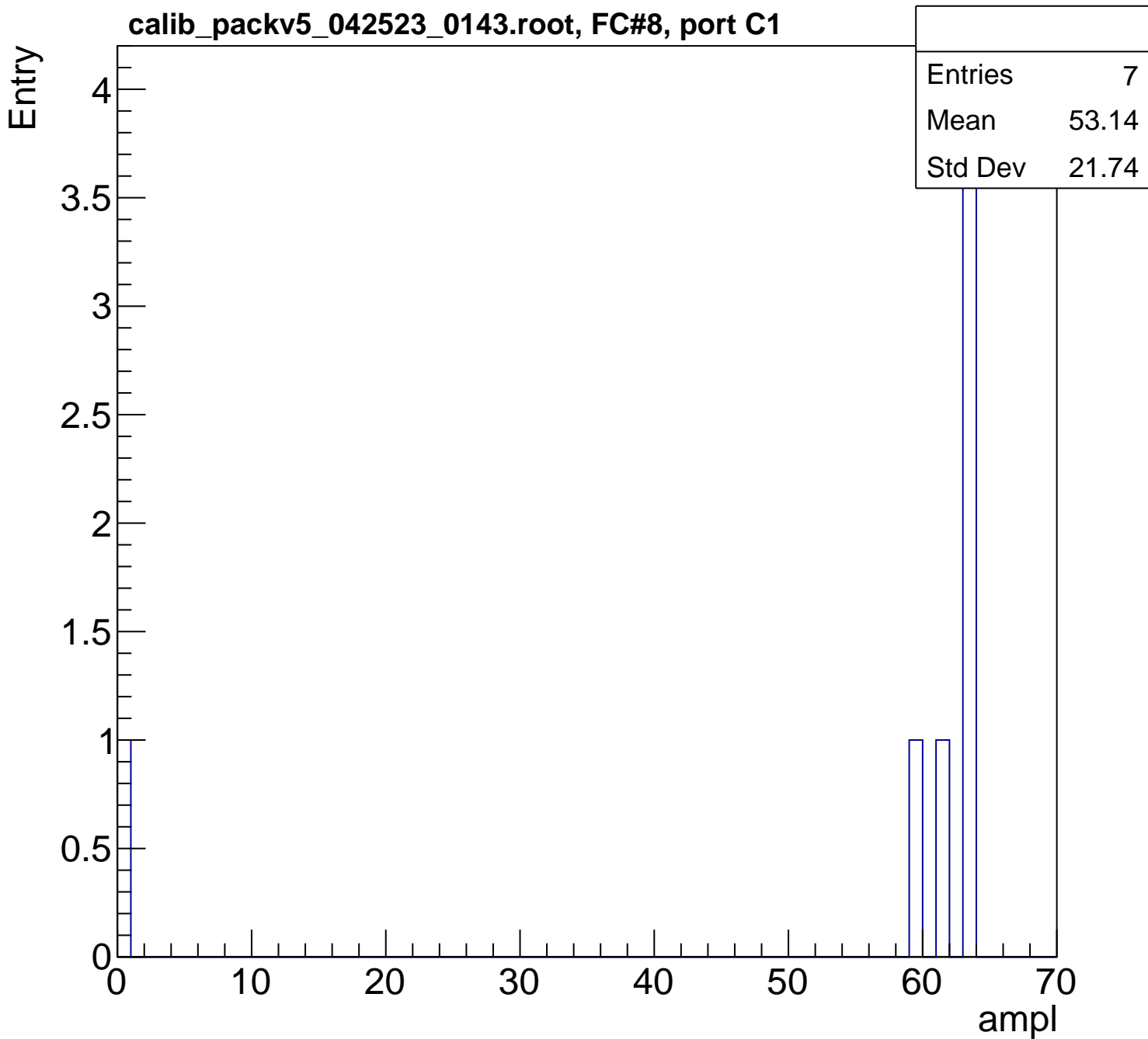
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	7
Mean	53.14
Std Dev	21.74

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

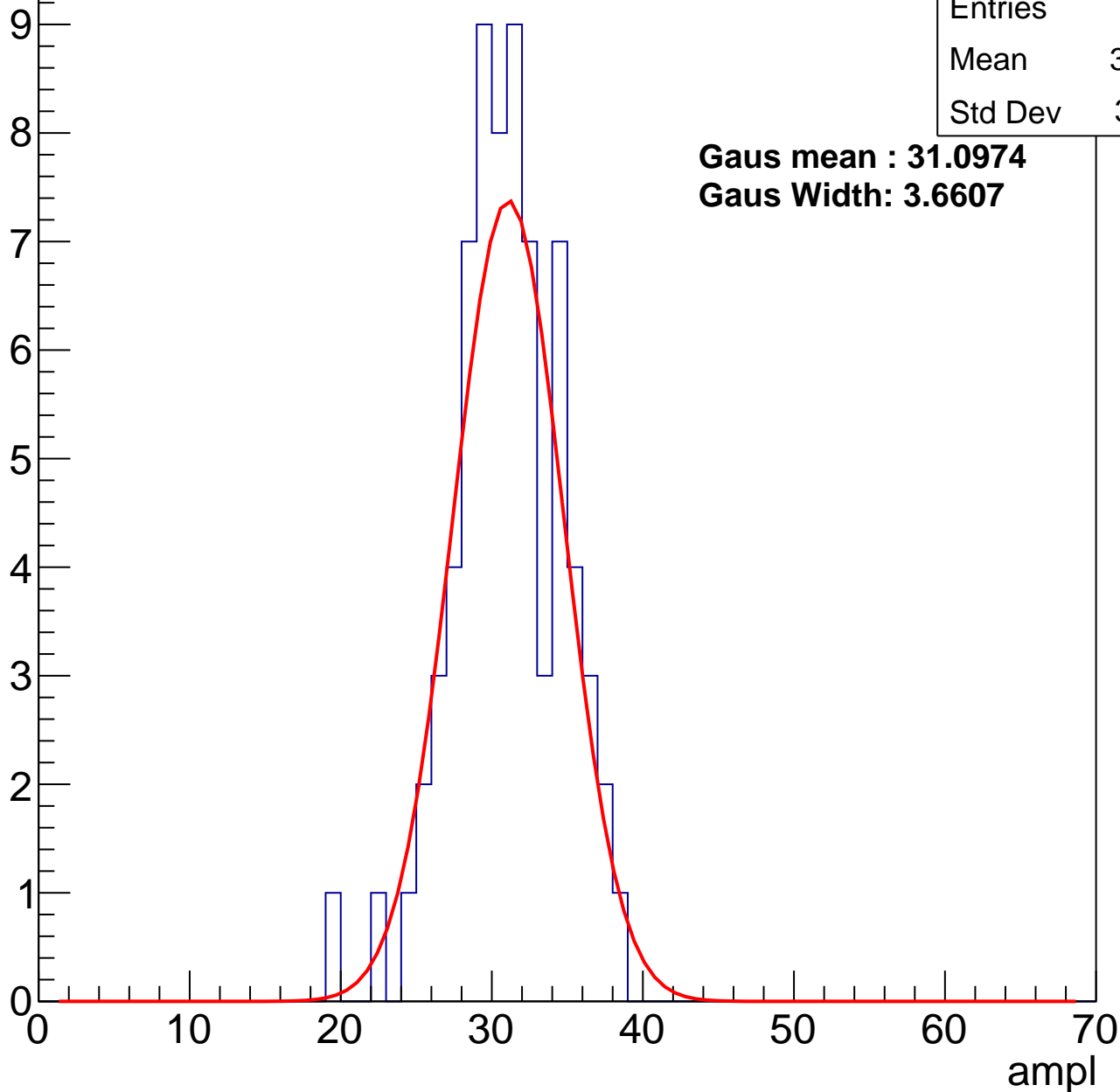
Entries	2
Mean	10.5
Std Dev	10.5

ampl

B0L002S, U7-ch8, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry



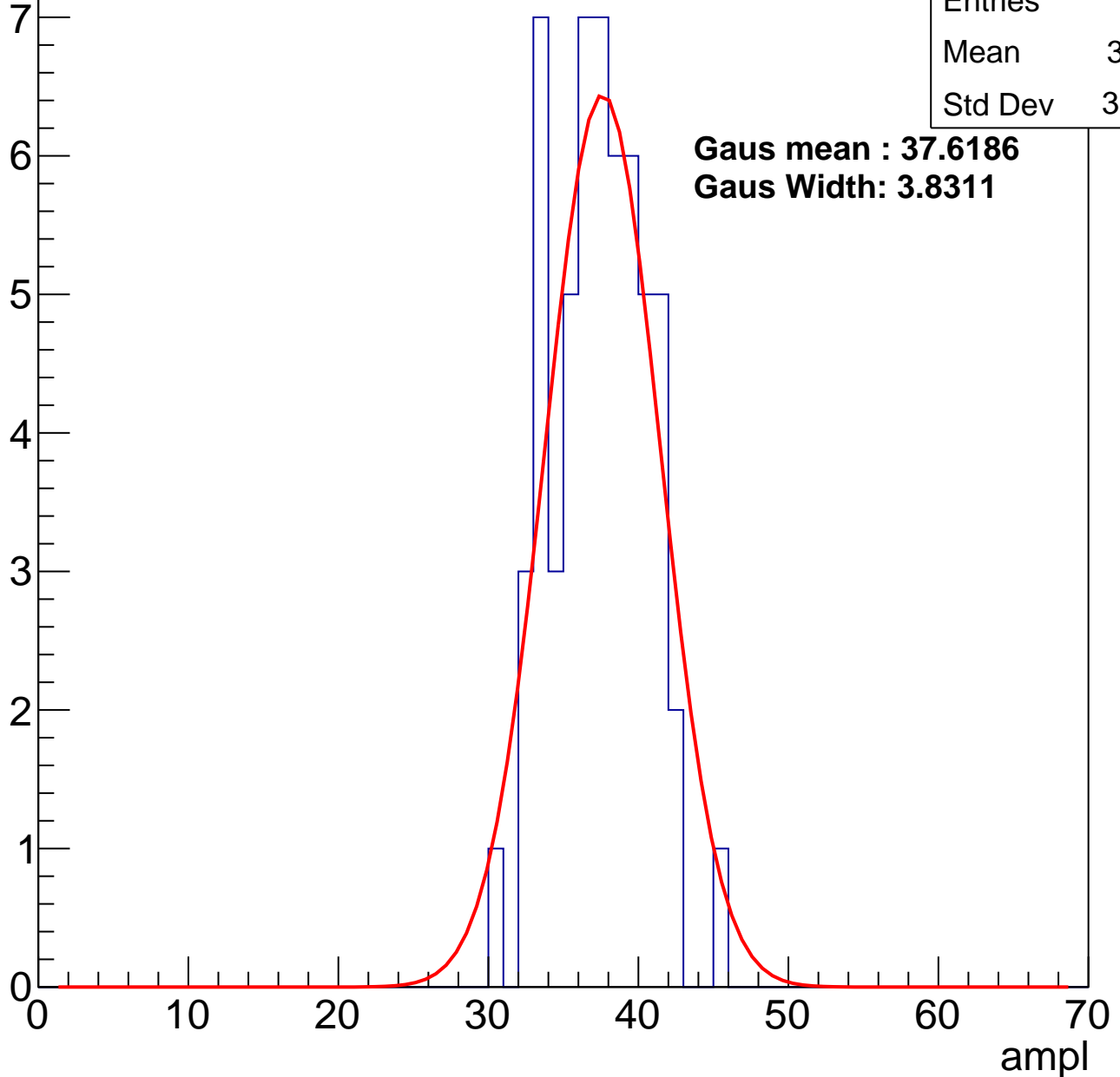
B0L002S, U7-ch8, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	36.91
Std Dev	3.103

Gaus mean : 37.6186
Gaus Width: 3.8311



B0L002S, U7-ch8, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	43.88
Std Dev	3.515

Gaus mean : 44.4277

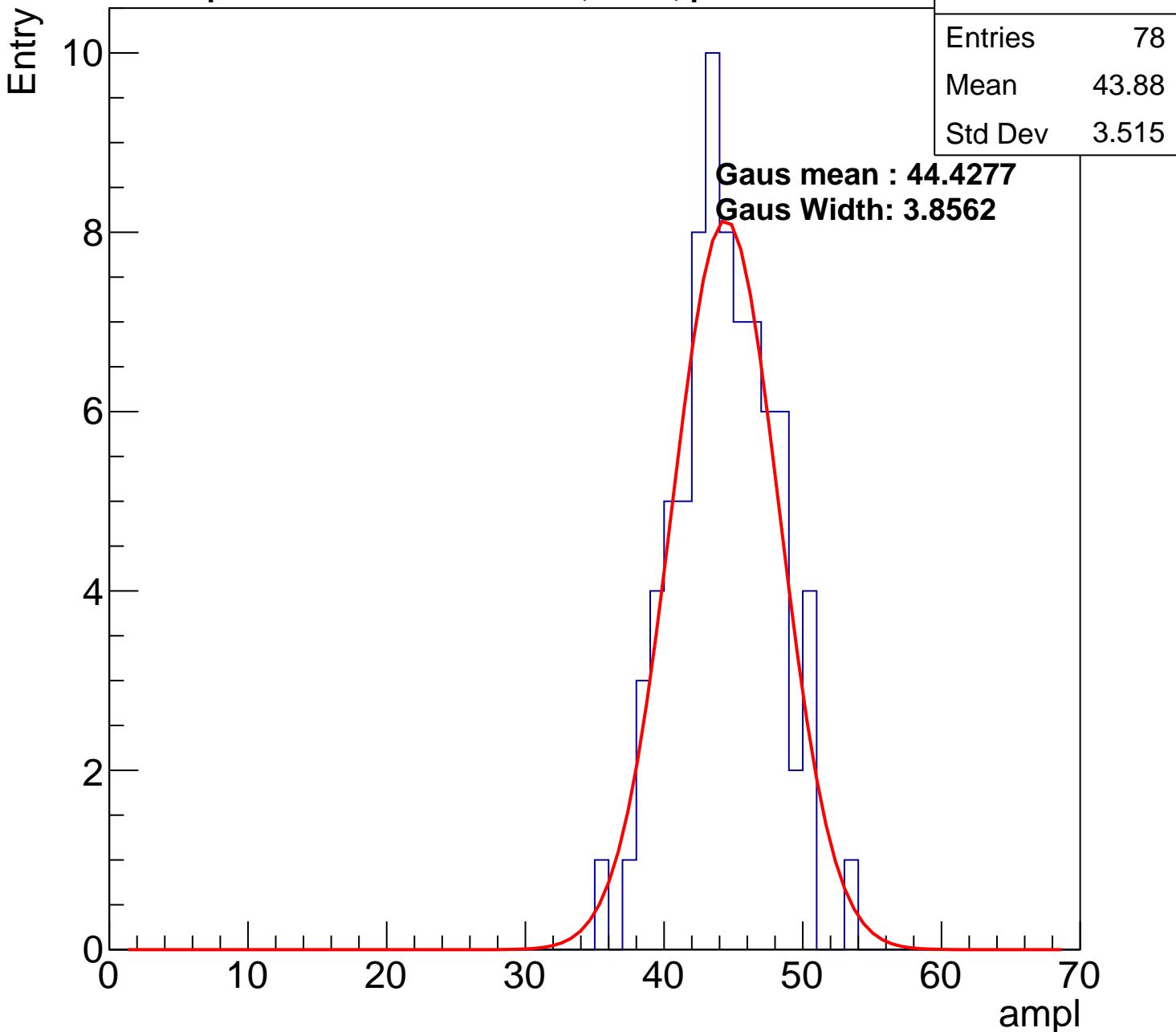
Gaus Width: 3.8562

Entry

10
8
6
4
2
0

ampl

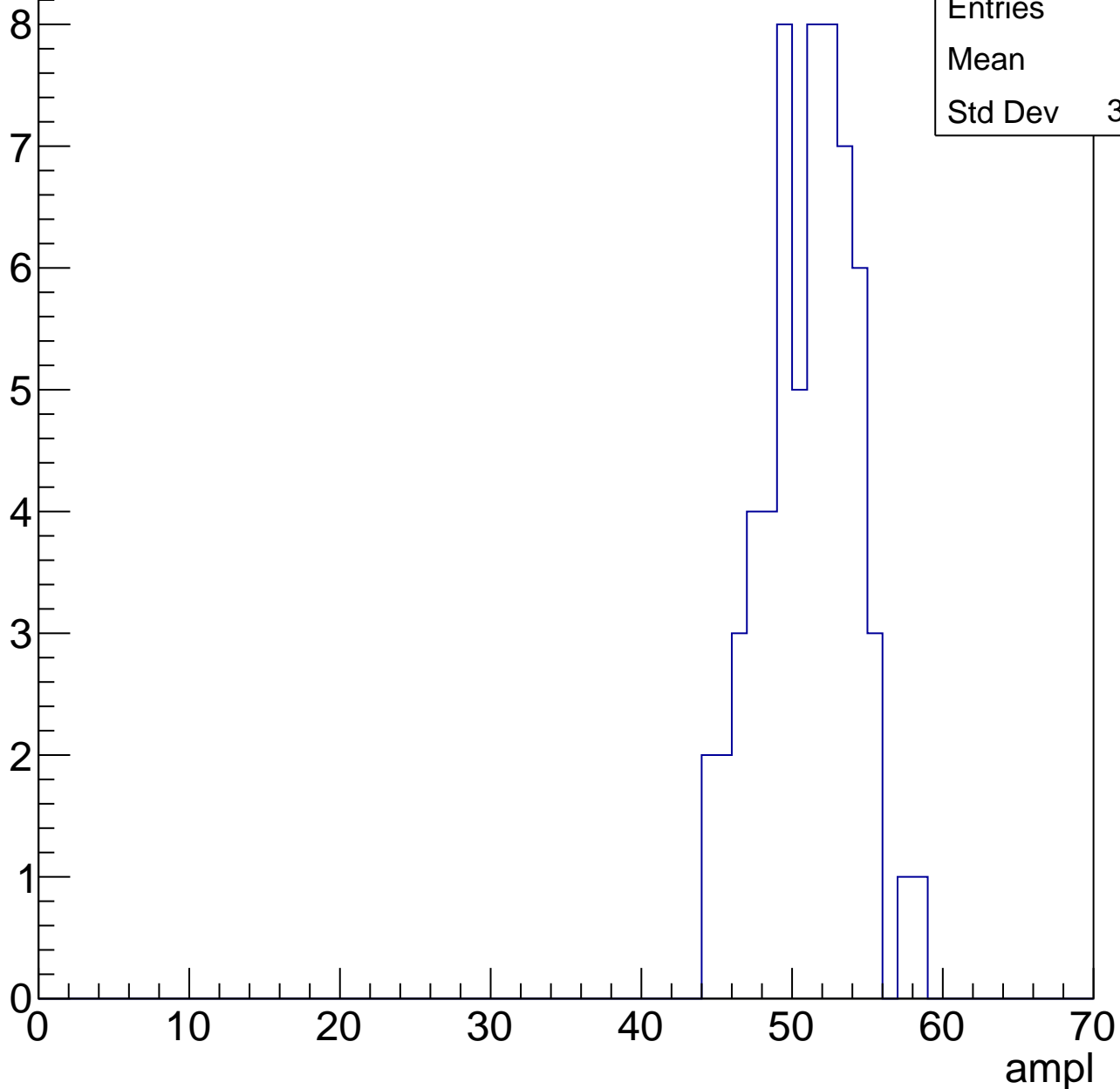
0 10 20 30 40 50 60 70



B0L002S, U7-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



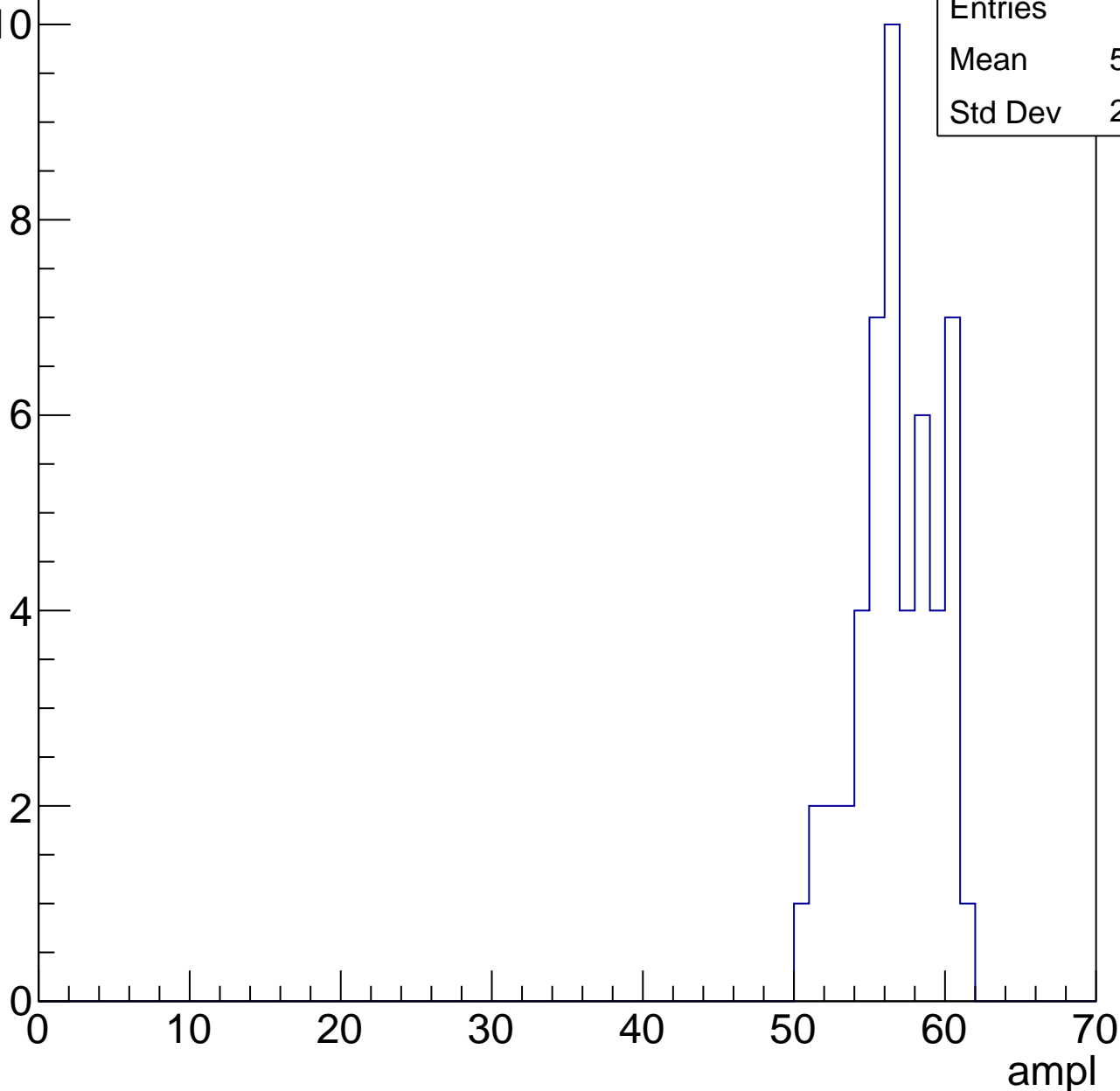
Entries	62
Mean	50.6
Std Dev	3.098

B0L002S, U7-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.32
Std Dev	2.664



B0L002S, U7-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	44
Mean	59.16
Std Dev	9.224

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

B0L002S, U7-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch9, adc0

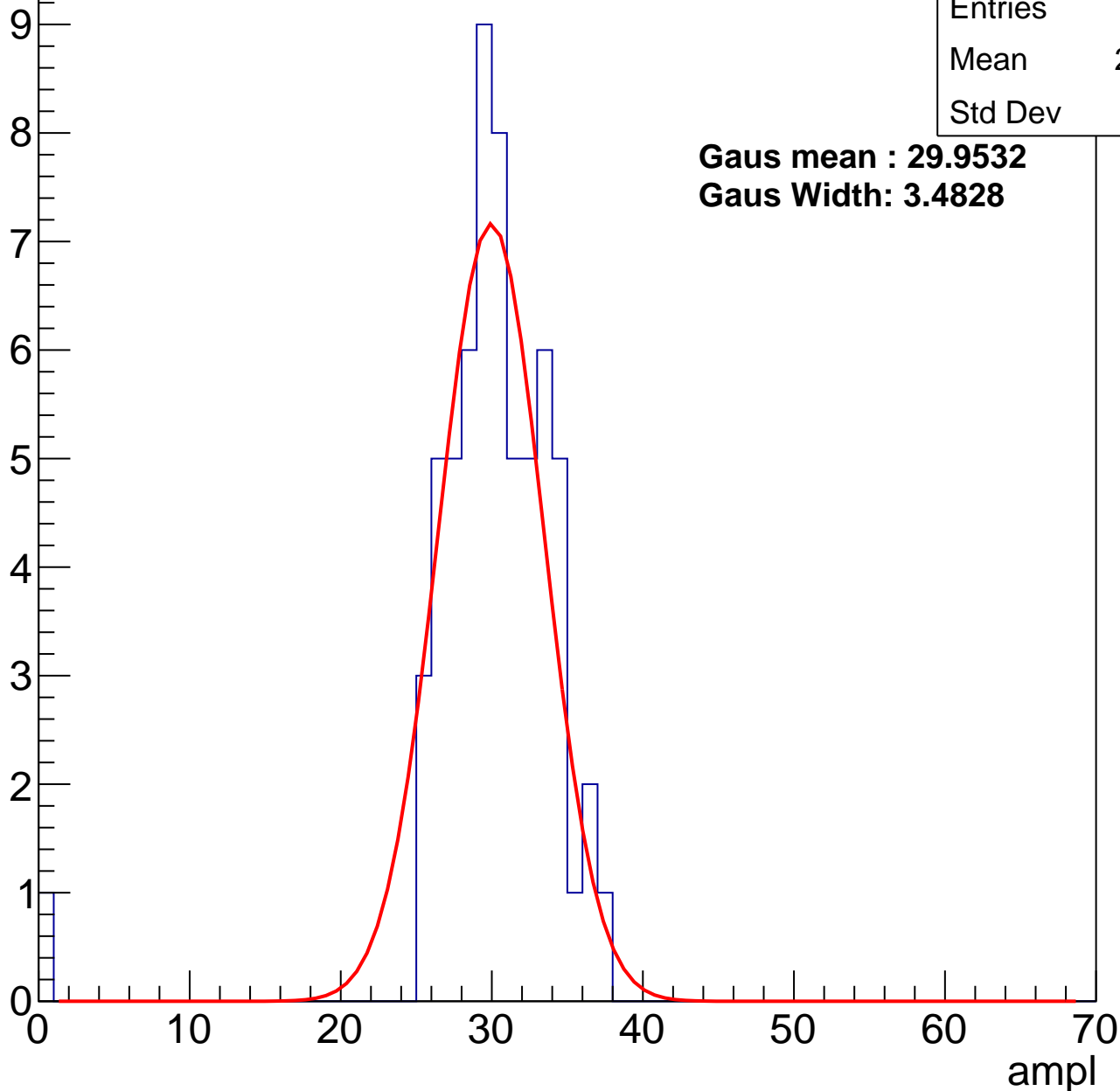
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.61
Std Dev	4.8

Gaus mean : 29.9532

Gaus Width: 3.4828



B0L002S, U7-ch9, adc1

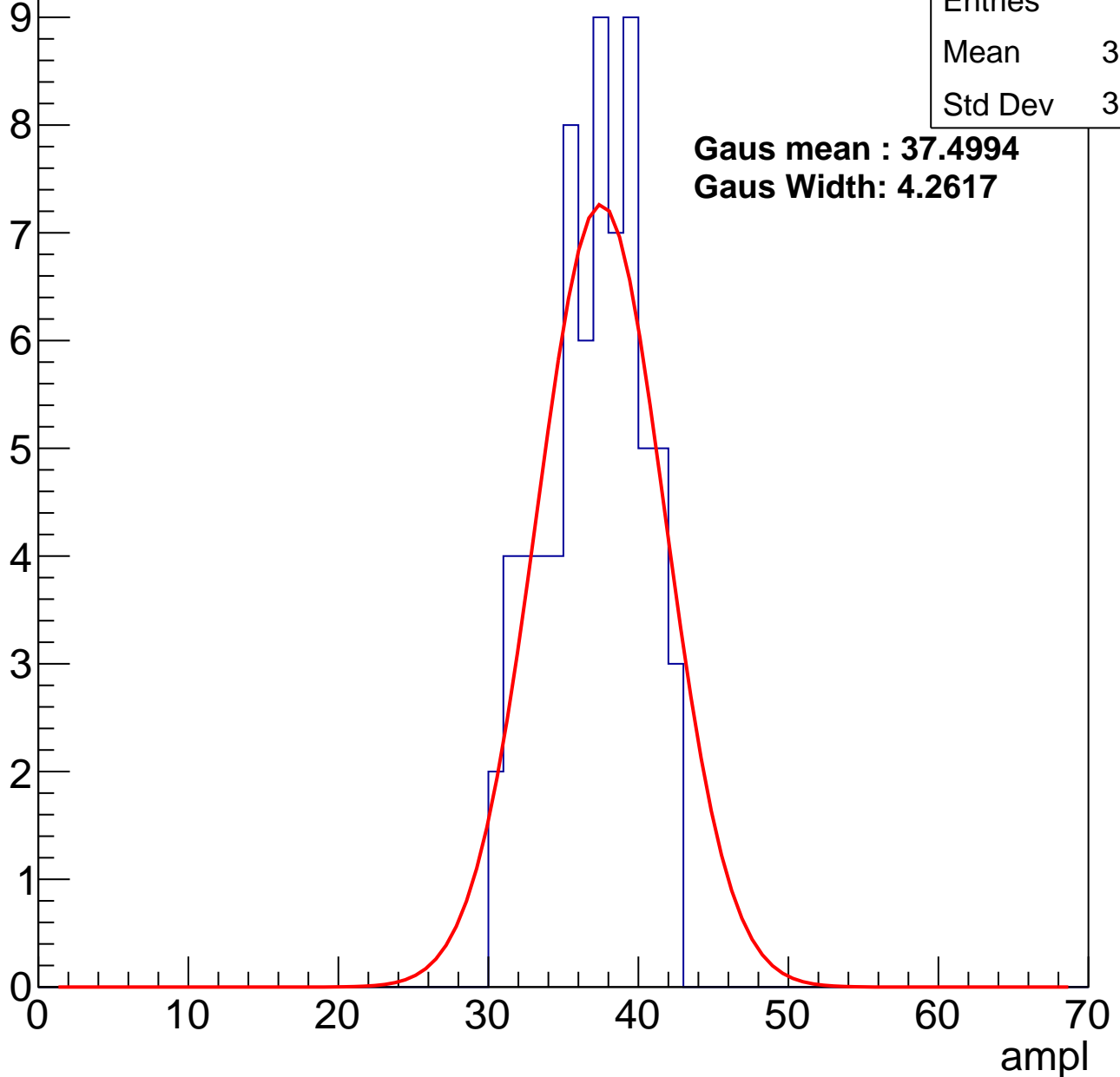
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36.53
Std Dev	3.179

Gaus mean : 37.4994

Gaus Width: 4.2617



B0L002S, U7-ch9, adc2

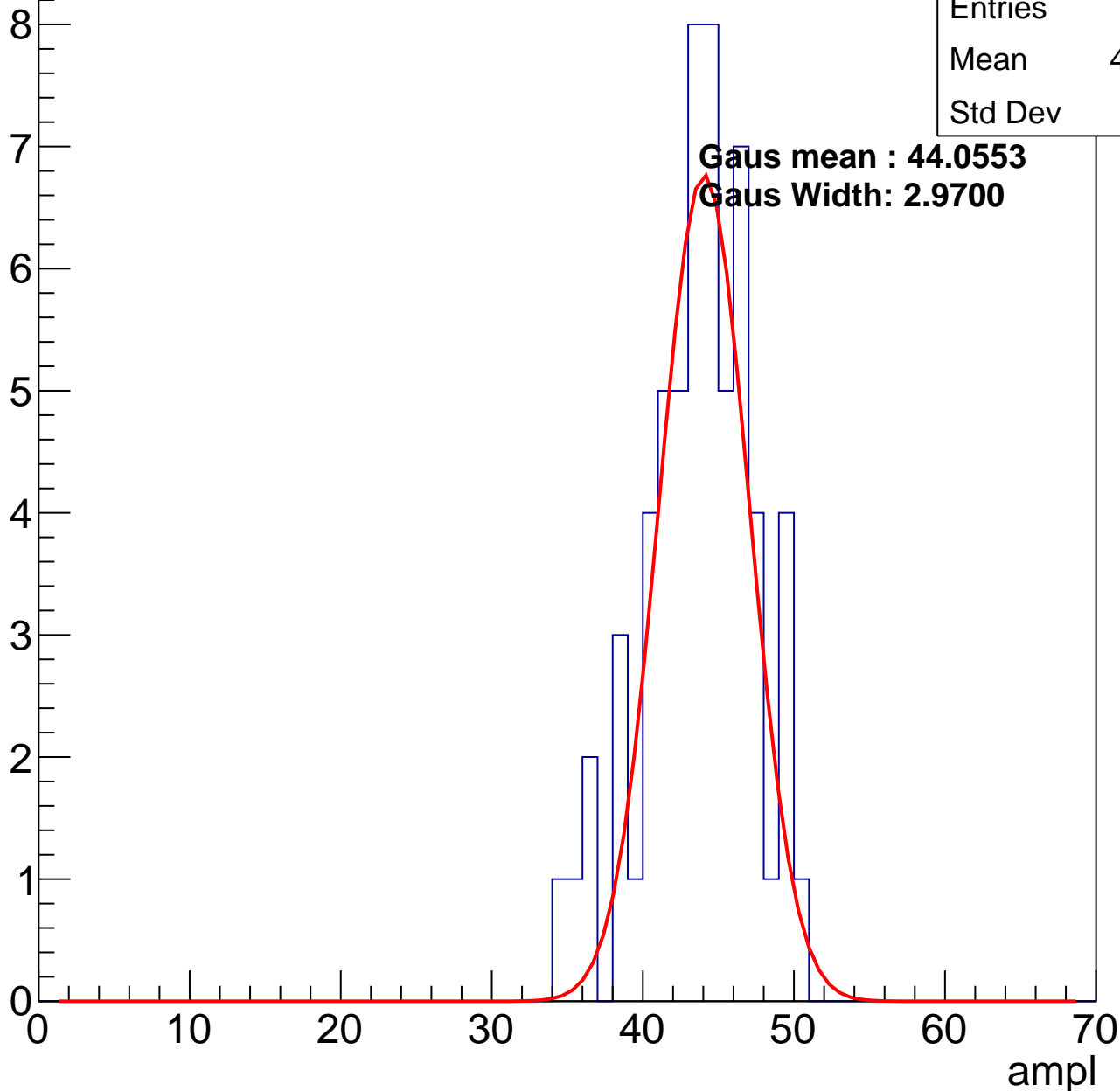
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.23
Std Dev	3.57

Gaus mean : 44.0553

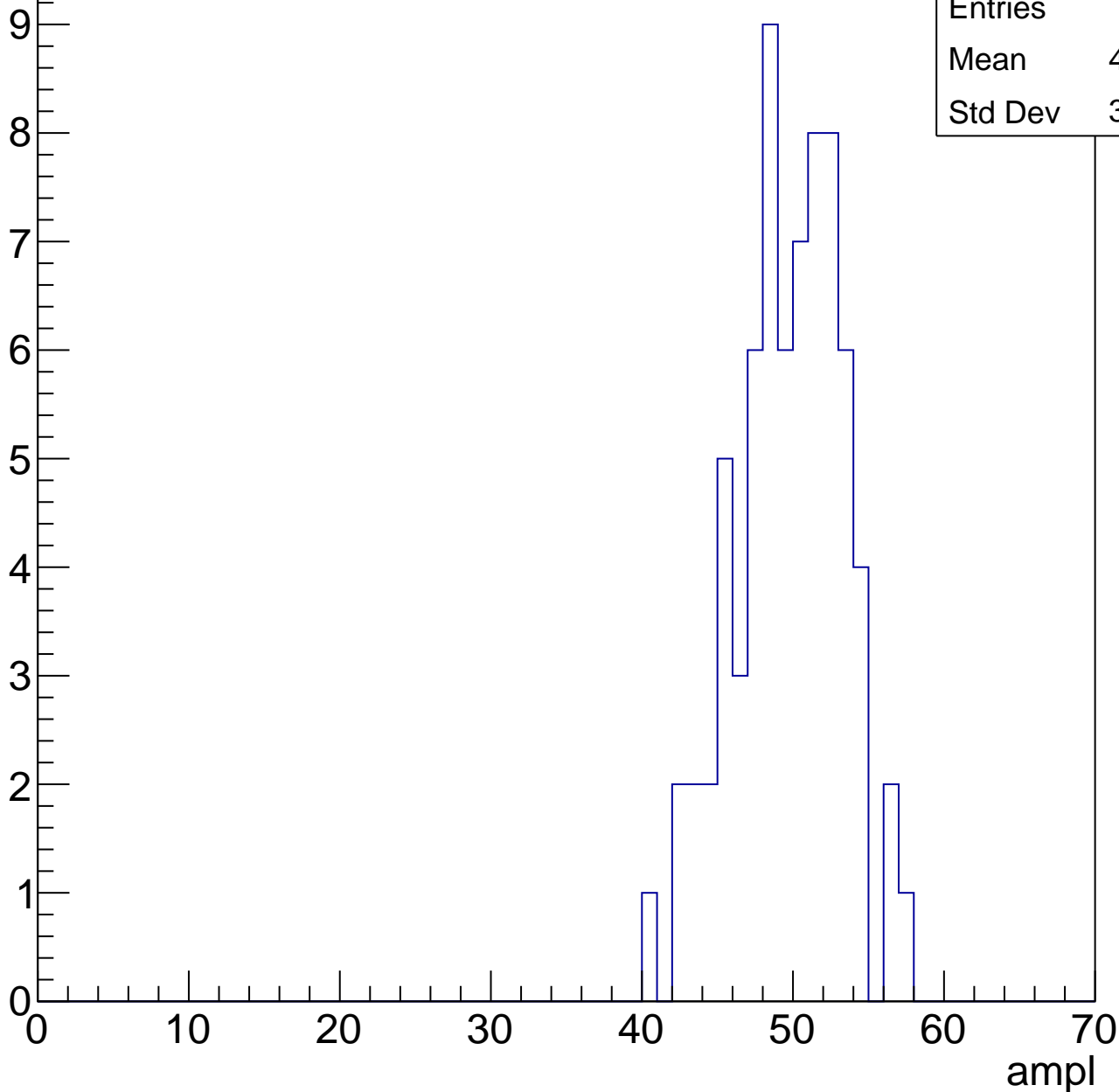
Gaus Width: 2.9700



B0L002S, U7-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

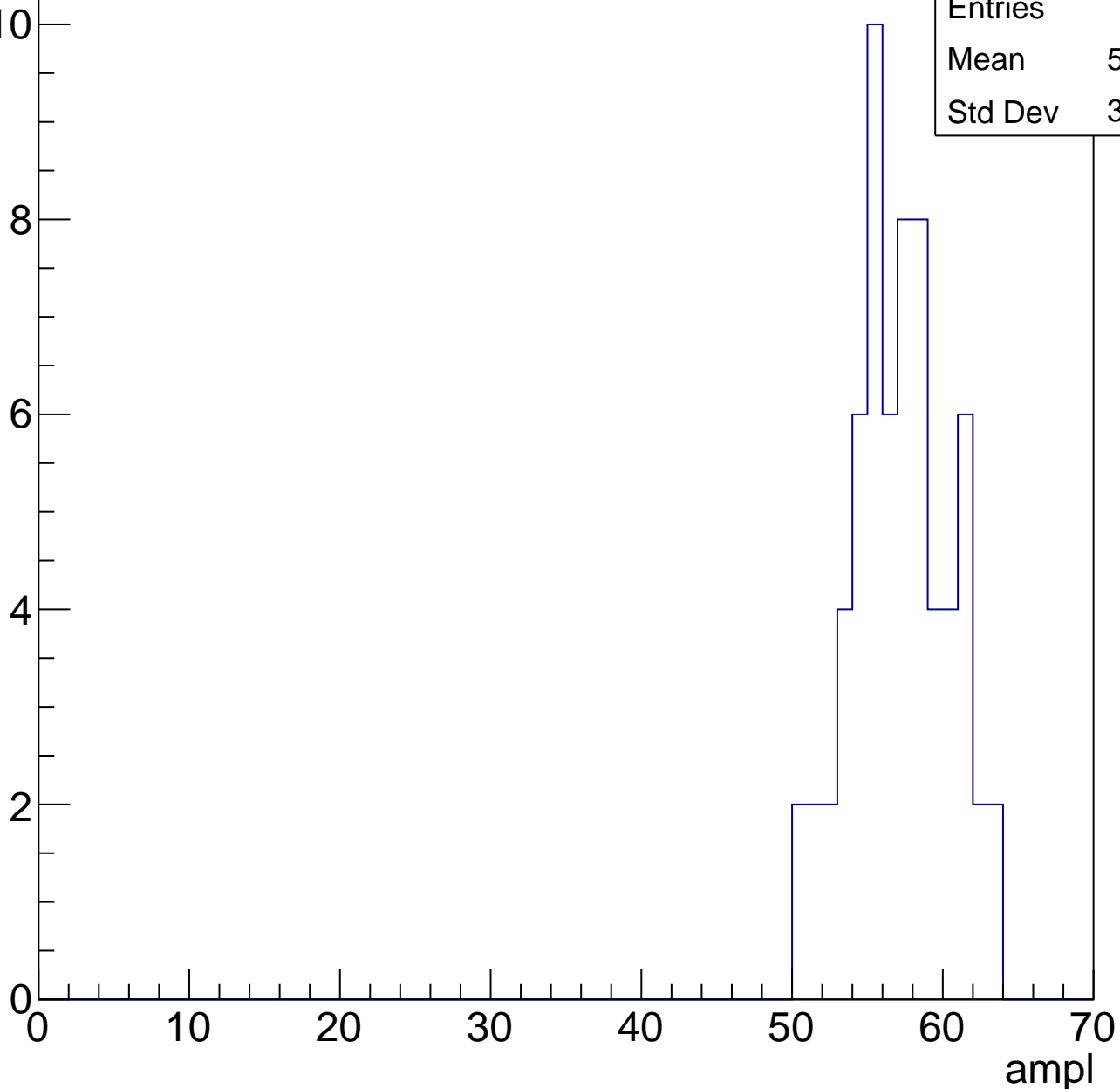


B0L002S, U7-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	56.67
Std Dev	3.149

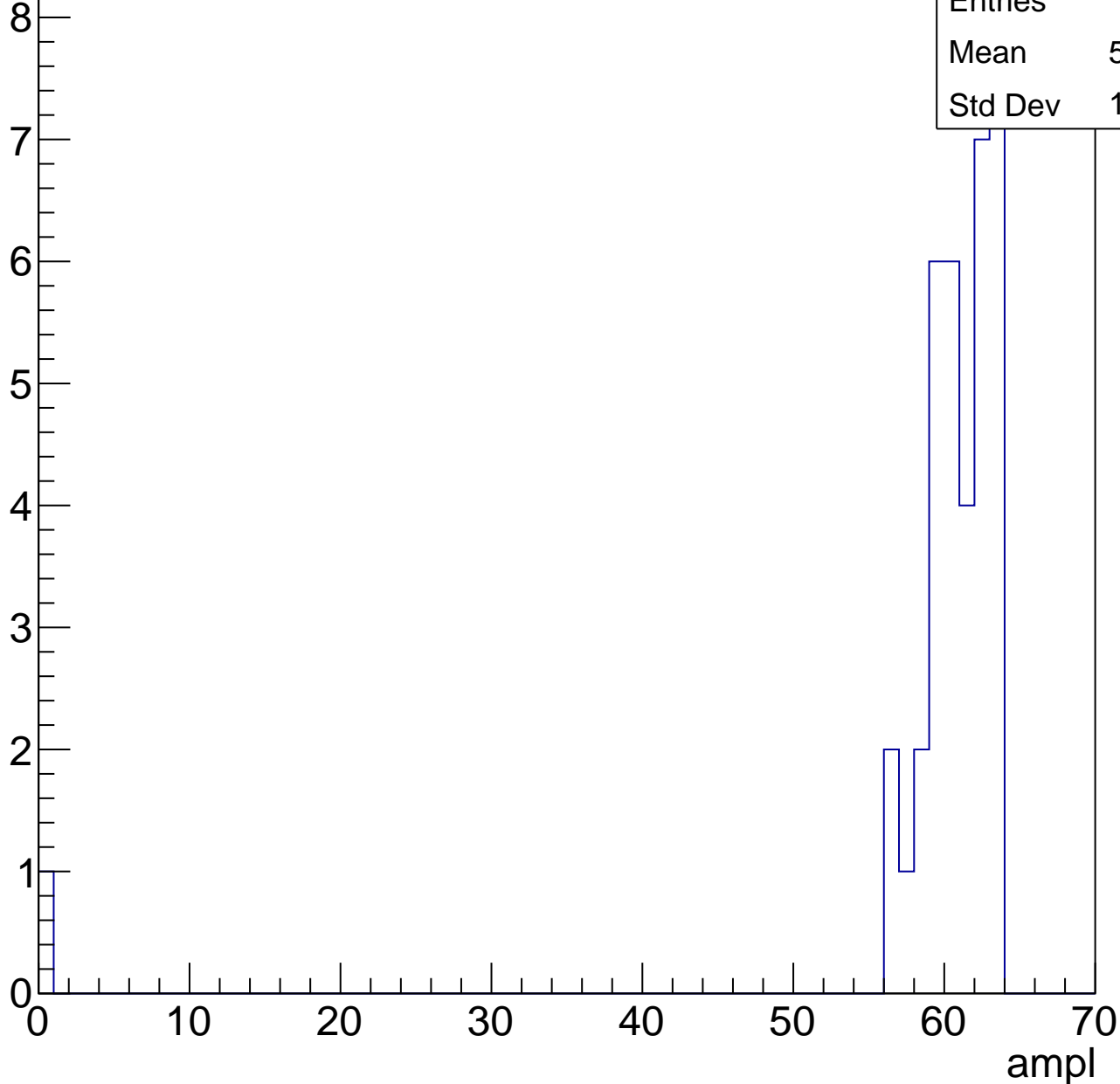


B0L002S, U7-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.95
Std Dev	10.02



B0L002S, U7-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch10, adc0

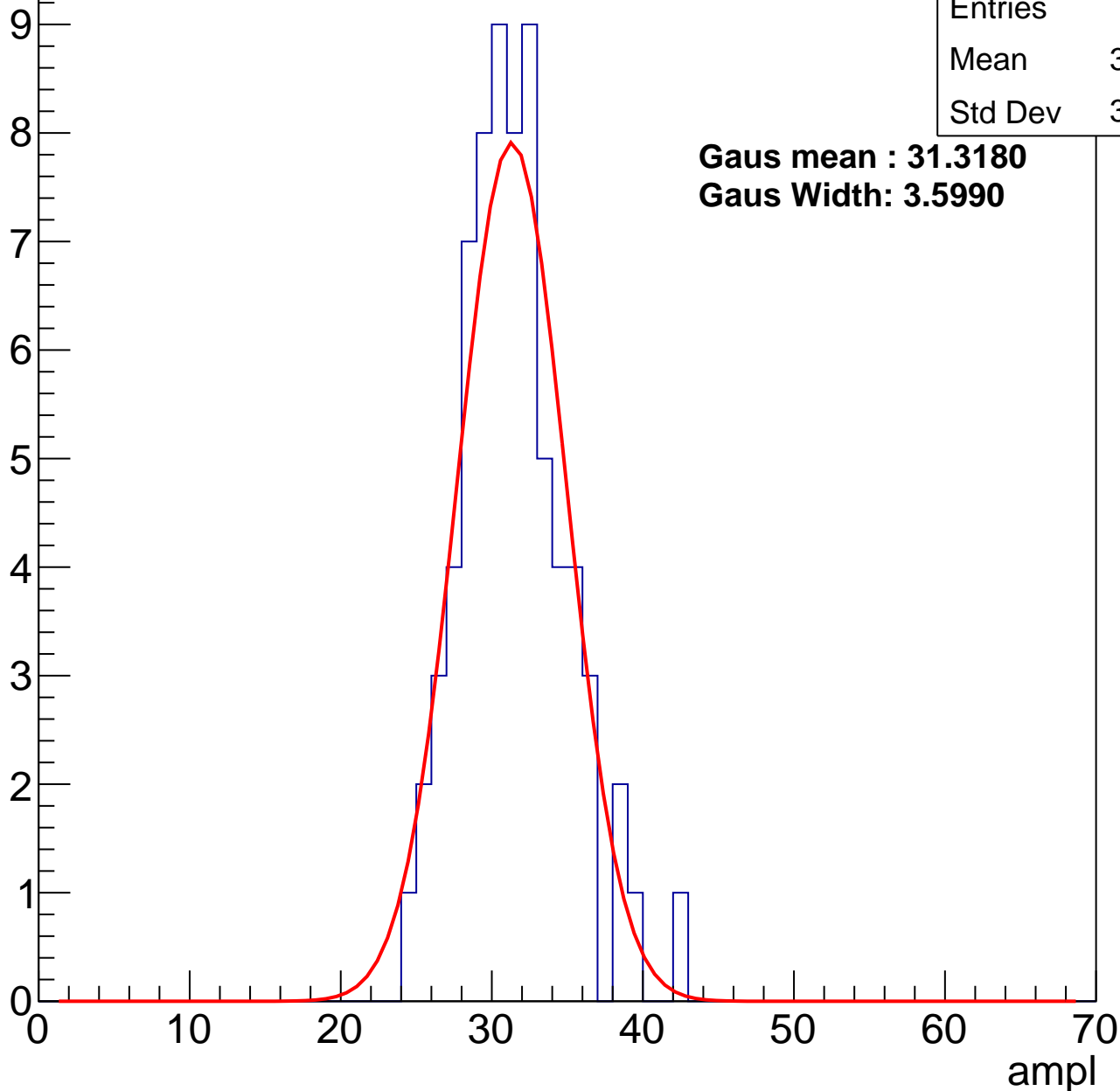
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.99
Std Dev	3.462

Gaus mean : 31.3180

Gaus Width: 3.5990



B0L002S, U7-ch10, adc1

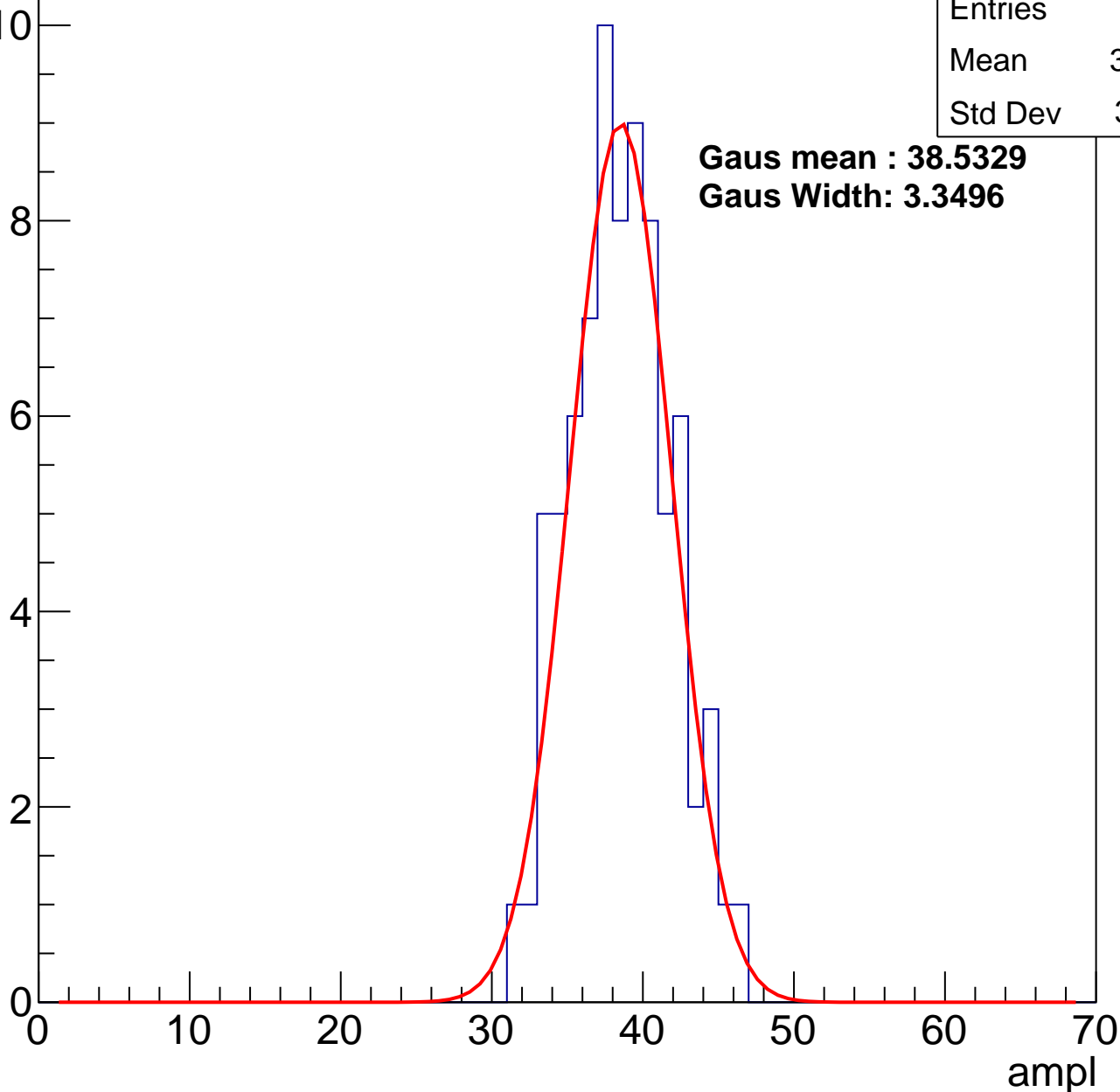
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	38.09
Std Dev	3.271

Gaus mean : 38.5329

Gaus Width: 3.3496



B0L002S, U7-ch10, adc2

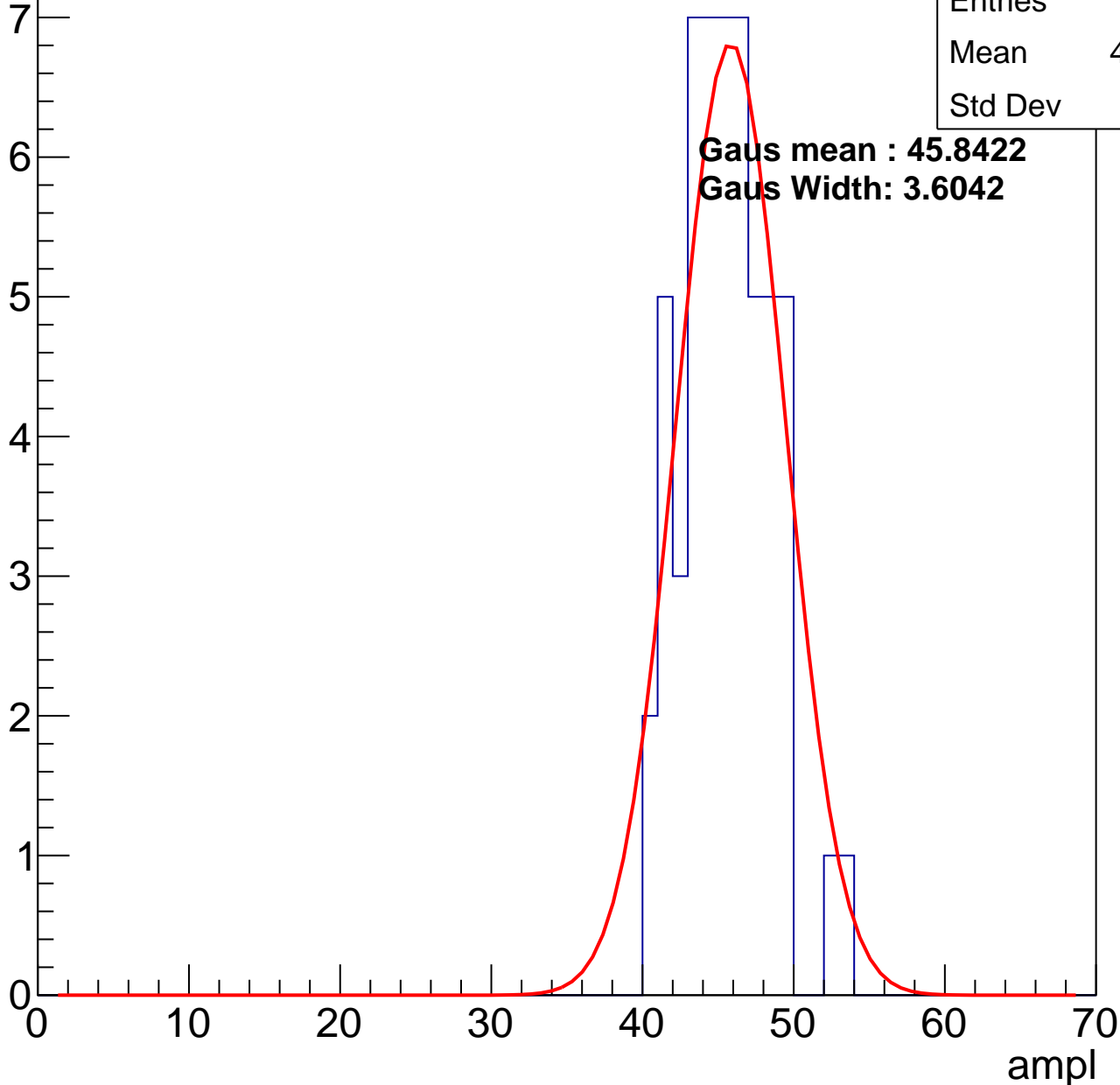
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.13
Std Dev	2.88

Gaus mean : 45.8422

Gaus Width: 3.6042

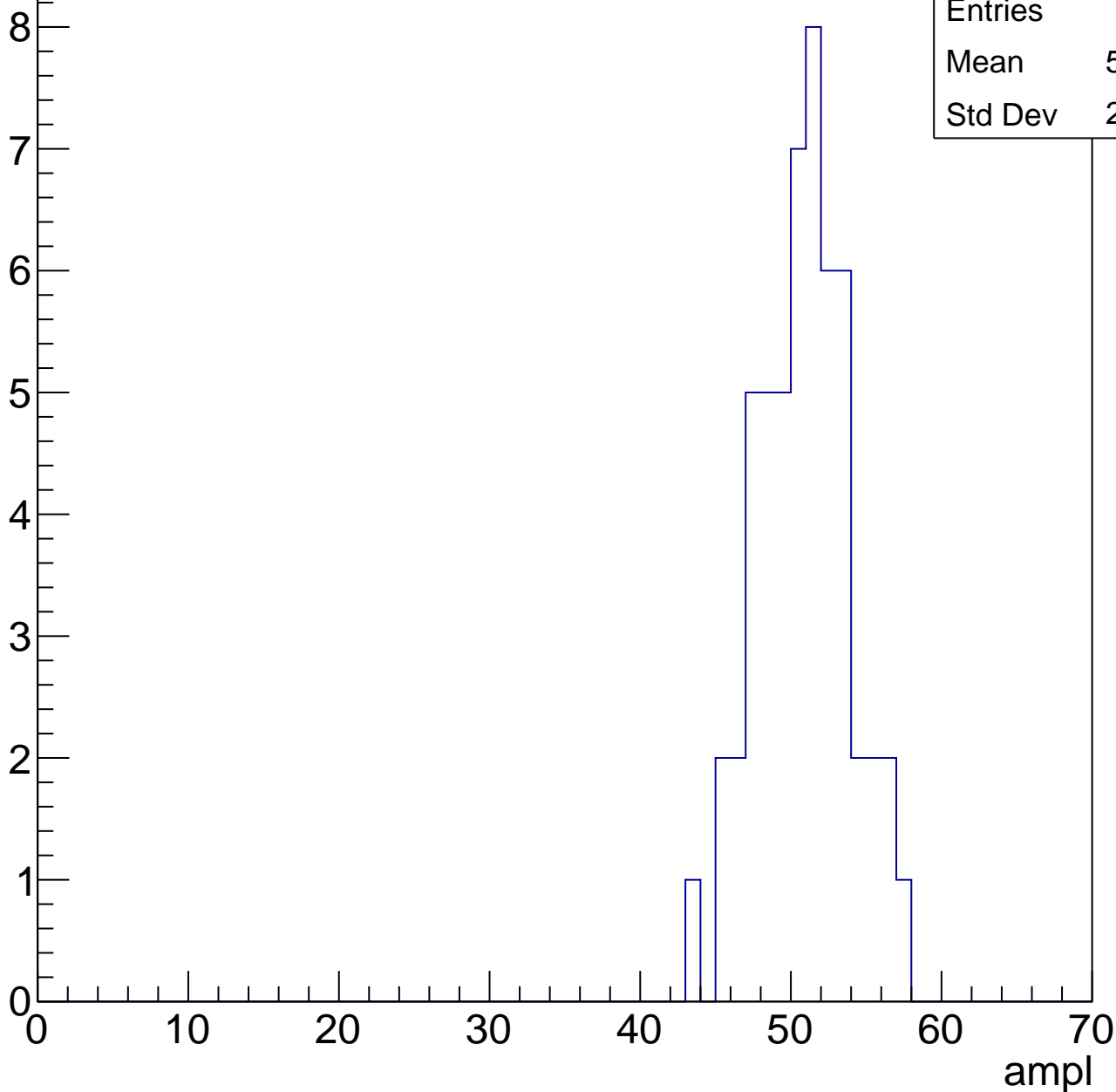


B0L002S, U7-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

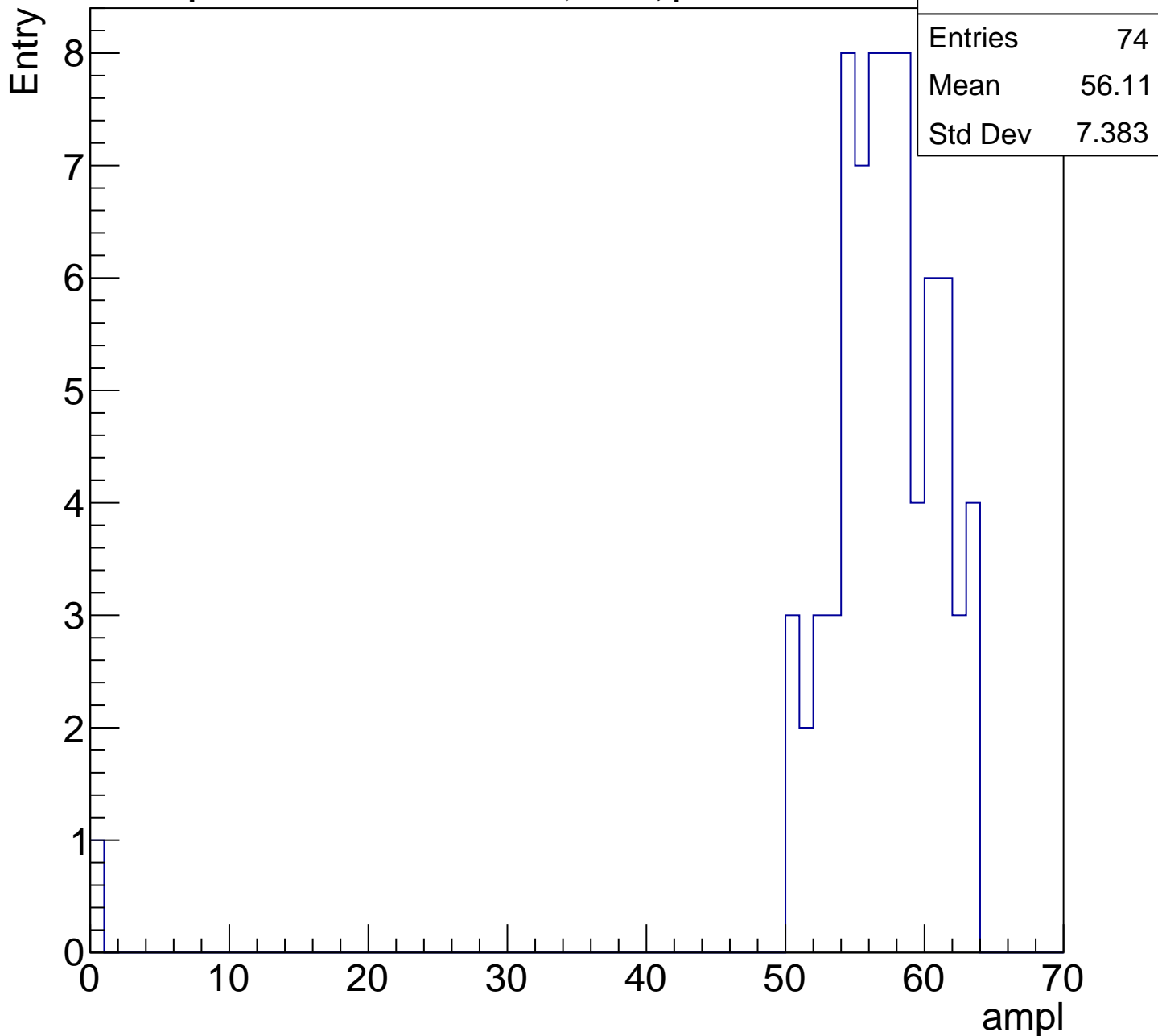
Entry

Entries	54
Mean	50.37
Std Dev	2.989



B0L002S, U7-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

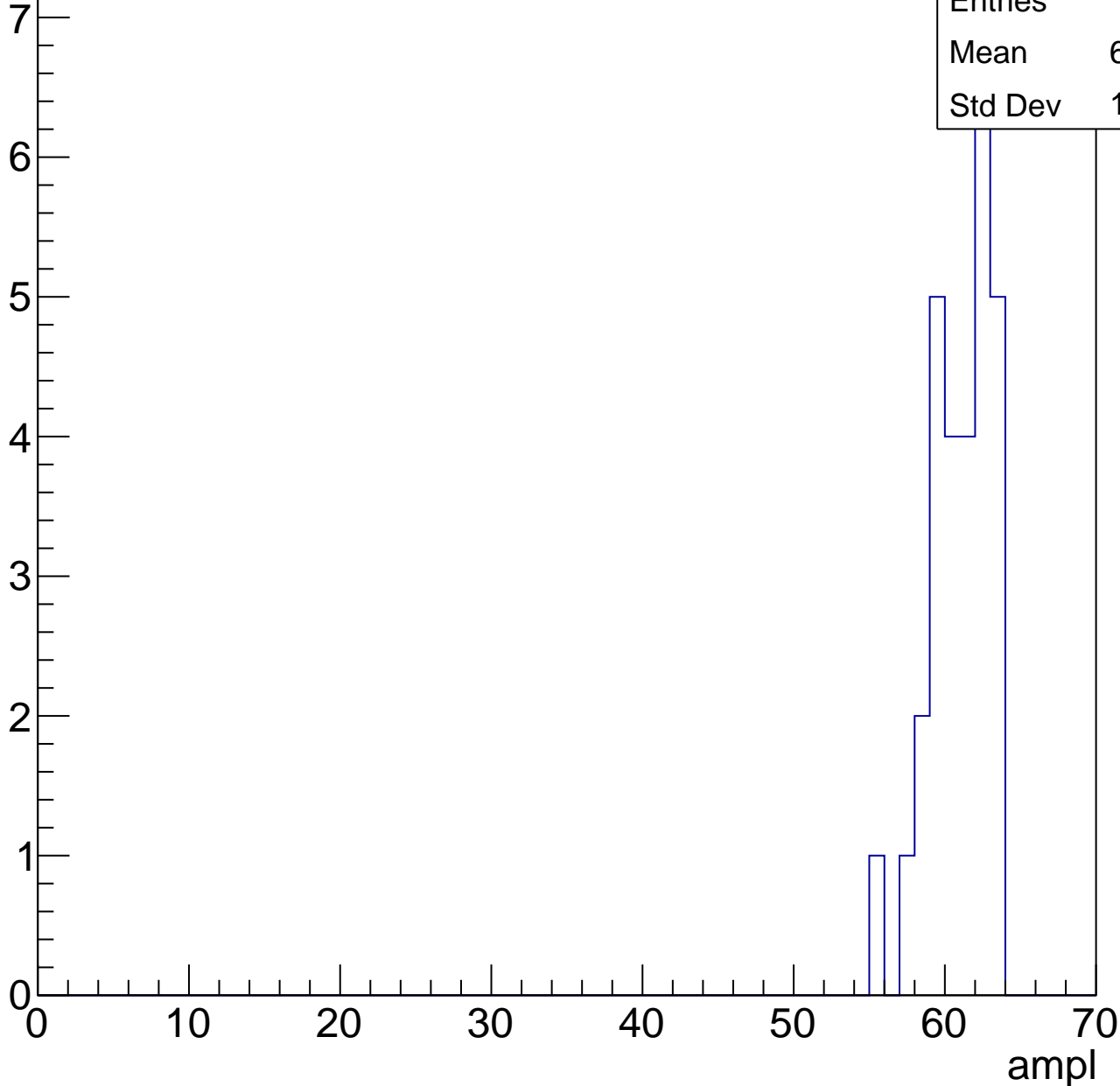


B0L002S, U7-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

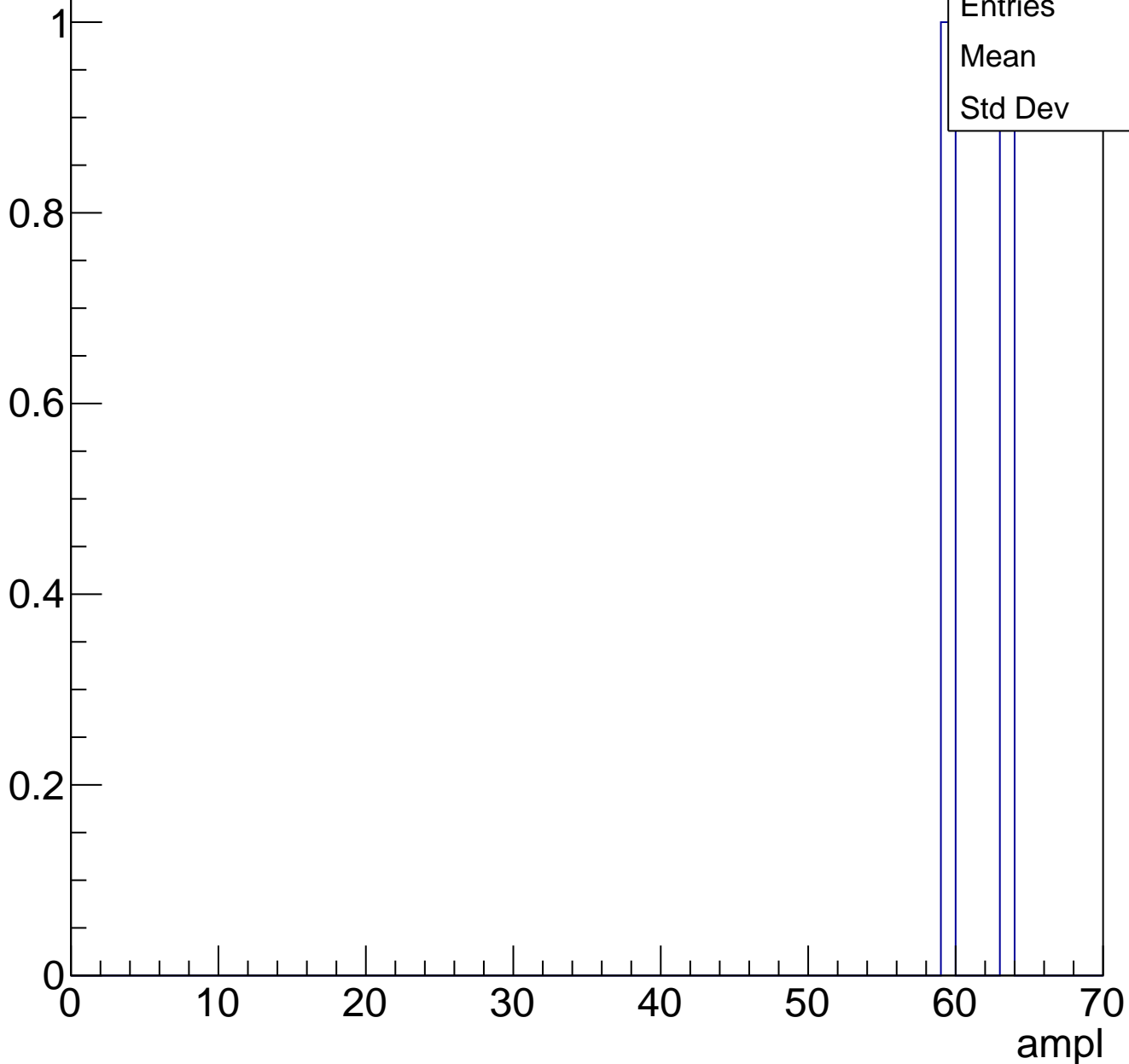
Entries	29
Mean	60.55
Std Dev	1.993



B0L002S, U7-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U7-ch11, adc0

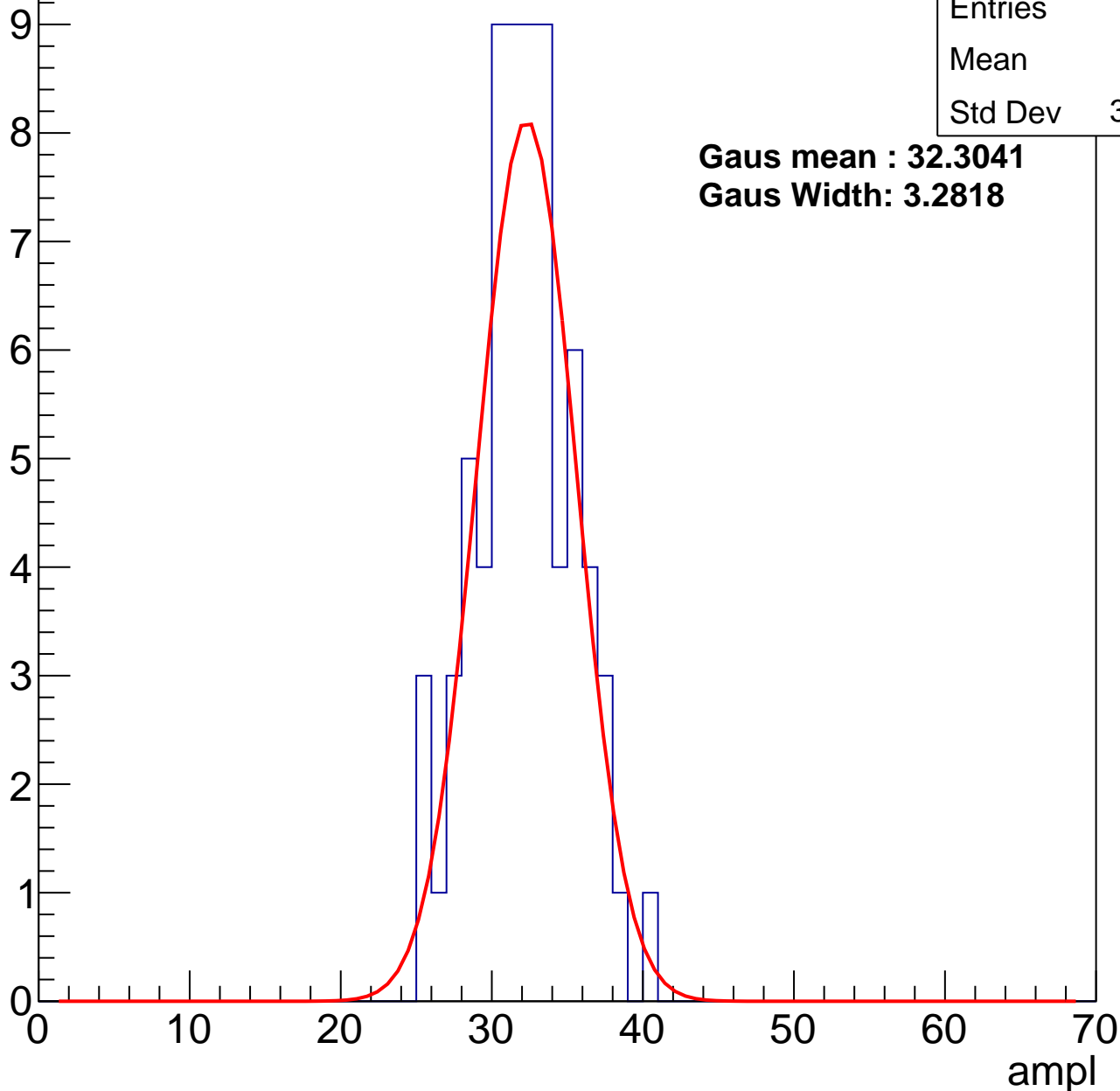
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.7
Std Dev	3.208

Gaus mean : 32.3041

Gaus Width: 3.2818



B0L002S, U7-ch11, adc1

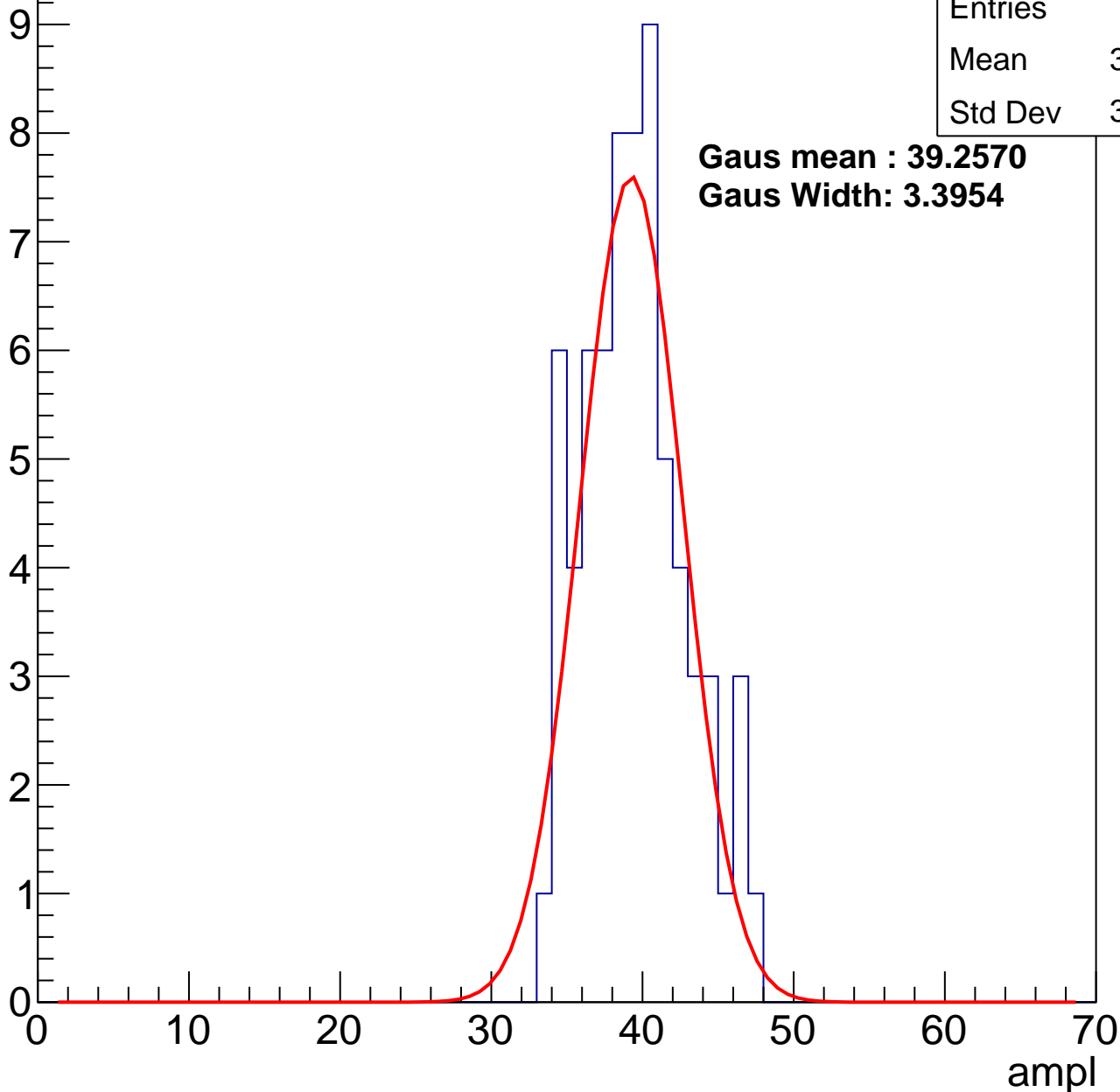
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	39.04
Std Dev	3.367

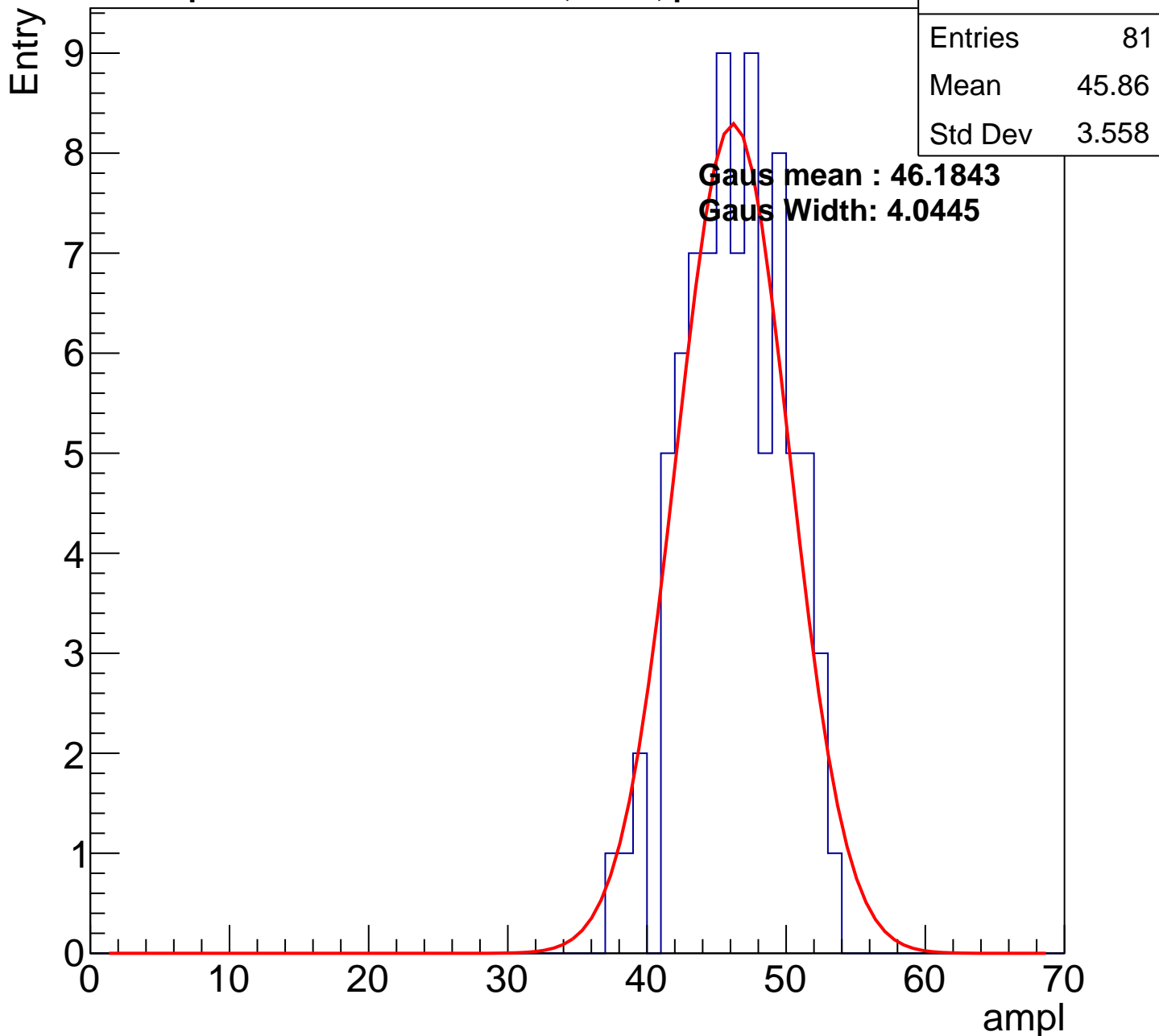
Gaus mean : 39.2570

Gaus Width: 3.3954



B0L002S, U7-ch11, adc2

calib_packv5_042523_0143.root, FC#8, port C1

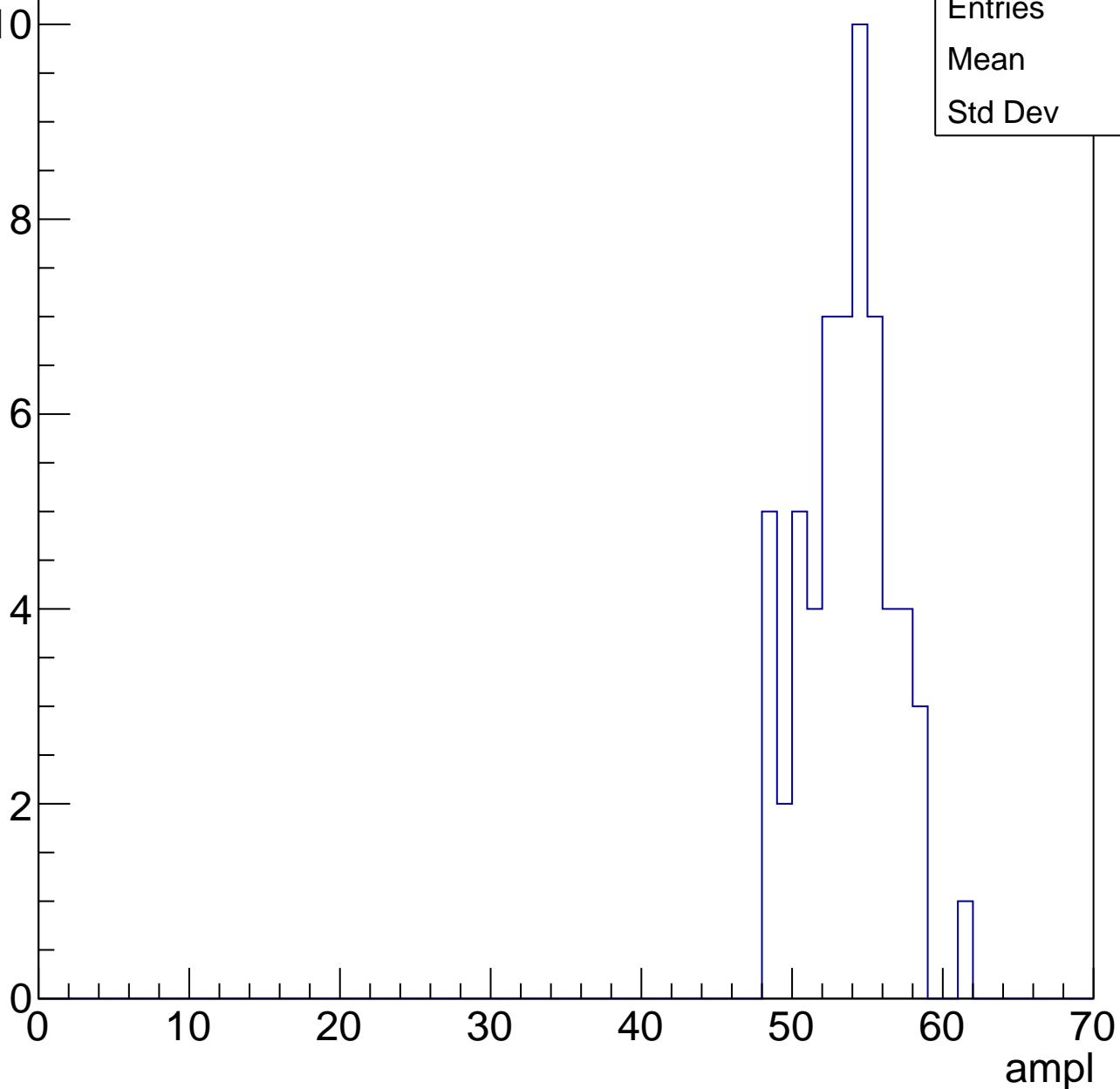


B0L002S, U7-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	53.2
Std Dev	2.91

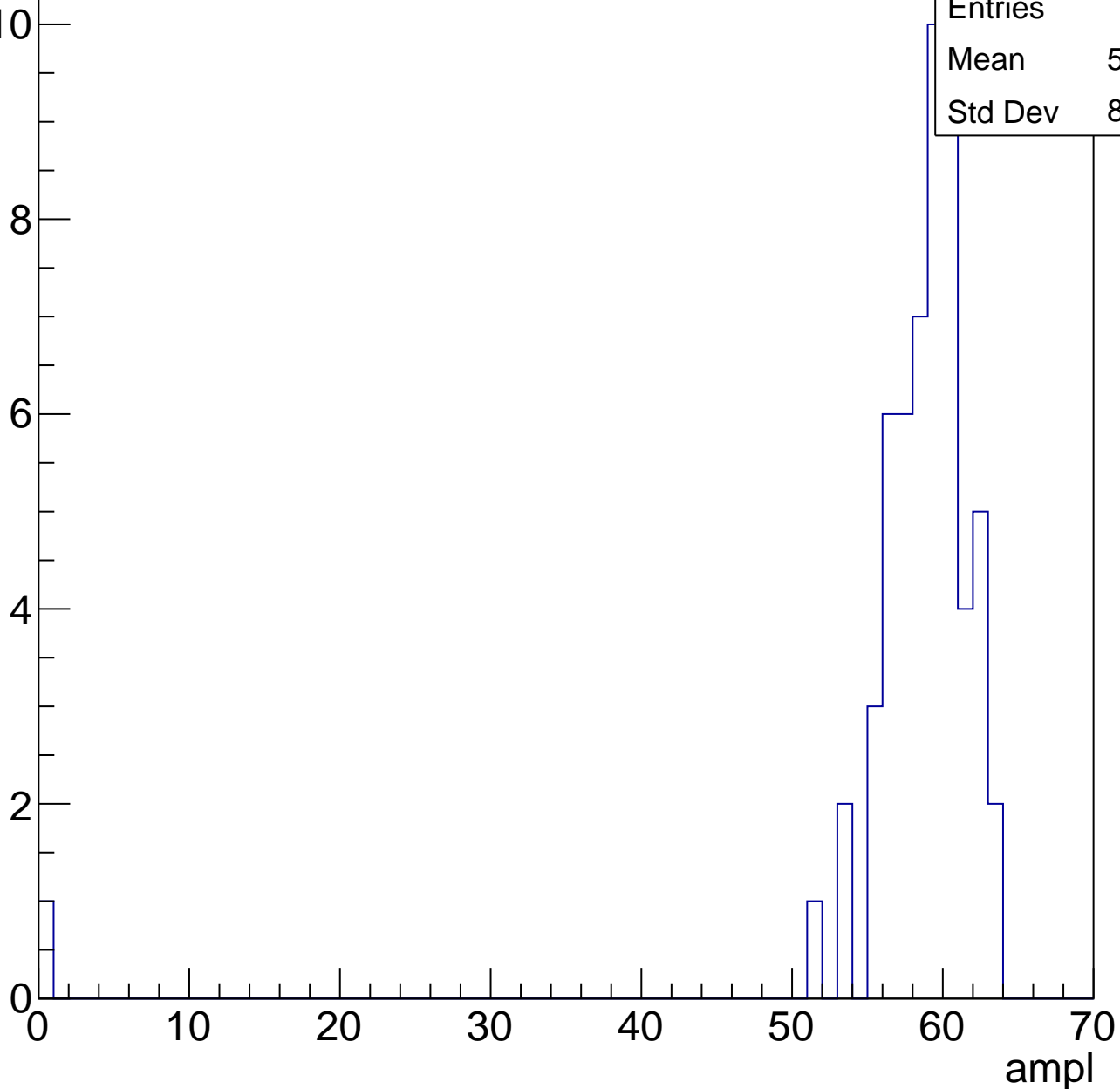


B0L002S, U7-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.43
Std Dev	8.143



B0L002S, U7-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

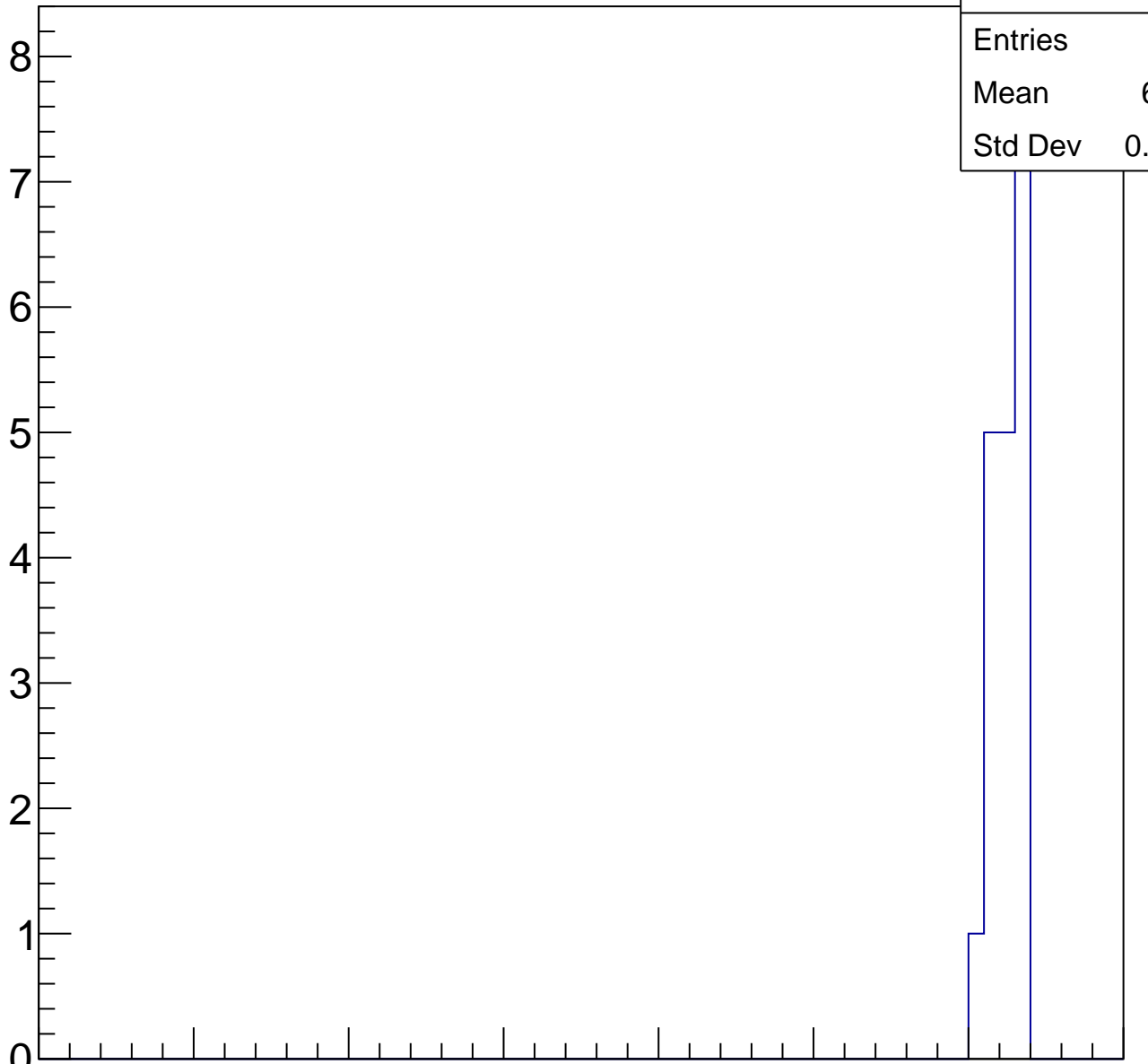
Entry

Entries	19
Mean	62.05
Std Dev	0.9444

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch12, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.59
Std Dev	3.04

Gaus mean : 32.1663

Gaus Width: 3.2993

10

8

6

4

2

0

0

10

20

30

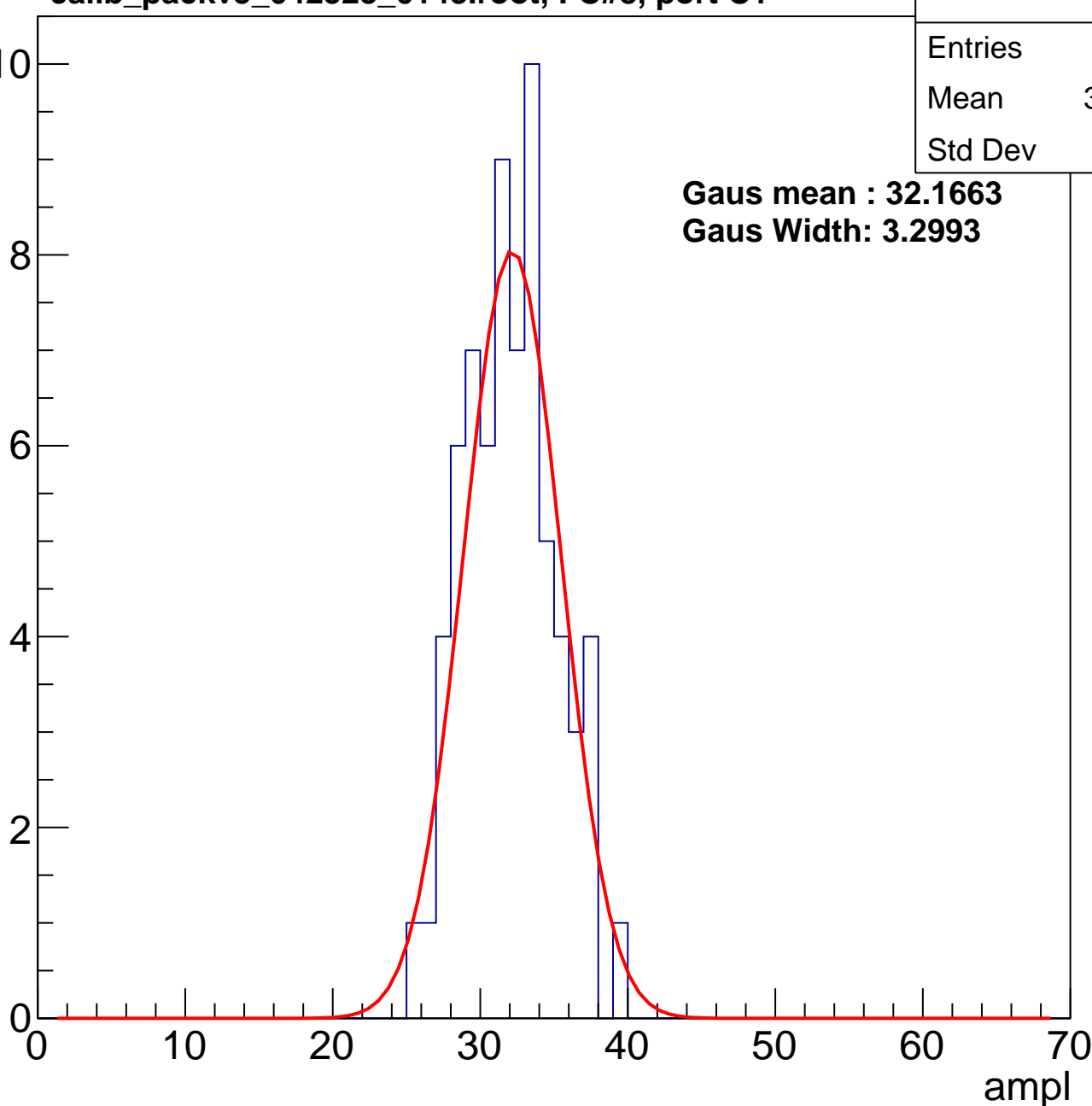
40

50

60

70

ampl



B0L002S, U7-ch12, adc1

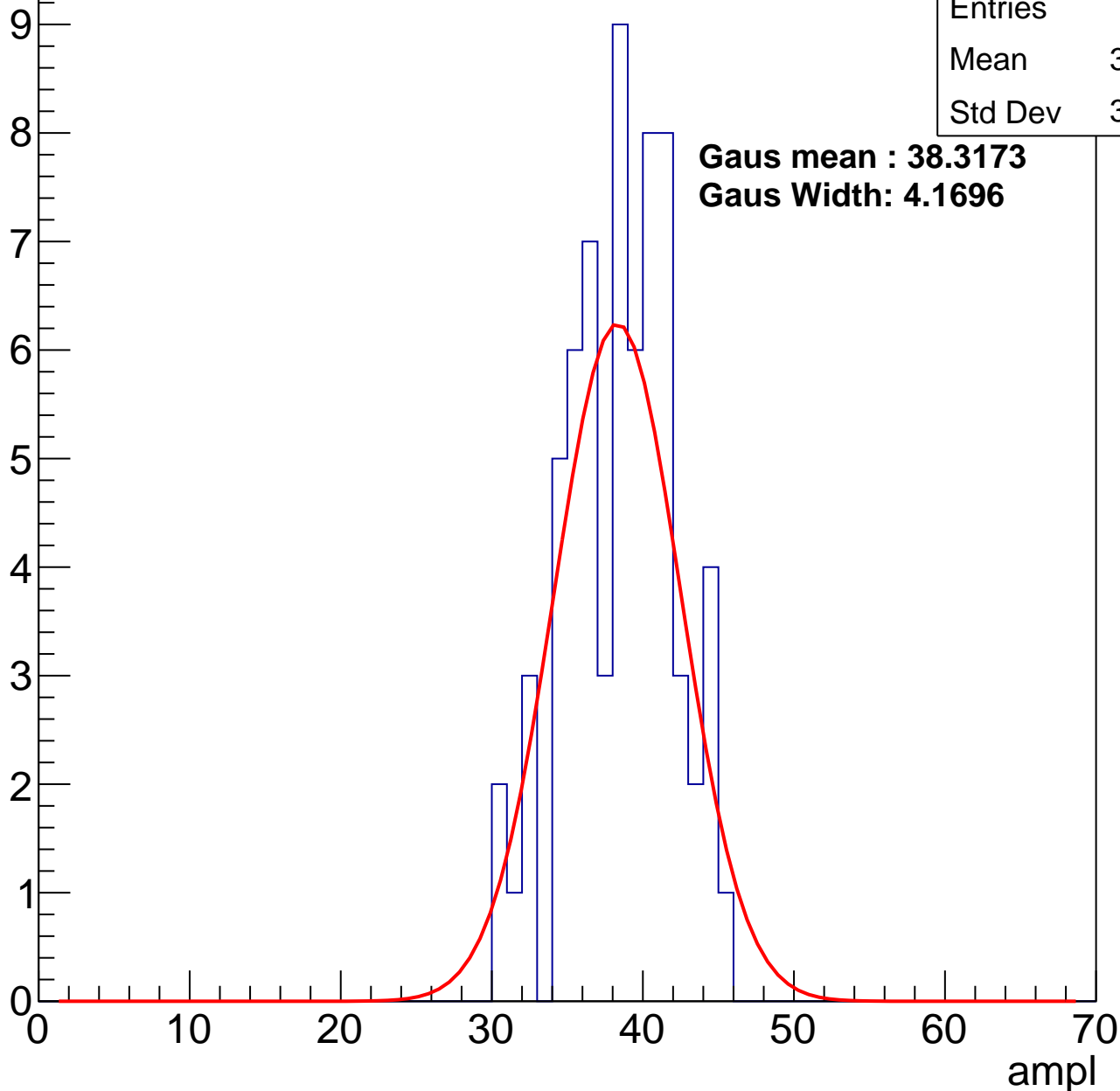
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.04
Std Dev	3.537

Gaus mean : 38.3173

Gaus Width: 4.1696



B0L002S, U7-ch12, adc2

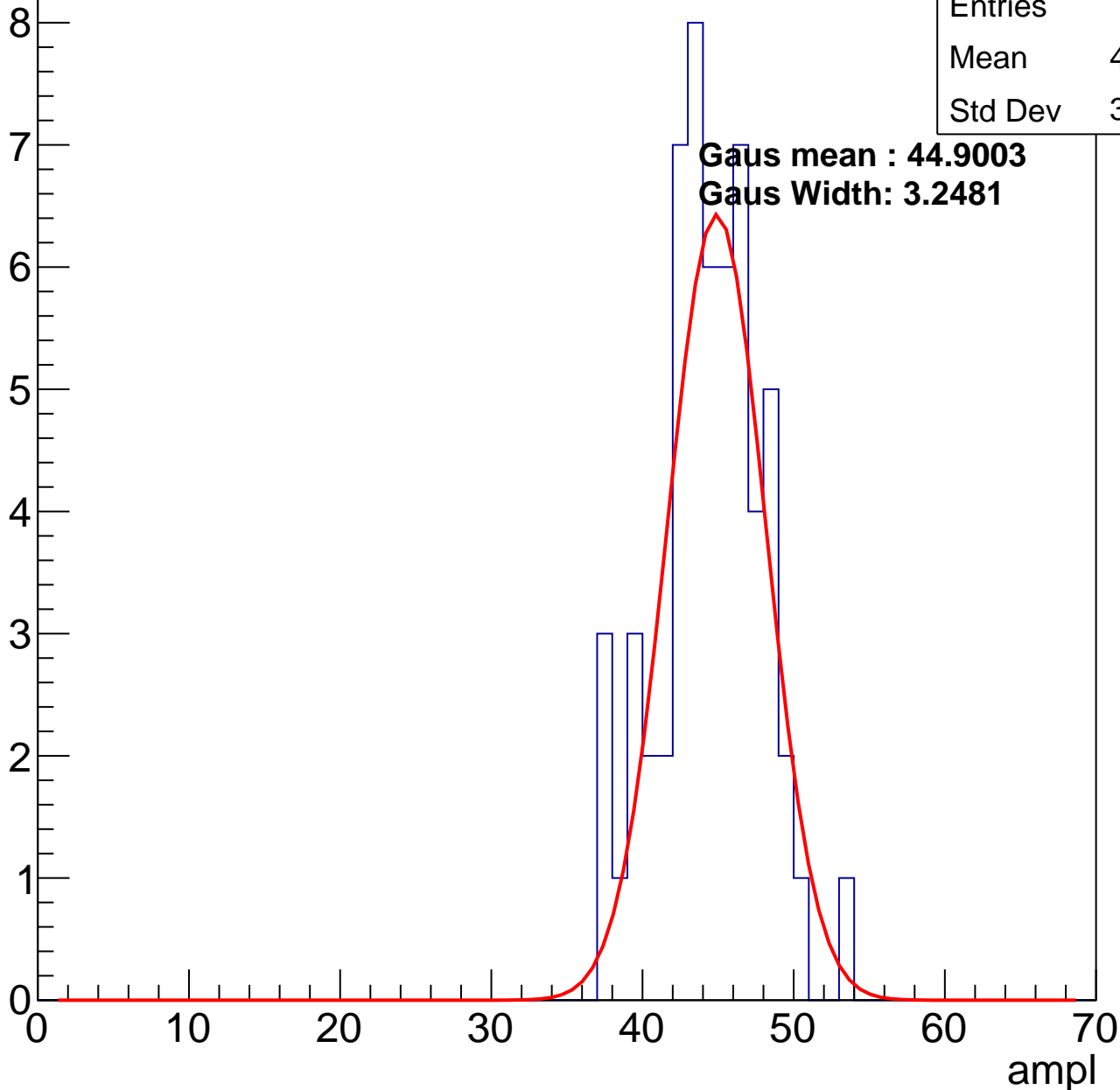
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.98
Std Dev	3.376

Gaus mean : 44.9003

Gaus Width: 3.2481

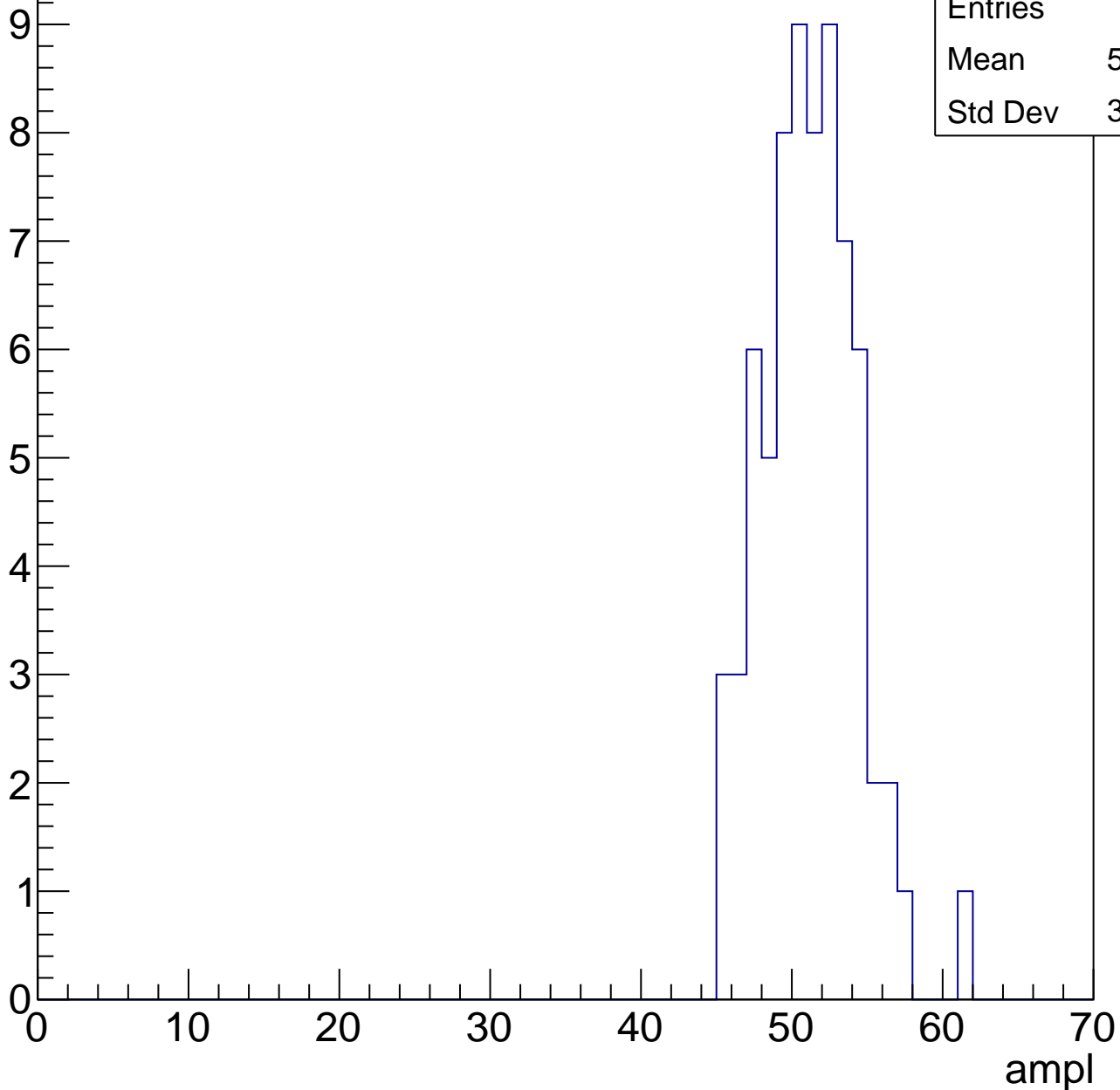


B0L002S, U7-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	50.69
Std Dev	3.087

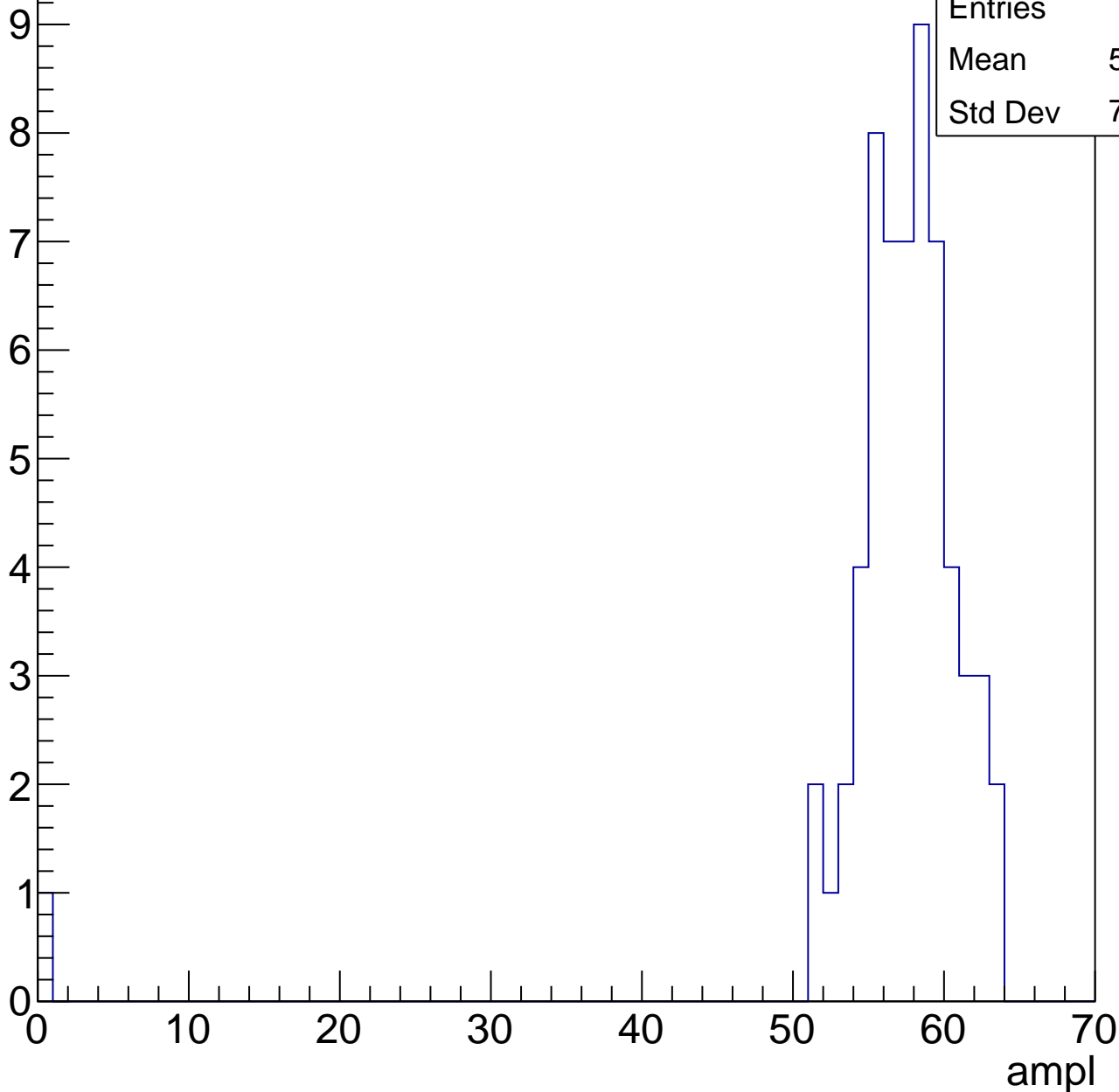


B0L002S, U7-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.28
Std Dev	7.842



B0L002S, U7-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries

32

Mean

60.72

Std Dev

1.875

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

60

70

B0L002S, U7-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U7-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U7-ch13, adc0

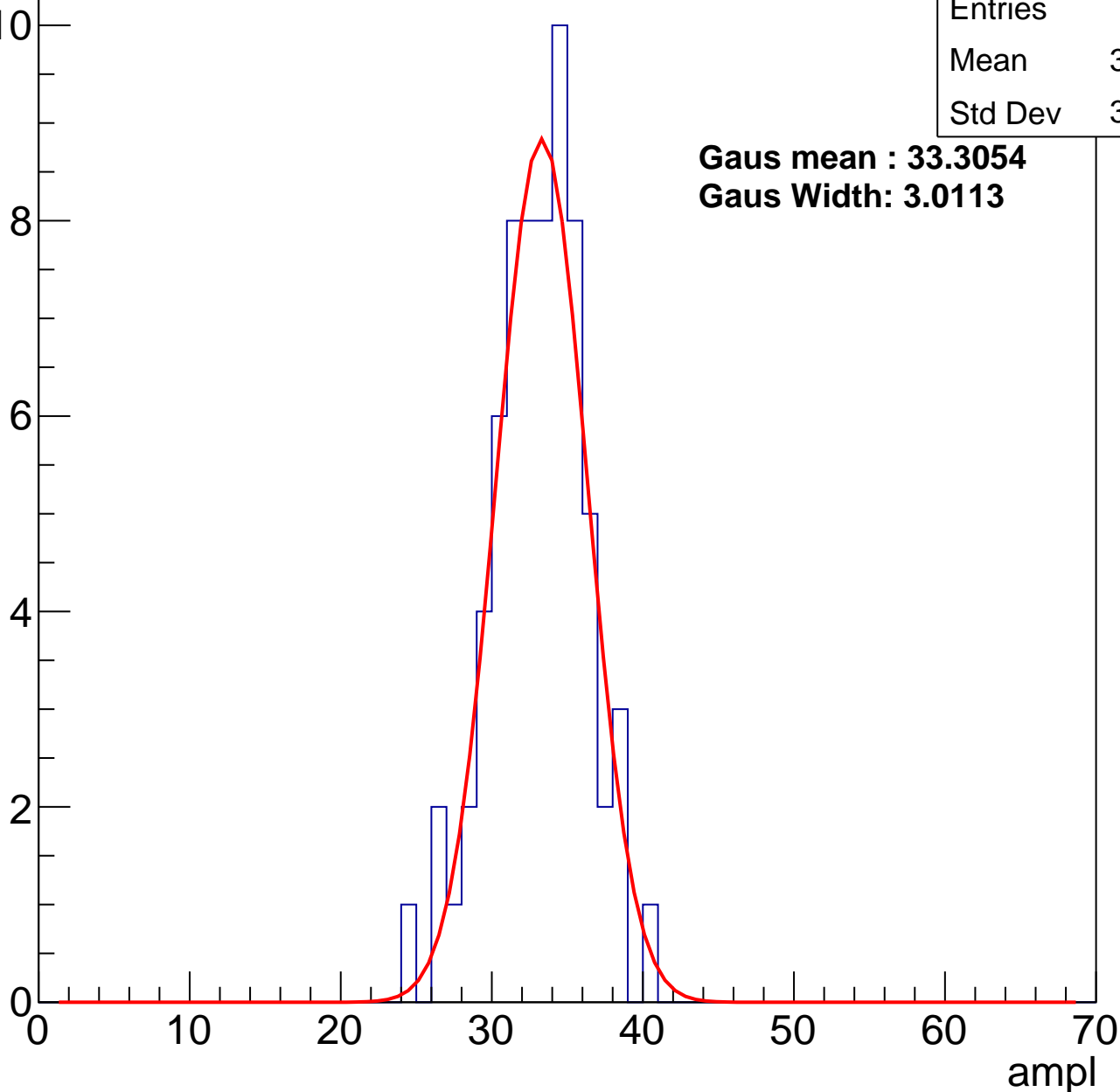
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	32.62
Std Dev	3.084

Gaus mean : 33.3054

Gaus Width: 3.0113



B0L002S, U7-ch13, adc1

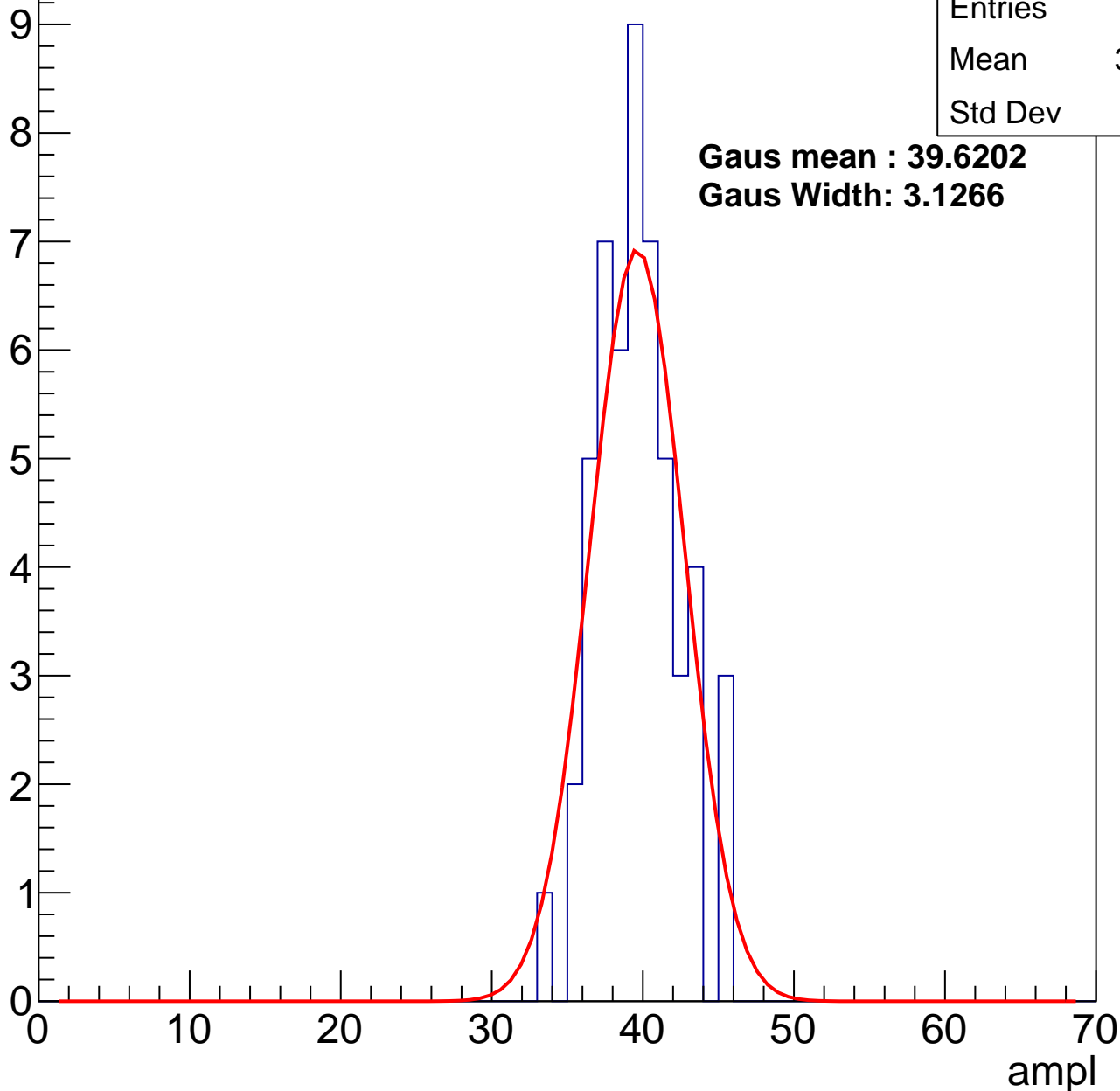
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	39.21
Std Dev	2.67

Gaus mean : 39.6202

Gaus Width: 3.1266



B0L002S, U7-ch13, adc2

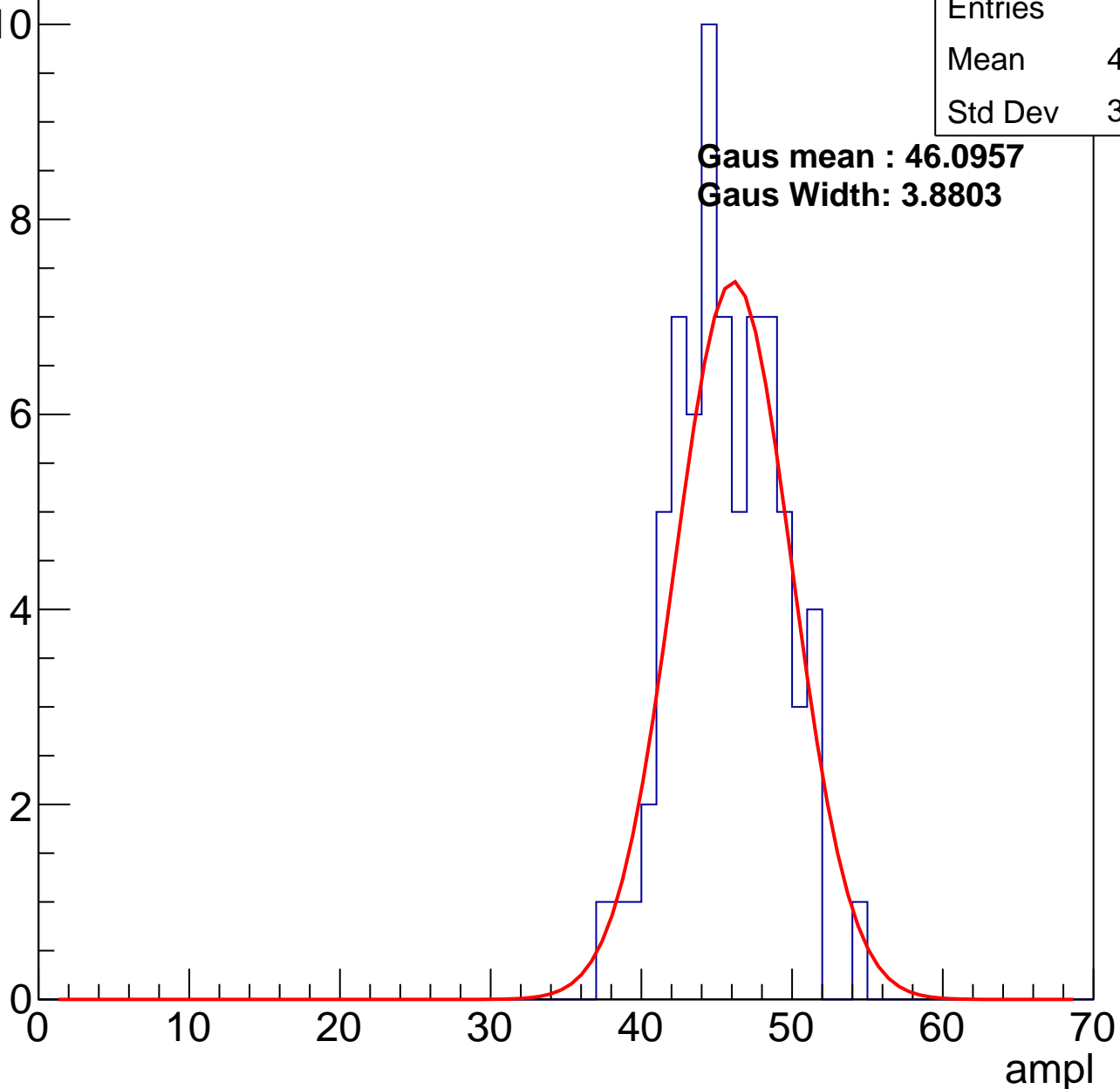
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	45.19
Std Dev	3.434

Gaus mean : 46.0957

Gaus Width: 3.8803

Entry

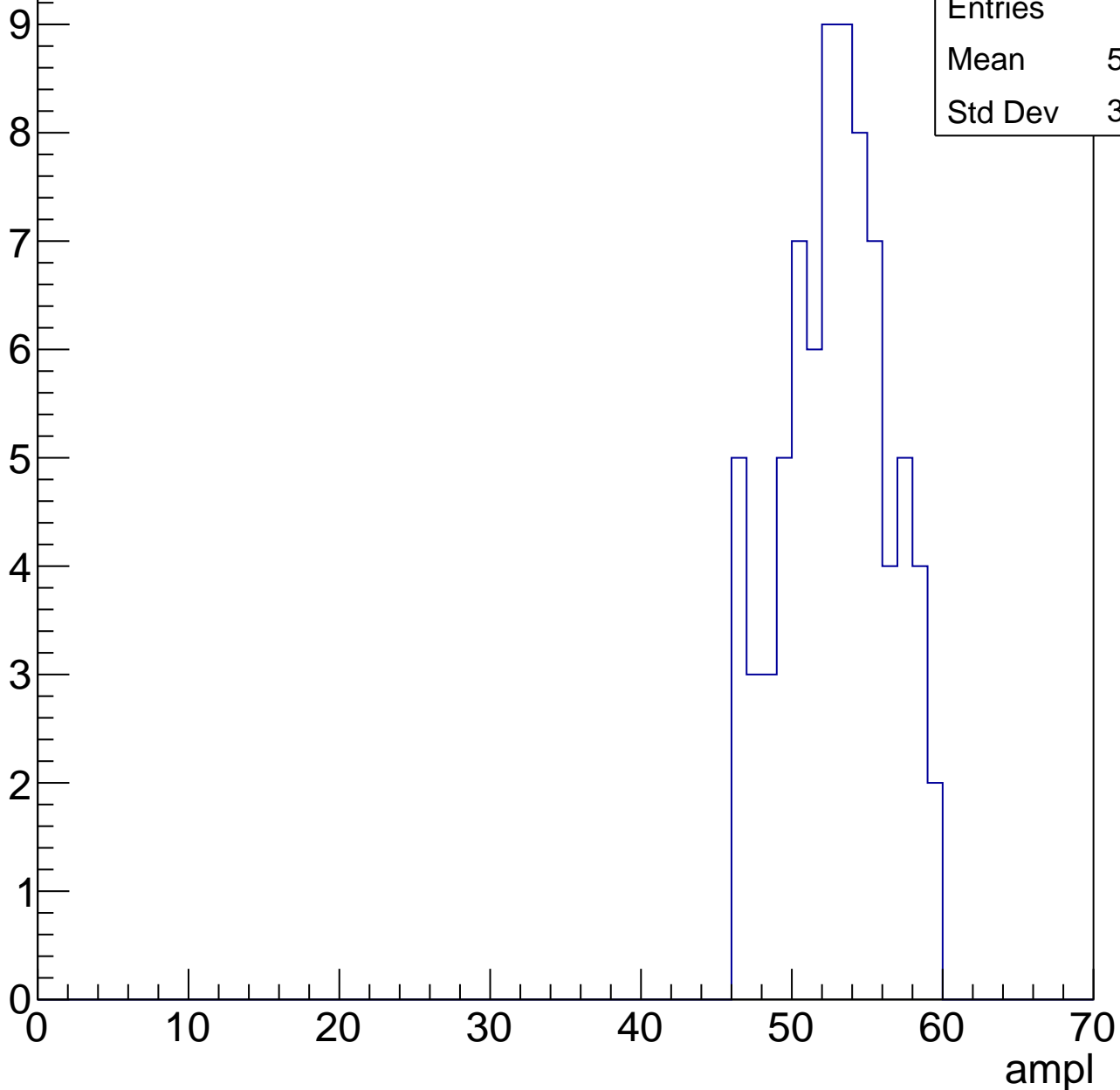


B0L002S, U7-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	52.43
Std Dev	3.424

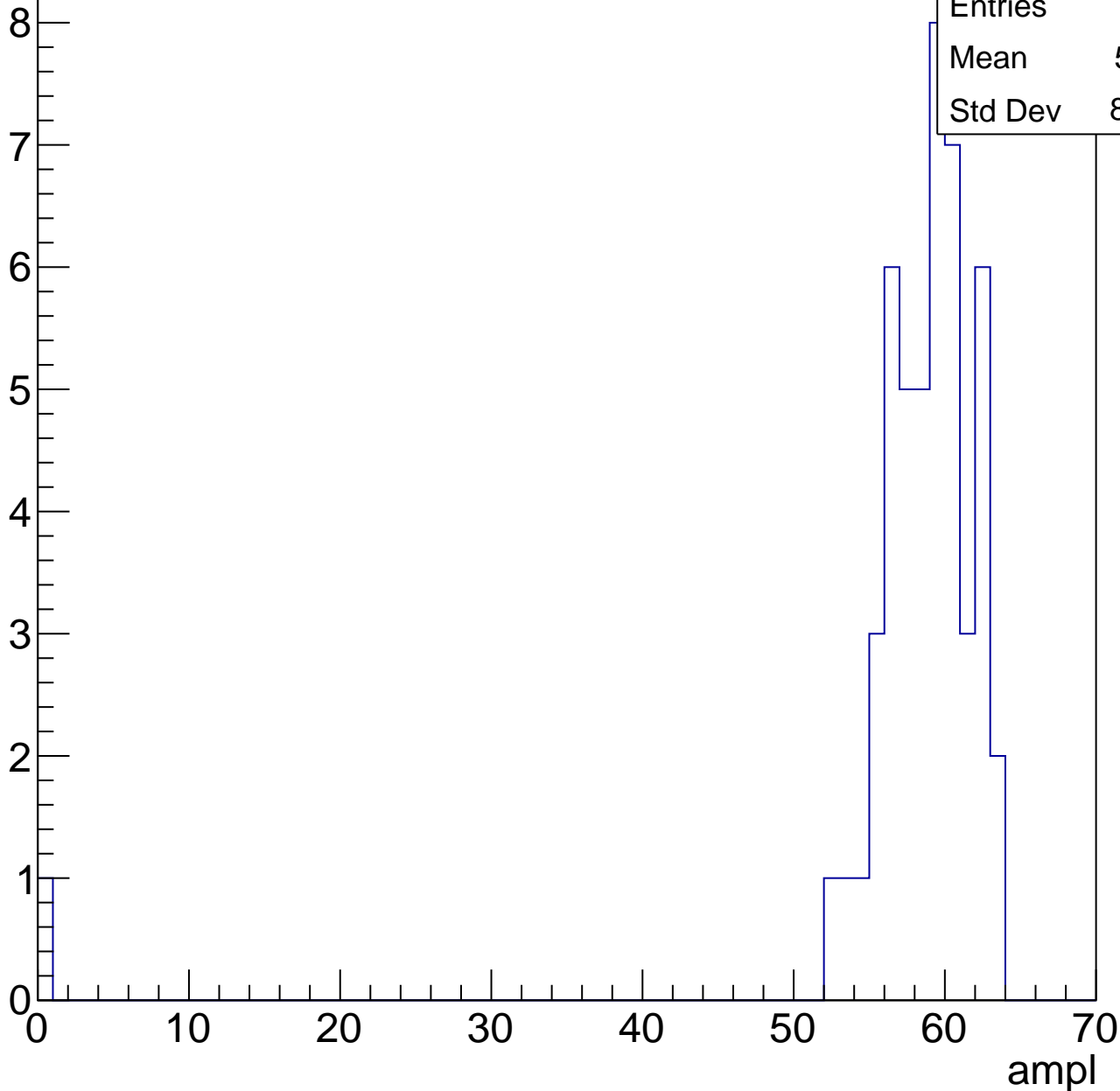


B0L002S, U7-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	57.31
Std Dev	8.669



B0L002S, U7-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	23
Mean	61.61
Std Dev	1.31

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

B0L002S, U7-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch14, adc0

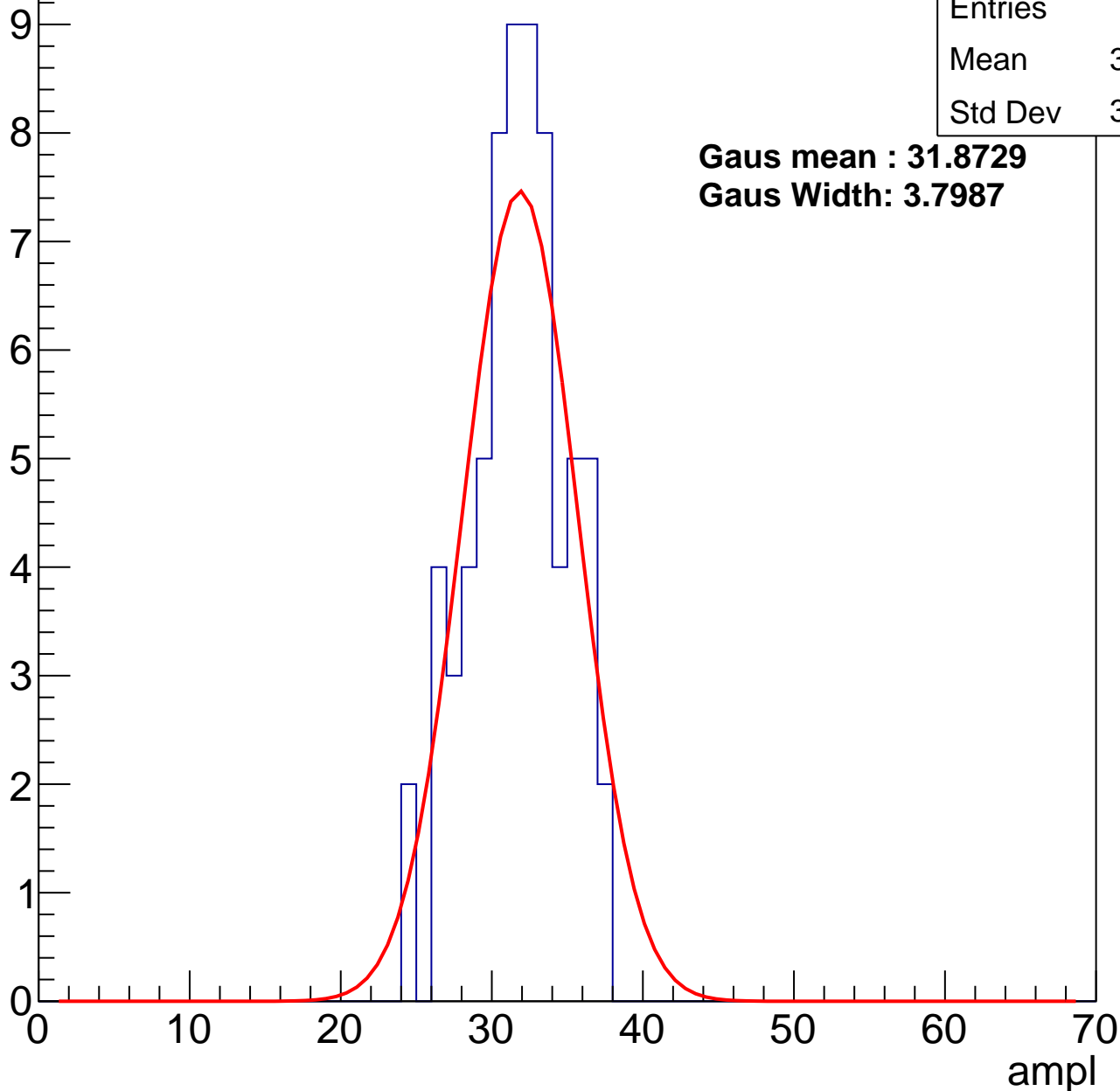
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.26
Std Dev	3.114

Gaus mean : 31.8729

Gaus Width: 3.7987



B0L002S, U7-ch14, adc1

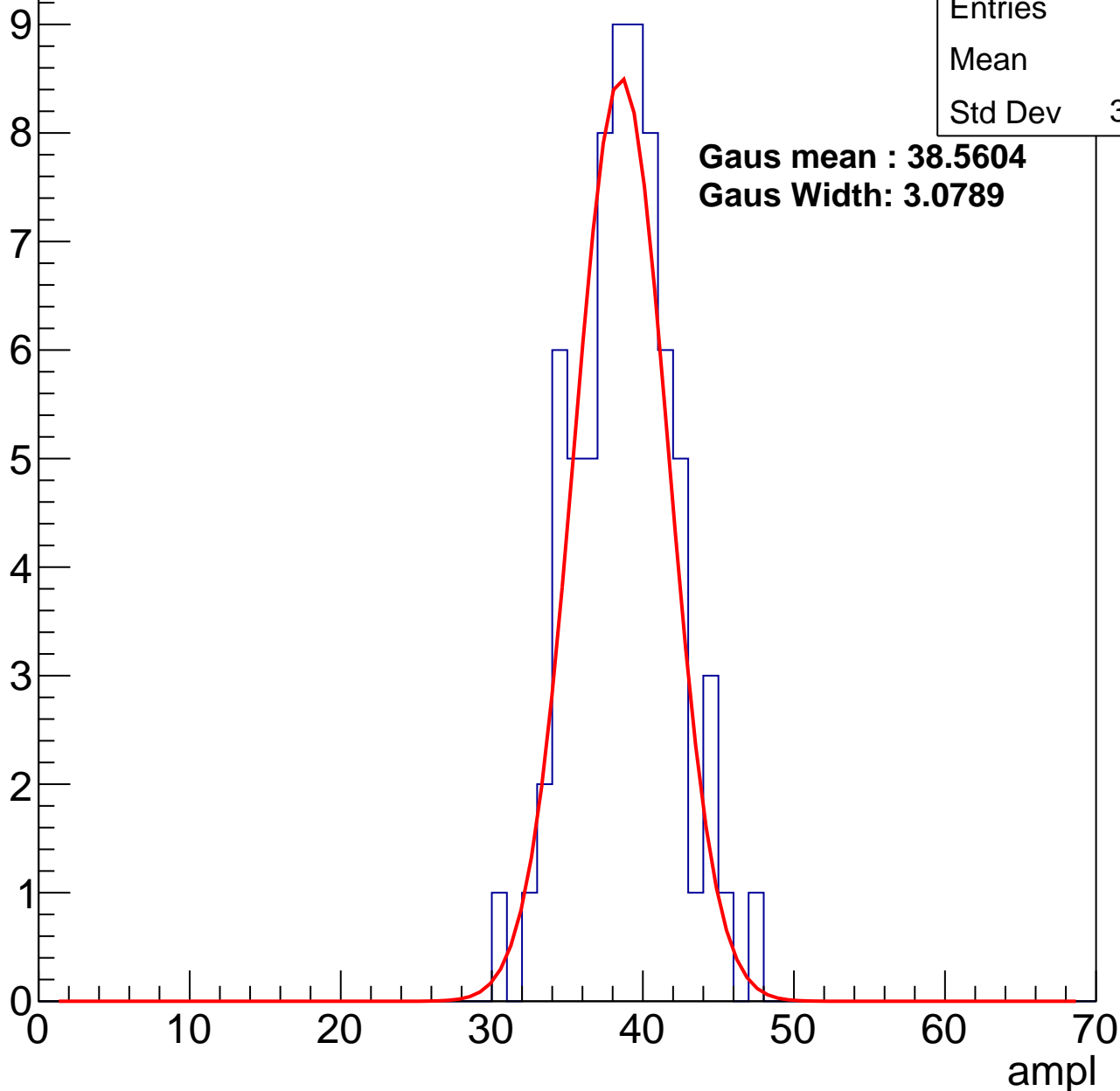
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.3
Std Dev	3.252

Gaus mean : 38.5604

Gaus Width: 3.0789



B0L002S, U7-ch14, adc2

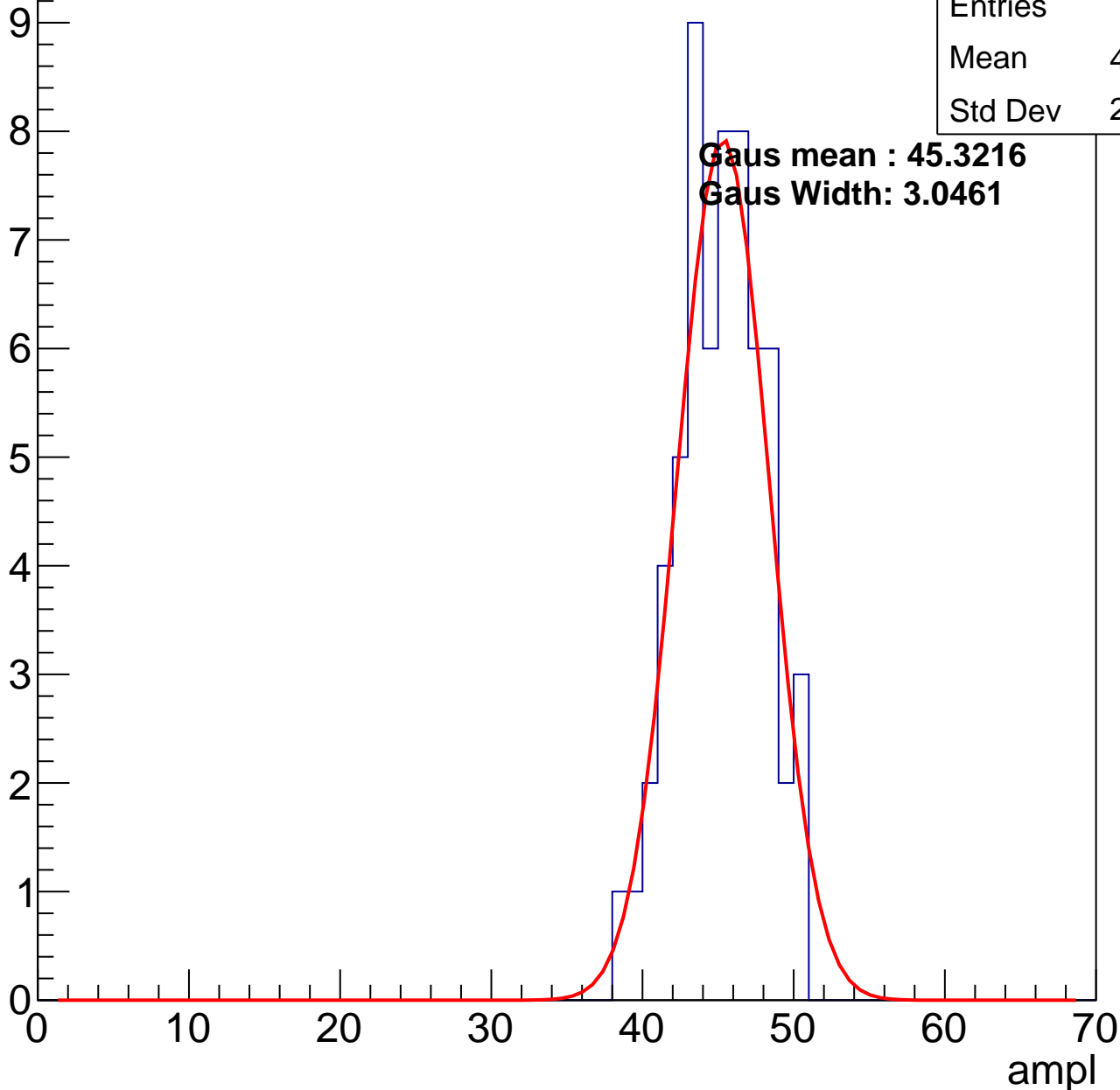
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.72
Std Dev	2.788

Gaus mean : 45.3216

Gaus Width: 3.0461



B0L002S, U7-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

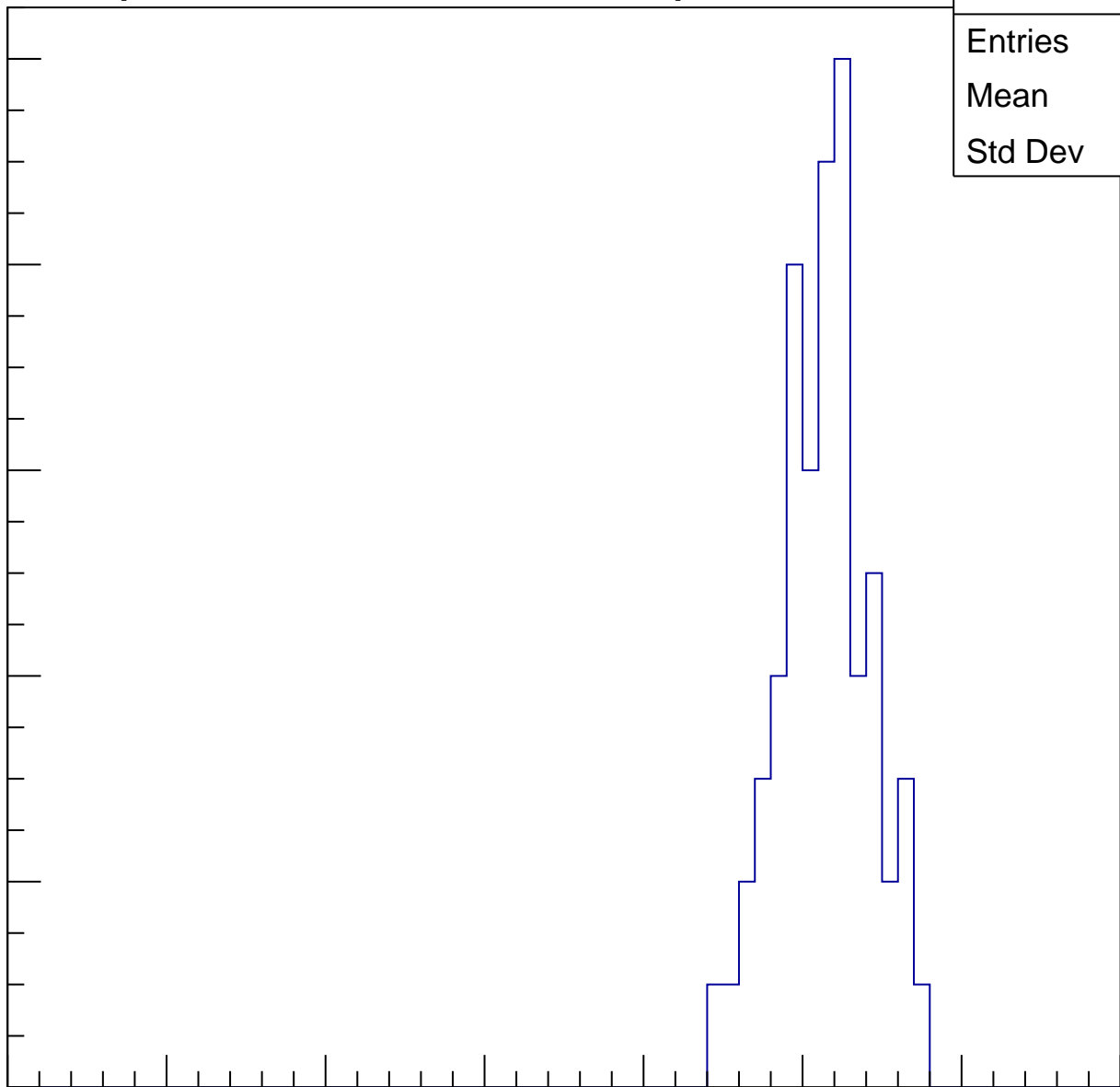
Entries	59
Mean	50.88
Std Dev	2.823

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

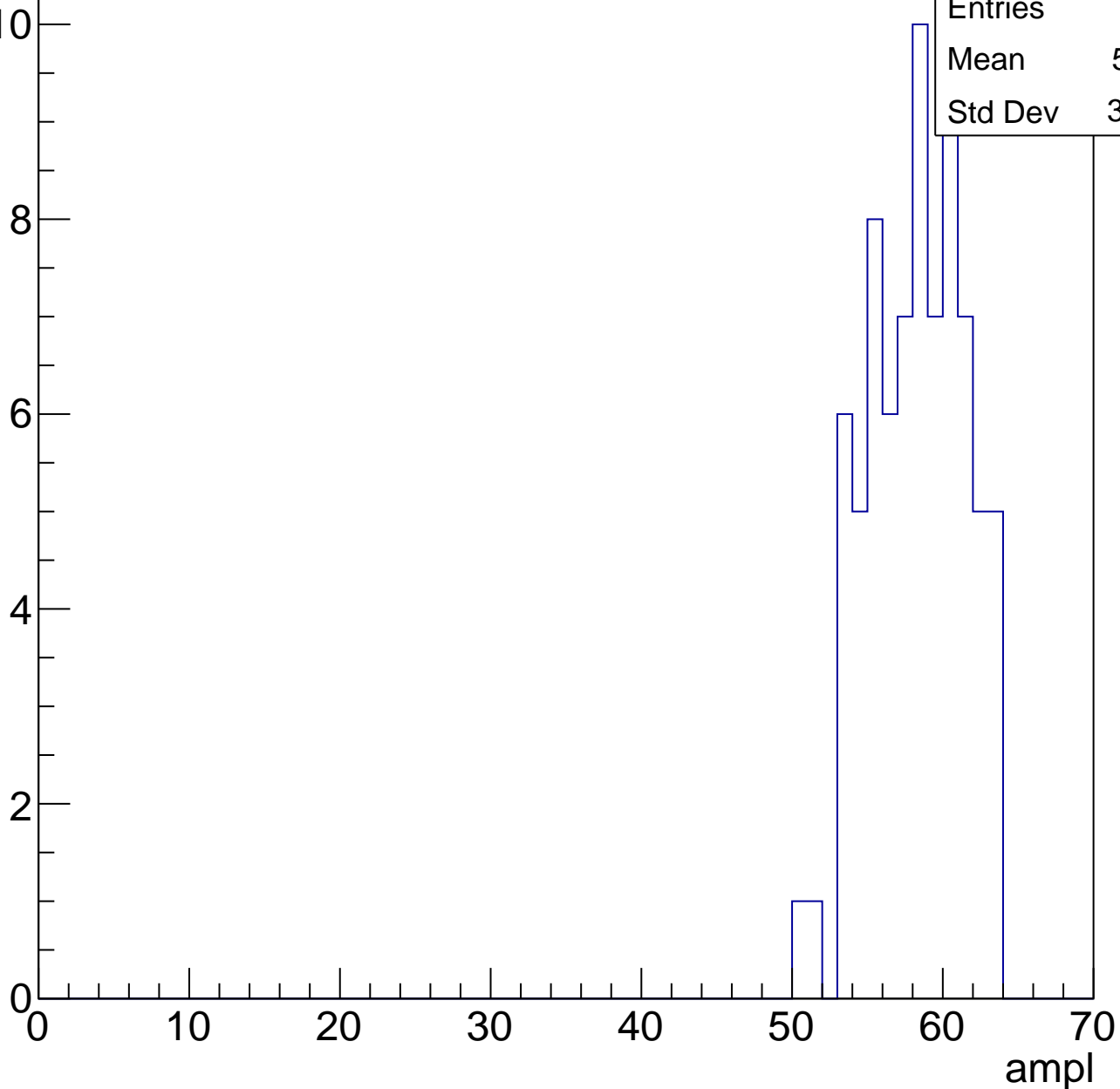


B0L002S, U7-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	57.81
Std Dev	3.118

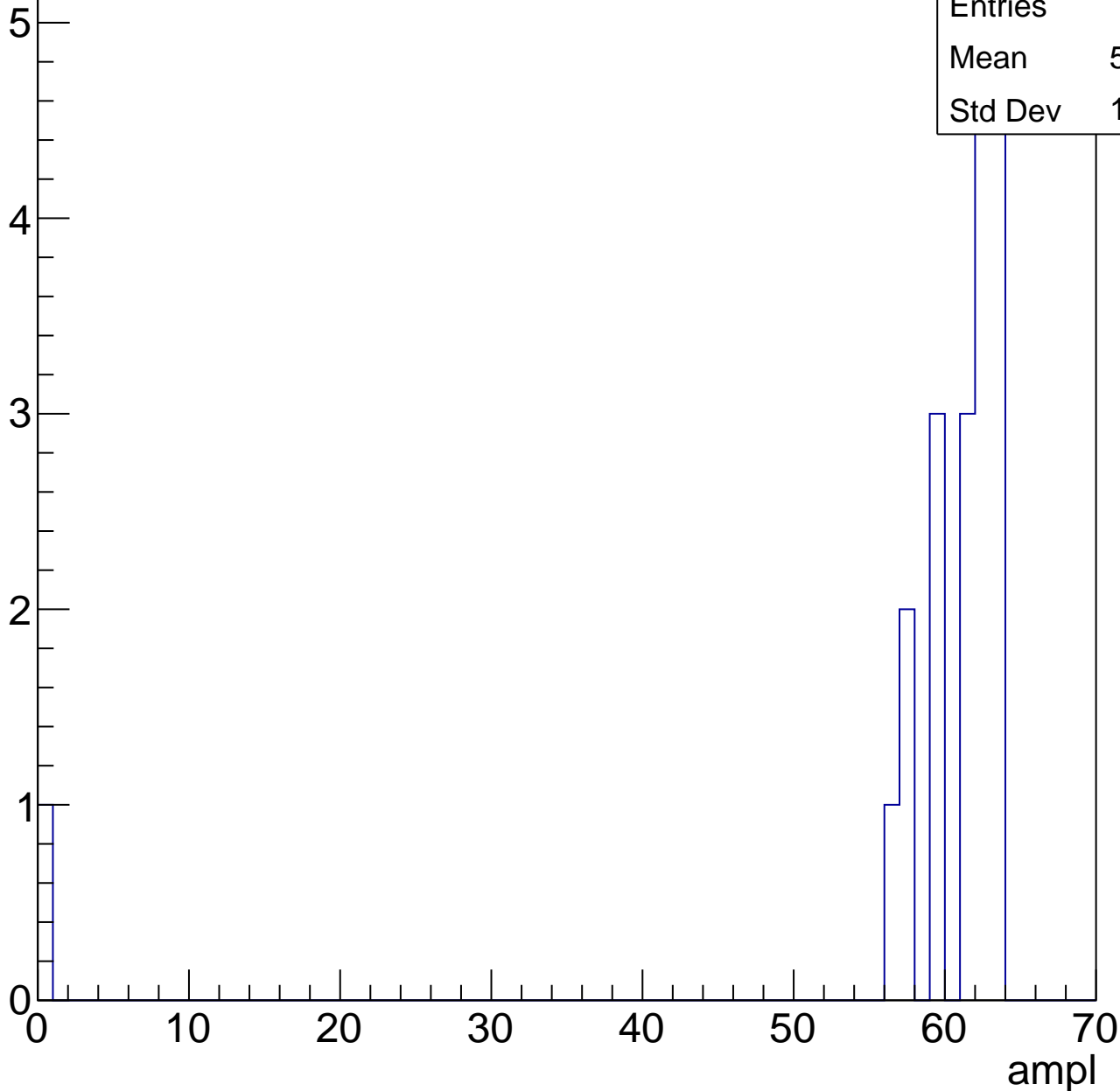


B0L002S, U7-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	57.75
Std Dev	13.42



B0L002S, U7-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U7-ch15, adc0

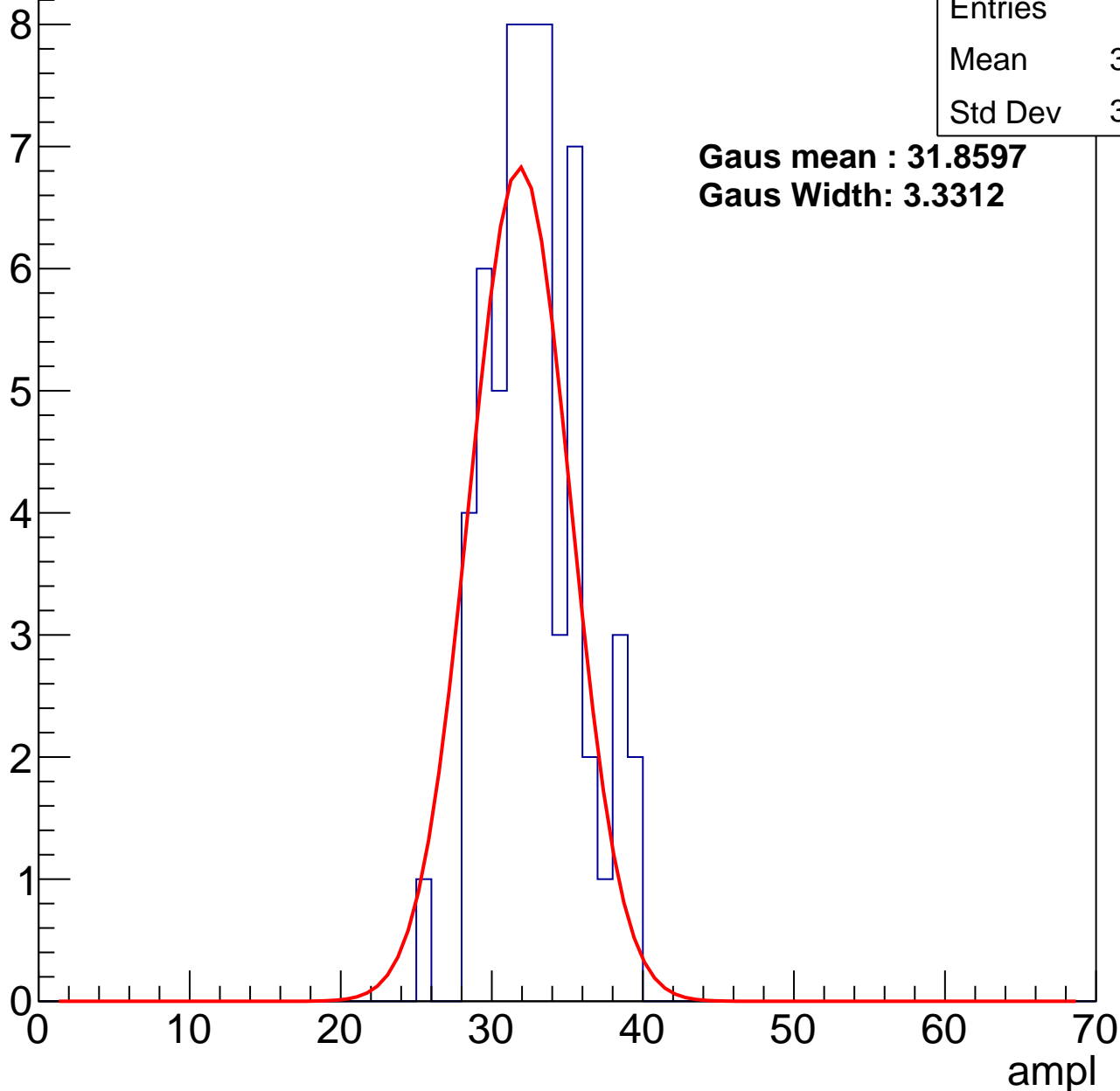
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	32.36
Std Dev	3.033

Gaus mean : 31.8597

Gaus Width: 3.3312



B0L002S, U7-ch15, adc1

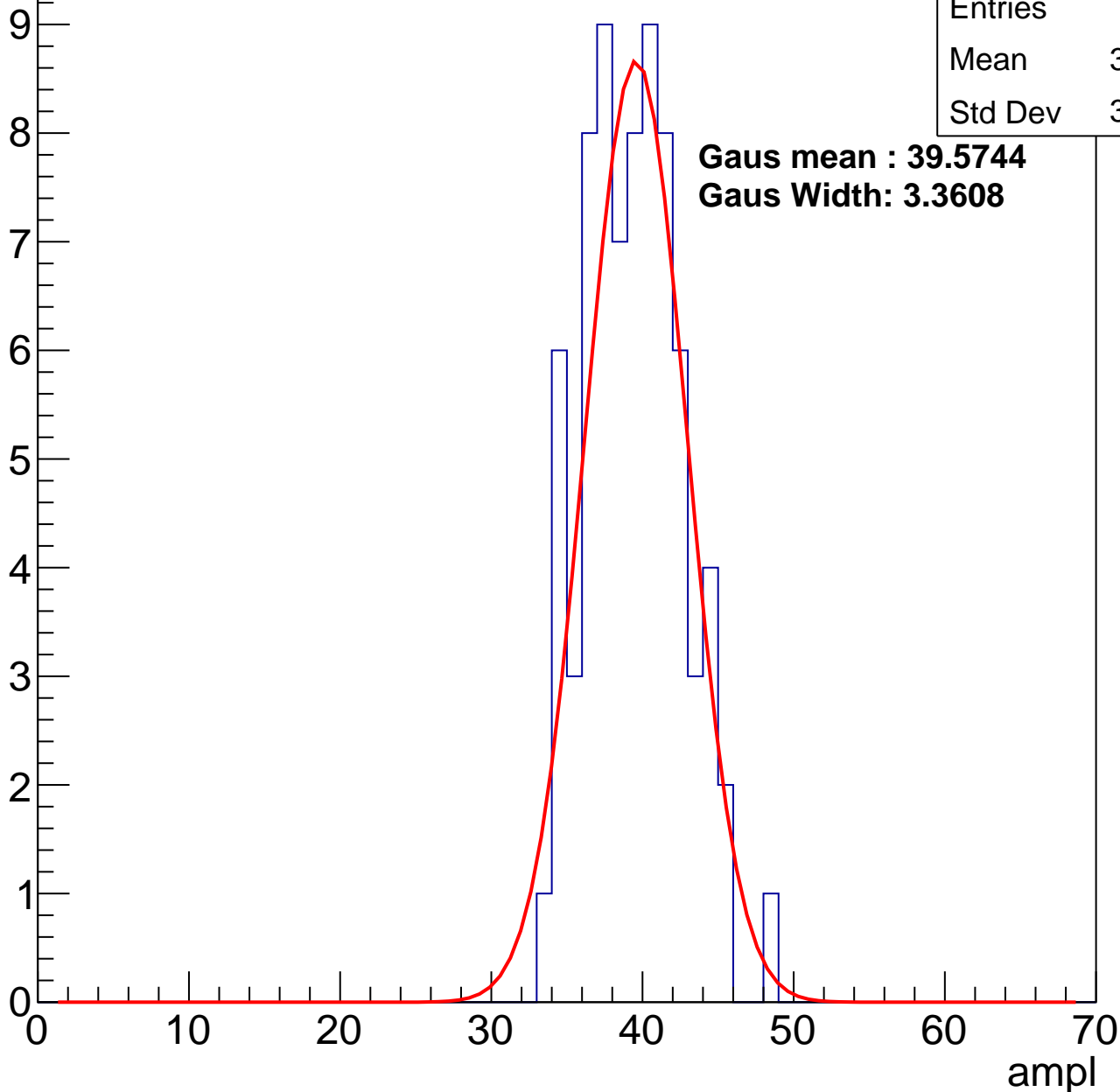
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.99
Std Dev	3.152

Gaus mean : 39.5744

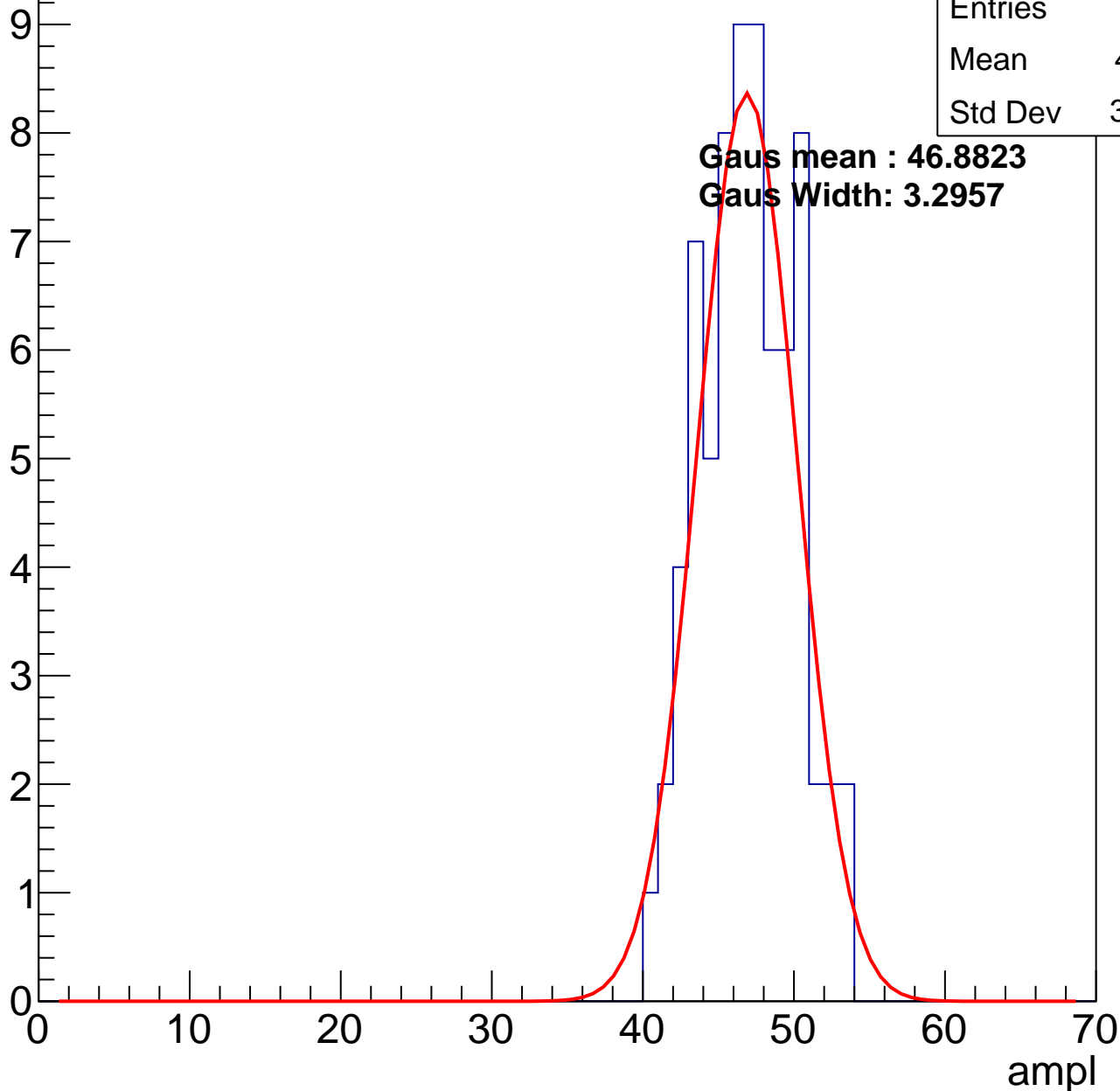
Gaus Width: 3.3608



B0L002S, U7-ch15, adc2

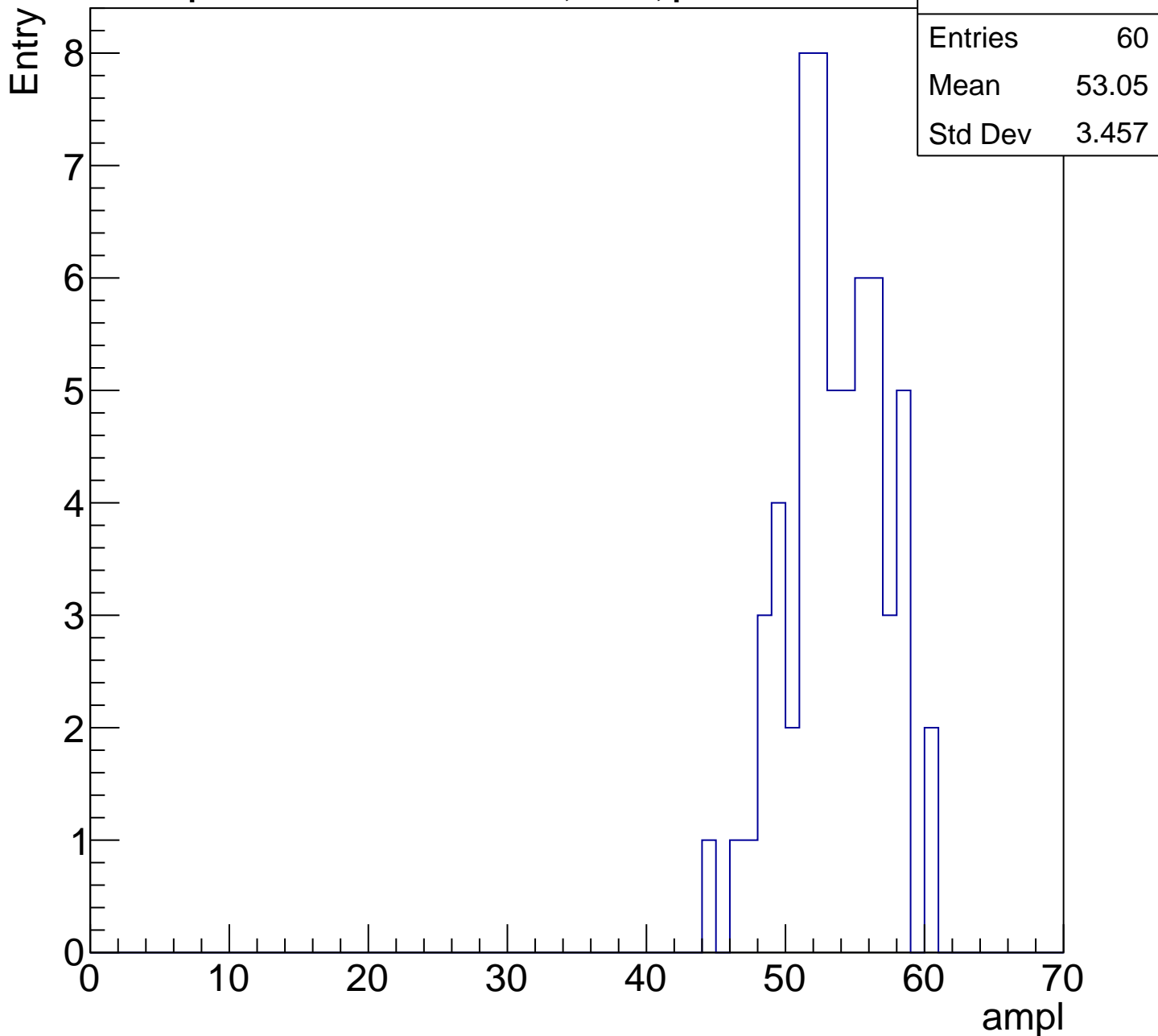
calib_packv5_042523_0143.root, FC#8, port C1

Entry



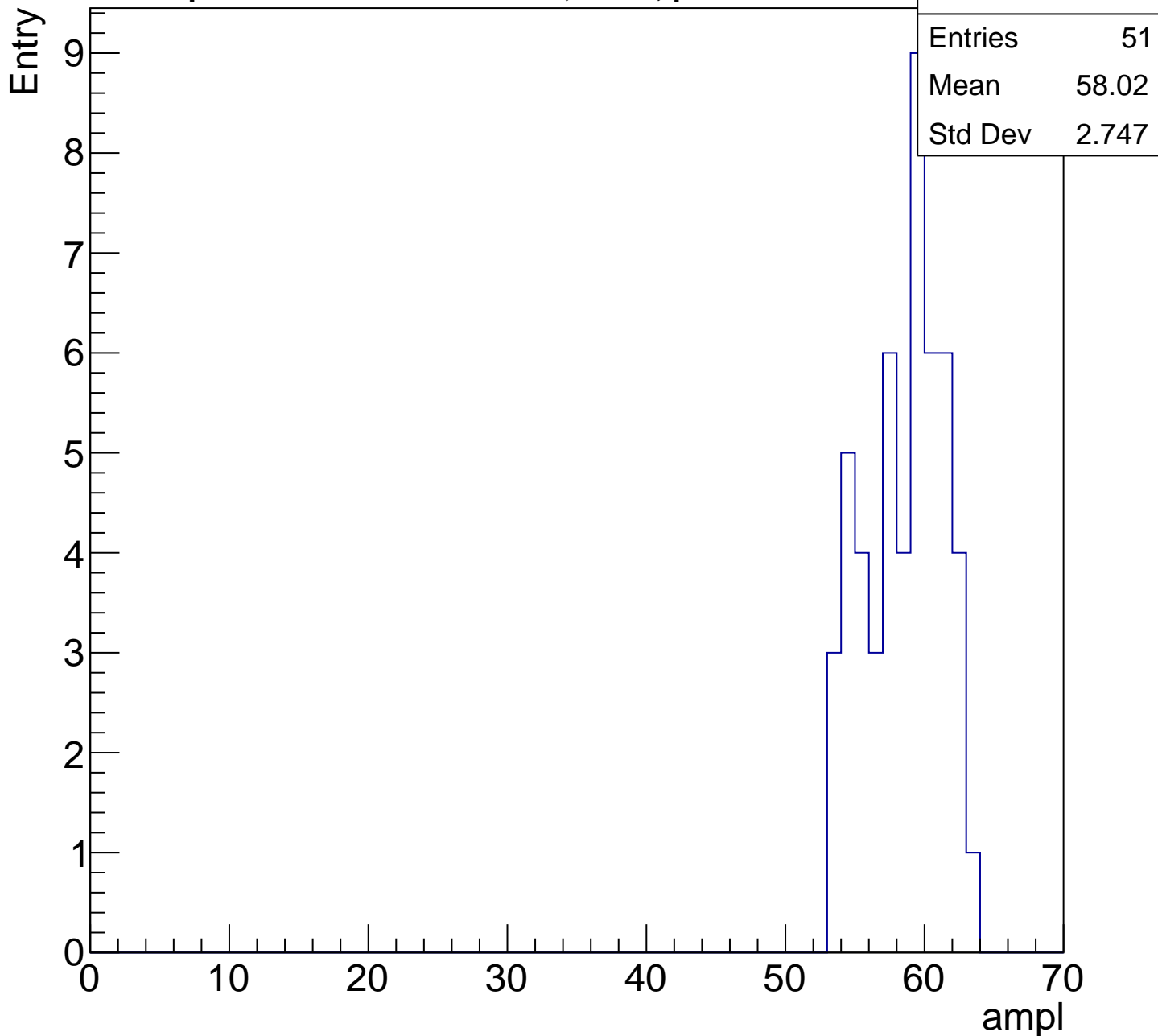
B0L002S, U7-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

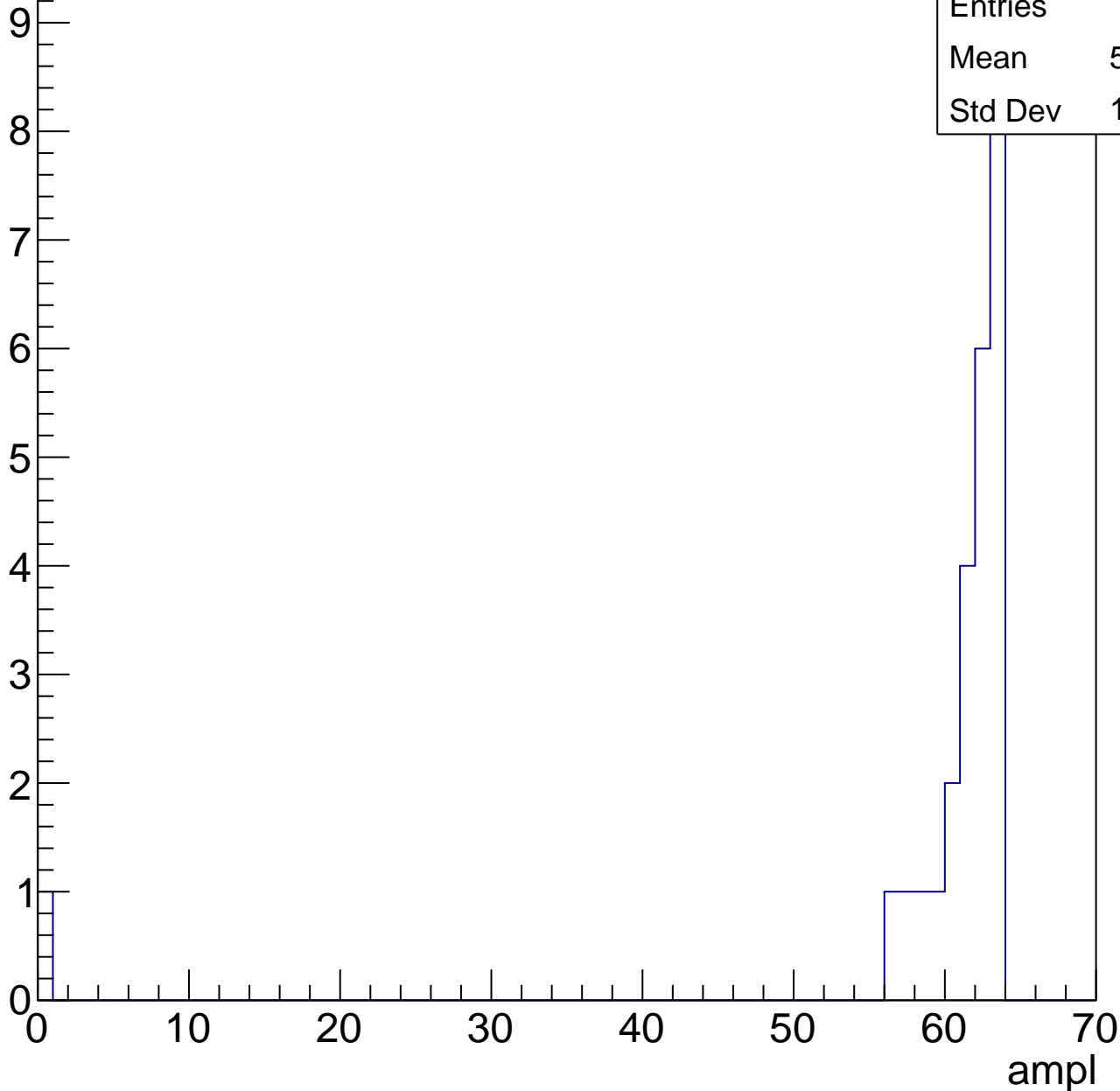


B0L002S, U7-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.96
Std Dev	11.95



B0L002S, U7-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch16, adc0

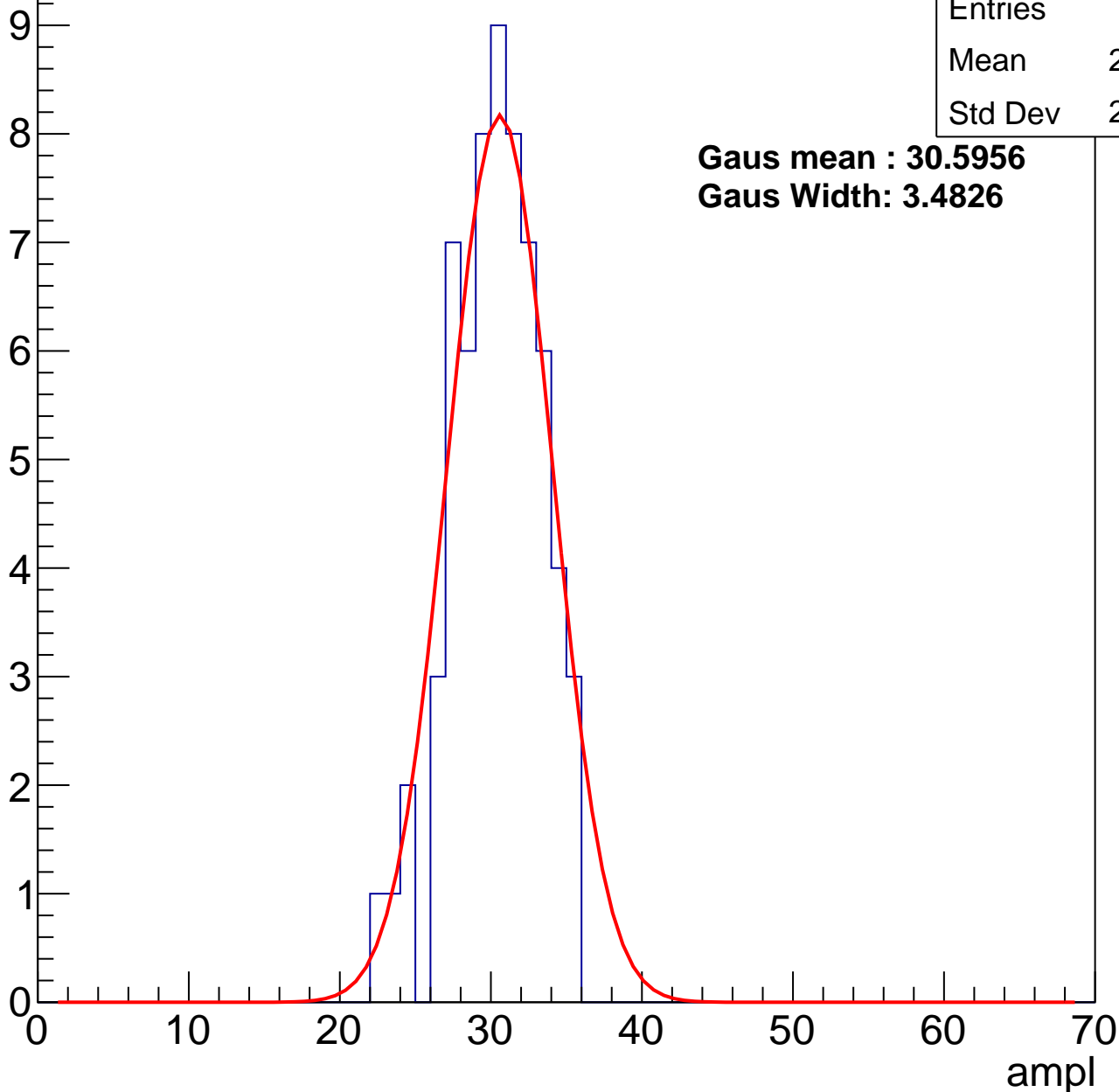
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.86
Std Dev	2.924

Gaus mean : 30.5956

Gaus Width: 3.4826



B0L002S, U7-ch16, adc1

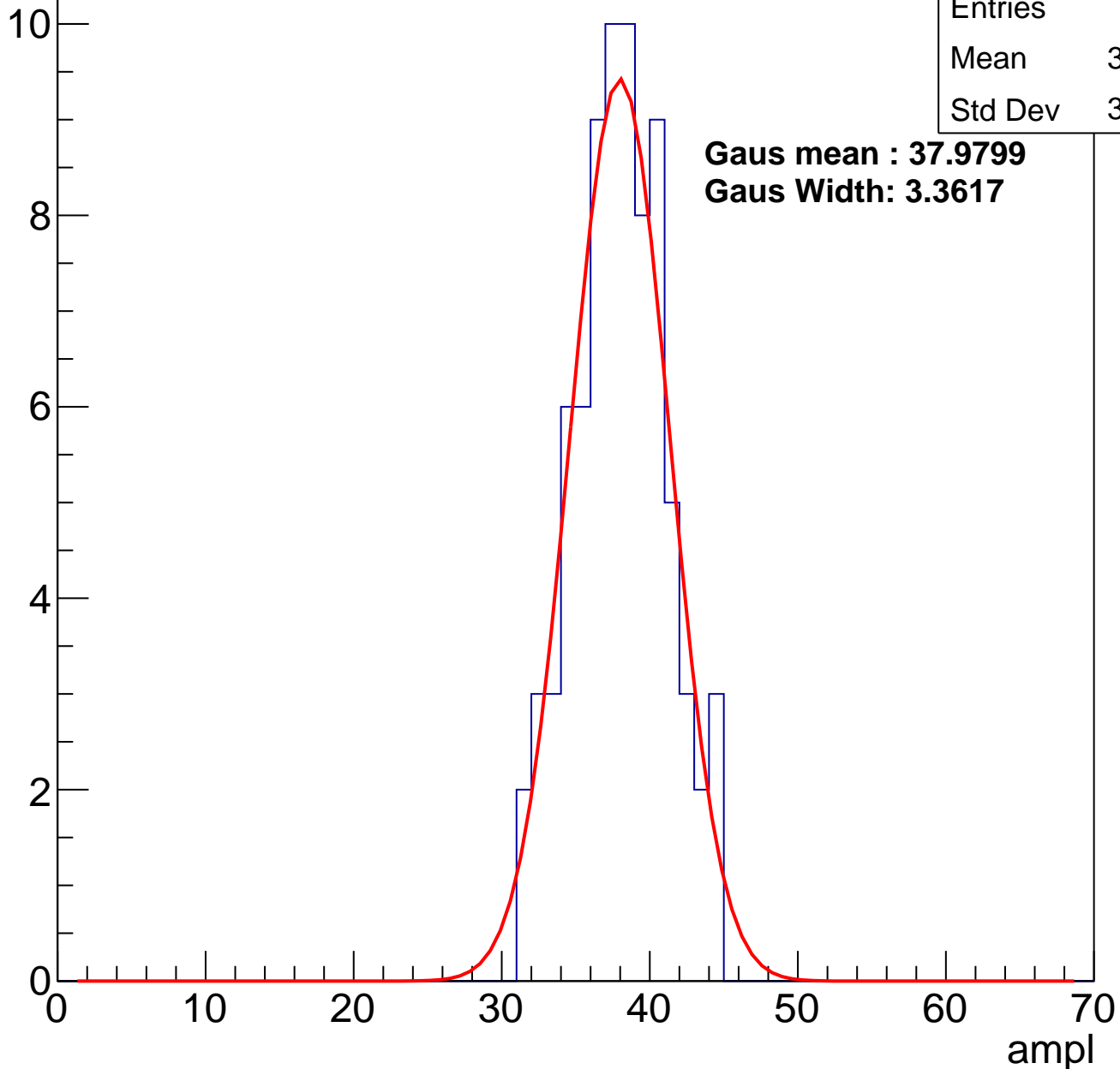
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	37.54
Std Dev	3.093

Gaus mean : 37.9799

Gaus Width: 3.3617

Entry



B0L002S, U7-ch16, adc2

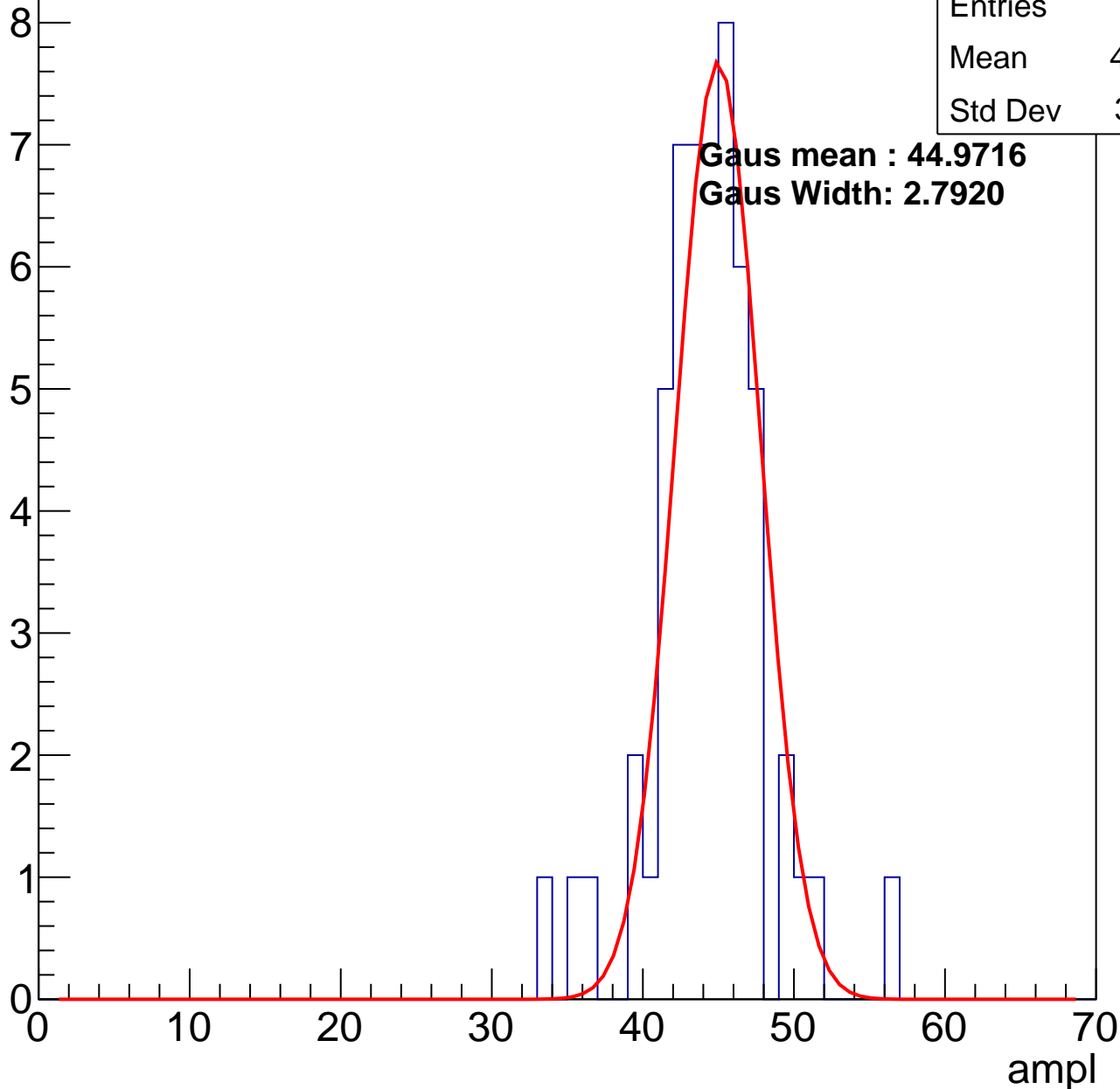
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.86
Std Dev	3.701

Gaus mean : 44.9716

Gaus Width: 2.7920

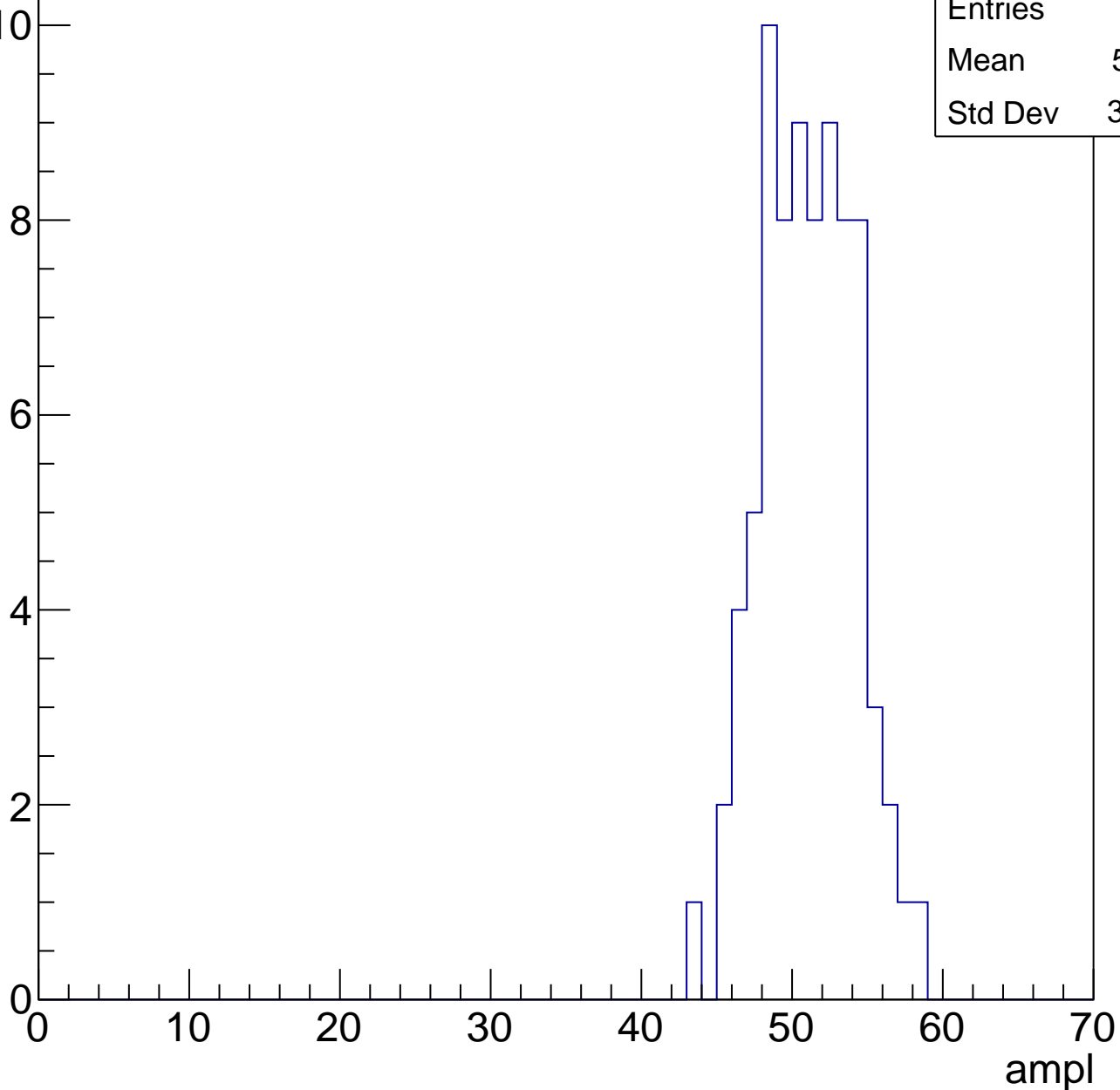


B0L002S, U7-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

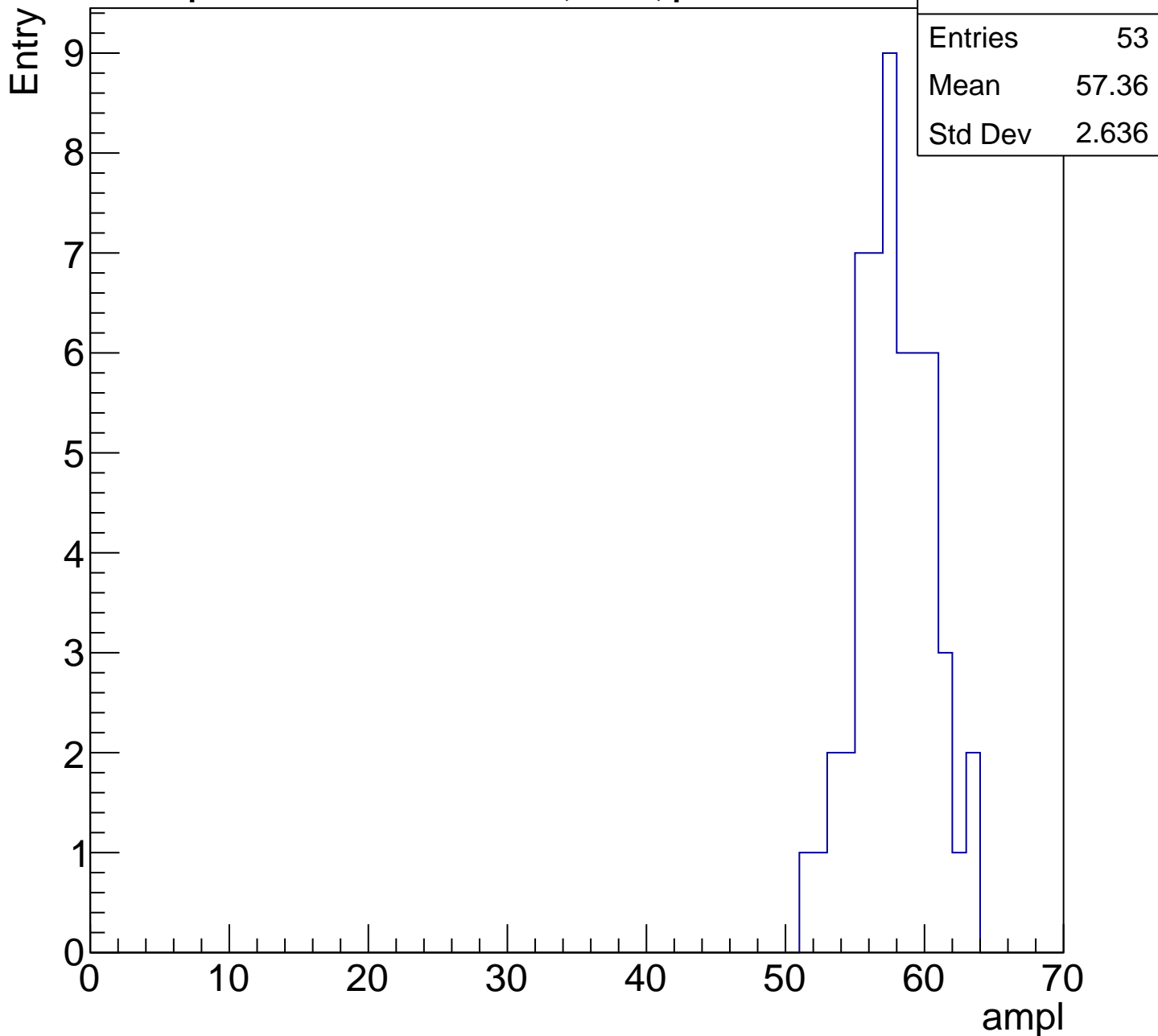
Entry

Entries	79
Mean	50.61
Std Dev	3.042



B0L002S, U7-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

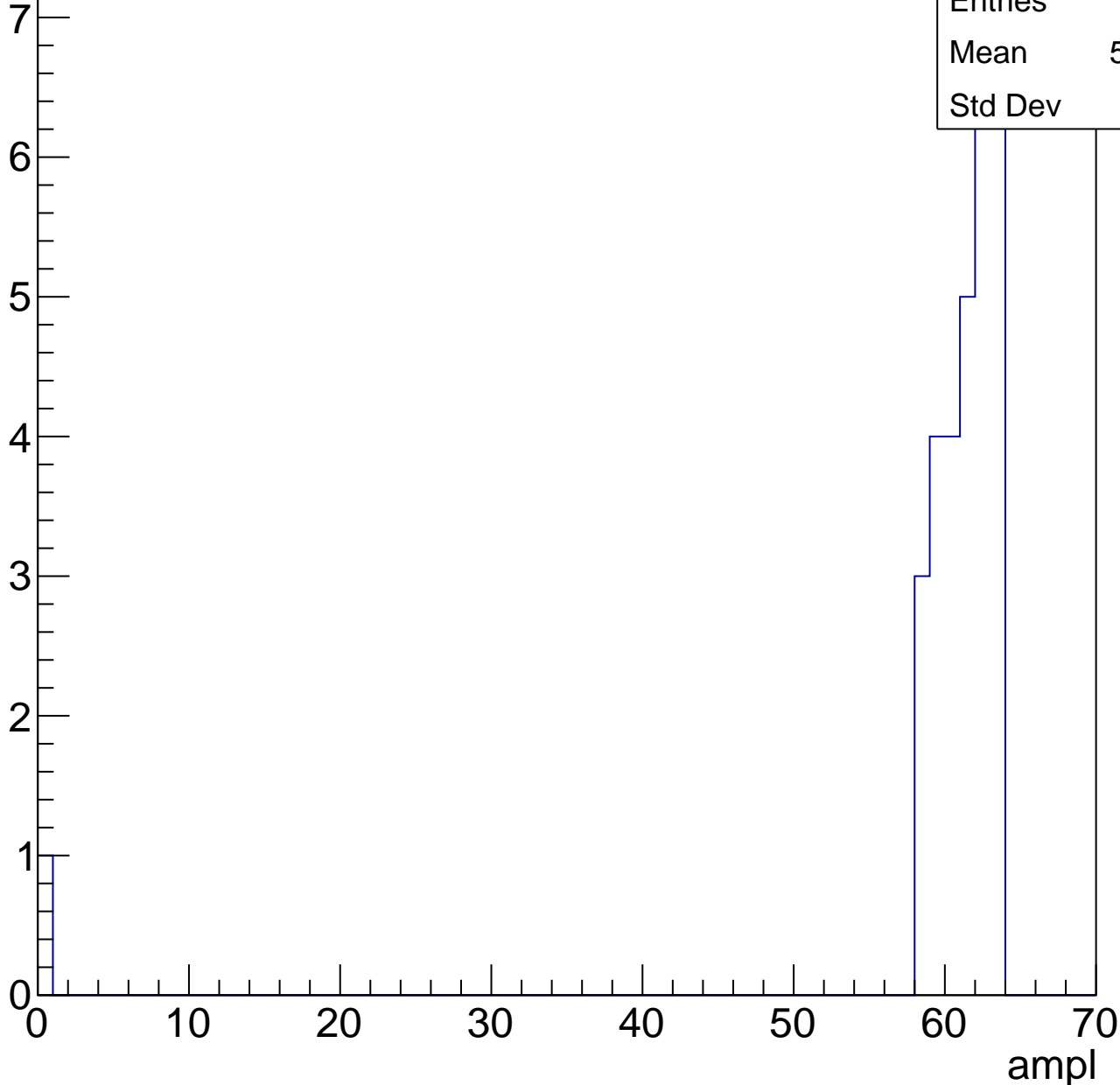


B0L002S, U7-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.03
Std Dev	10.9



B0L002S, U7-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L002S, U7-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch17, adc0

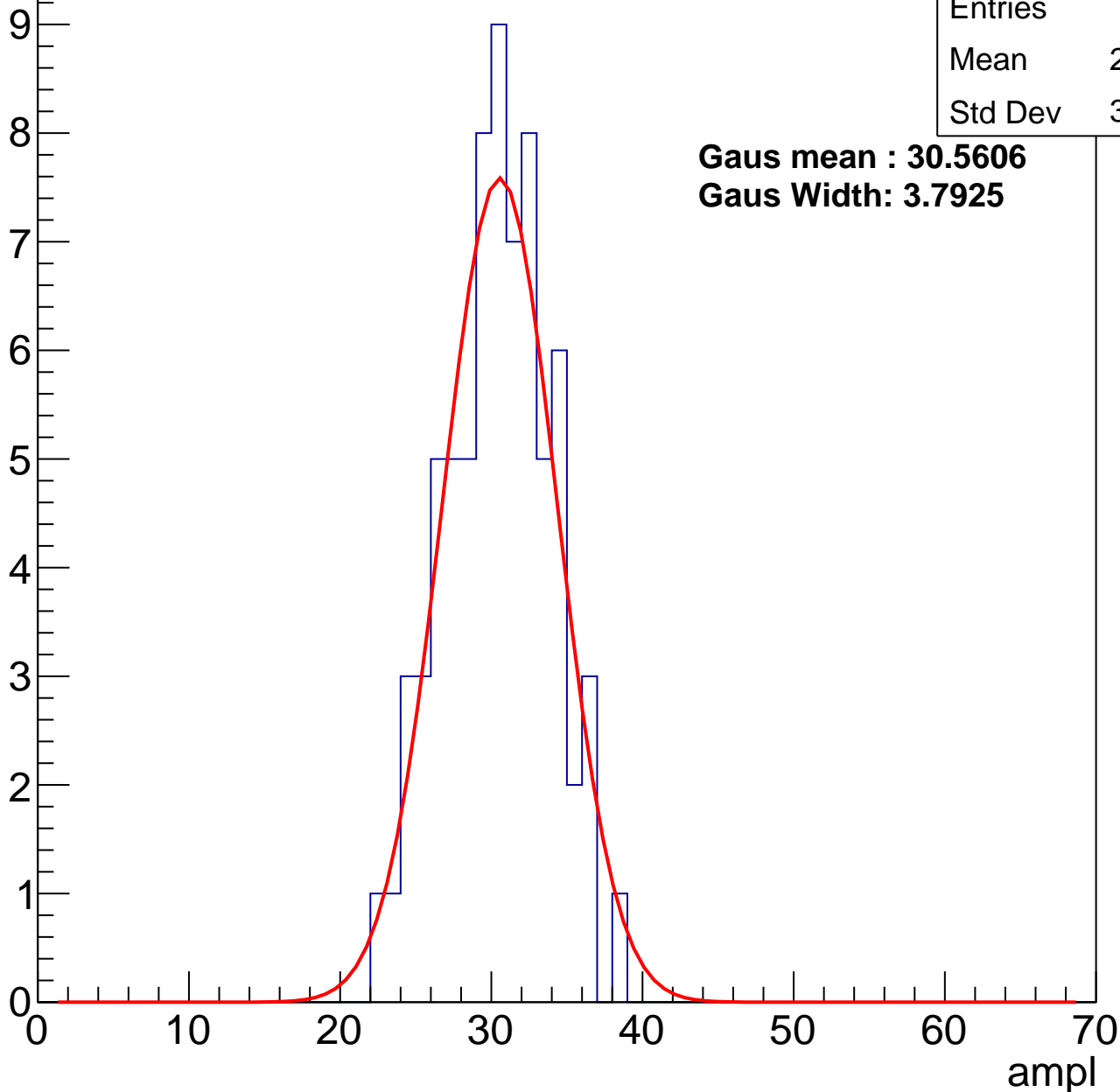
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.96
Std Dev	3.438

Gaus mean : 30.5606

Gaus Width: 3.7925



B0L002S, U7-ch17, adc1

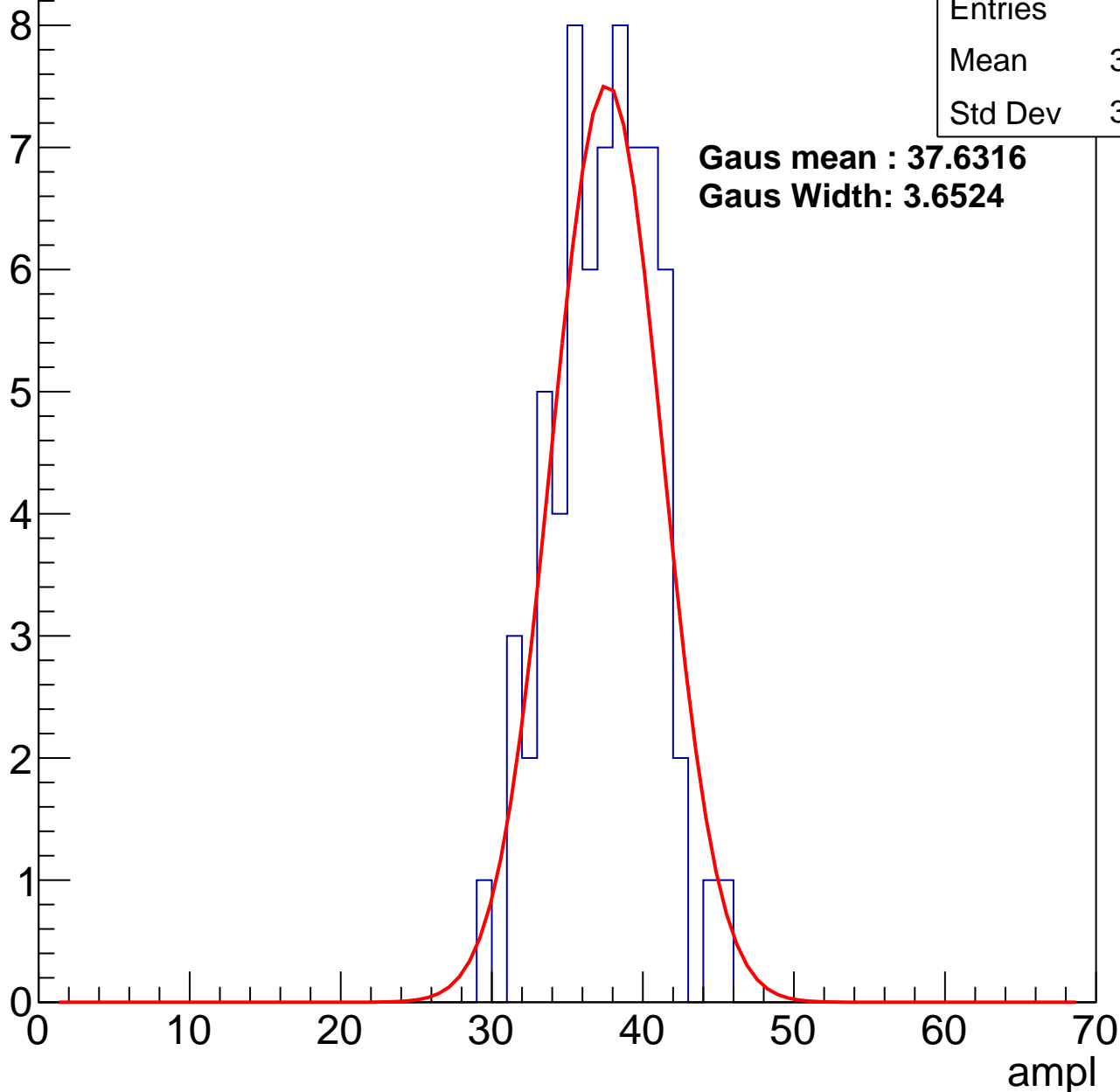
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.03
Std Dev	3.285

Gaus mean : 37.6316

Gaus Width: 3.6524



B0L002S, U7-ch17, adc2

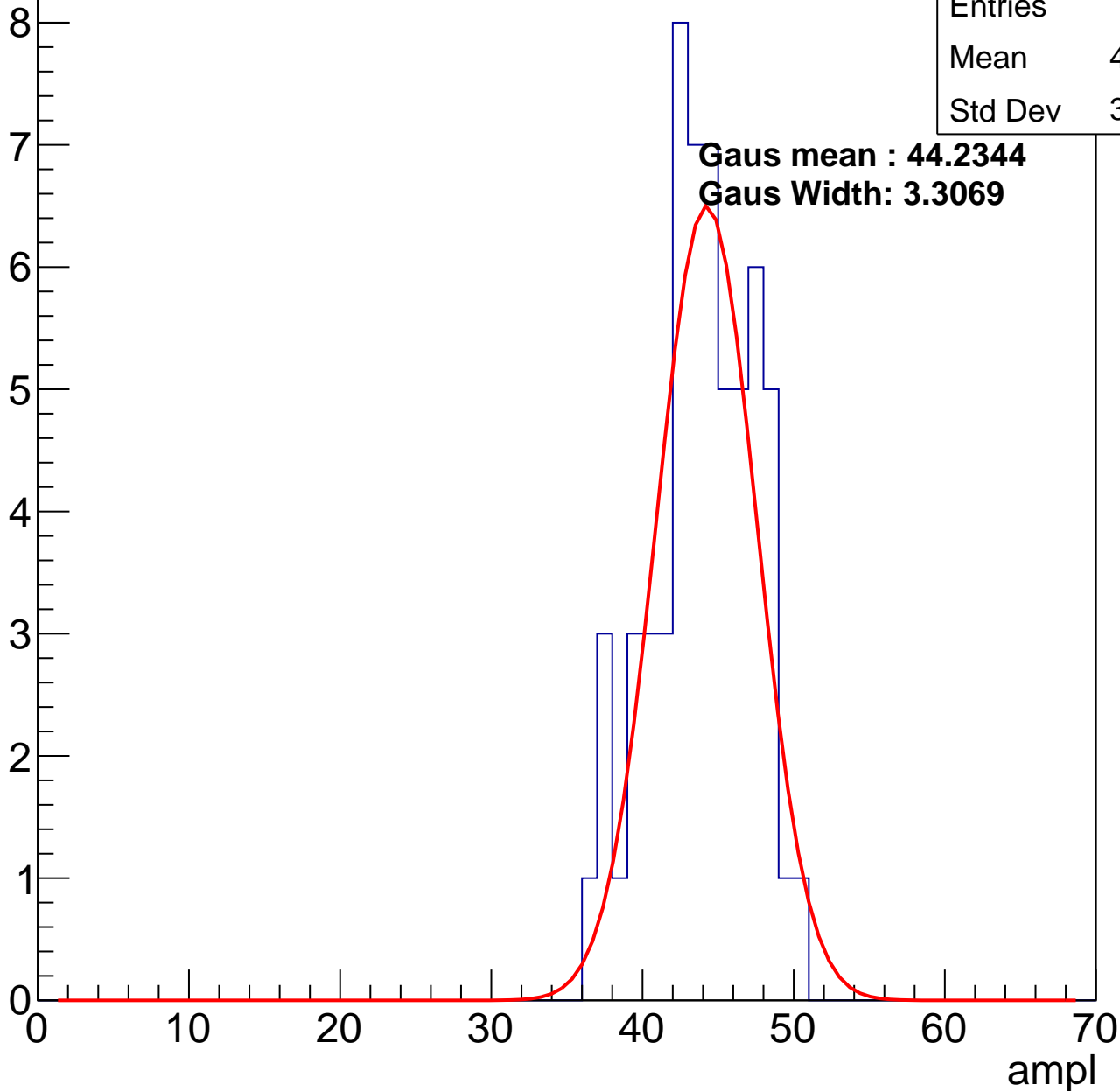
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.49
Std Dev	3.295

Gaus mean : 44.2344

Gaus Width: 3.3069

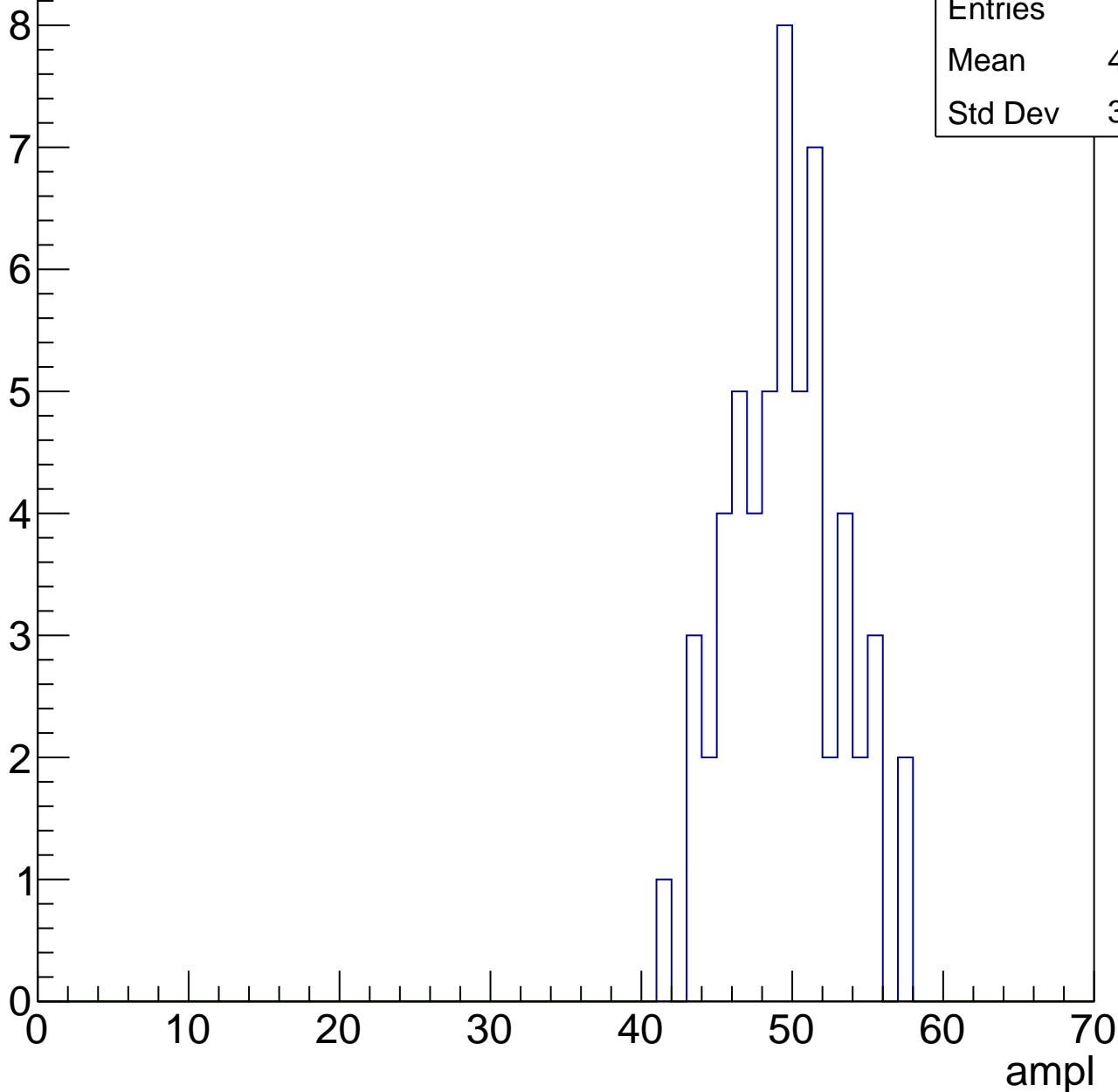


B0L002S, U7-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.09
Std Dev	3.634

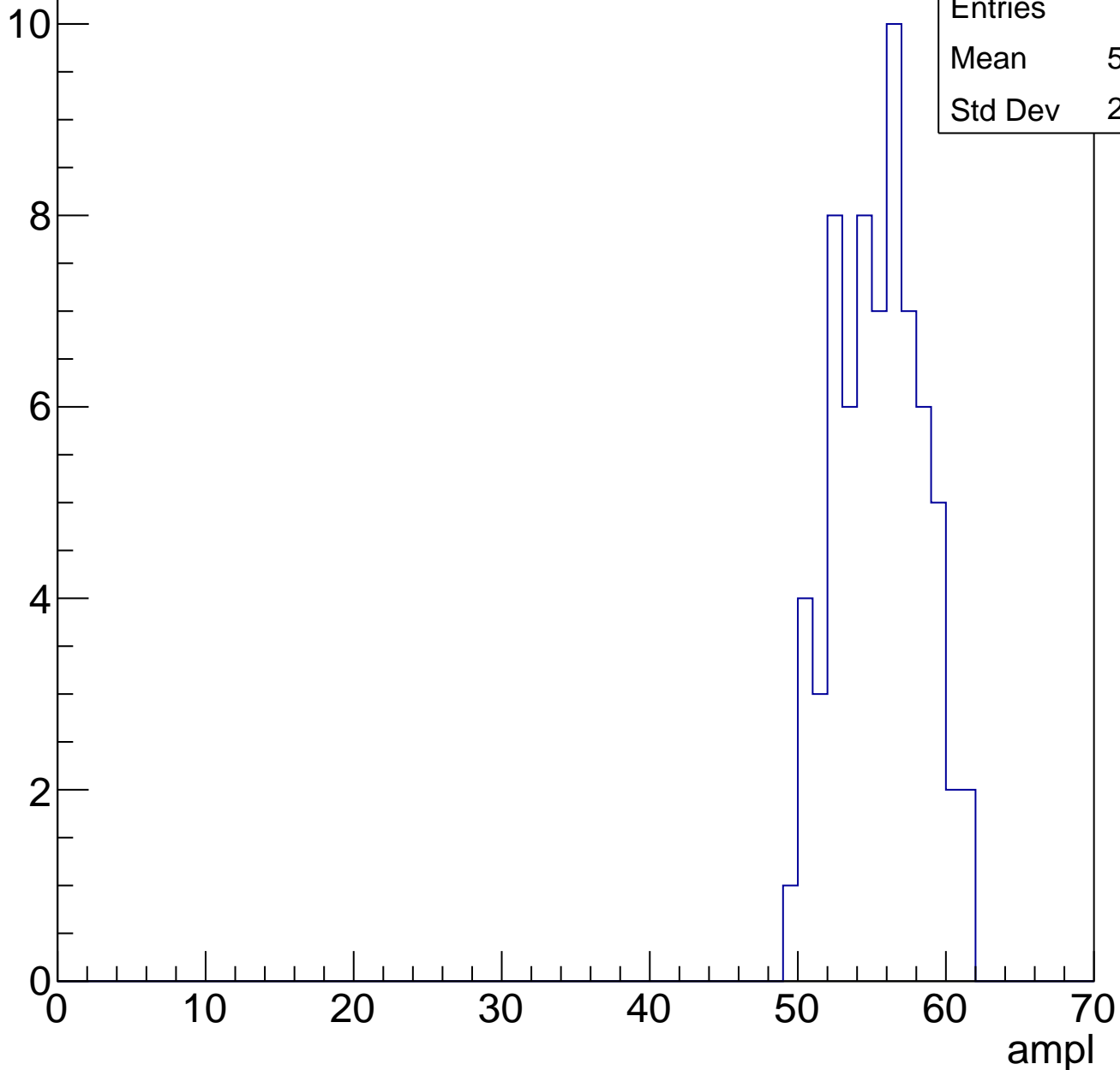


B0L002S, U7-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	55.03
Std Dev	2.904

Entry



B0L002S, U7-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries	47
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Mean	59.62
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Std Dev	8.946
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8

6

4

2

0

0

10

20

30

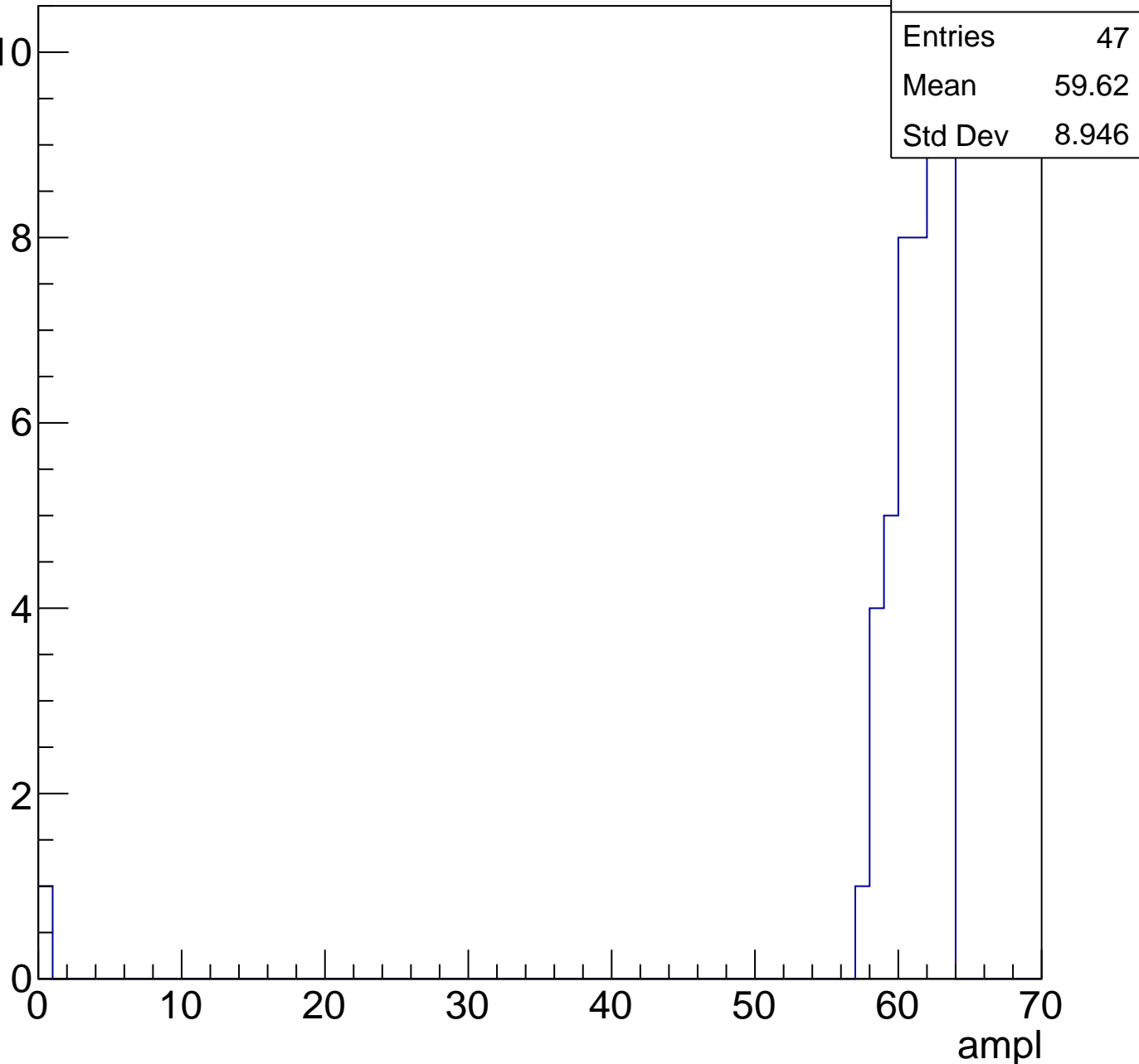
40

50

60

70

ampl



B0L002S, U7-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch18, adc0

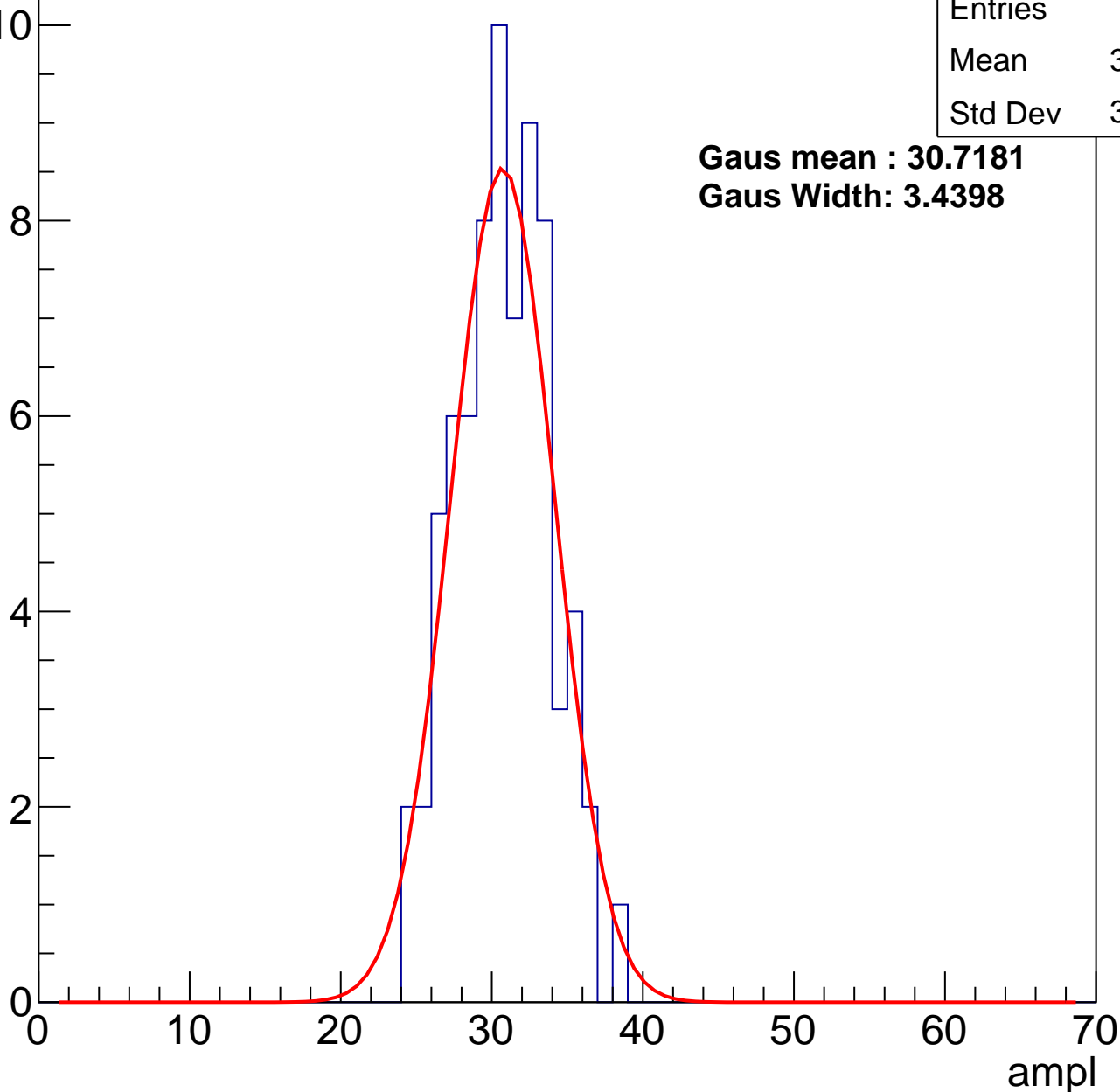
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.29
Std Dev	3.054

Gaus mean : 30.7181

Gaus Width: 3.4398



B0L002S, U7-ch18, adc1

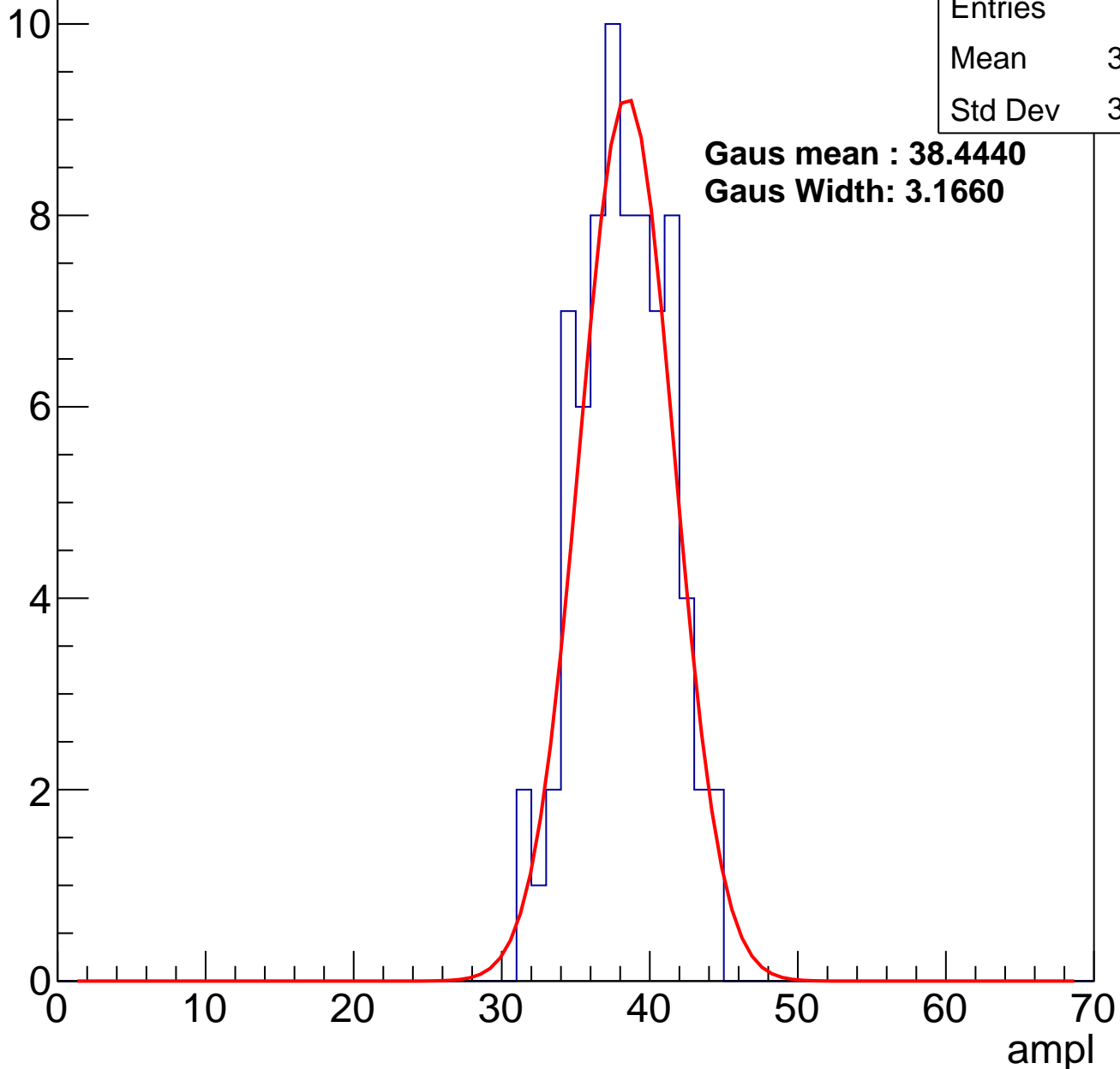
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.76
Std Dev	3.015

Gaus mean : 38.4440

Gaus Width: 3.1660

Entry



B0L002S, U7-ch18, adc2

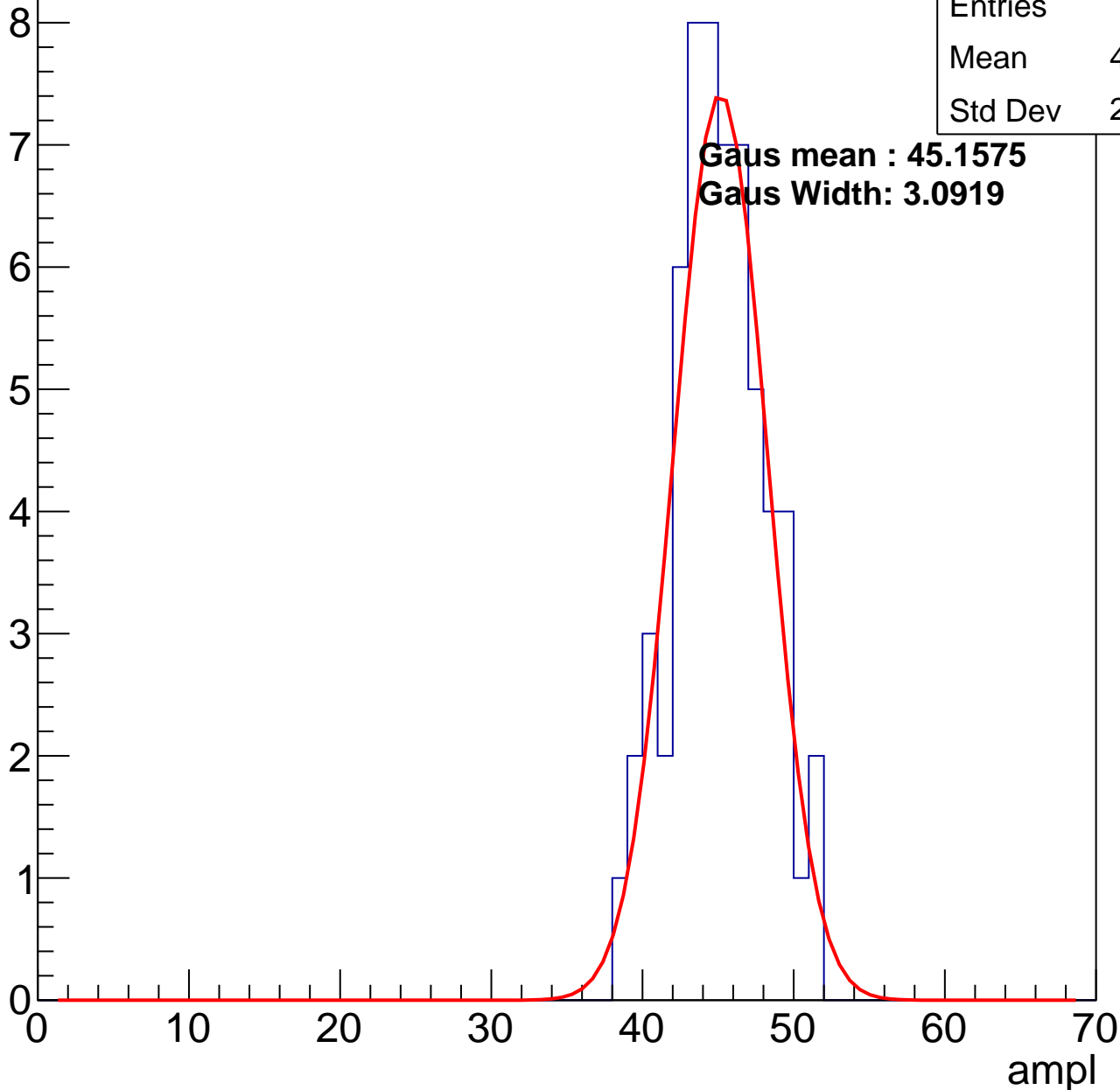
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.63
Std Dev	2.994

Gaus mean : 45.1575

Gaus Width: 3.0919

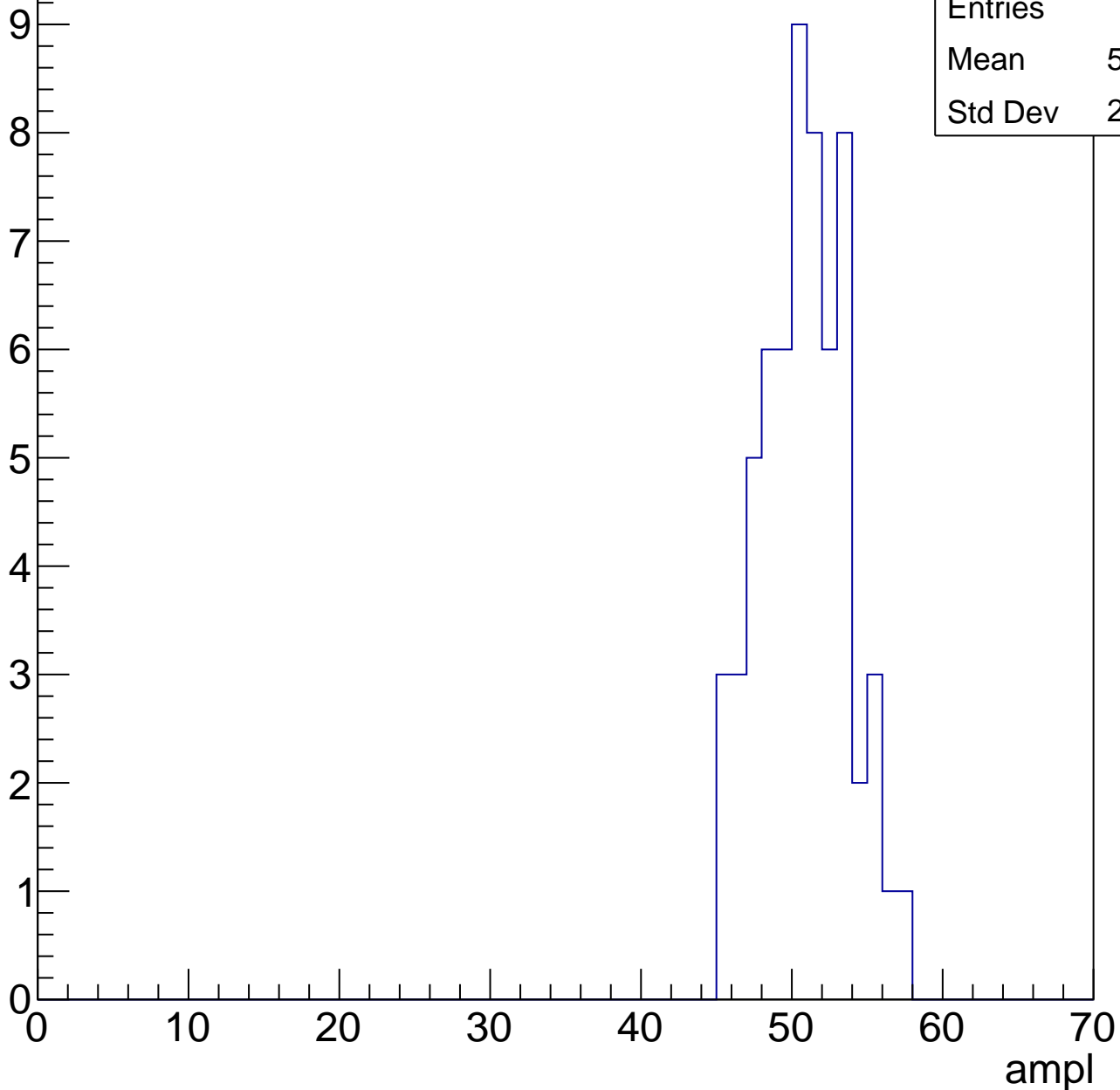


B0L002S, U7-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.33
Std Dev	2.827



B0L002S, U7-ch18, adc4

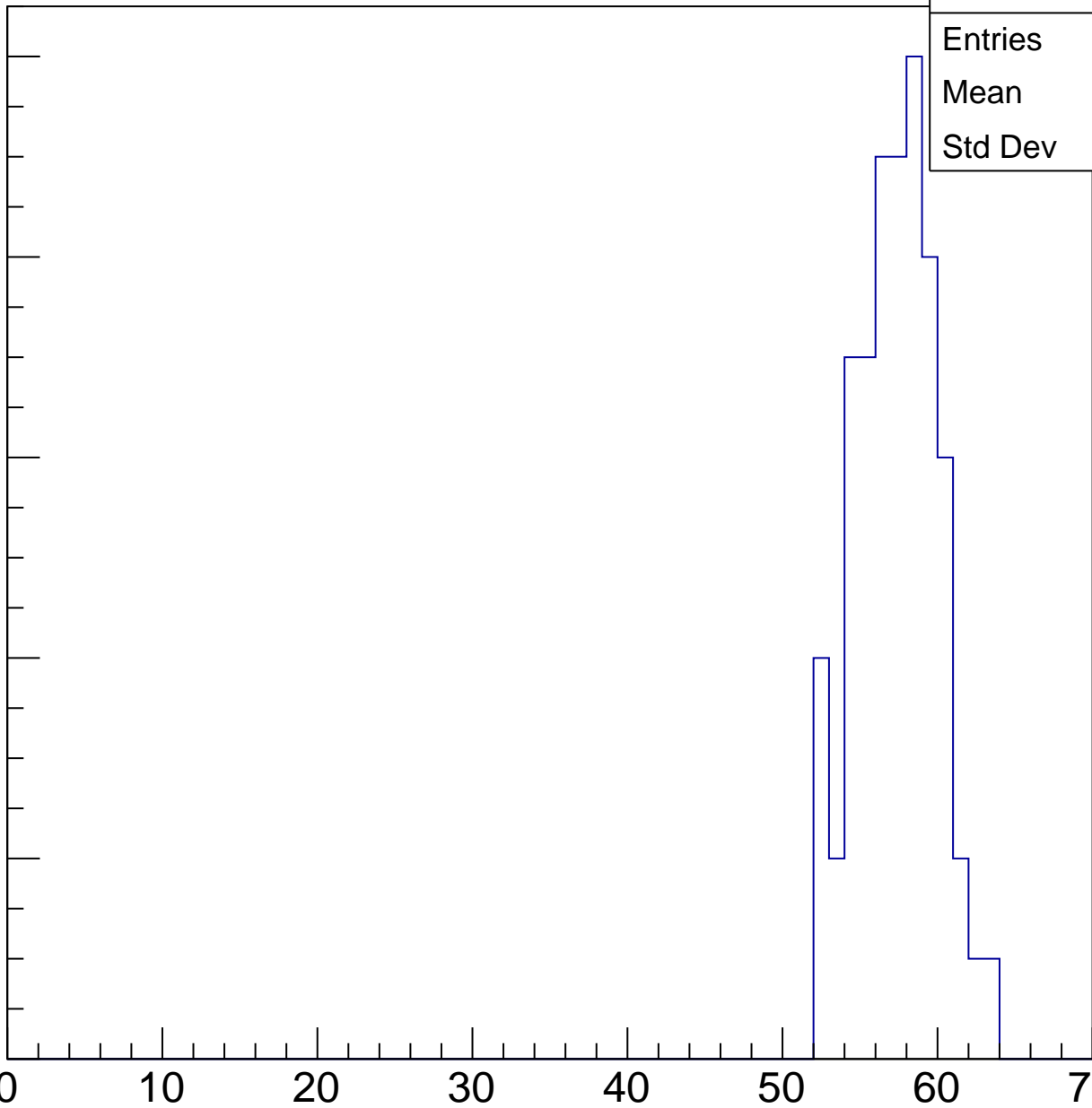
calib_packv5_042523_0143.root, FC#8, port C1

Entry

10
8
6
4
2
0

Entries	66
Mean	56.86
Std Dev	2.522

ampl

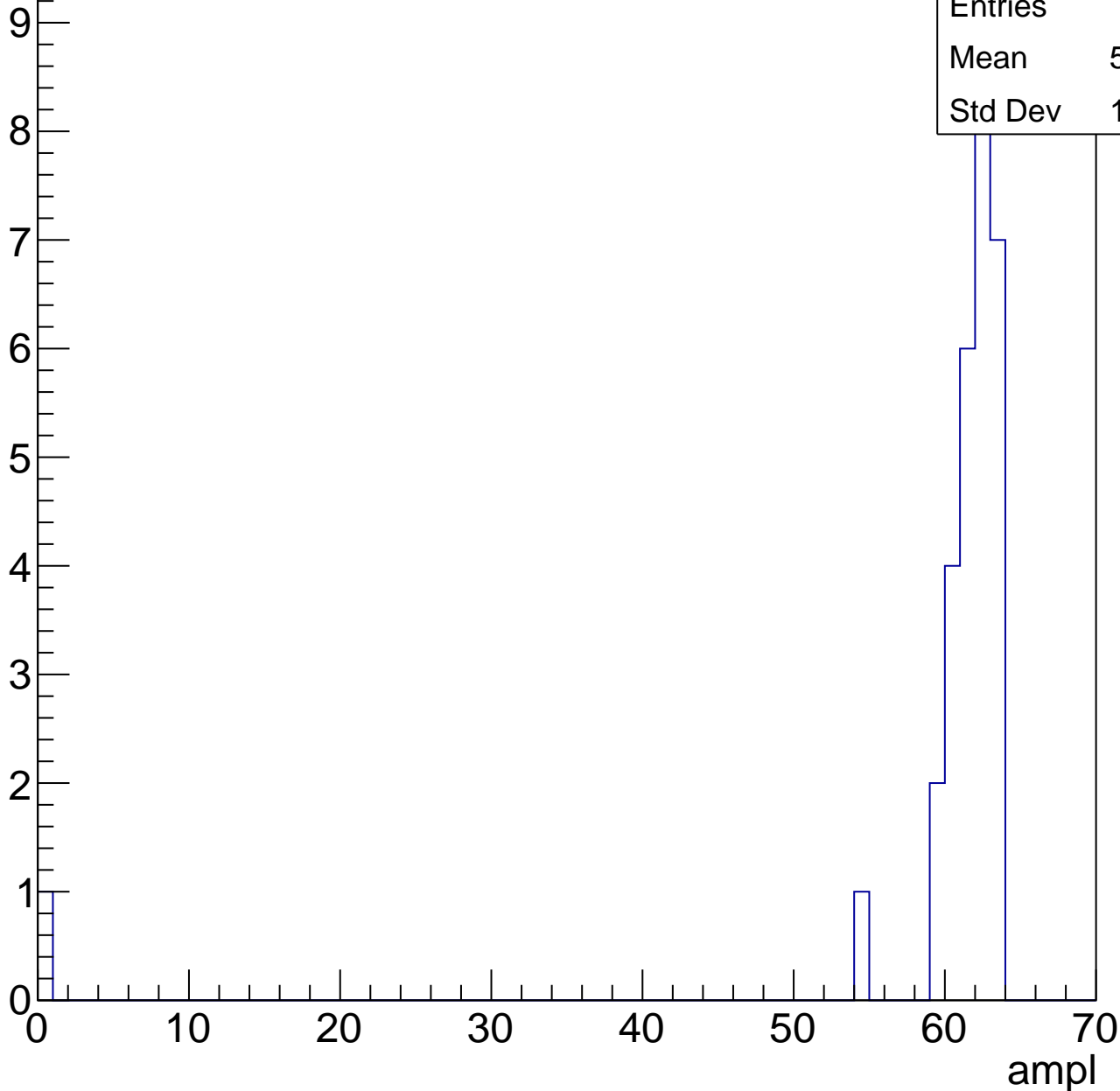


B0L002S, U7-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.23
Std Dev	11.14



B0L002S, U7-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U7-ch19, adc0

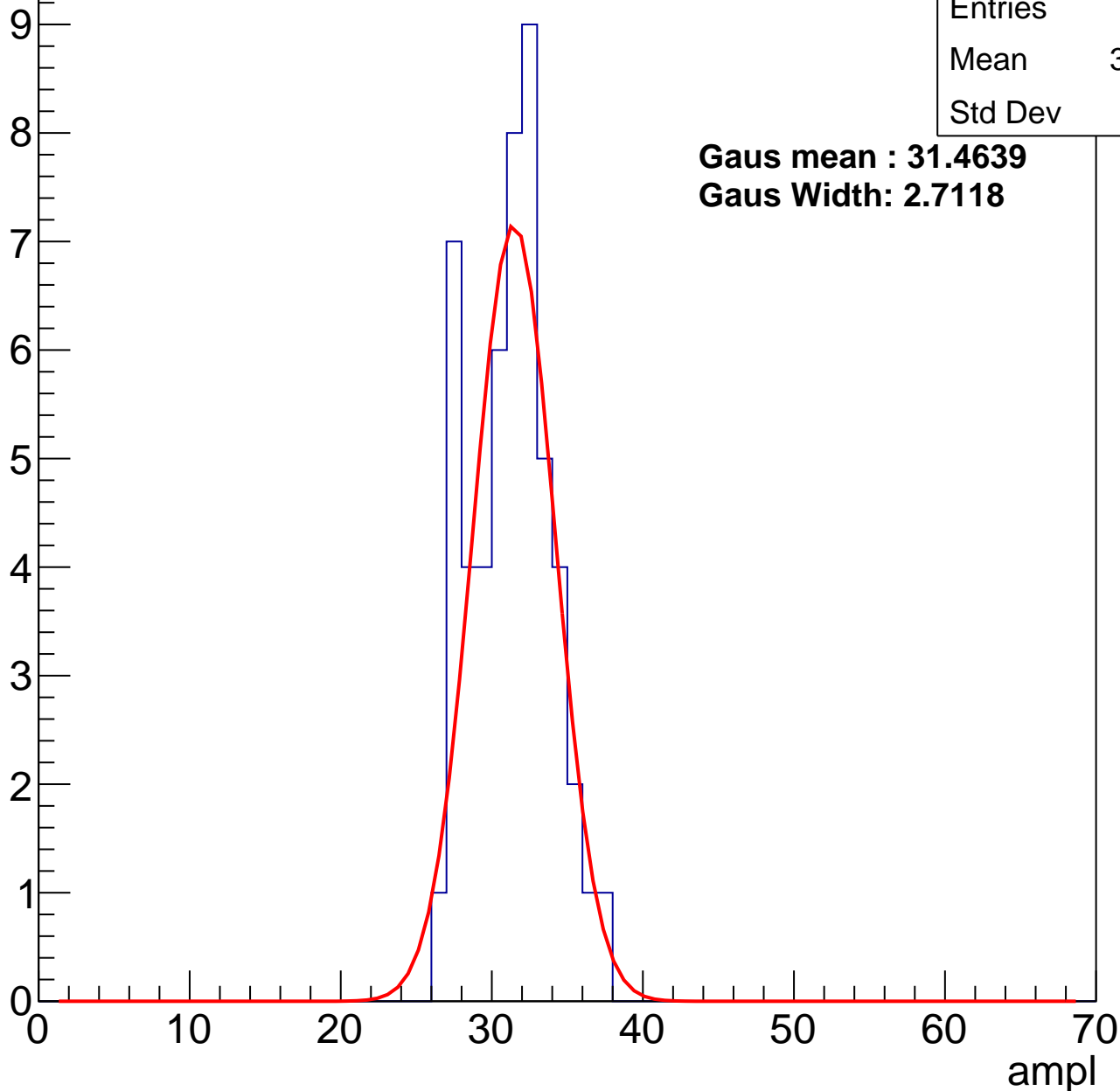
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.83
Std Dev	2.6

Gaus mean : 31.4639

Gaus Width: 2.7118



B0L002S, U7-ch19, adc1

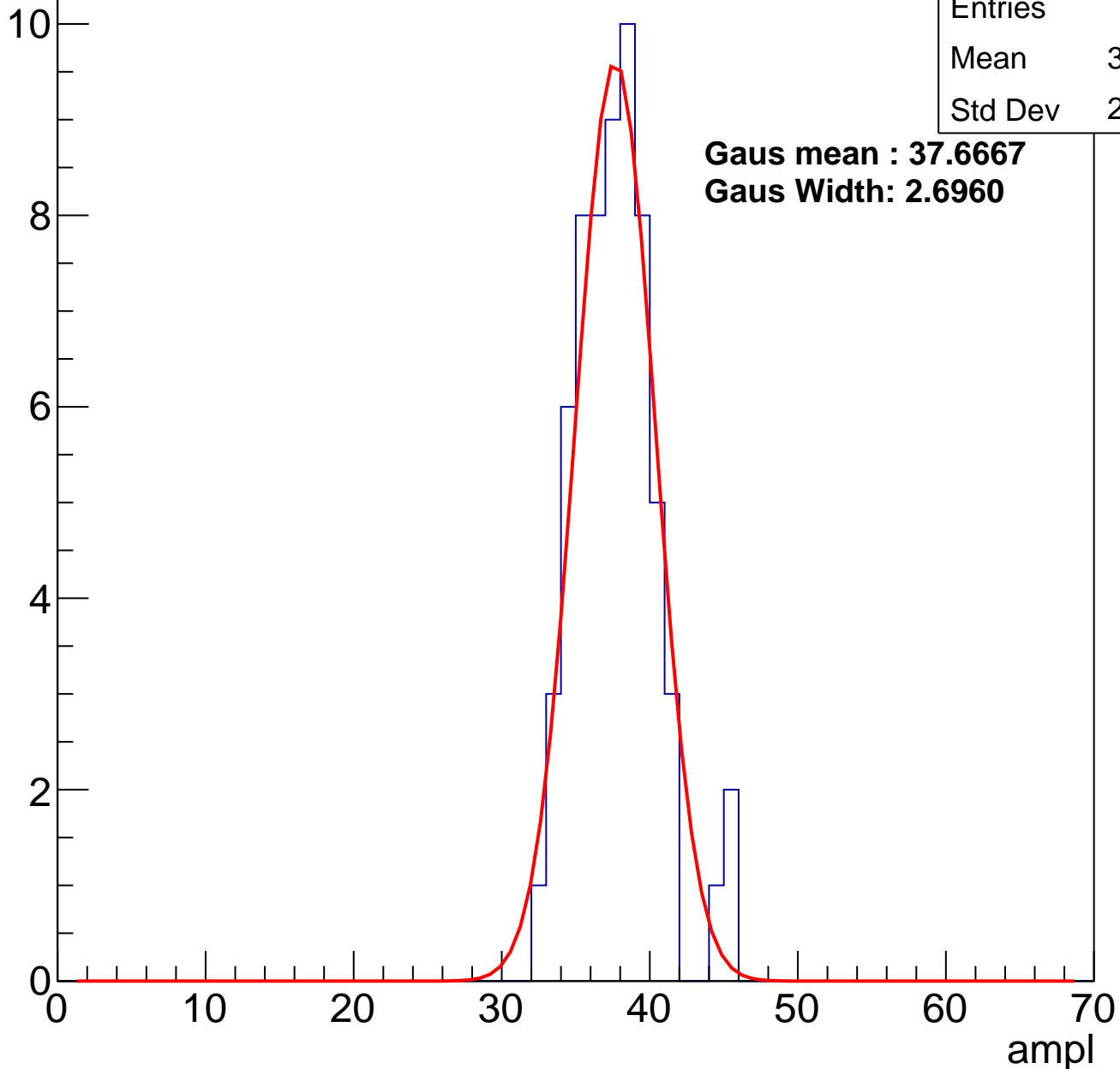
calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	37.27
Std Dev	2.723

Gaus mean : 37.6667

Gaus Width: 2.6960

Entry



B0L002S, U7-ch19, adc2

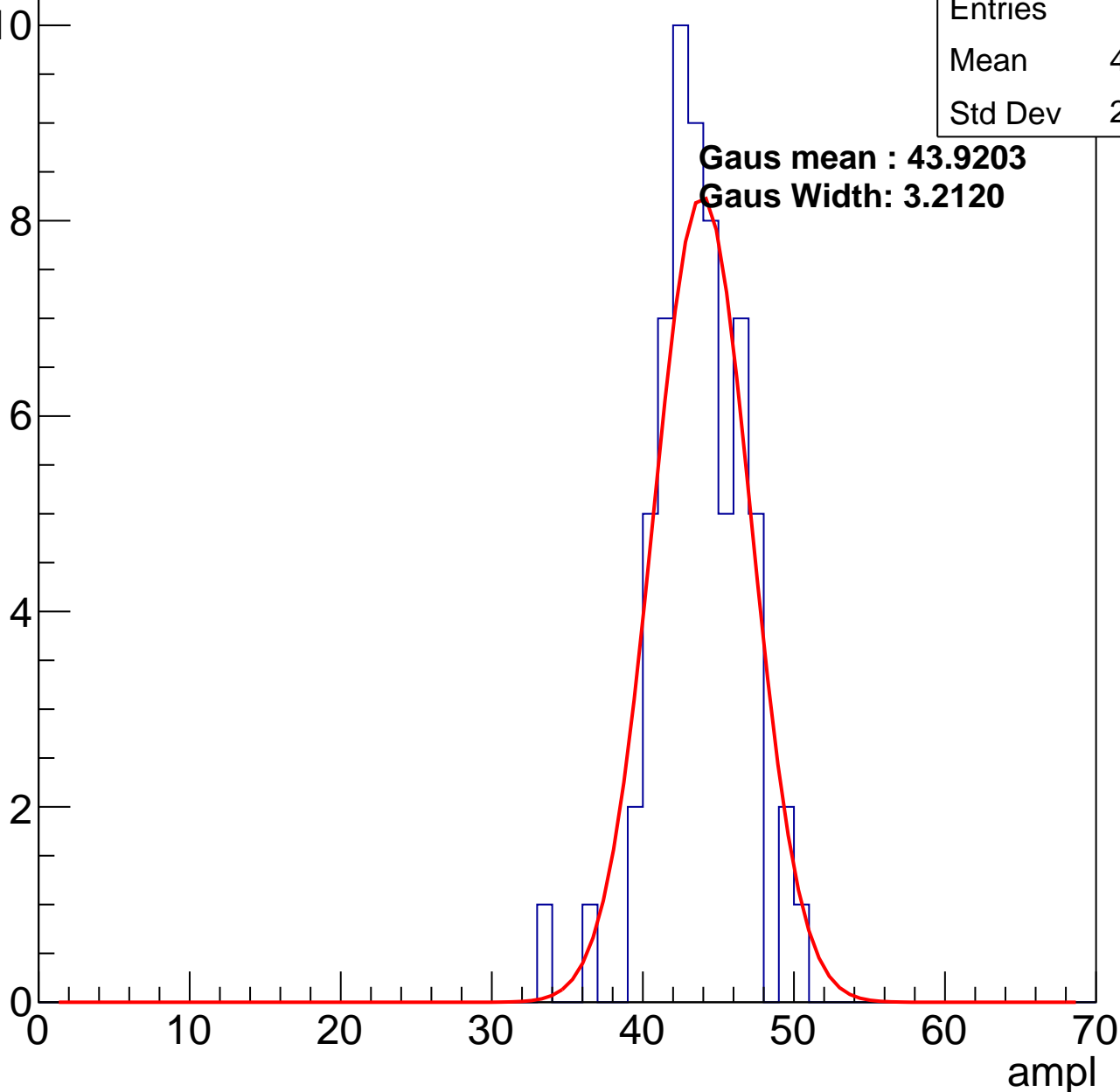
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.22
Std Dev	2.962

Gaus mean : 43.9203

Gaus Width: 3.2120

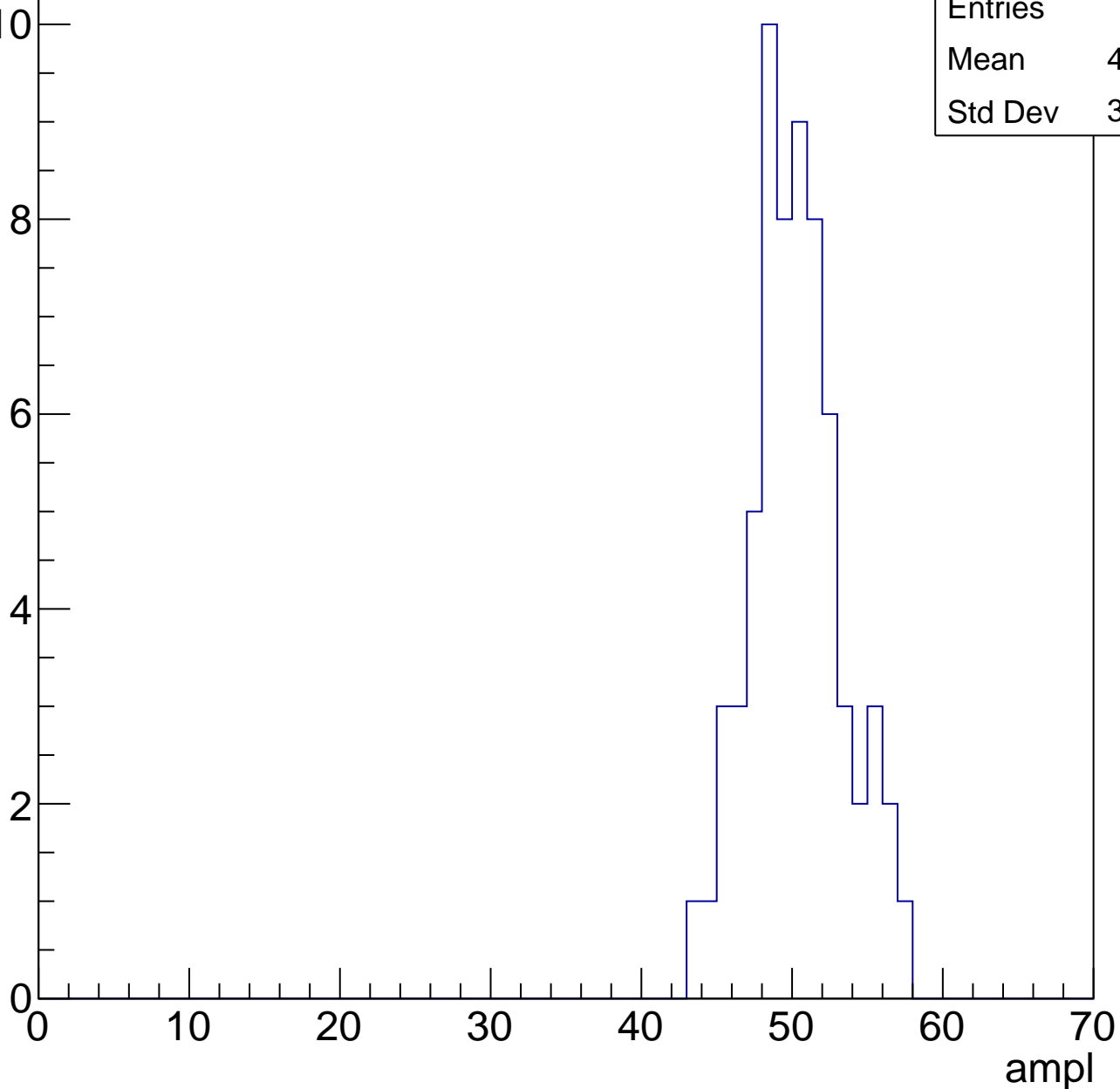


B0L002S, U7-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.82
Std Dev	3.002

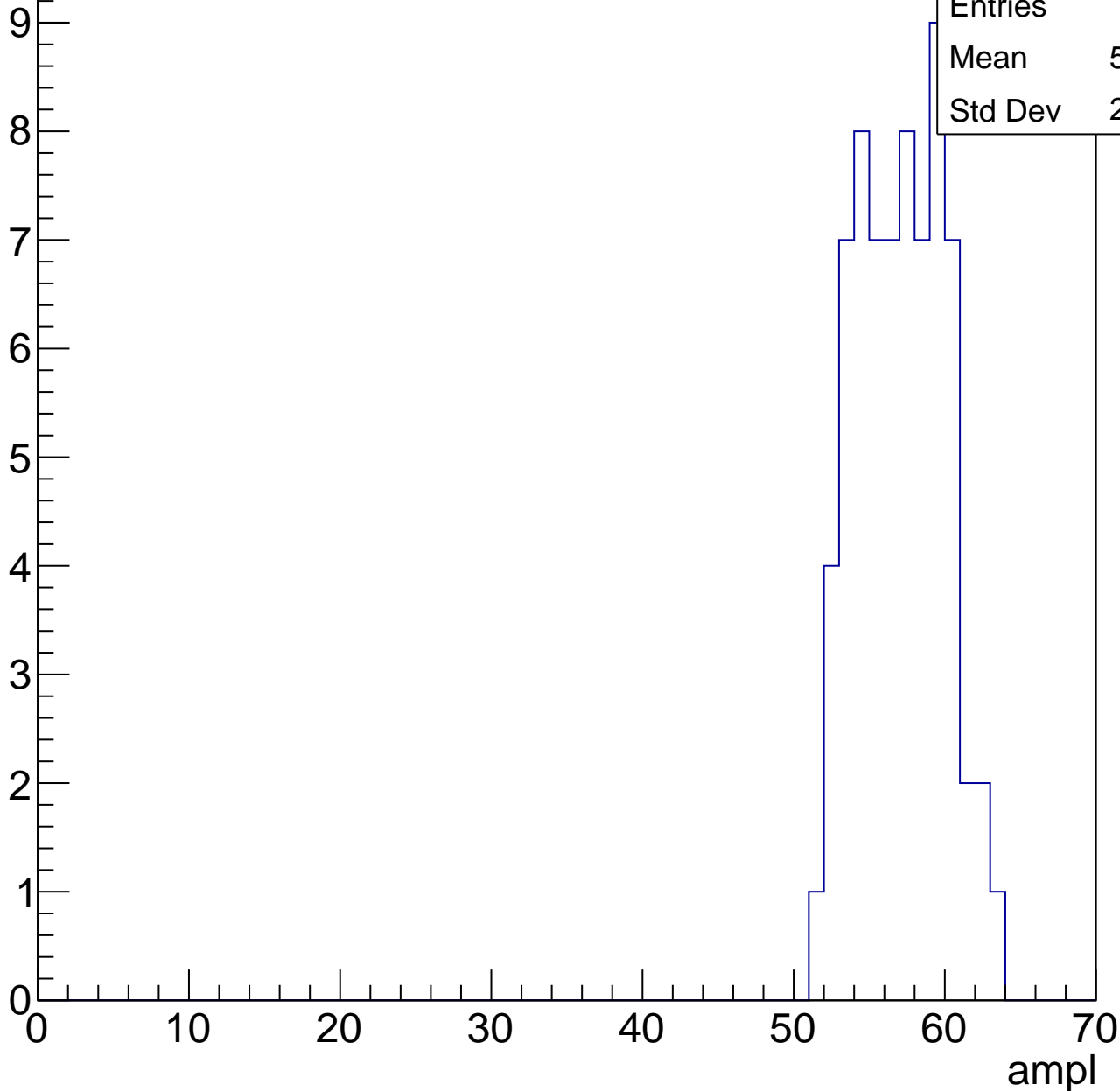


B0L002S, U7-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	56.59
Std Dev	2.846

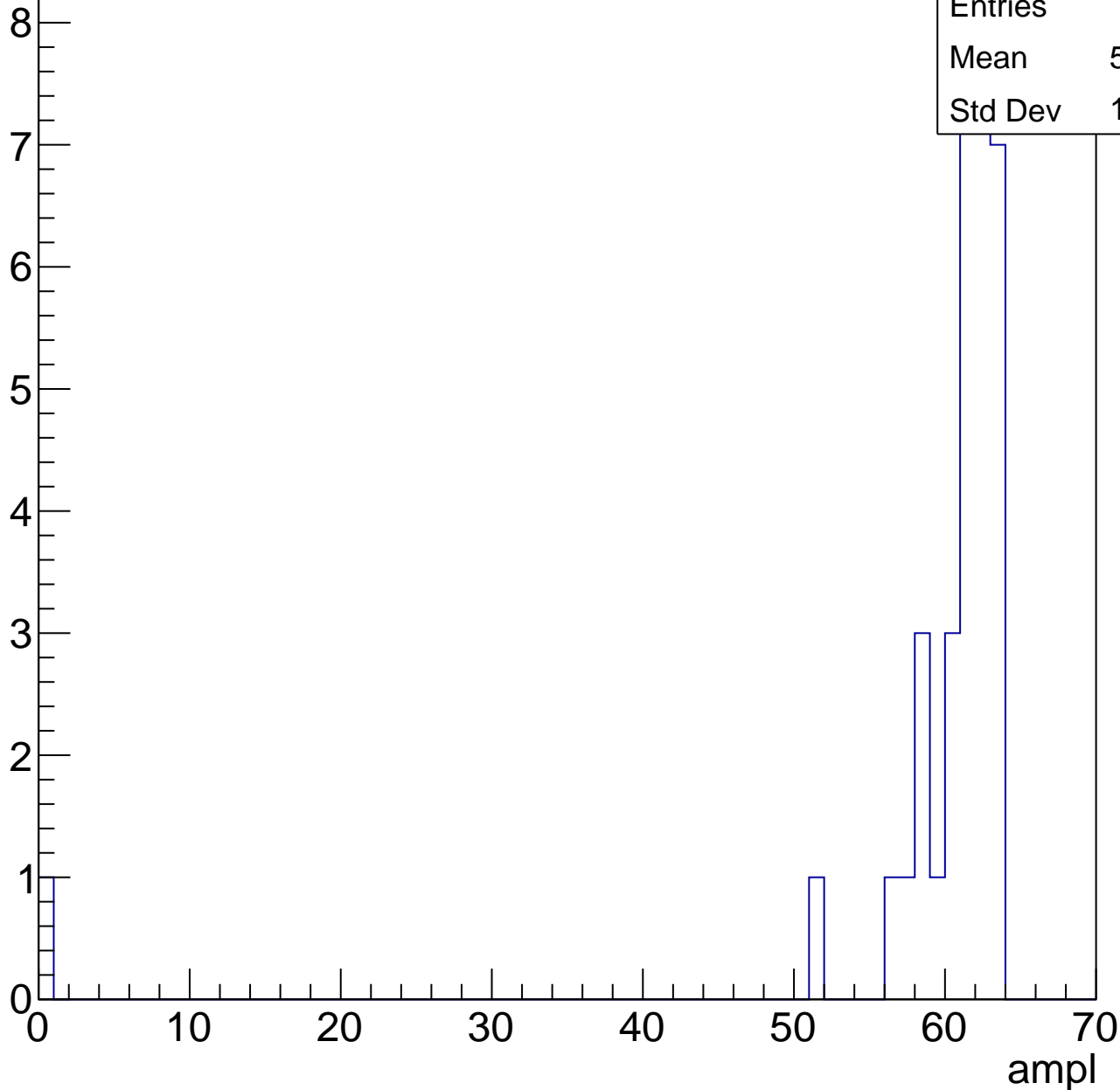


B0L002S, U7-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.88
Std Dev	10.54



B0L002S, U7-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

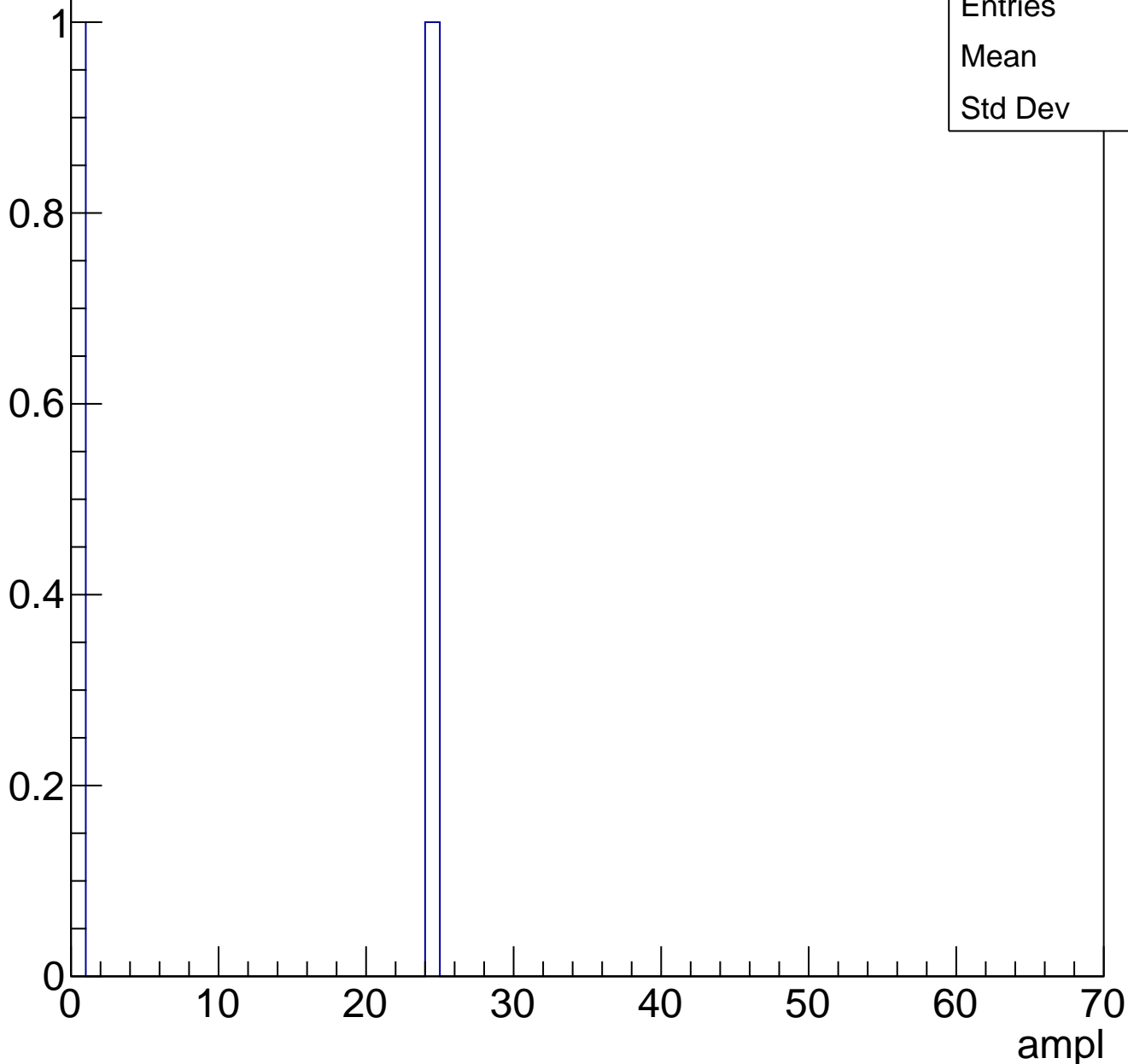
Entry



B0L002S, U7-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch20, adc0

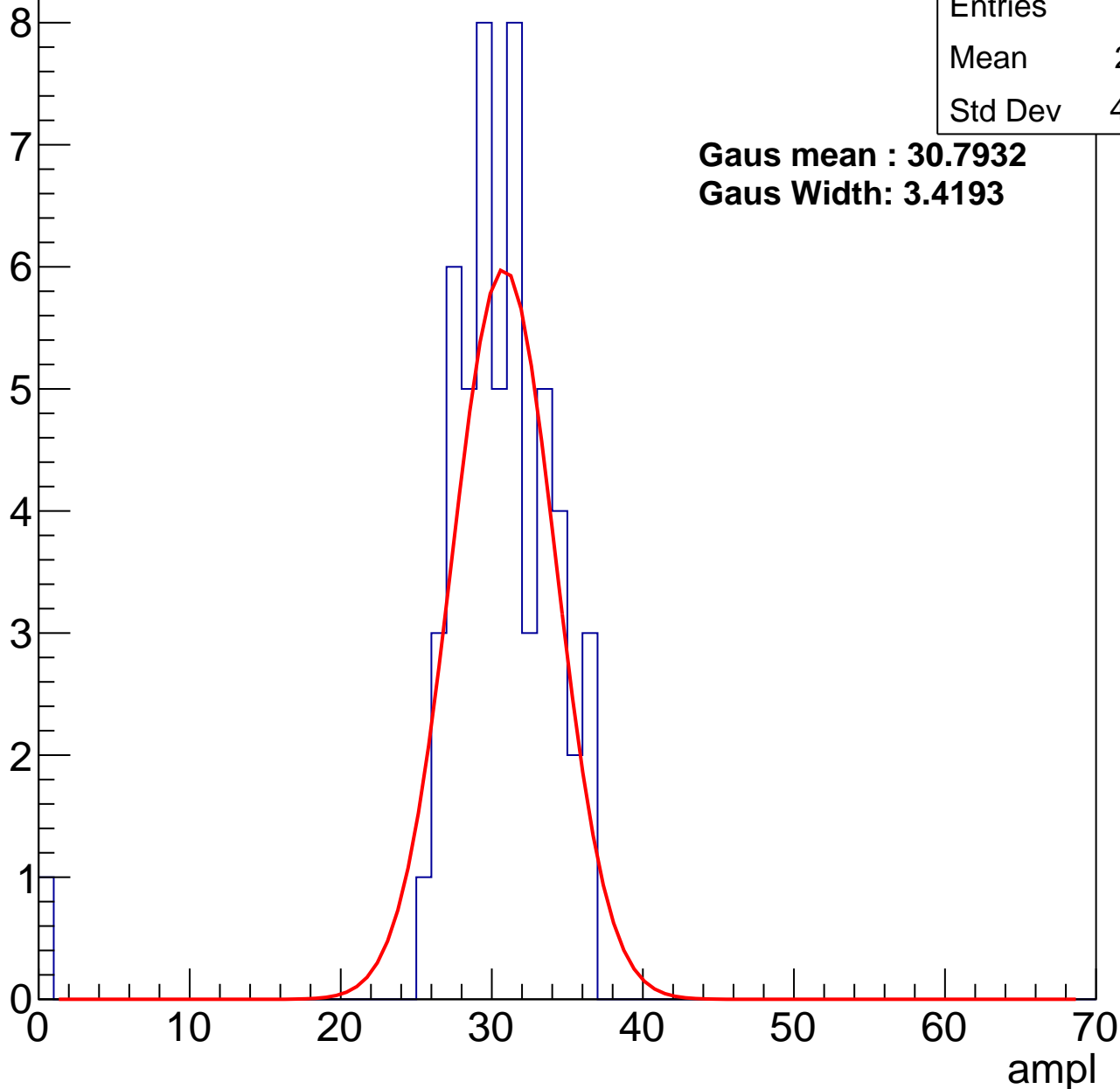
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	29.81
Std Dev	4.982

Gaus mean : 30.7932

Gaus Width: 3.4193



B0L002S, U7-ch20, adc1

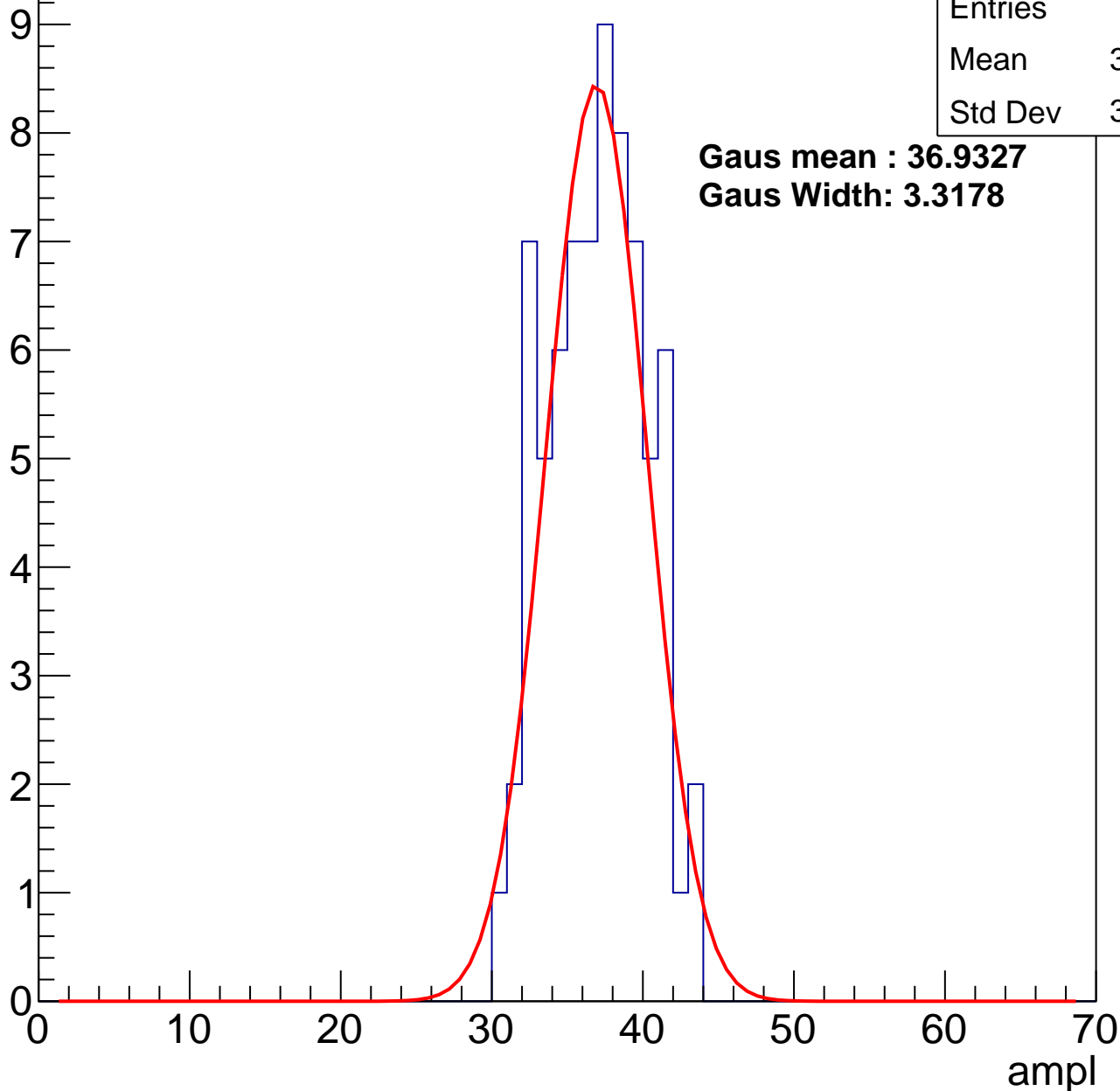
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.52
Std Dev	3.145

Gaus mean : 36.9327

Gaus Width: 3.3178



B0L002S, U7-ch20, adc2

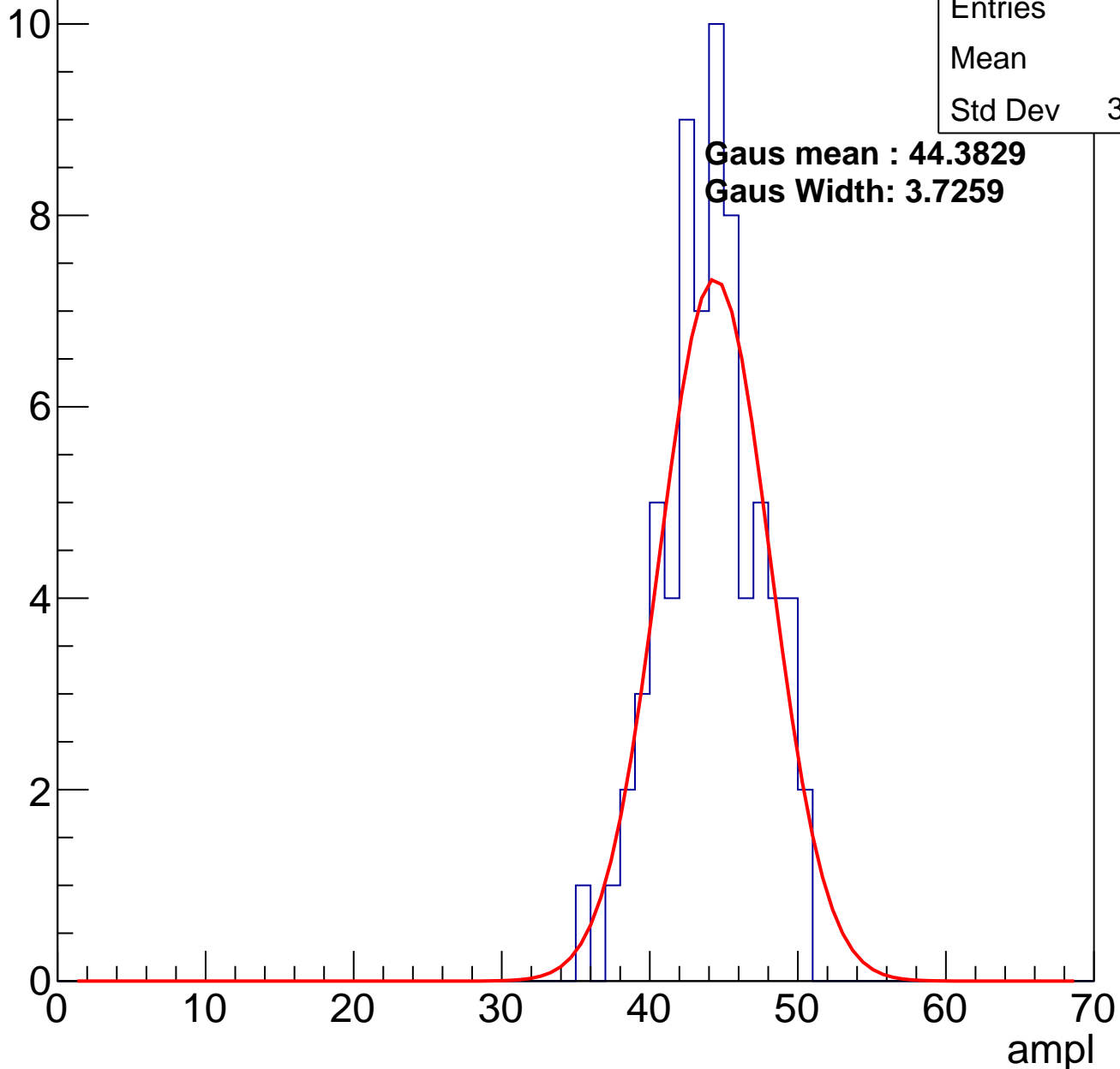
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	43.7
Std Dev	3.263

Gaus mean : 44.3829

Gaus Width: 3.7259

Entry

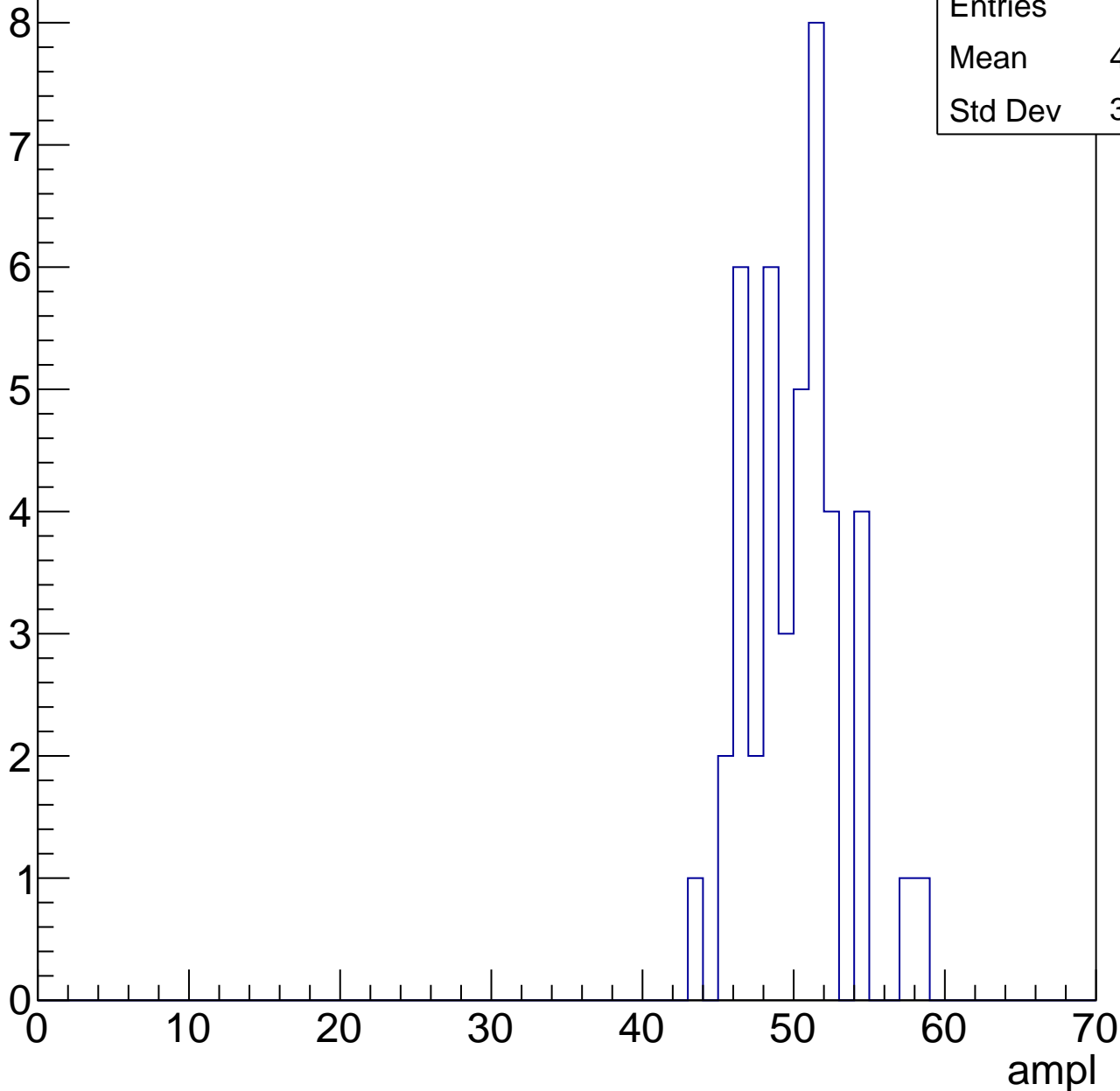


B0L002S, U7-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	49.65
Std Dev	3.183



B0L002S, U7-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

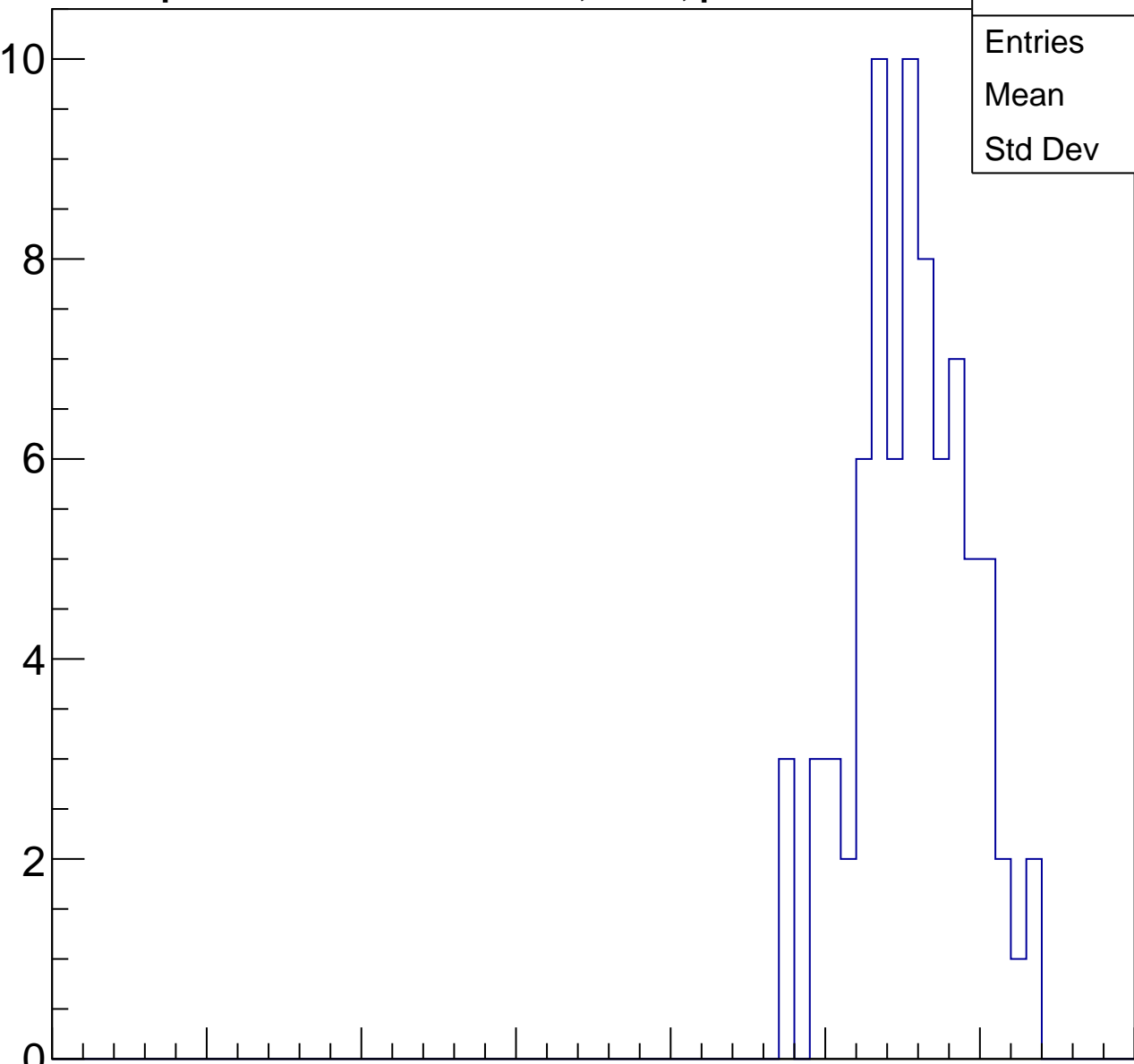
Entries	79
Mean	55.15
Std Dev	3.653

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

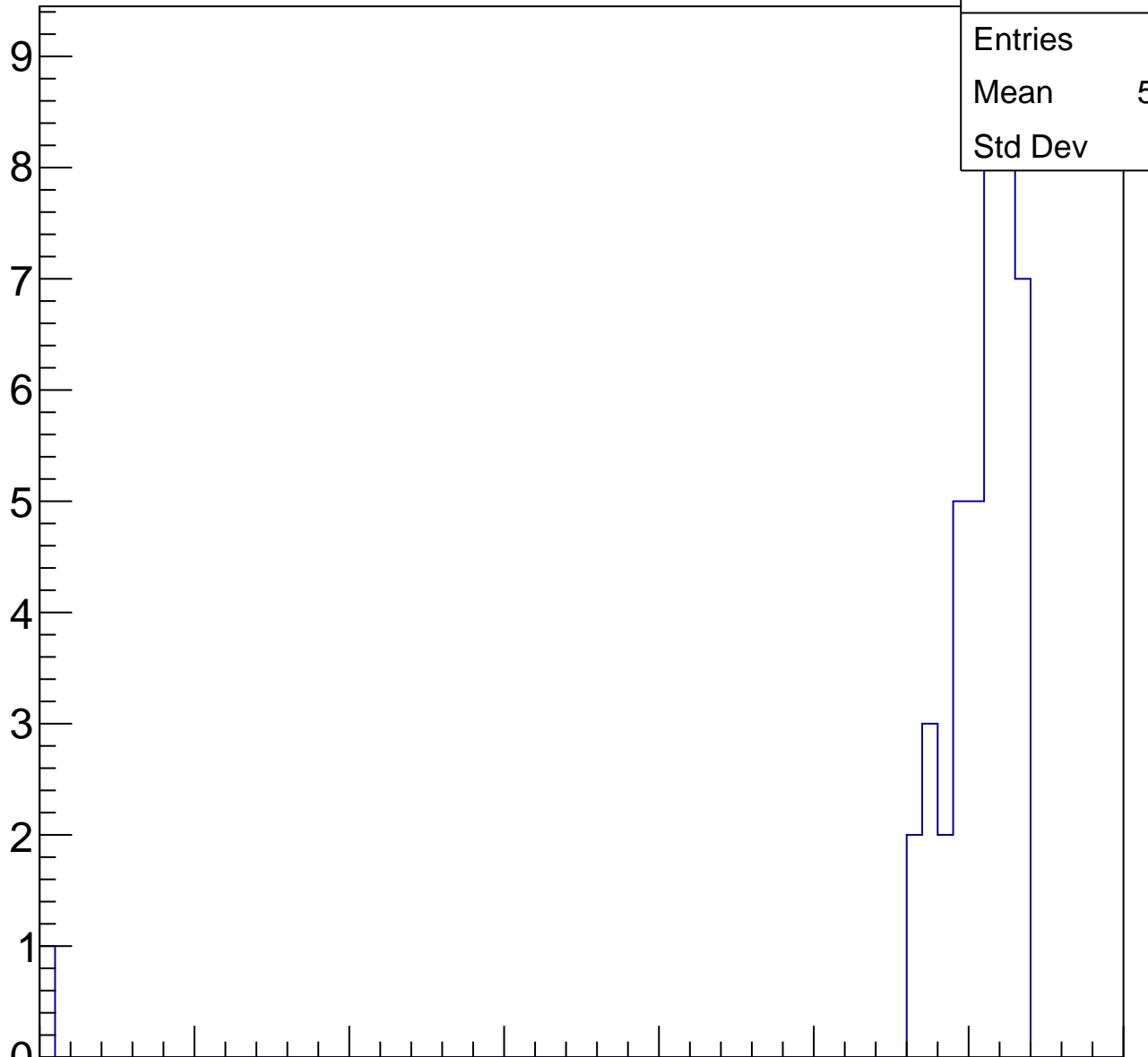
Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.07
Std Dev	9.44

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U7-ch21, adc0

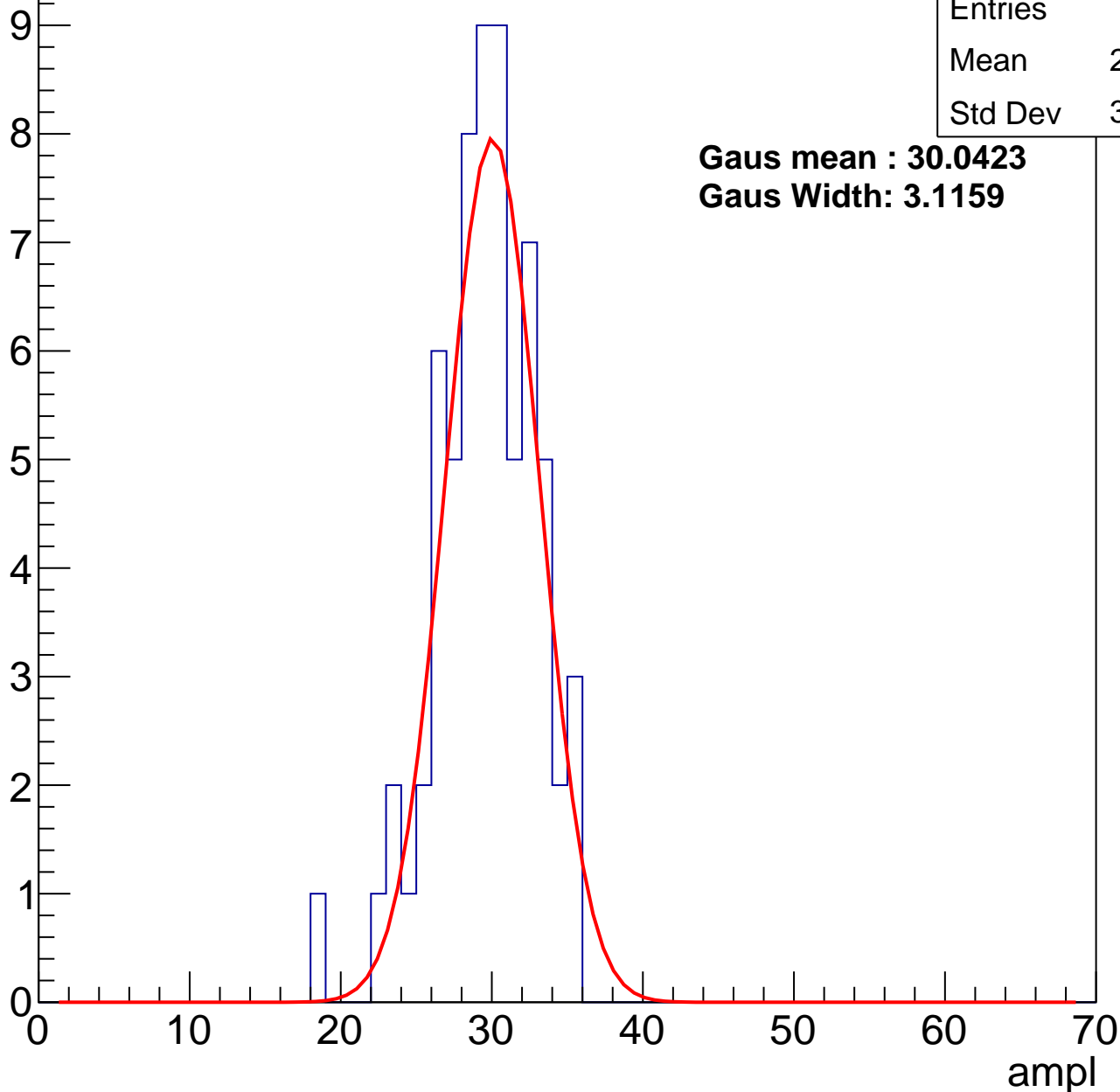
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.14
Std Dev	3.279

Gaus mean : 30.0423

Gaus Width: 3.1159



B0L002S, U7-ch21, adc1

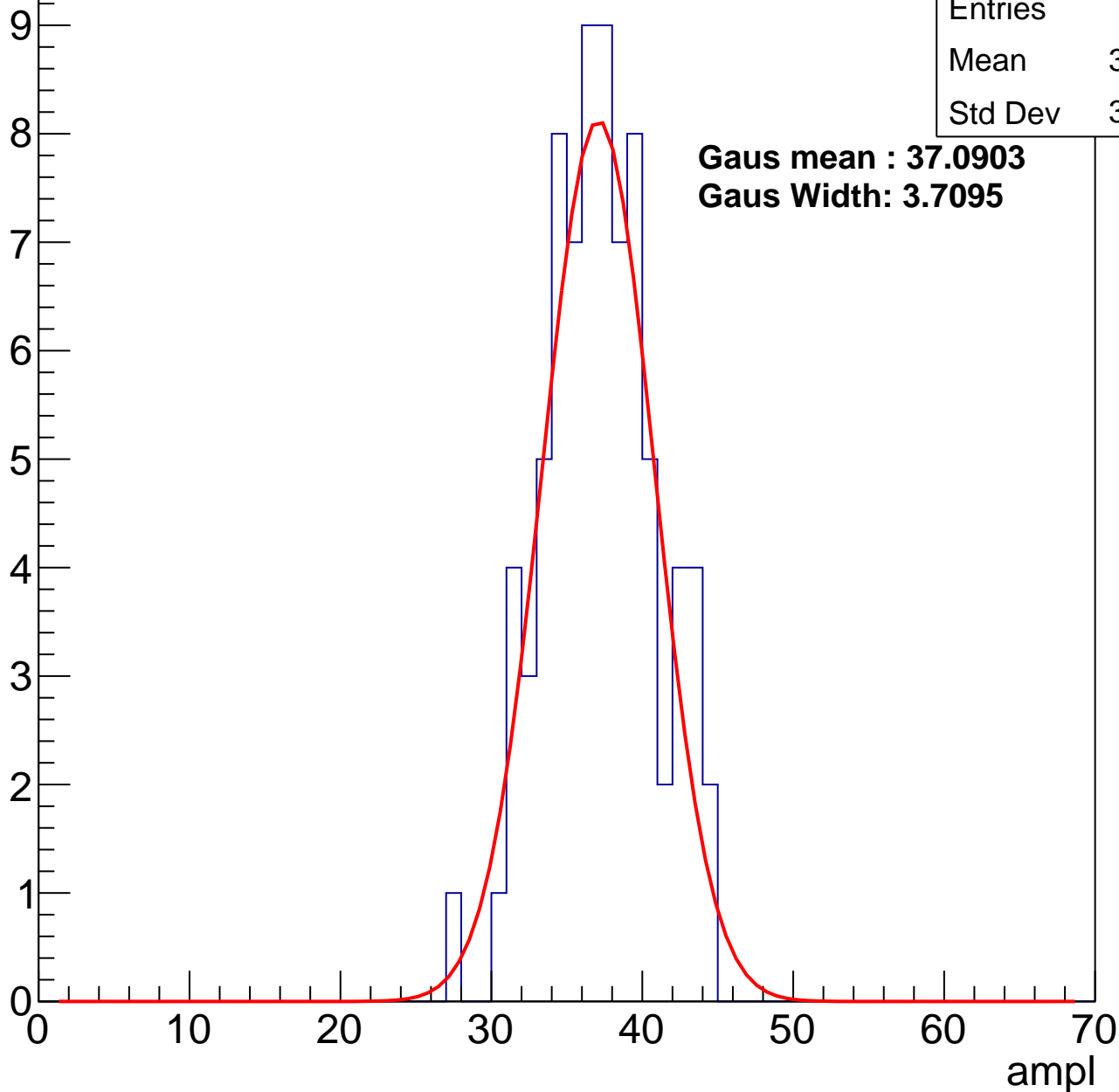
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.76
Std Dev	3.576

Gaus mean : 37.0903

Gaus Width: 3.7095

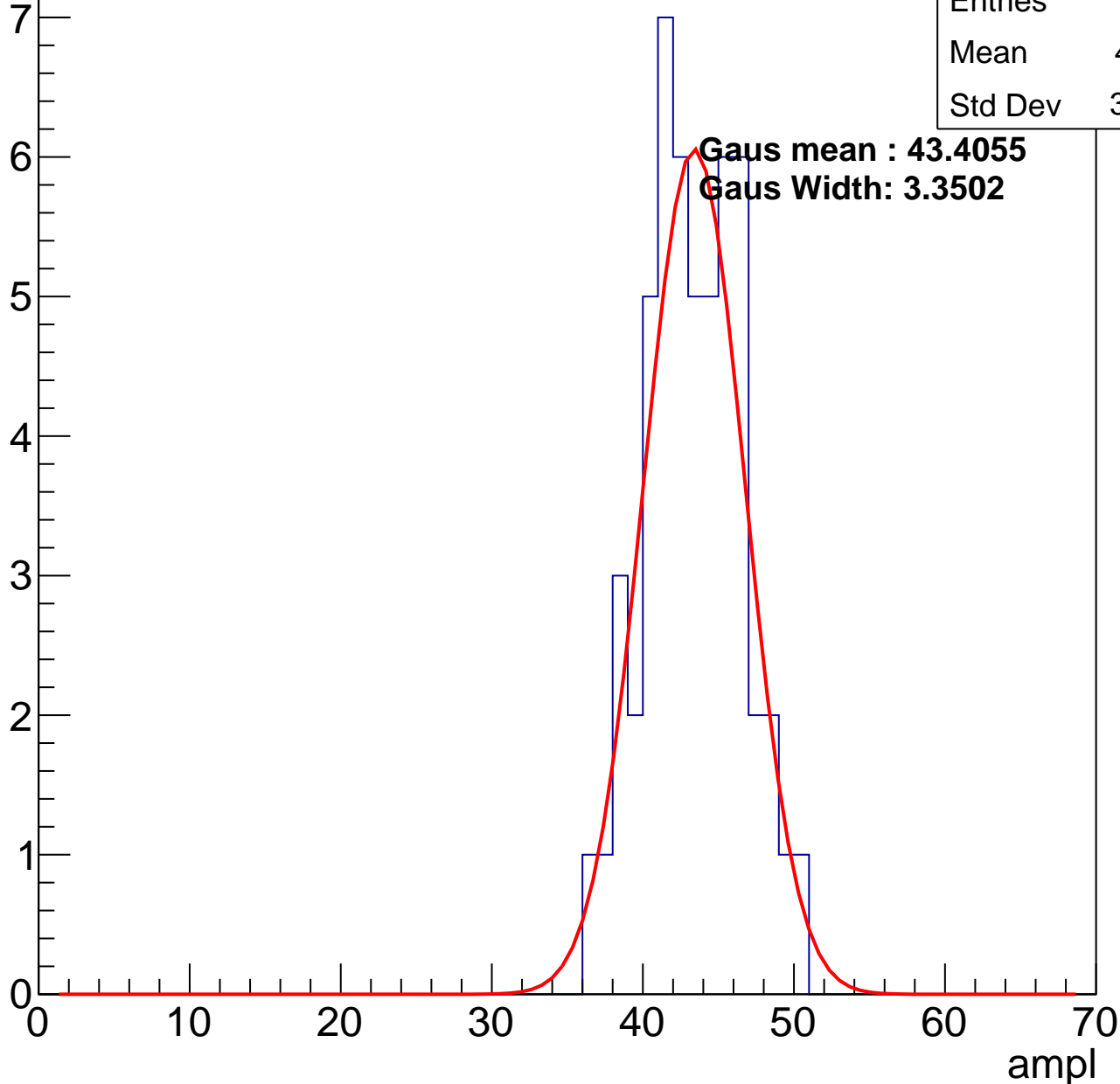


B0L002S, U7-ch21, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.91
Std Dev	3.134

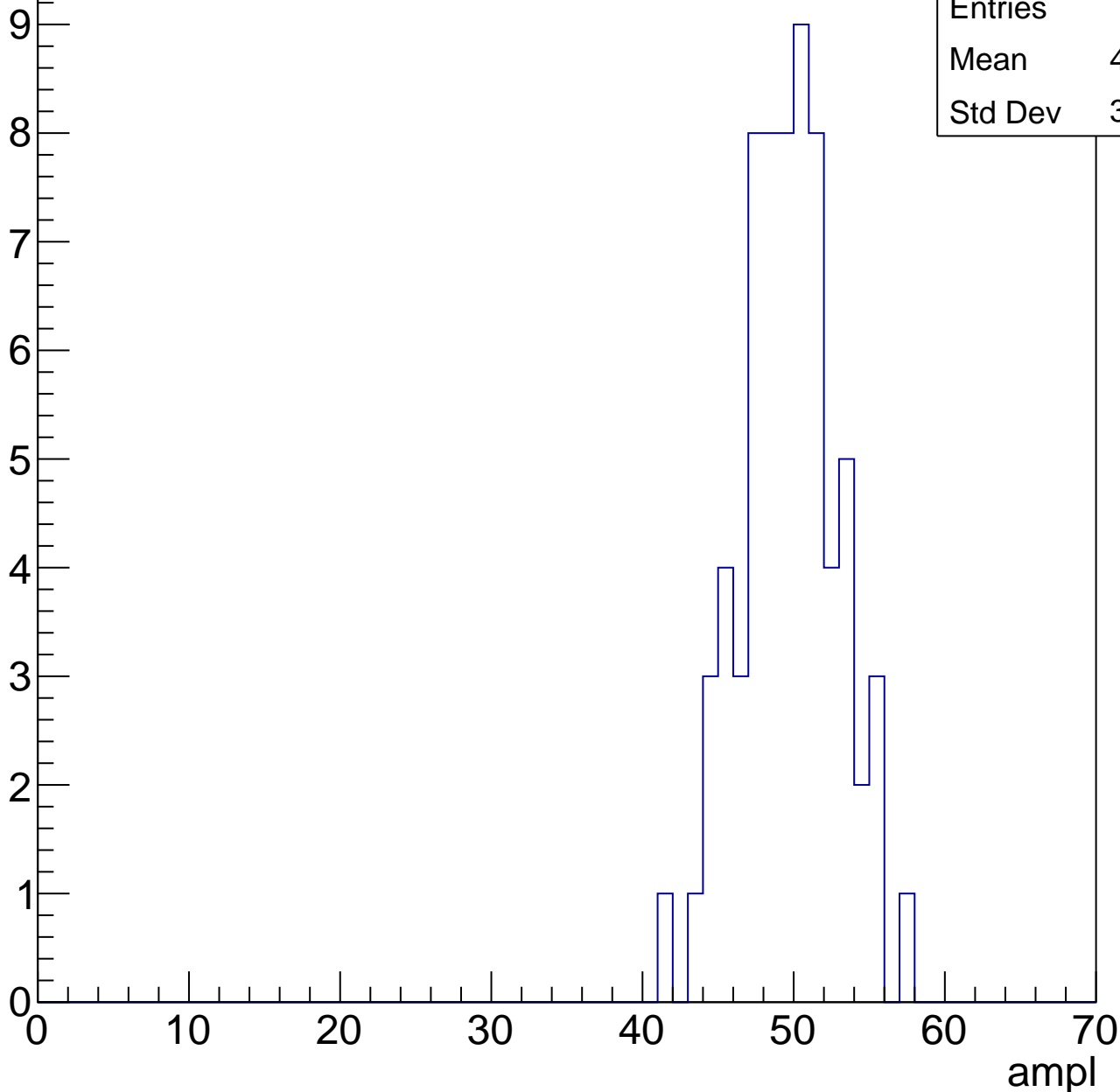


B0L002S, U7-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

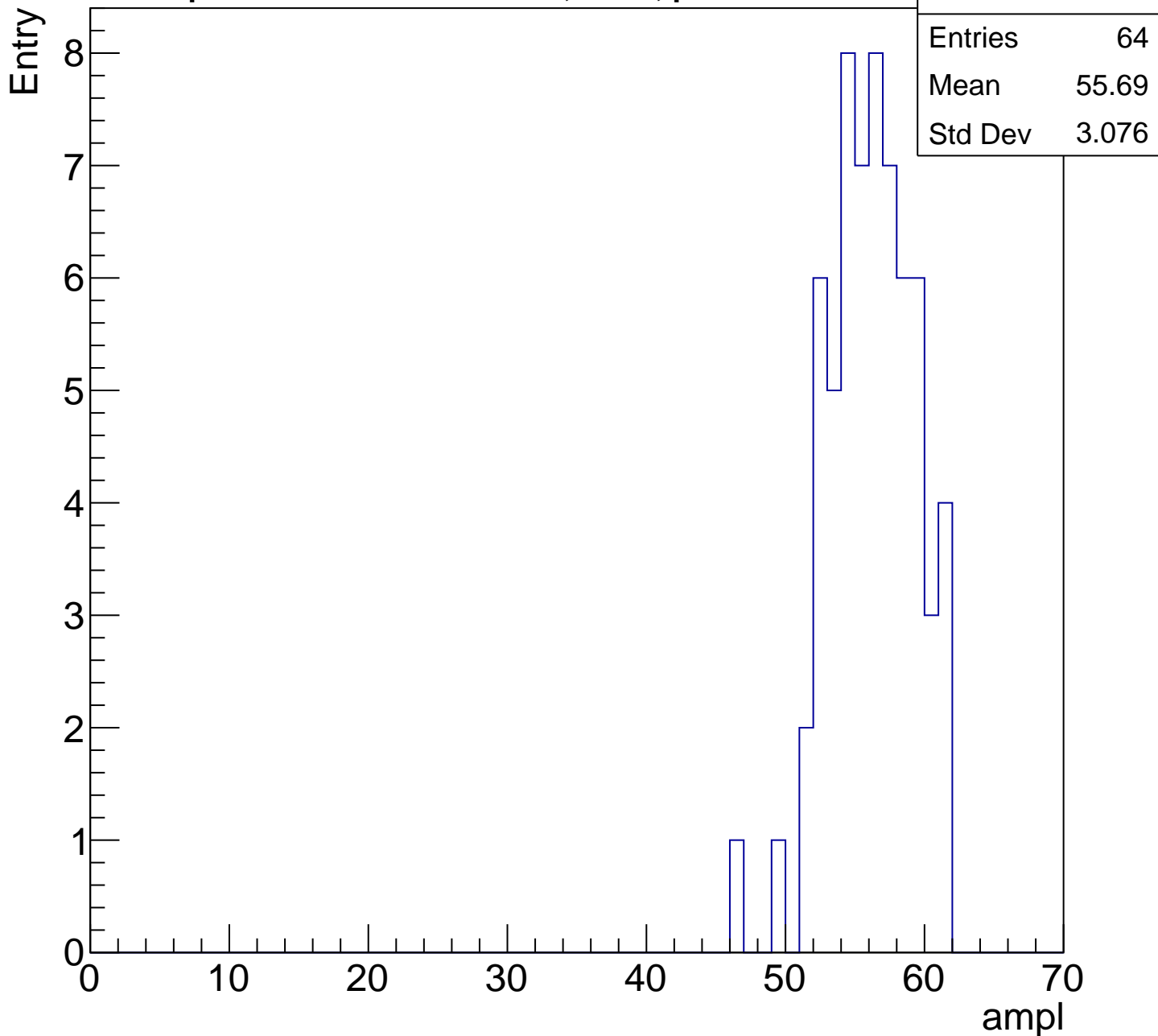
Entry

Entries	68
Mean	49.22
Std Dev	3.166



B0L002S, U7-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch21, adc5

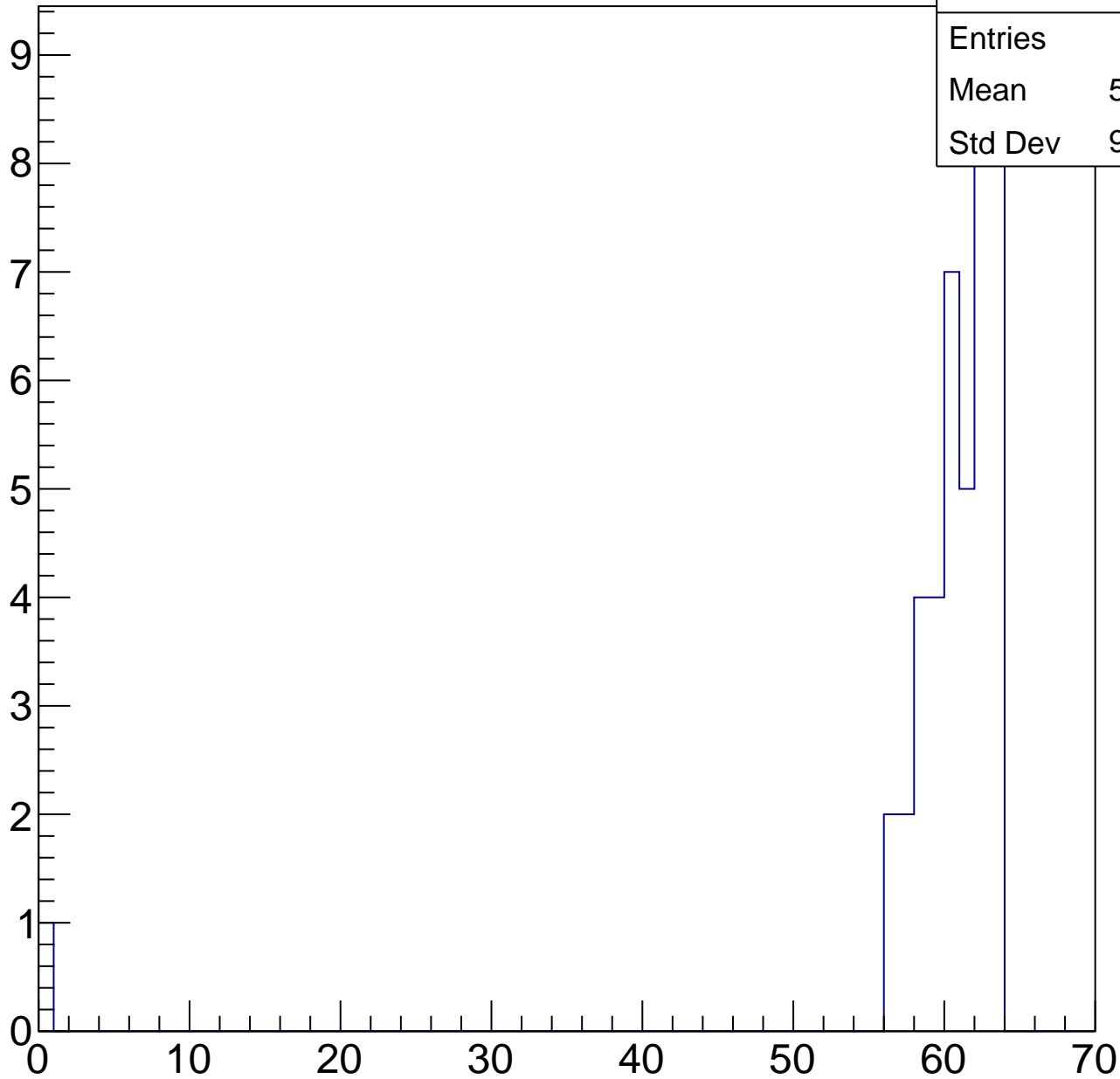
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.16
Std Dev	9.353

ampl



B0L002S, U7-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch22, adc0

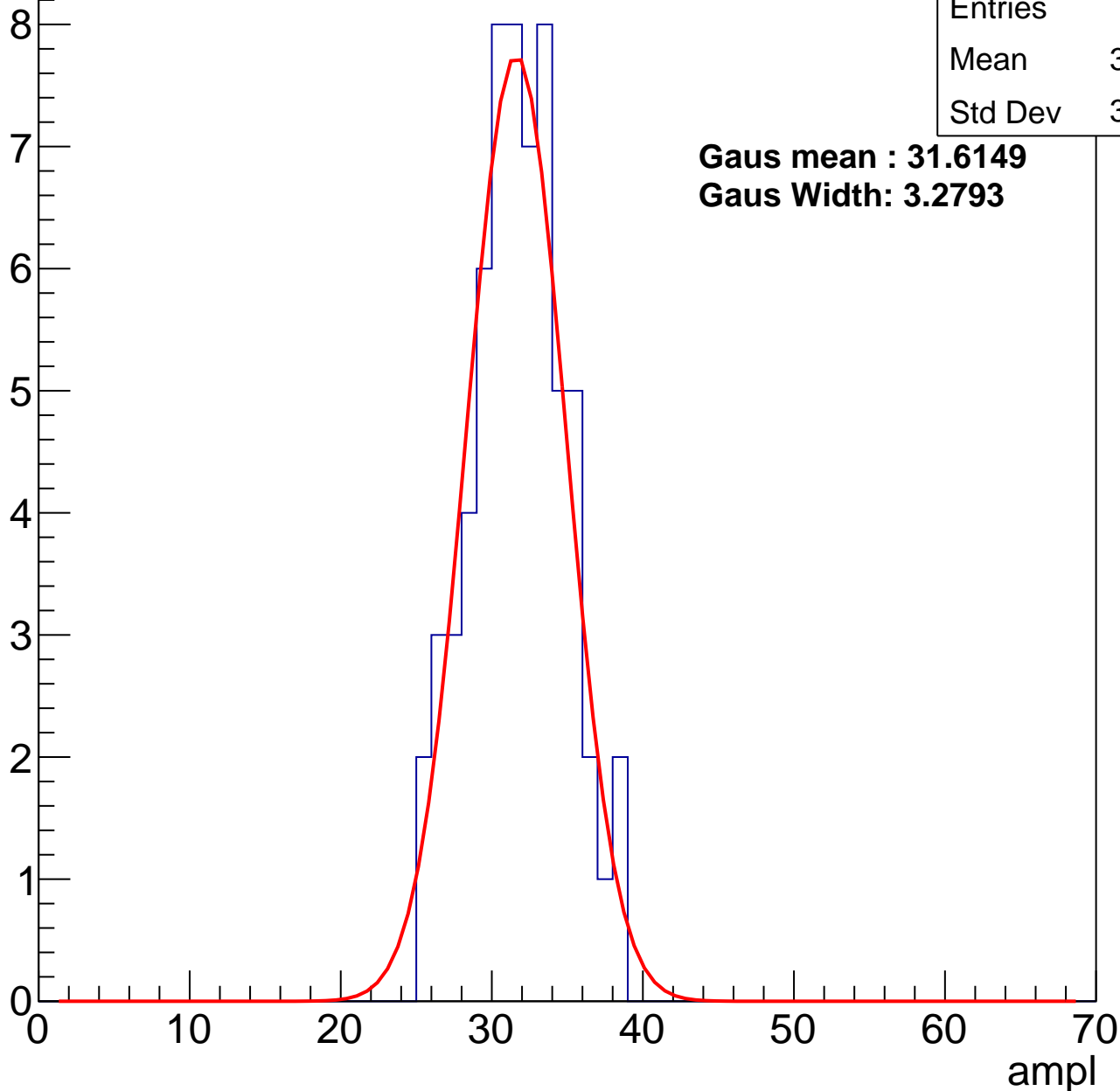
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.27
Std Dev	3.078

Gaus mean : 31.6149

Gaus Width: 3.2793



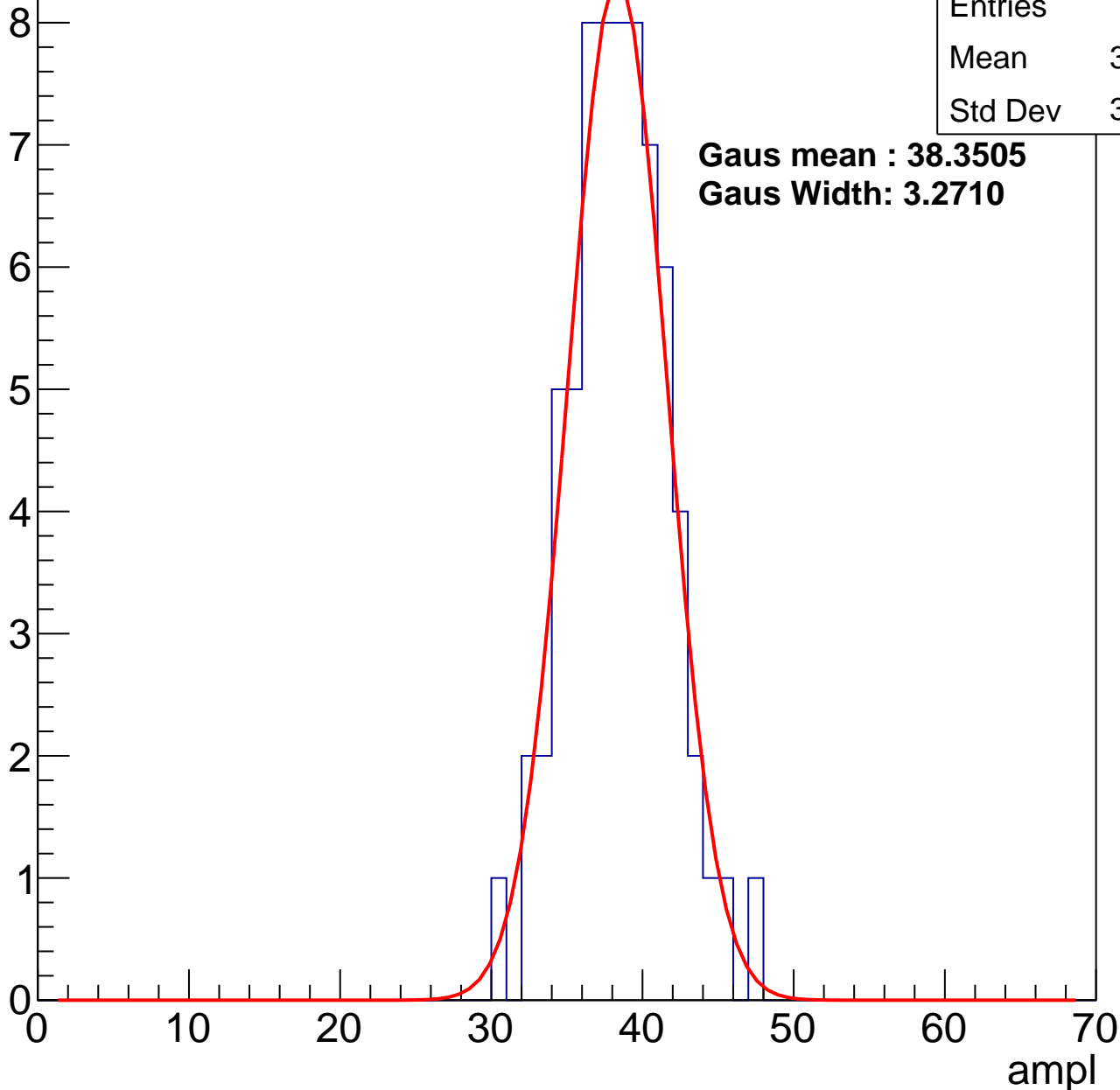
B0L002S, U7-ch22, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.99
Std Dev	3.233

Gaus mean : 38.3505
Gaus Width: 3.2710



B0L002S, U7-ch22, adc2

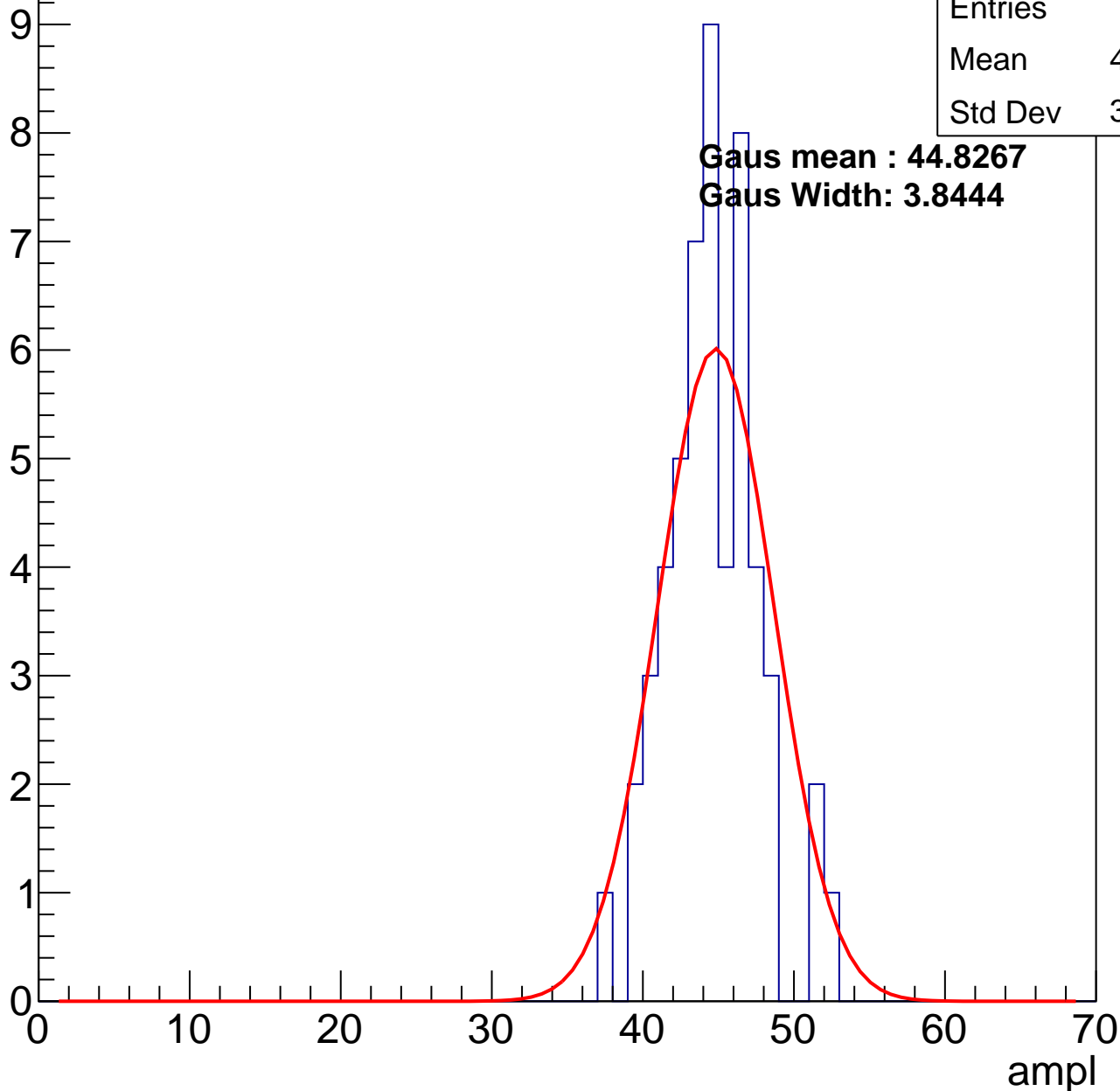
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.15
Std Dev	3.043

Gaus mean : 44.8267

Gaus Width: 3.8444

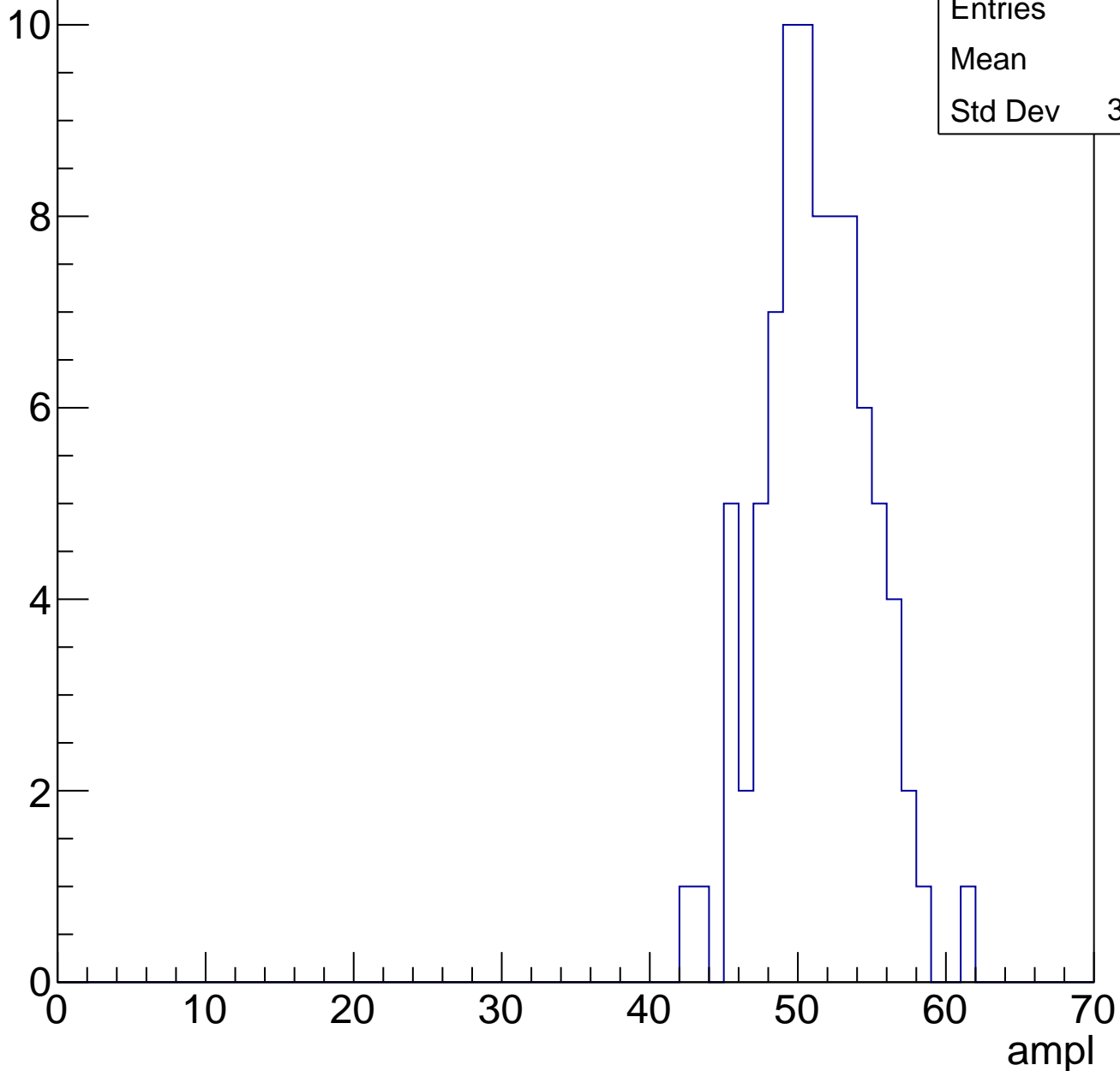


B0L002S, U7-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	50.8
Std Dev	3.552

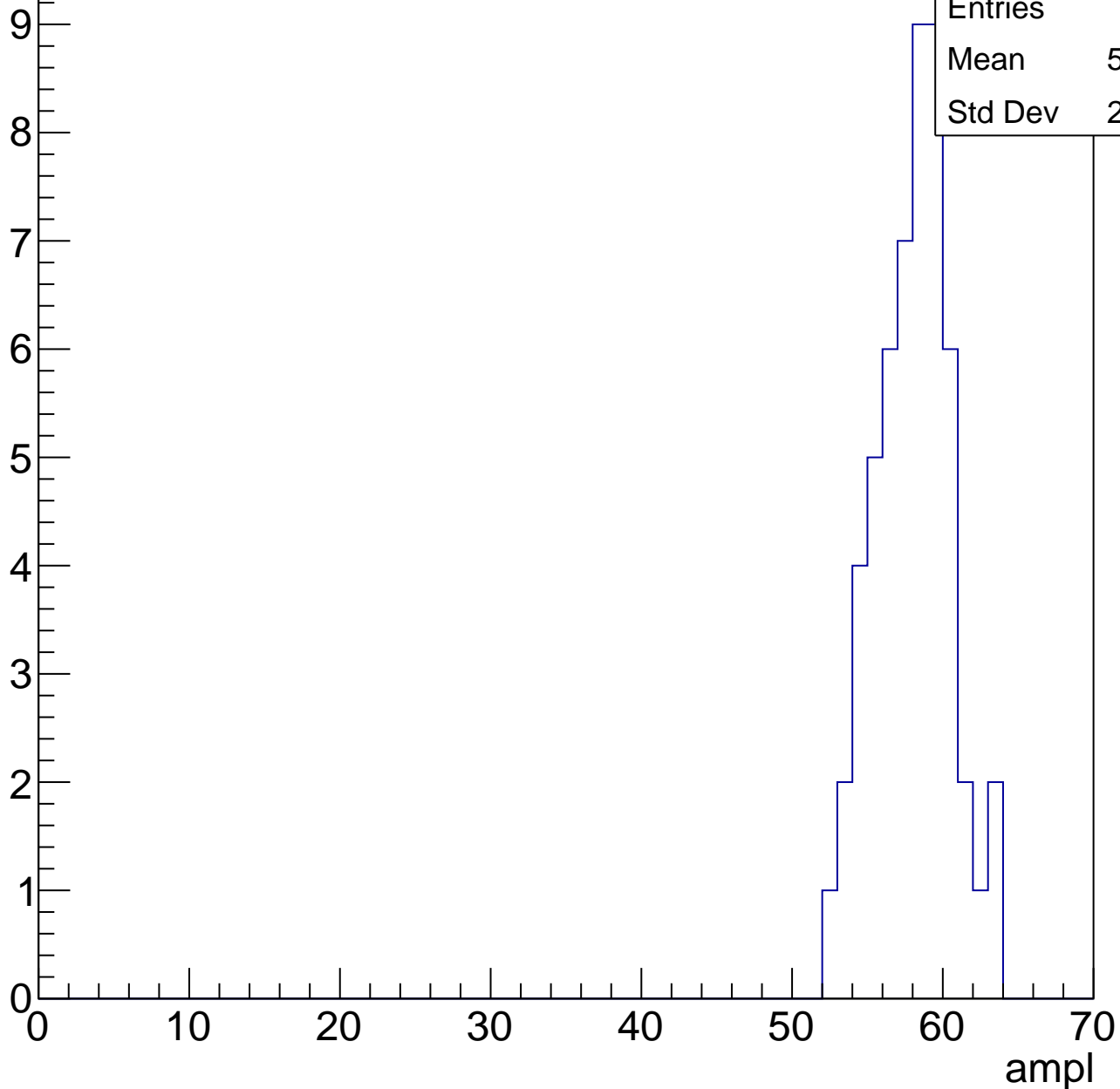
Entry



B0L002S, U7-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



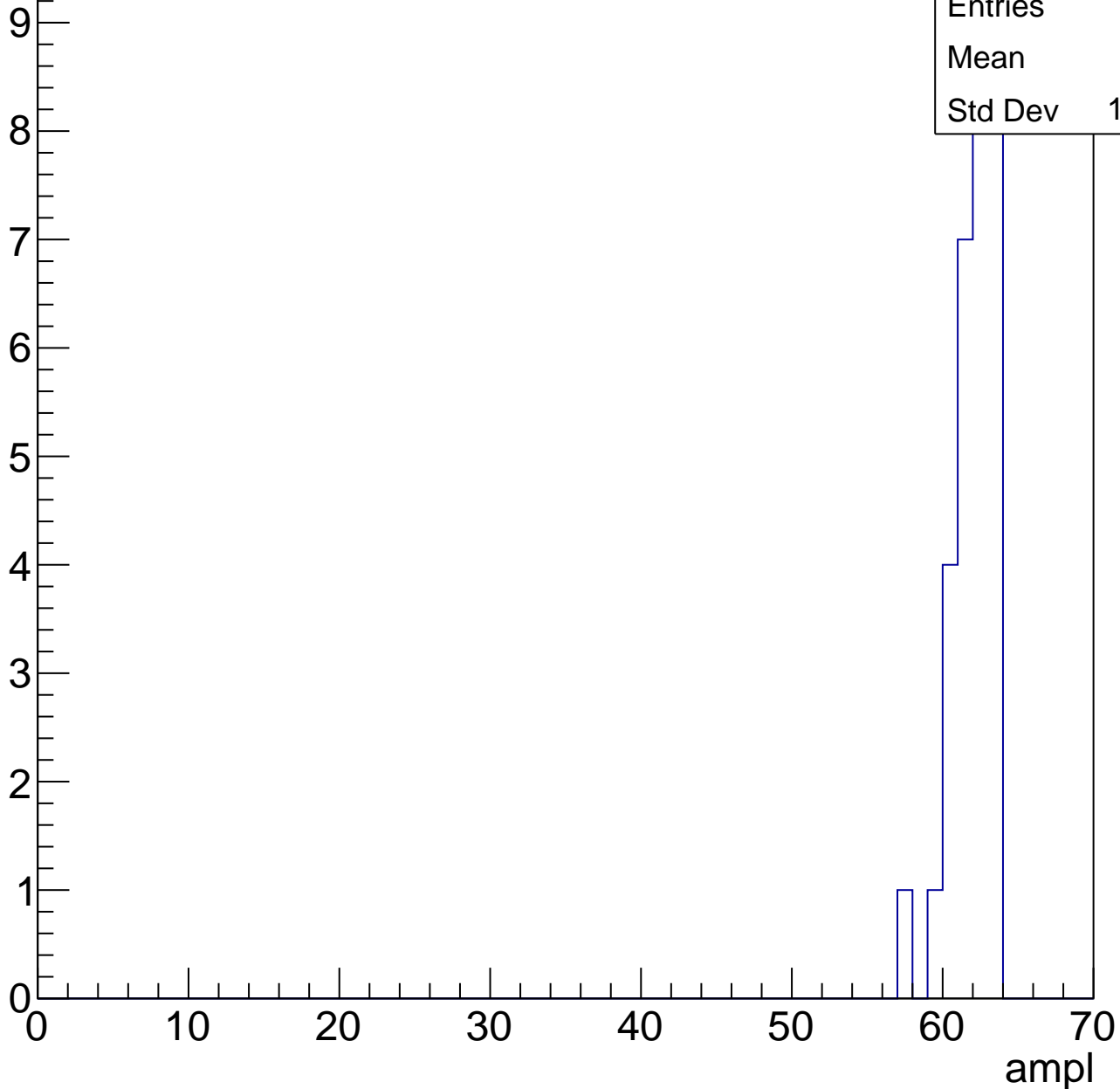
Entries	54
Mean	57.54
Std Dev	2.477

B0L002S, U7-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

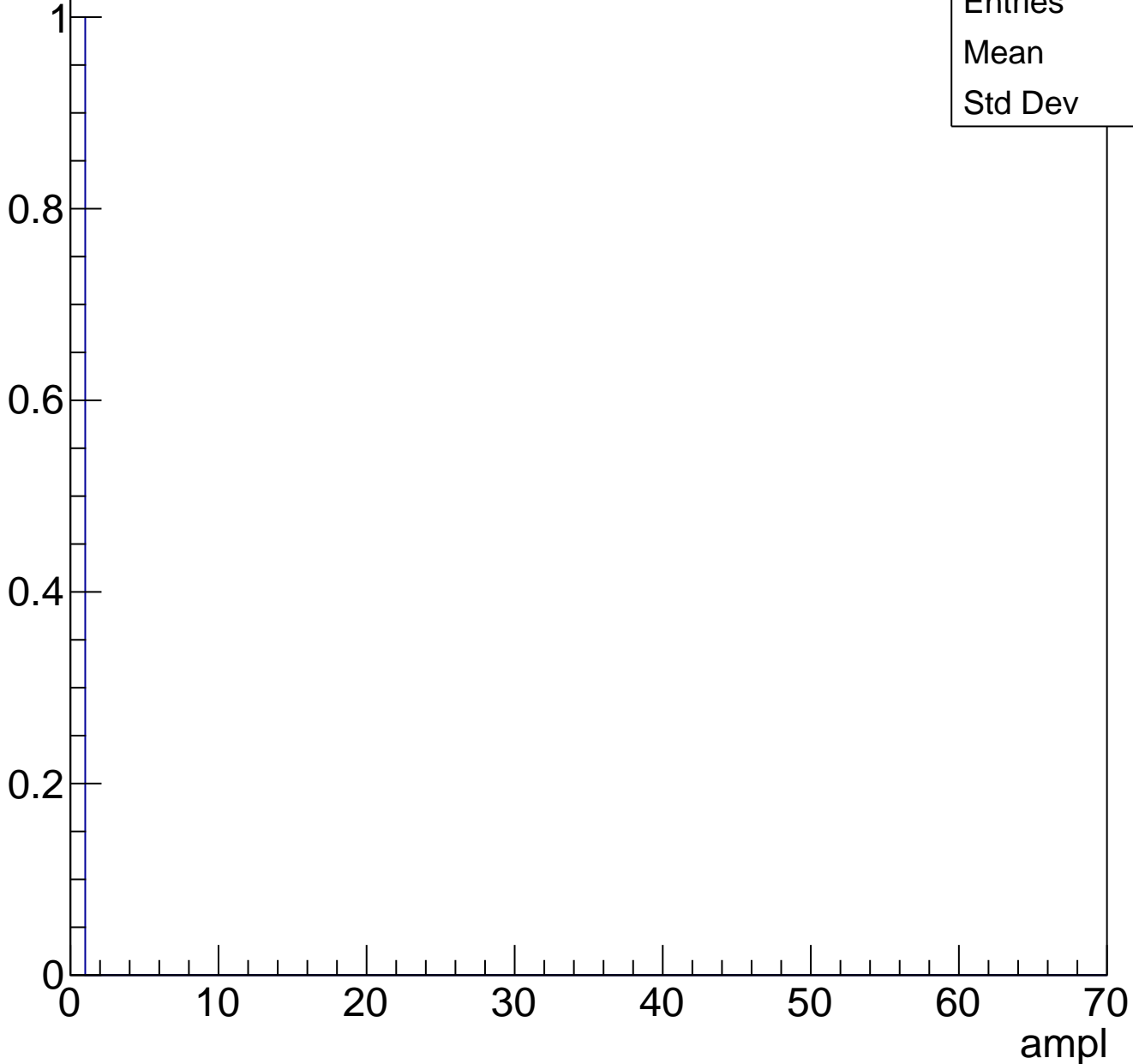
Entries	30
Mean	61.5
Std Dev	1.384



B0L002S, U7-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch23, adc0

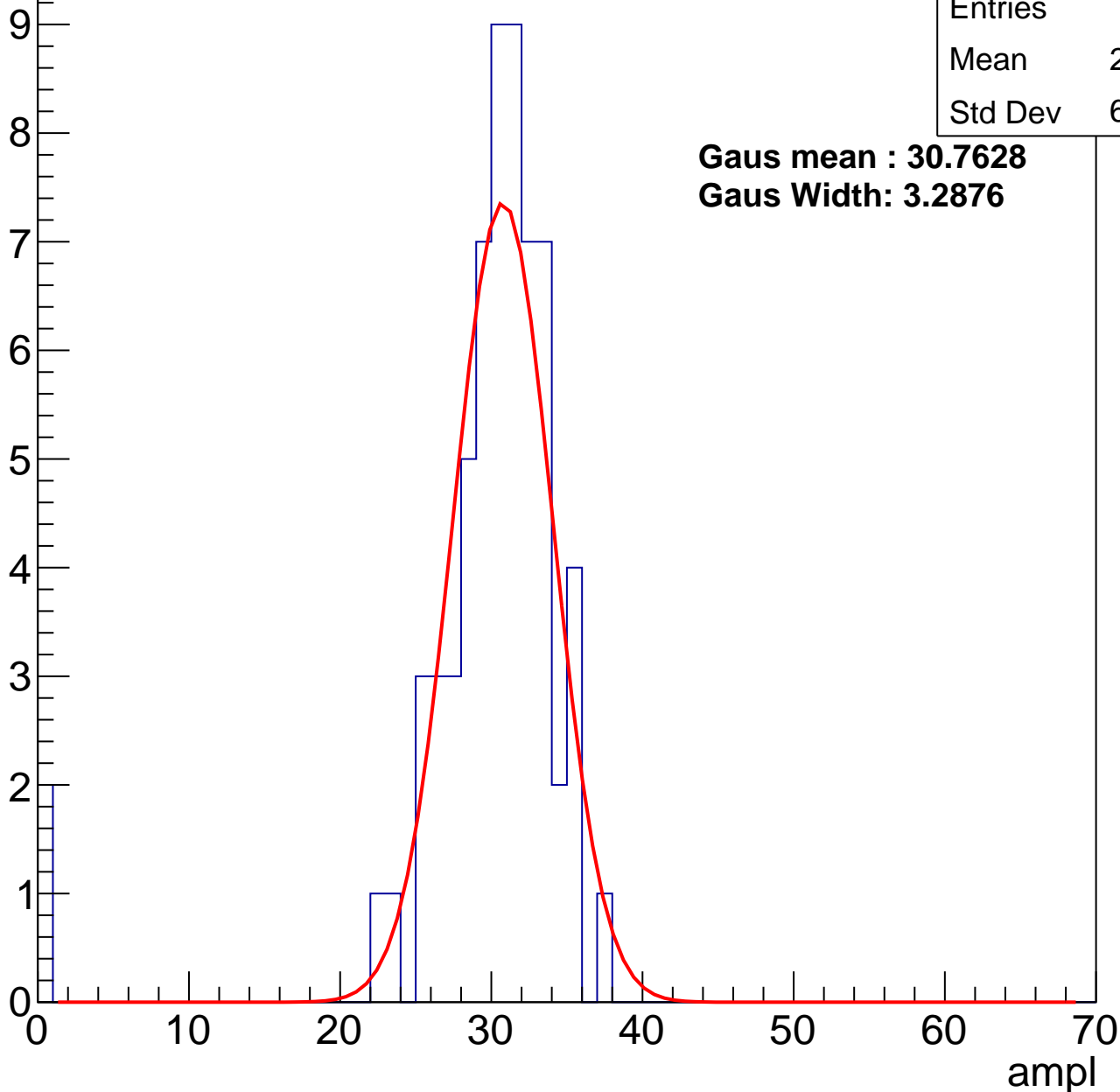
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.23
Std Dev	6.038

Gaus mean : 30.7628

Gaus Width: 3.2876



B0L002S, U7-ch23, adc1

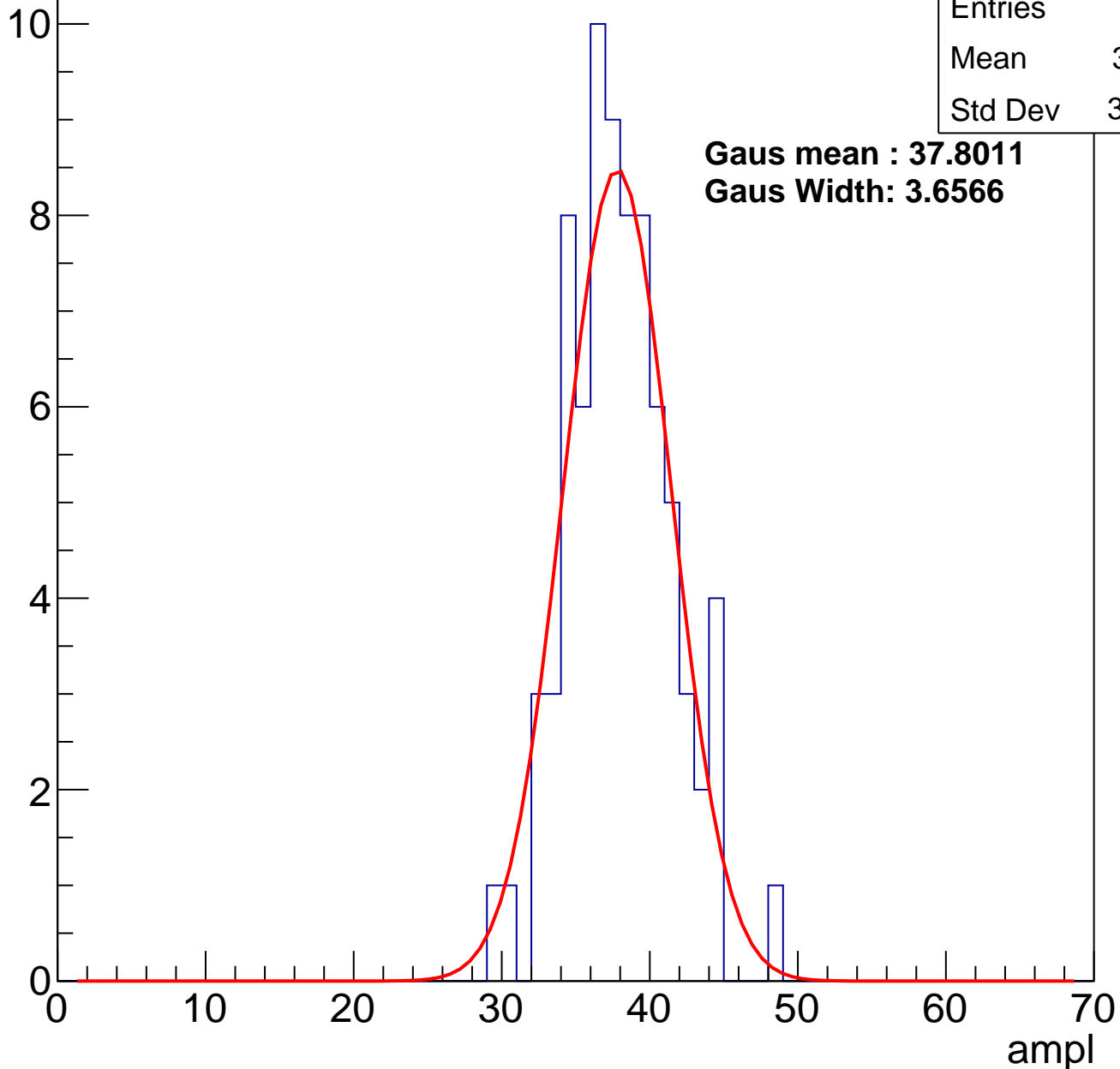
calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	37.51
Std Dev	3.507

Gaus mean : 37.8011

Gaus Width: 3.6566

Entry



B0L002S, U7-ch23, adc2

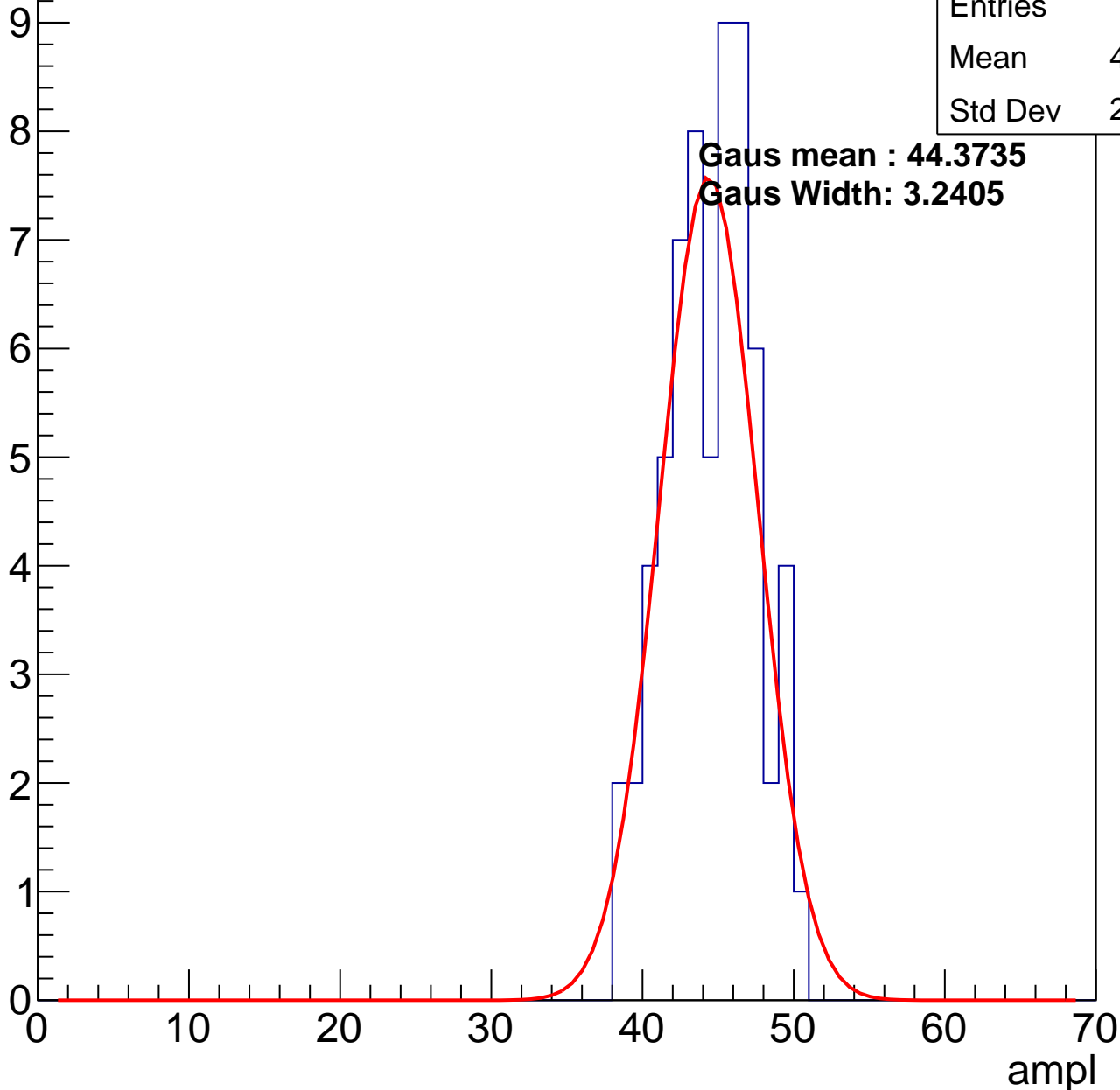
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.06
Std Dev	2.888

Gaus mean : 44.3735

Gaus Width: 3.2405

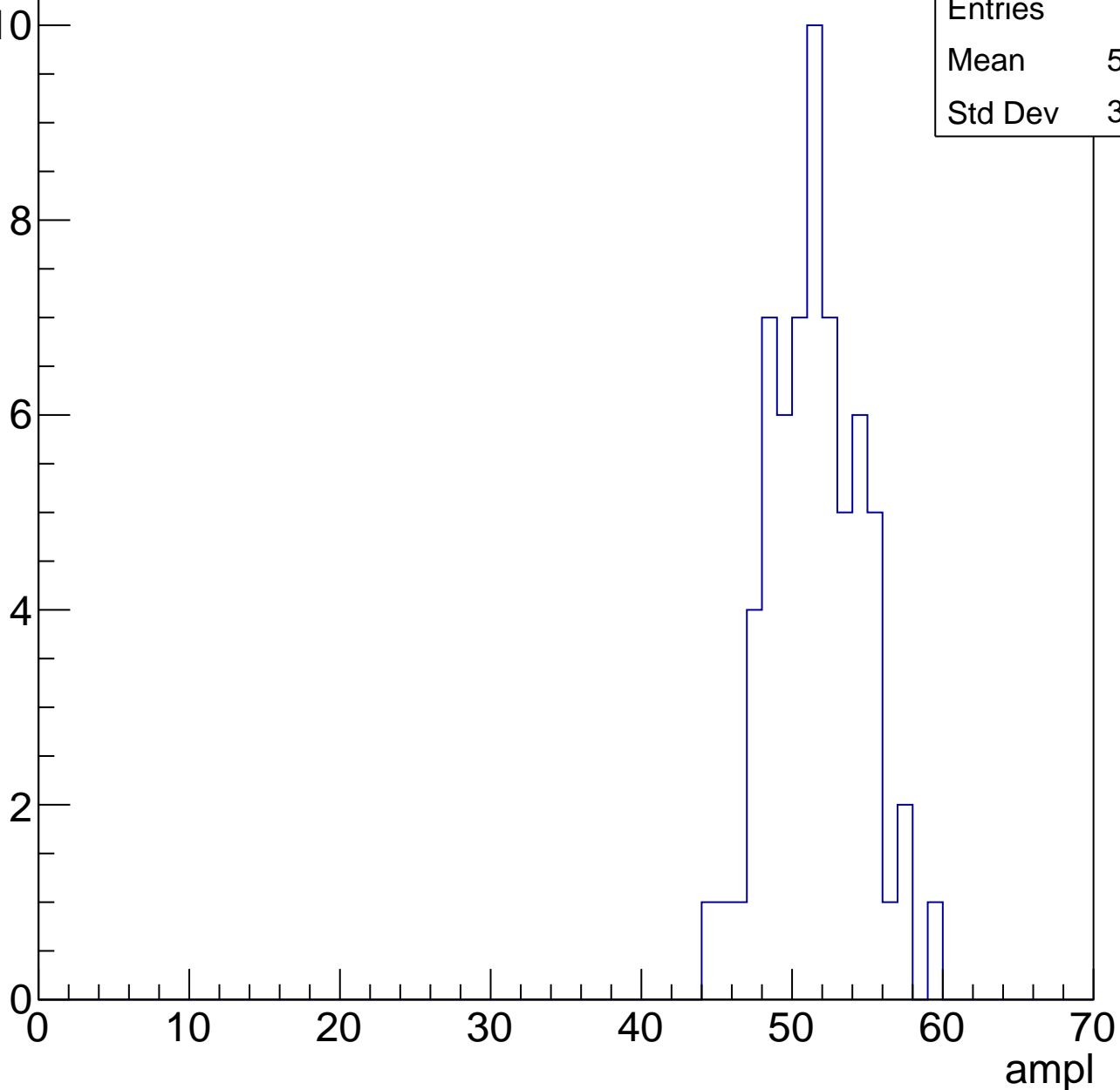


B0L002S, U7-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.09
Std Dev	3.035

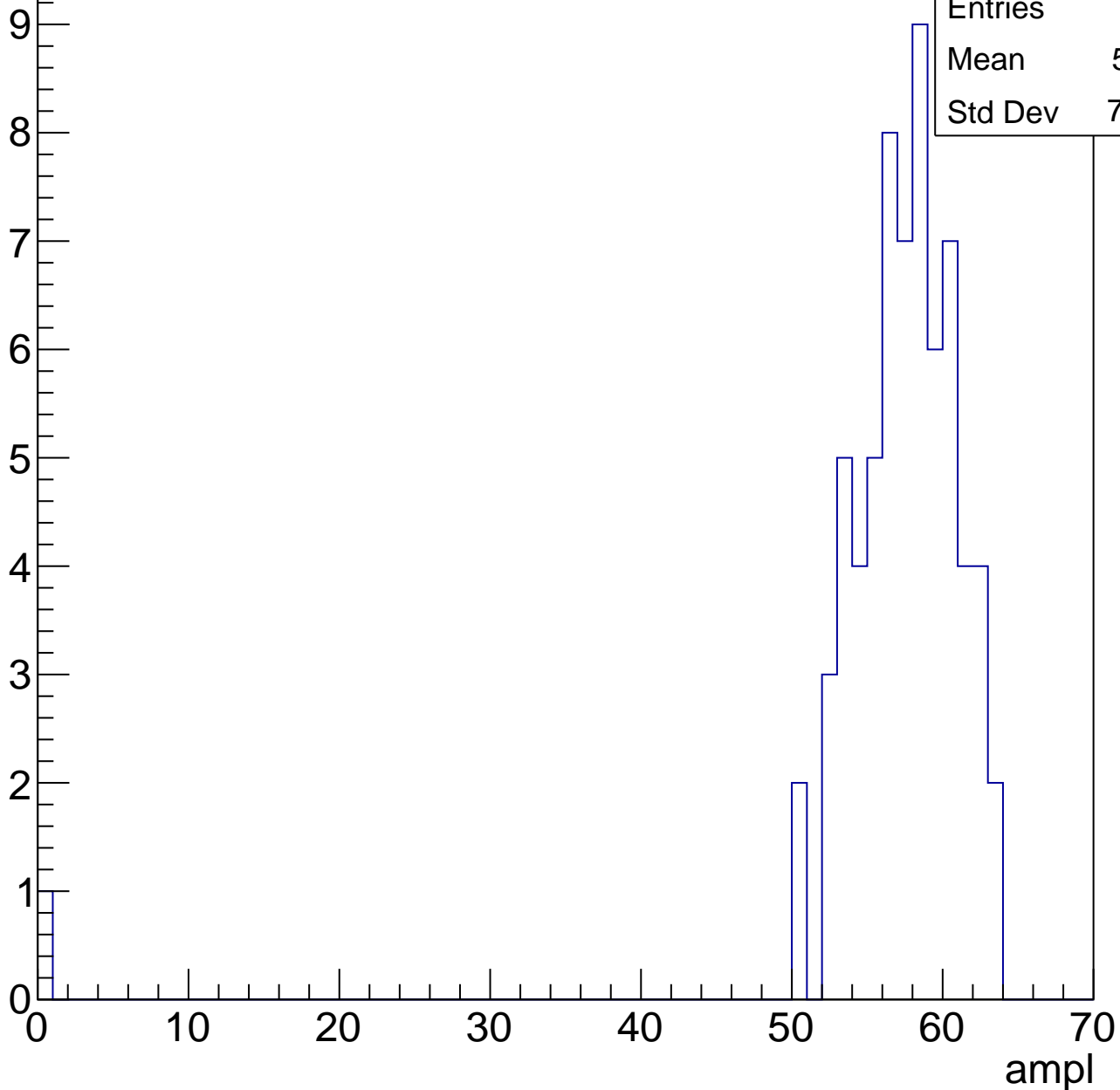


B0L002S, U7-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.31
Std Dev	7.597



B0L002S, U7-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	25
Mean	60.8
Std Dev	1.918

0

10

20

30

40

50

60

70

ampl

B0L002S, U7-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.5
Std Dev	0.5

B0L002S, U7-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch24, adc0

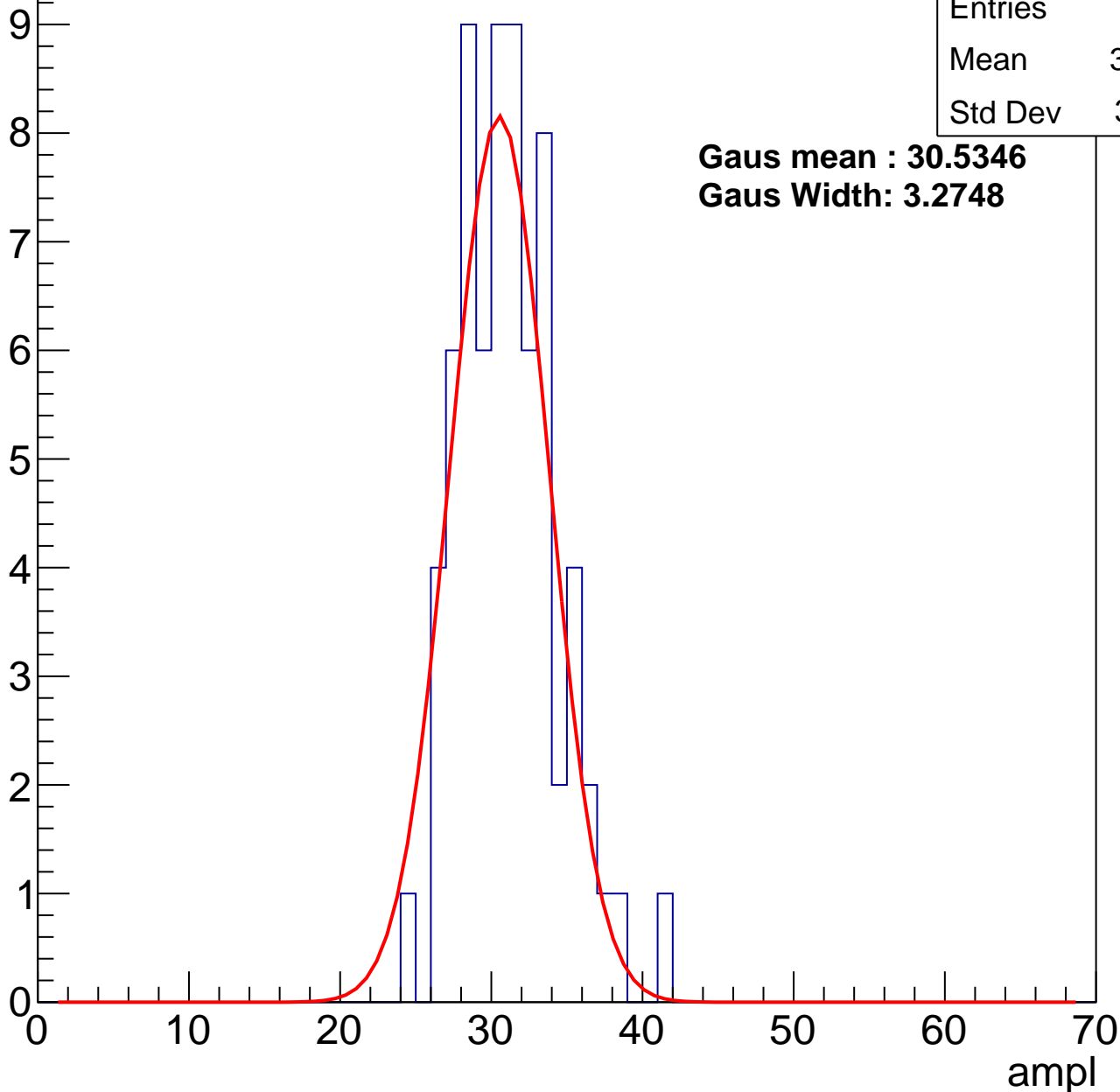
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.68
Std Dev	3.201

Gaus mean : 30.5346

Gaus Width: 3.2748



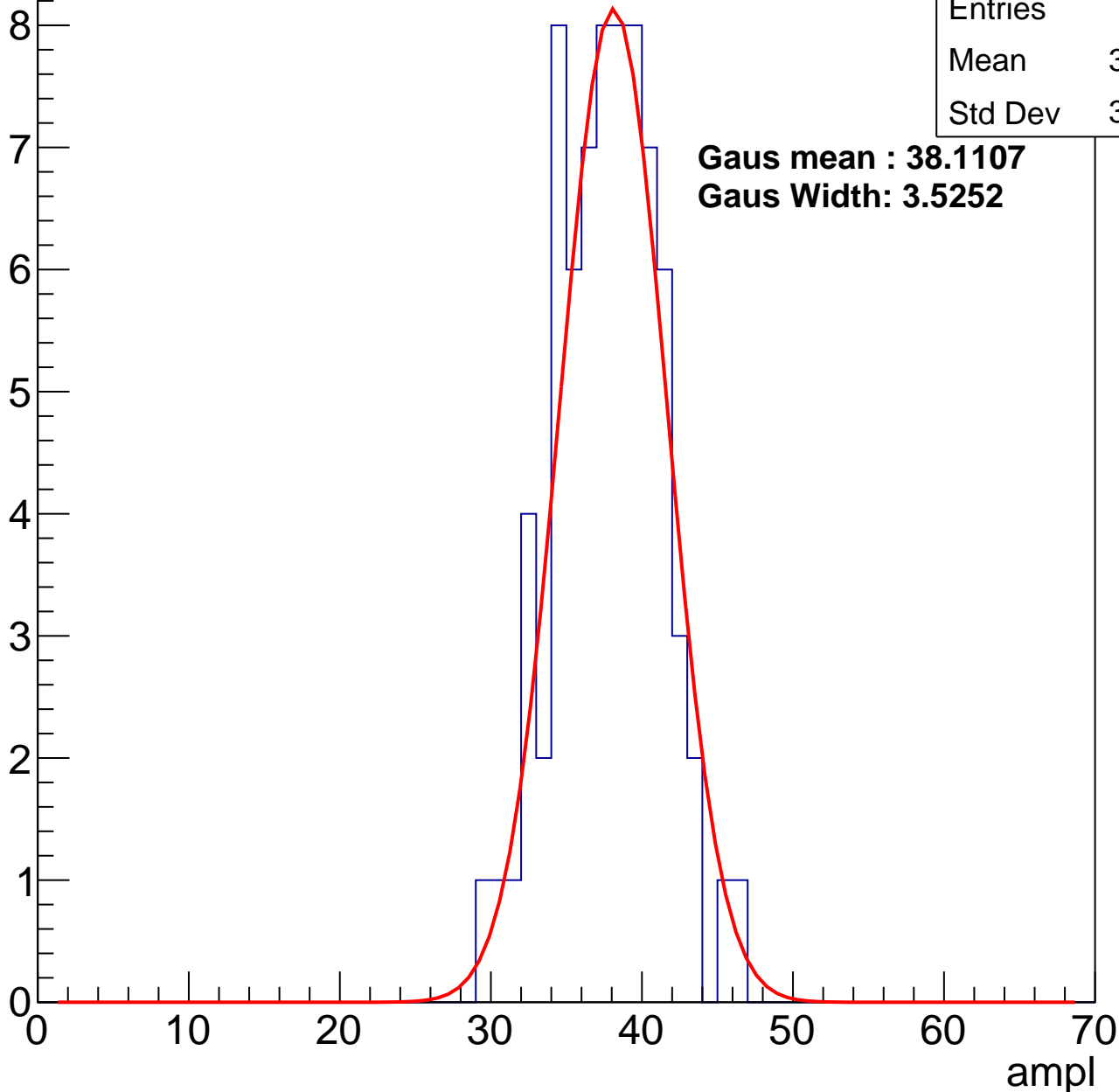
B0L002S, U7-ch24, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.28
Std Dev	3.427

Gaus mean : 38.1107
Gaus Width: 3.5252



B0L002S, U7-ch24, adc2

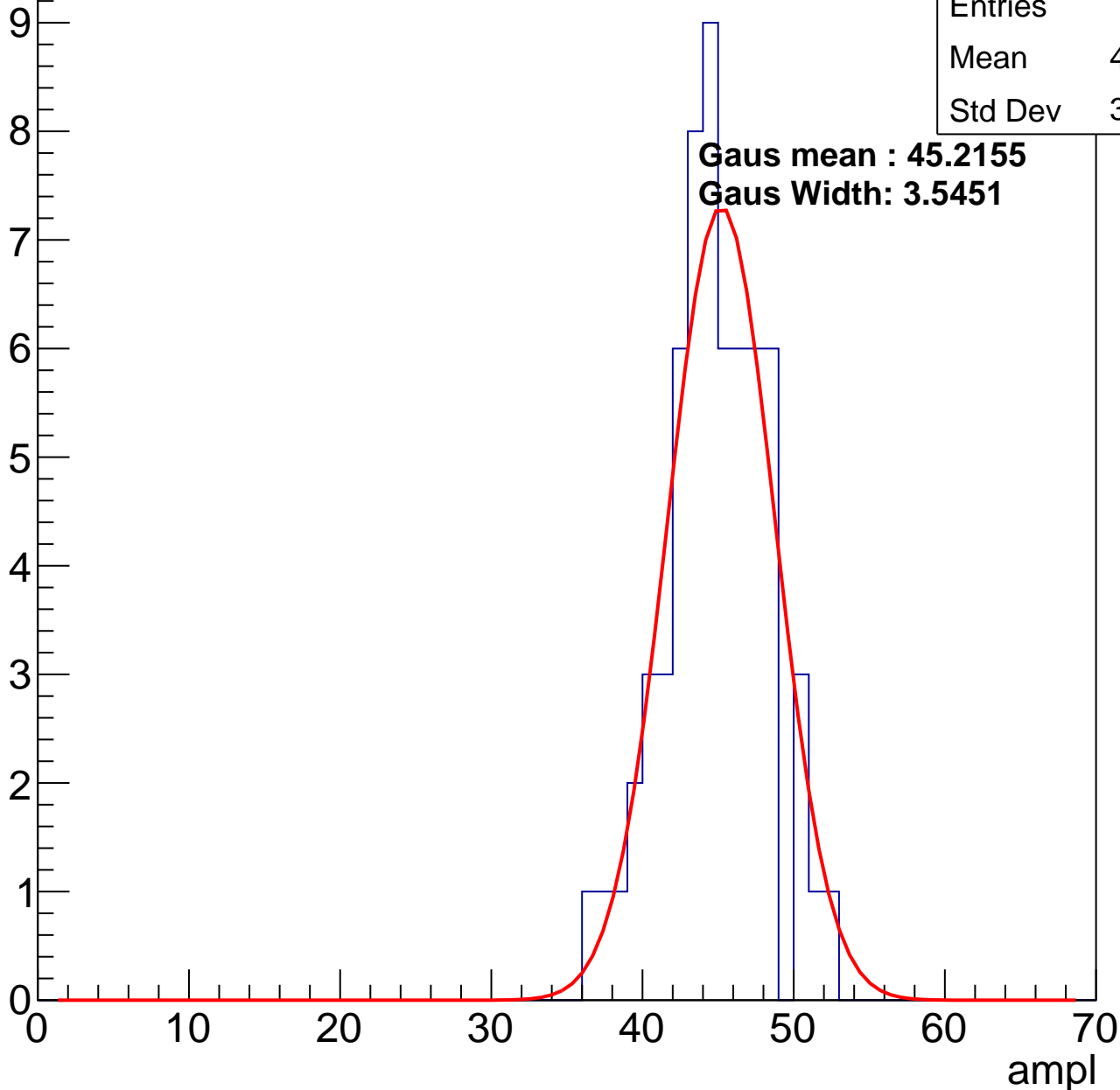
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.33
Std Dev	3.333

Gaus mean : 45.2155

Gaus Width: 3.5451

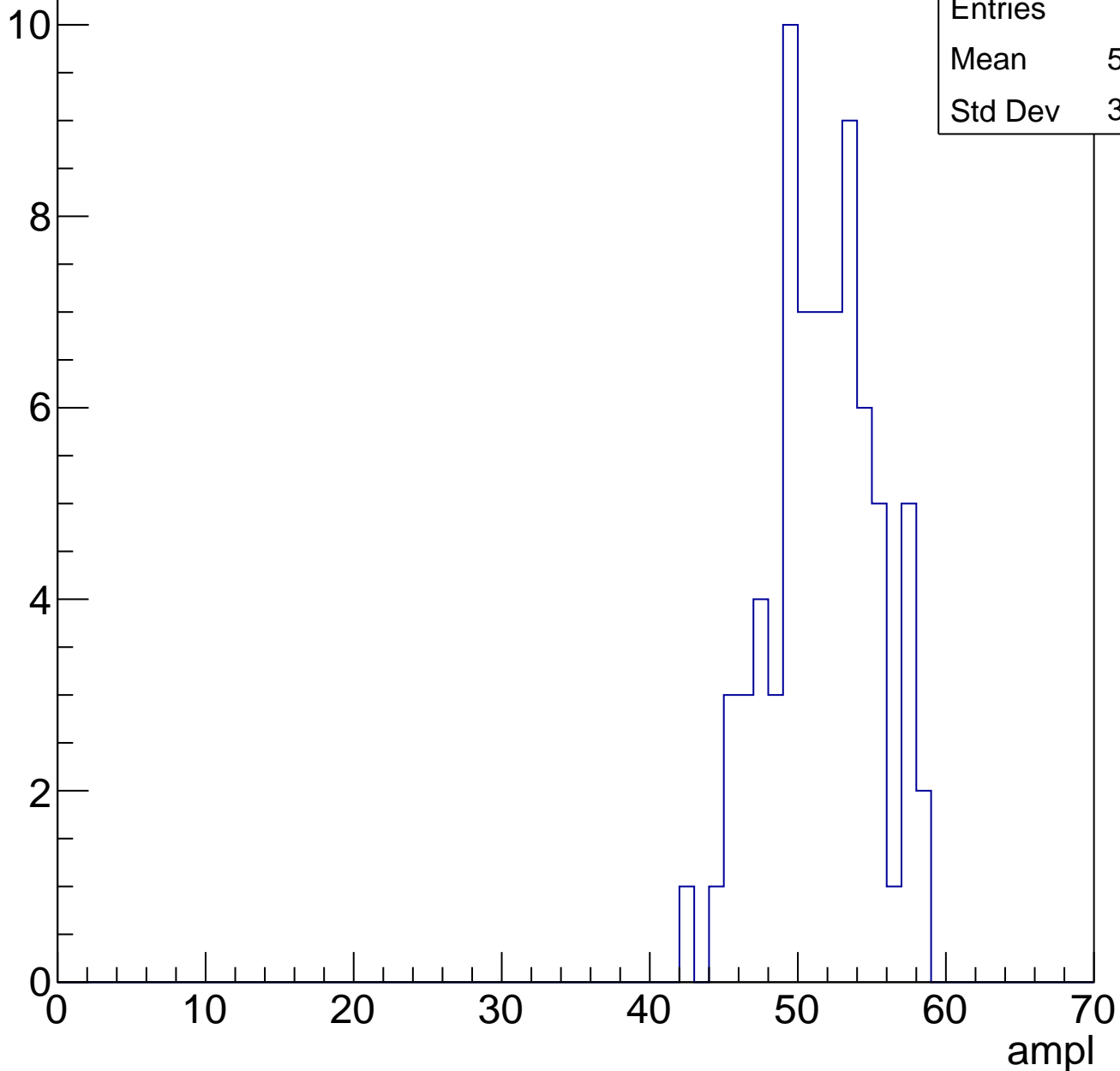


B0L002S, U7-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	51.15
Std Dev	3.544

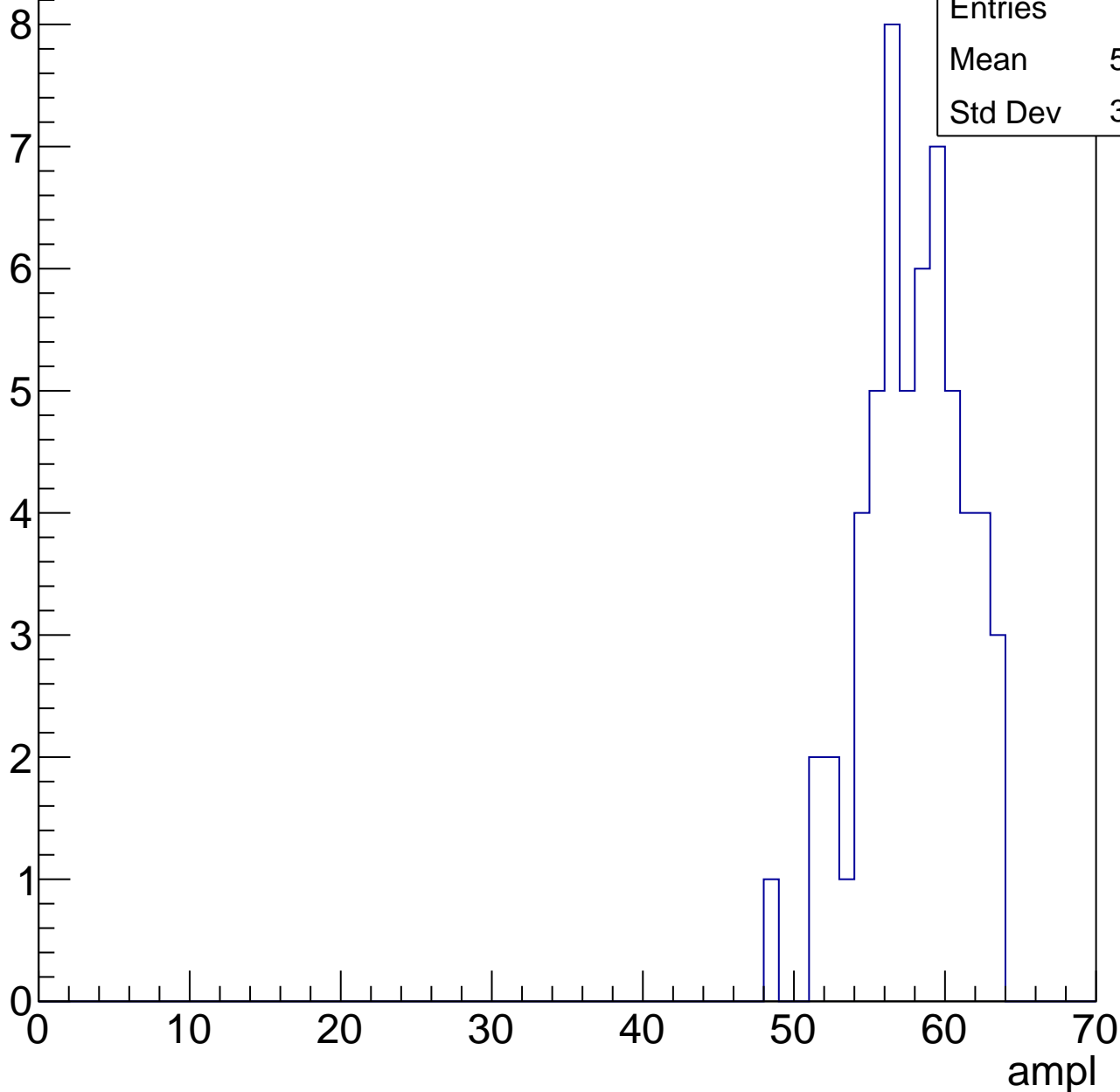
Entry



B0L002S, U7-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

6

5

4

3

2

1

0

Entries	30
Mean	58.83
Std Dev	11.06

10

20

30

40

50

60

70

ampl

B0L002S, U7-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch25, adc0

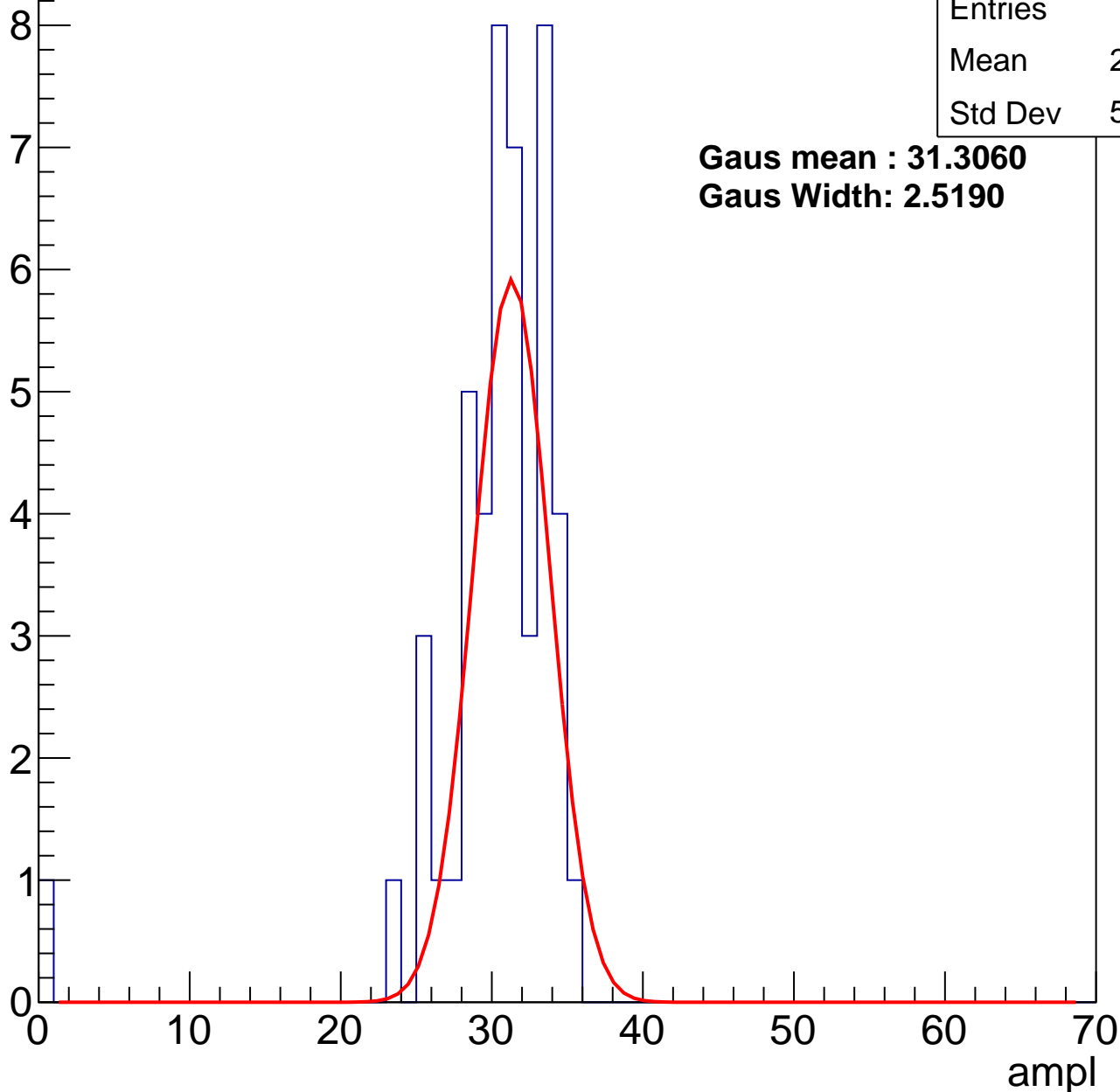
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	29.68
Std Dev	5.153

Gaus mean : 31.3060

Gaus Width: 2.5190



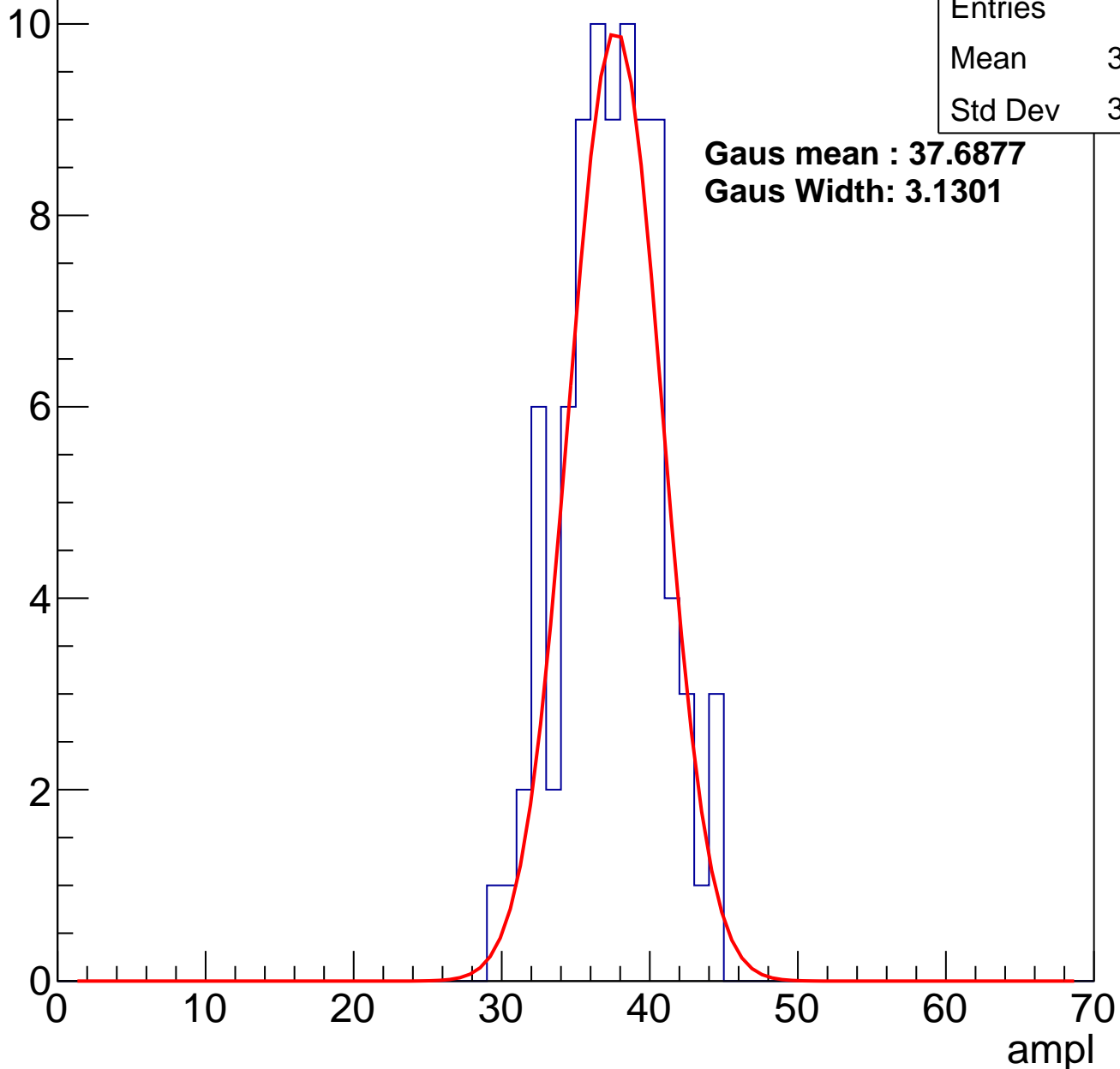
B0L002S, U7-ch25, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	37.02
Std Dev	3.283

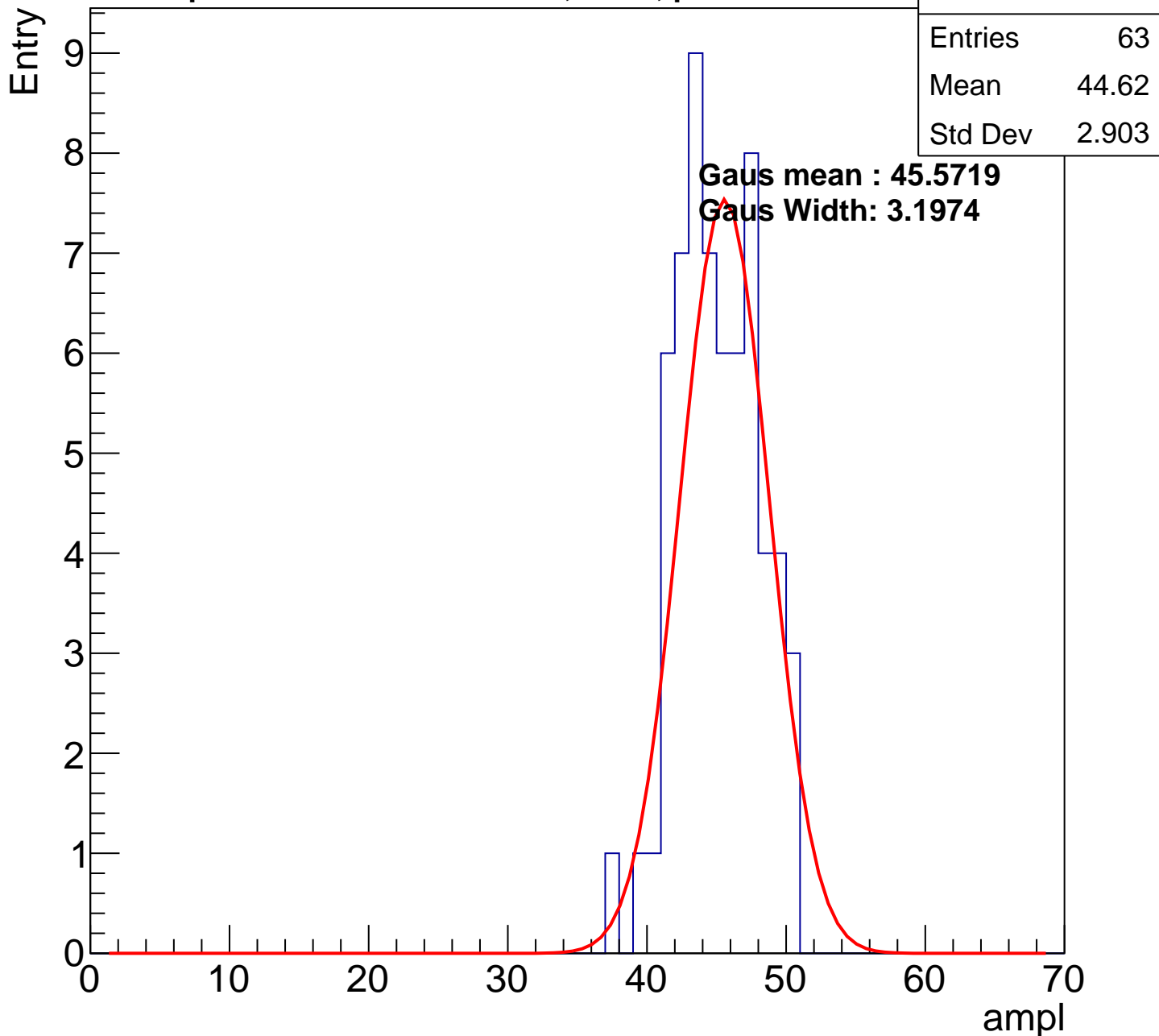
Gaus mean : 37.6877
Gaus Width: 3.1301

Entry



B0L002S, U7-ch25, adc2

calib_packv5_042523_0143.root, FC#8, port C1

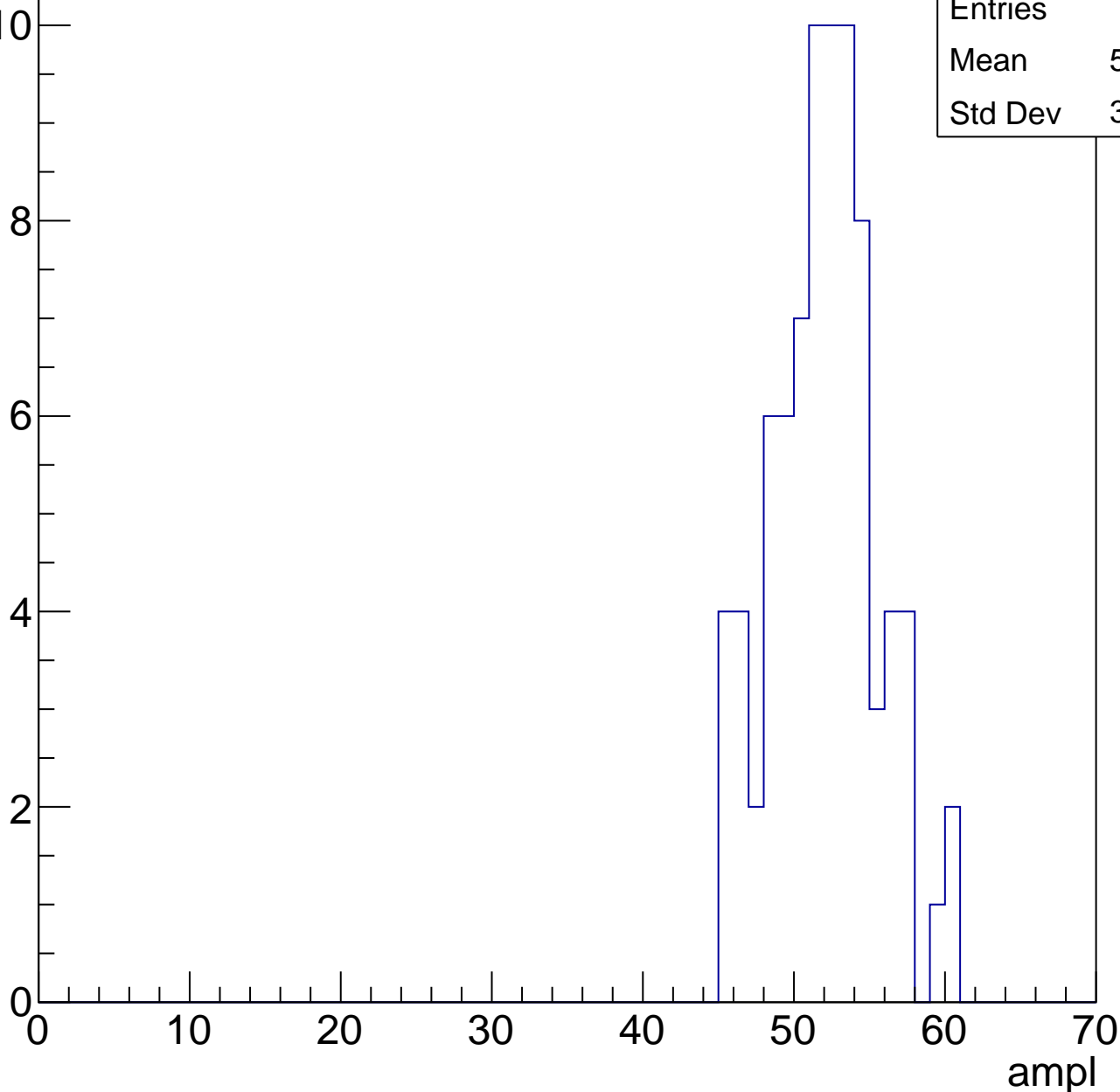


B0L002S, U7-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	51.58
Std Dev	3.467

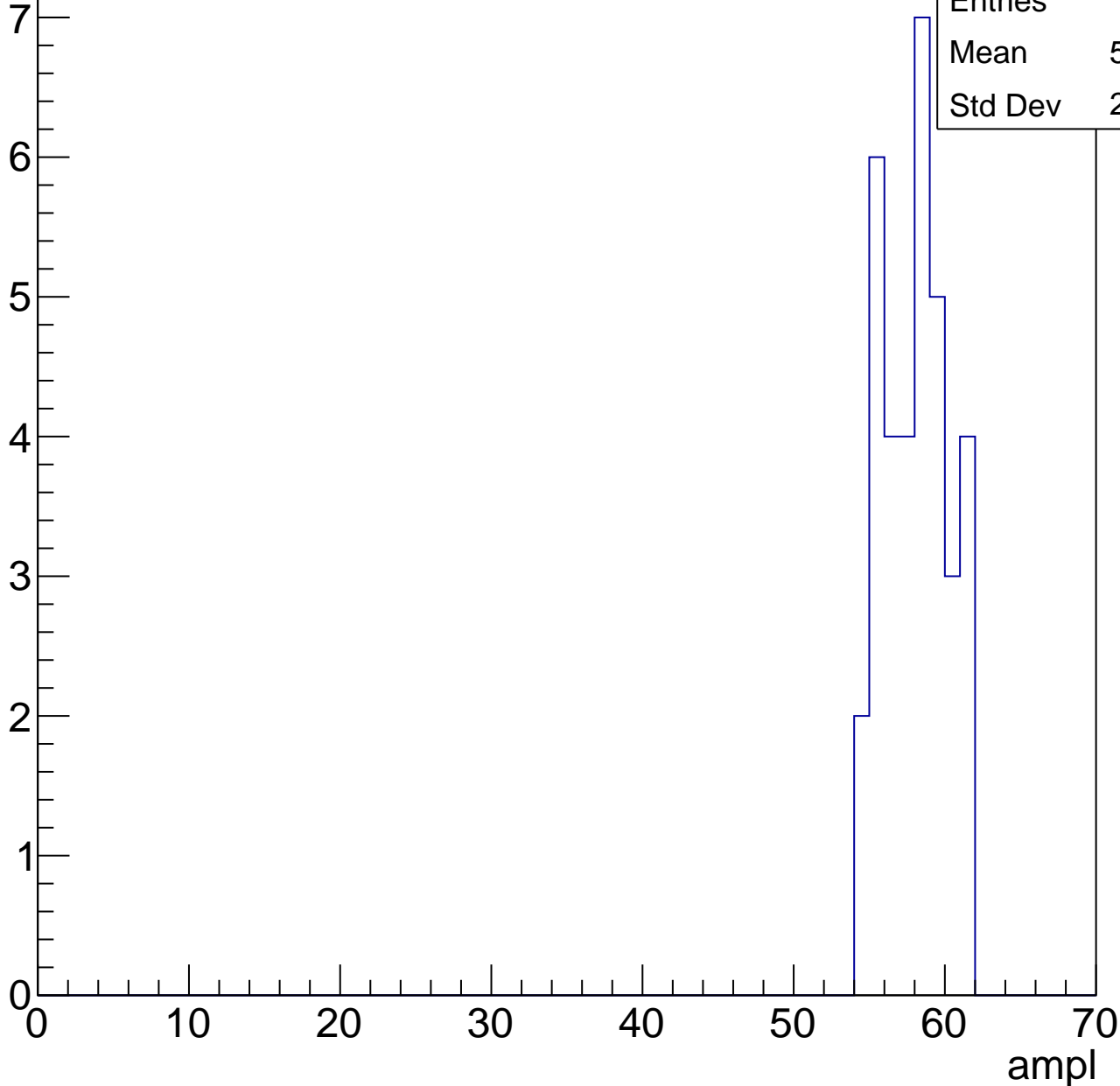


B0L002S, U7-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	57.57
Std Dev	2.088



B0L002S, U7-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 41

Mean 59.02

Std Dev 9.588

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B0L002S, U7-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch26, adc0

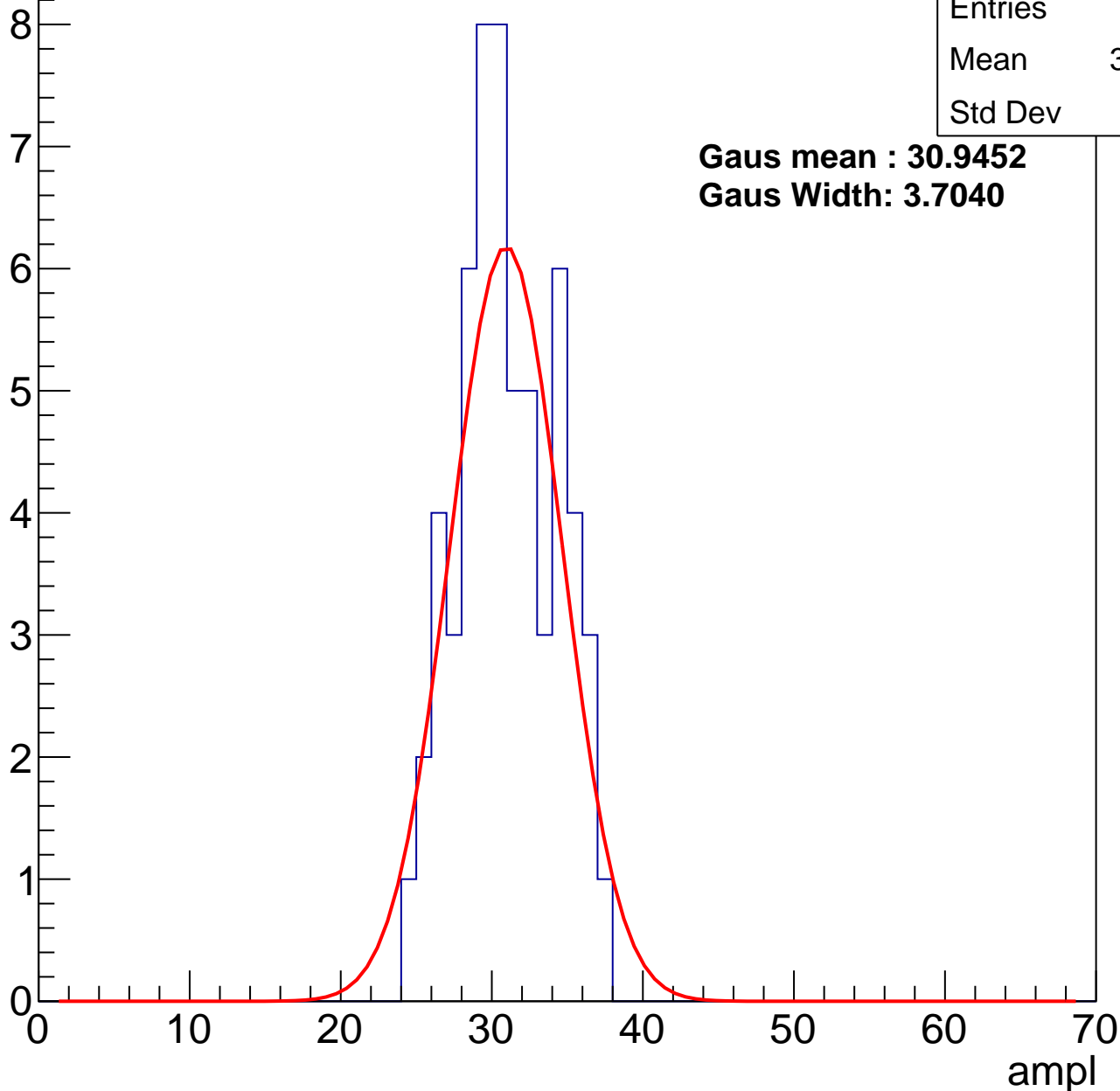
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.54
Std Dev	3.18

Gaus mean : 30.9452

Gaus Width: 3.7040



B0L002S, U7-ch26, adc1

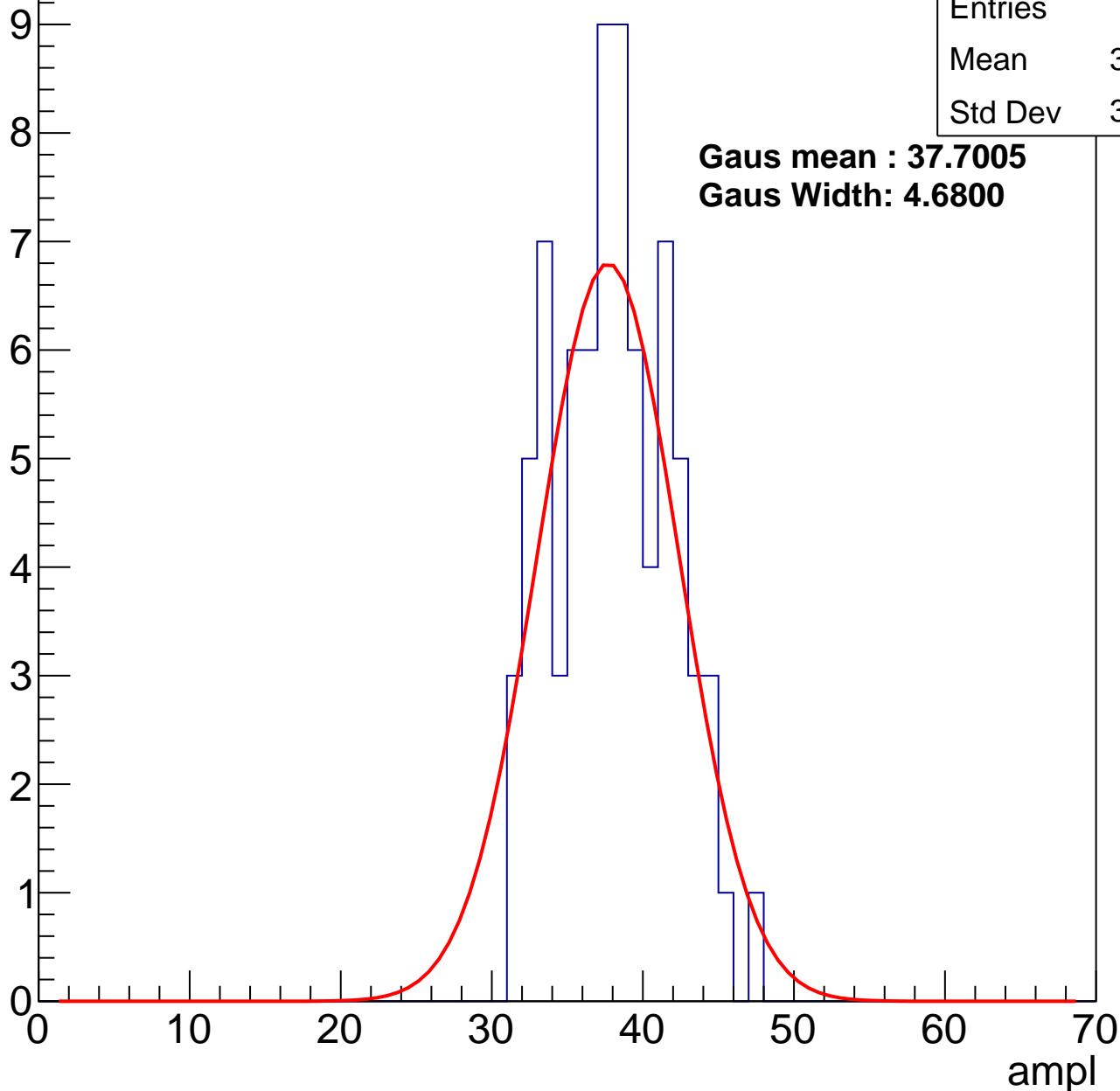
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	37.58
Std Dev	3.757

Gaus mean : 37.7005

Gaus Width: 4.6800



B0L002S, U7-ch26, adc2

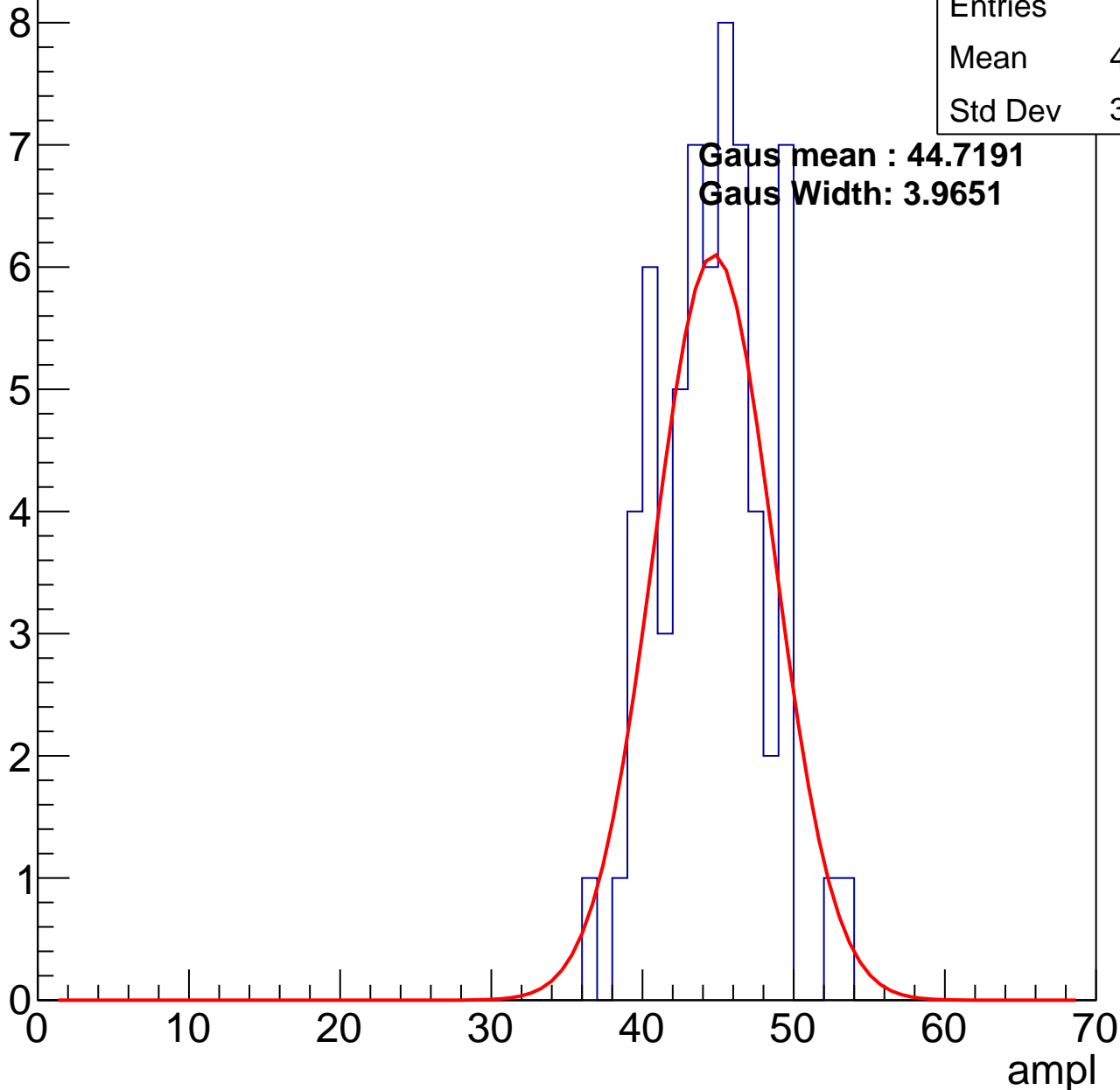
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.16
Std Dev	3.502

Gaus mean : 44.7191

Gaus Width: 3.9651



B0L002S, U7-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

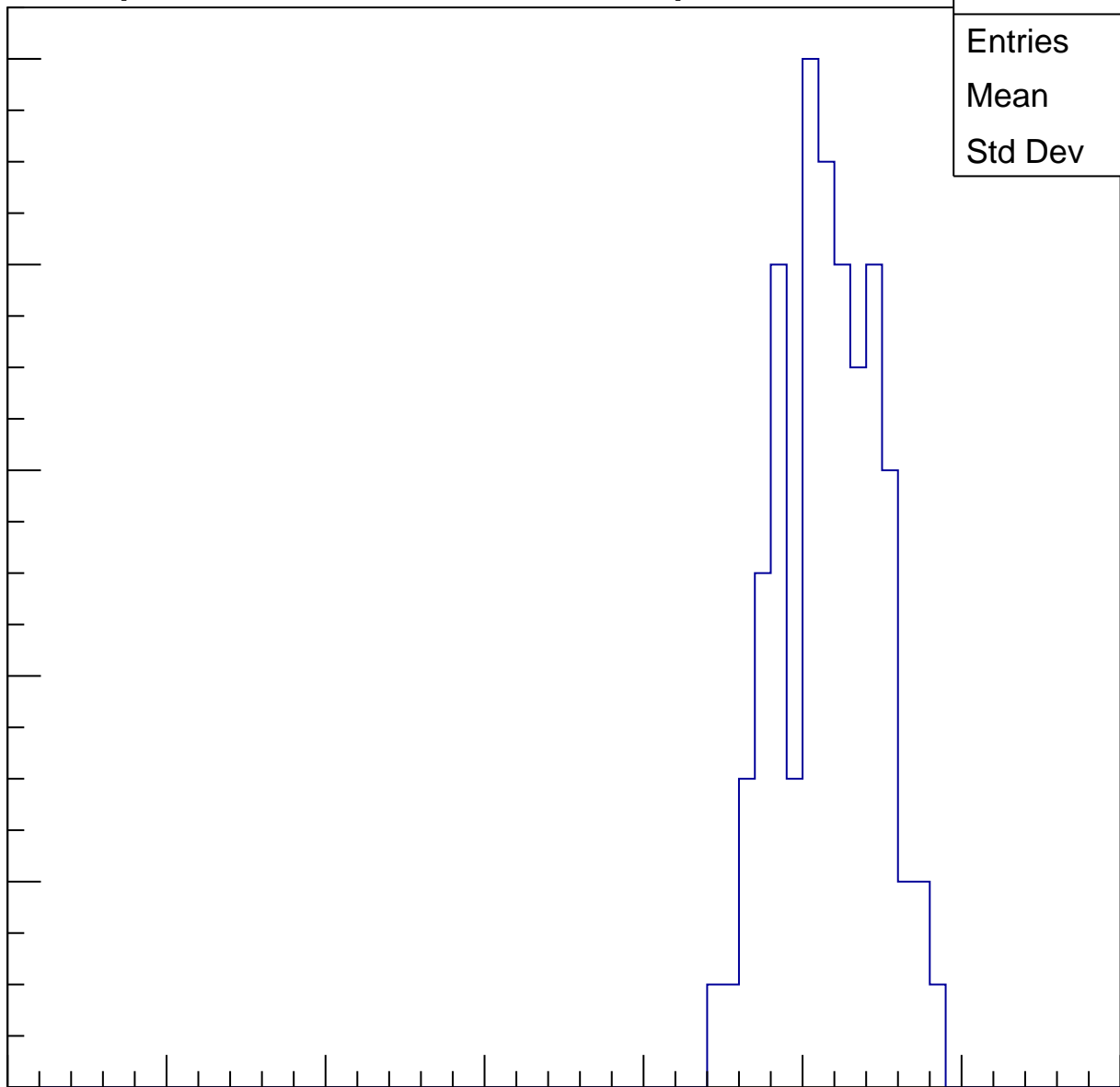
Entries	74
Mean	51.15
Std Dev	3.092

Entry

10
8
6
4
2
0

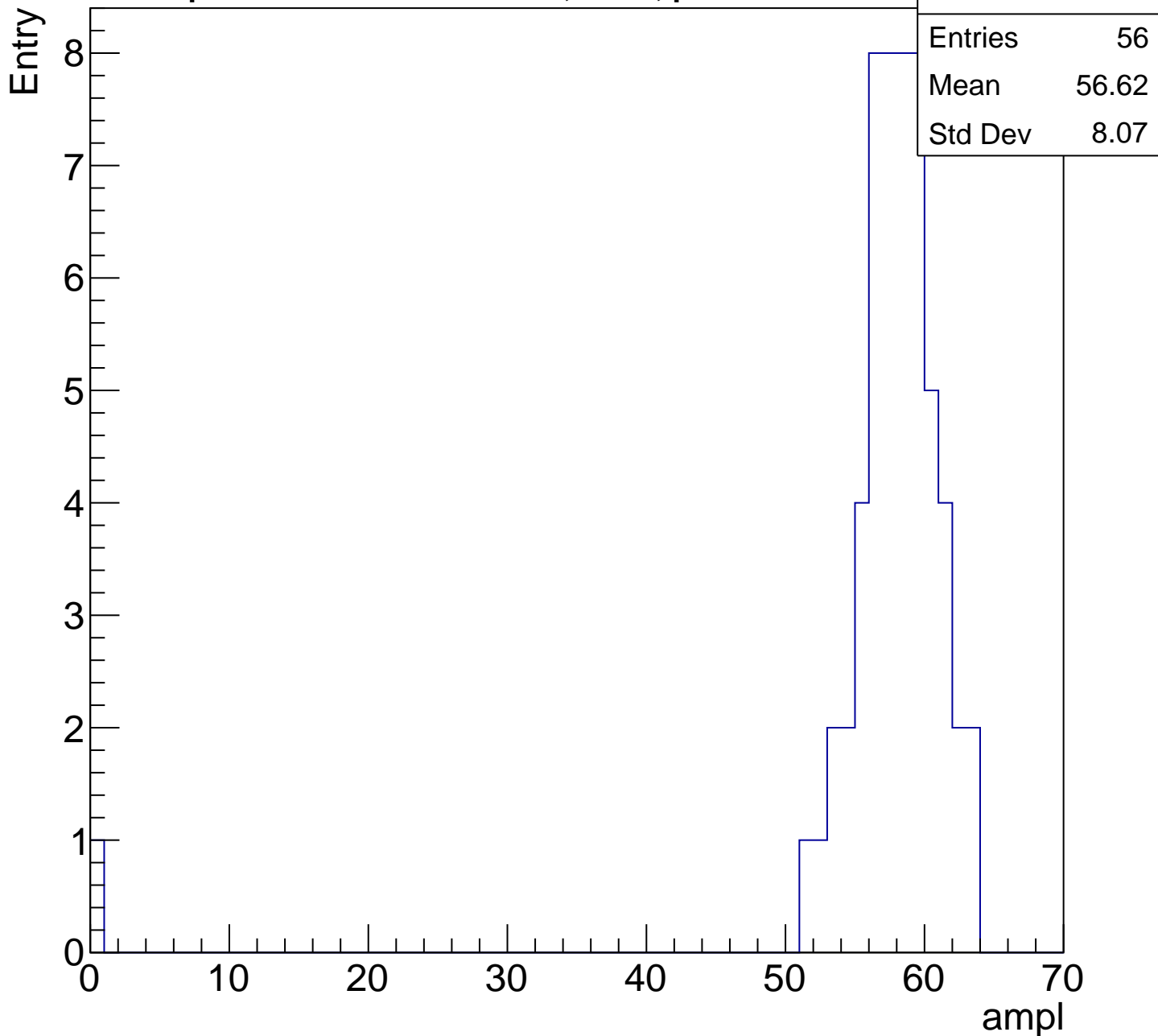
0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

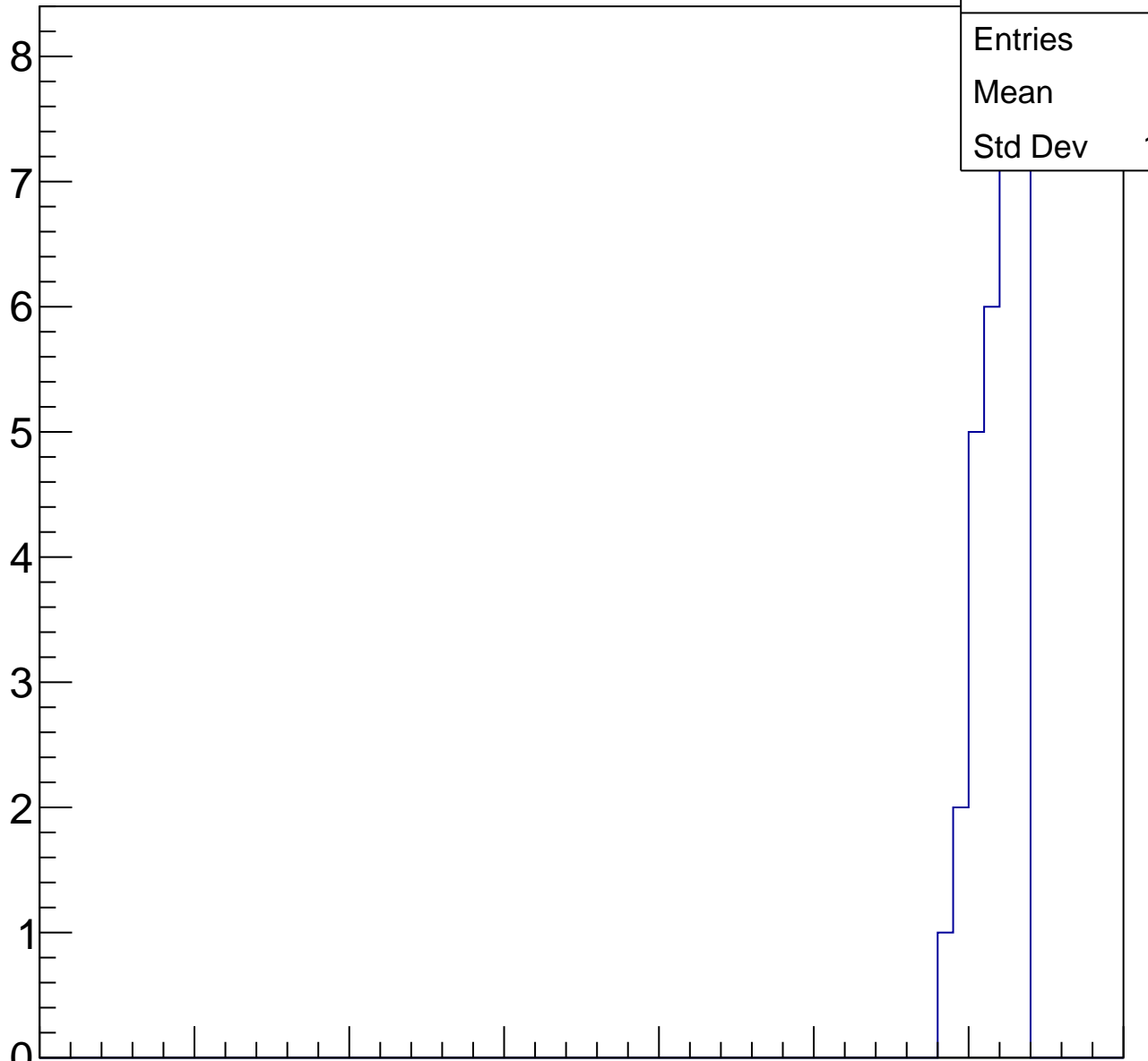
Entry

8
7
6
5
4
3
2
1
0

Entries	30
Mean	61.4
Std Dev	1.381

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch27, adc0

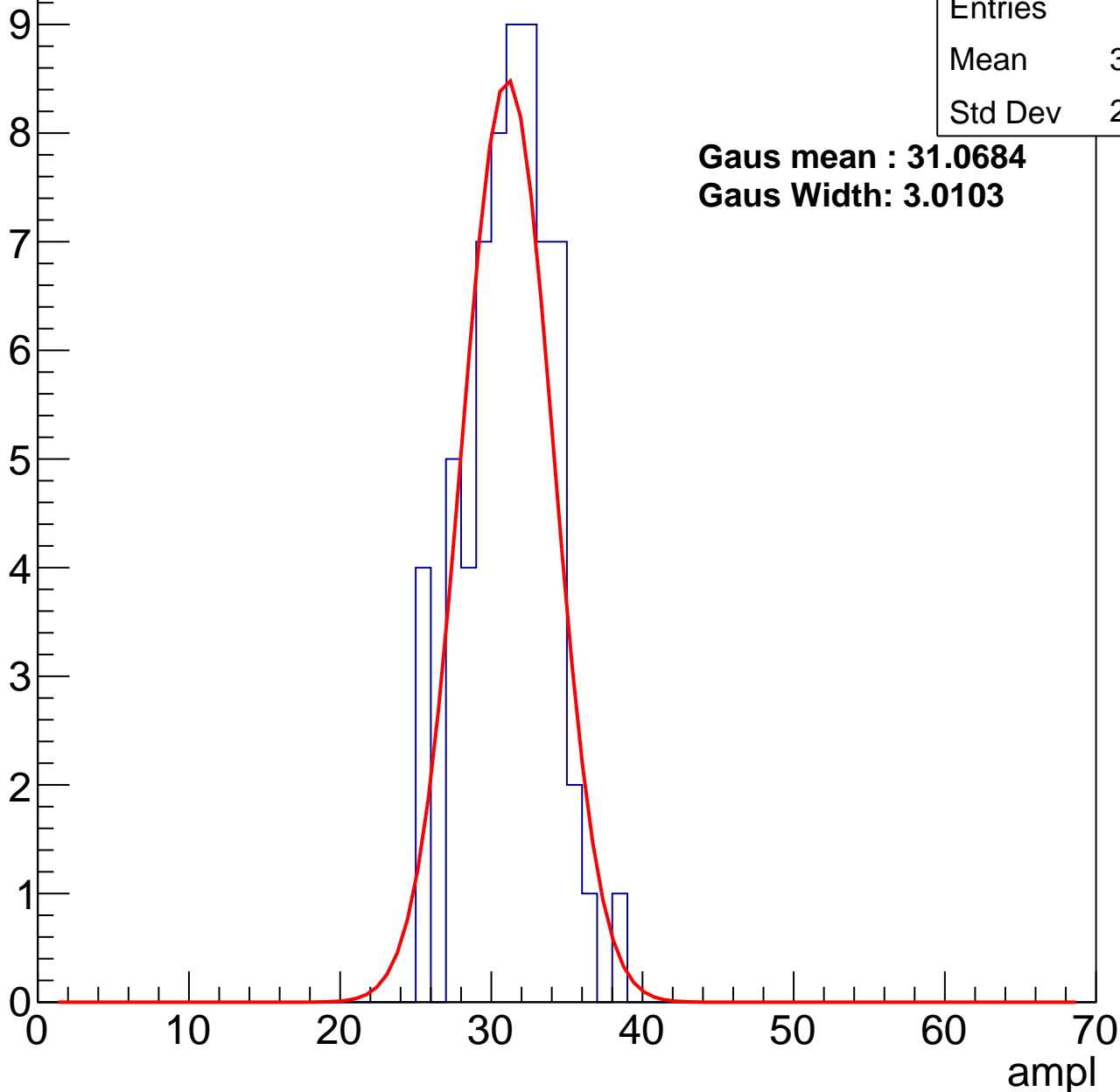
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.78
Std Dev	2.792

Gaus mean : 31.0684

Gaus Width: 3.0103



B0L002S, U7-ch27, adc1

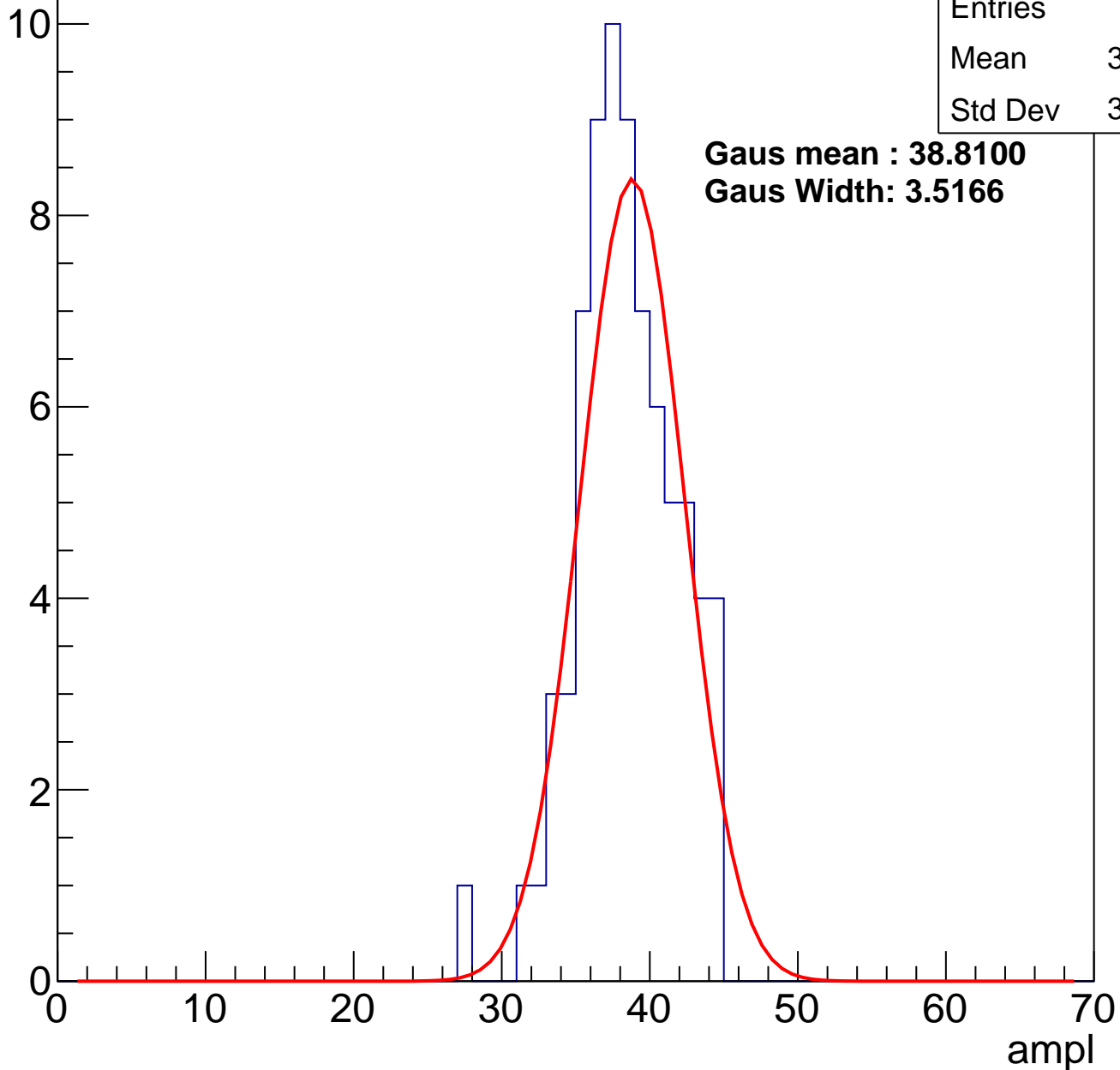
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.97
Std Dev	3.343

Gaus mean : 38.8100

Gaus Width: 3.5166

Entry



B0L002S, U7-ch27, adc2

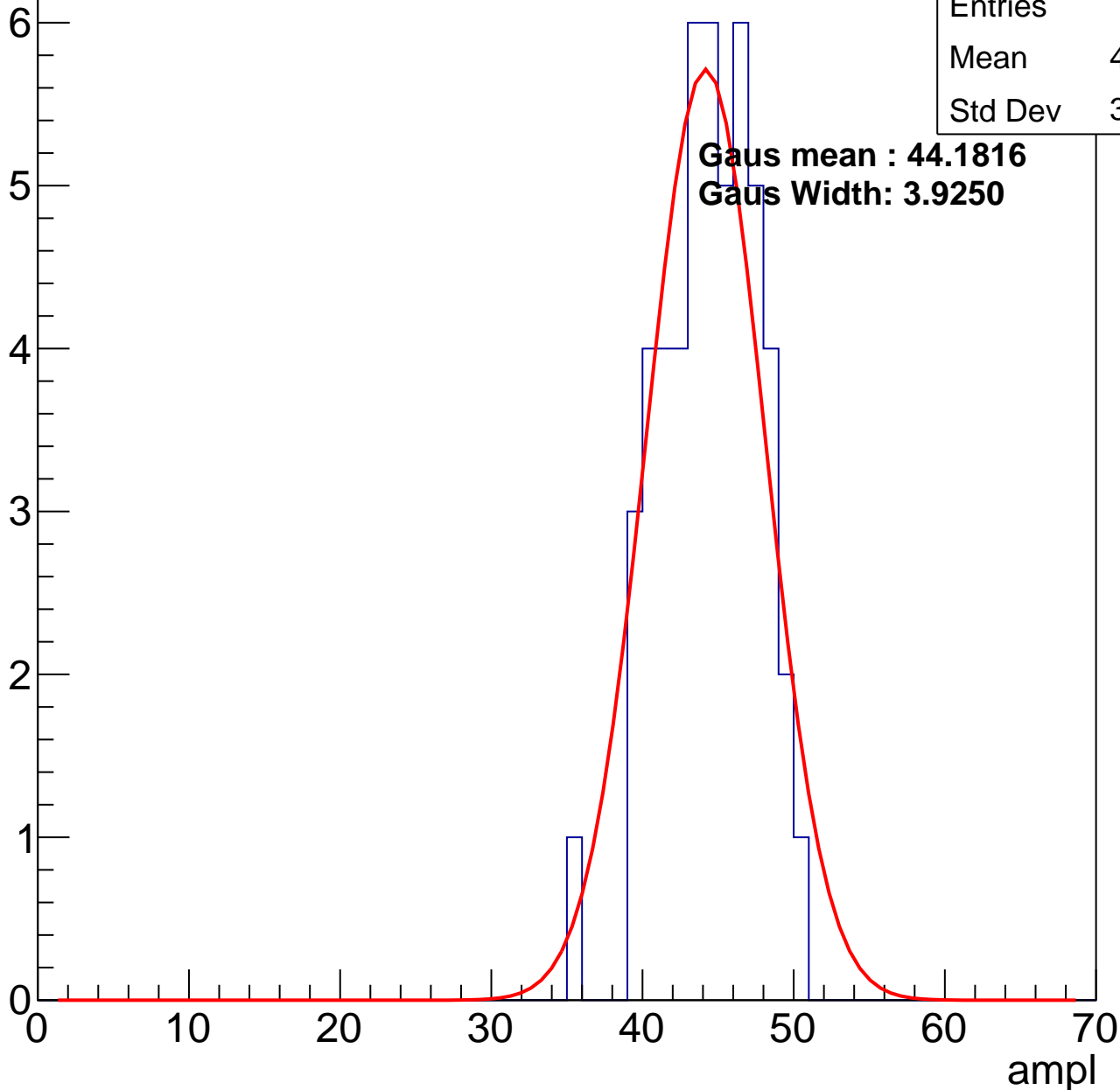
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.96
Std Dev	3.137

Gaus mean : 44.1816

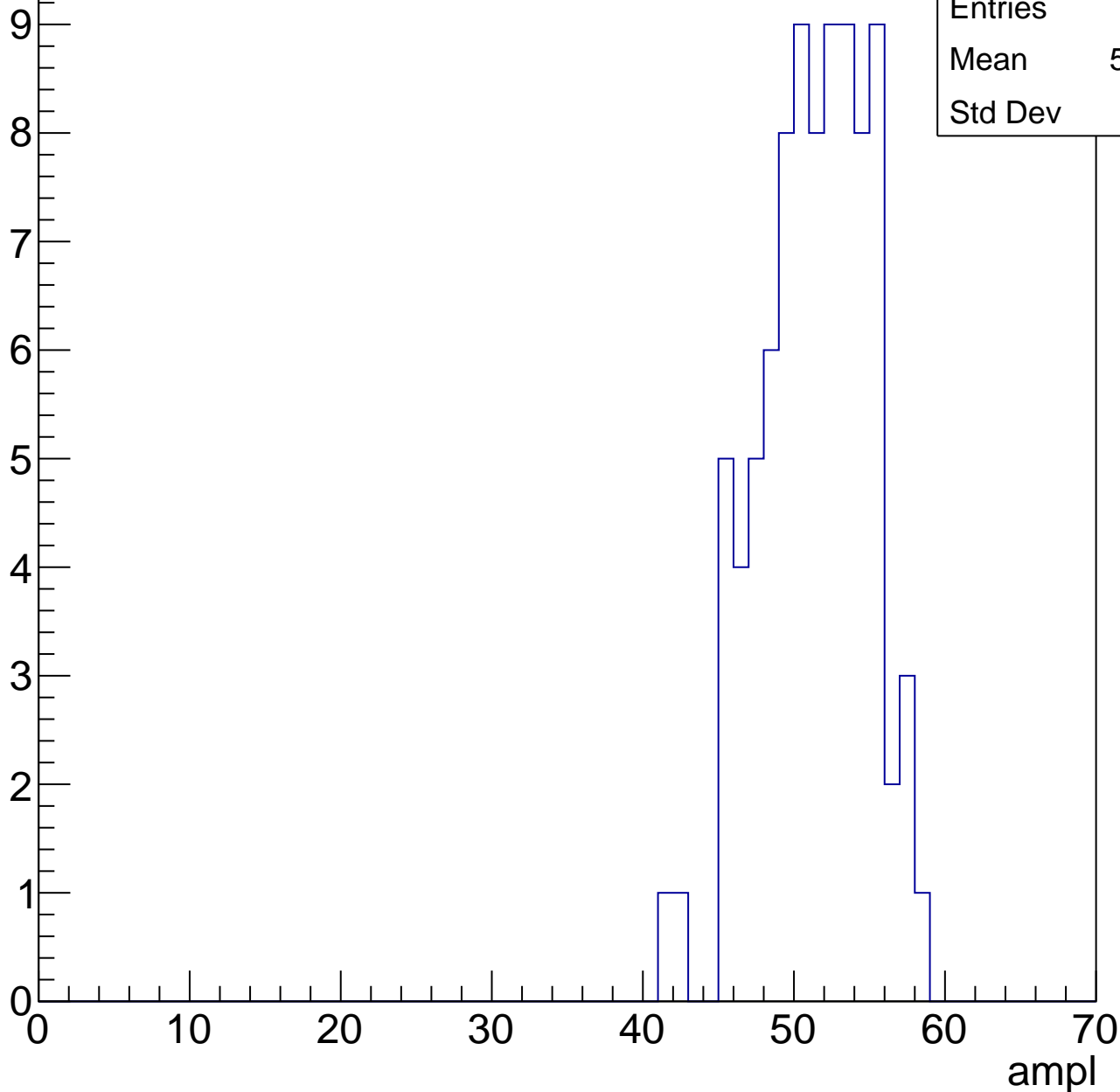
Gaus Width: 3.9250



B0L002S, U7-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	88
Mean	50.89
Std Dev	3.54

B0L002S, U7-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	40
Mean	57.42
Std Dev	2.783

ampl

0

10

20

30

40

50

60

70

B0L002S, U7-ch27, adc5

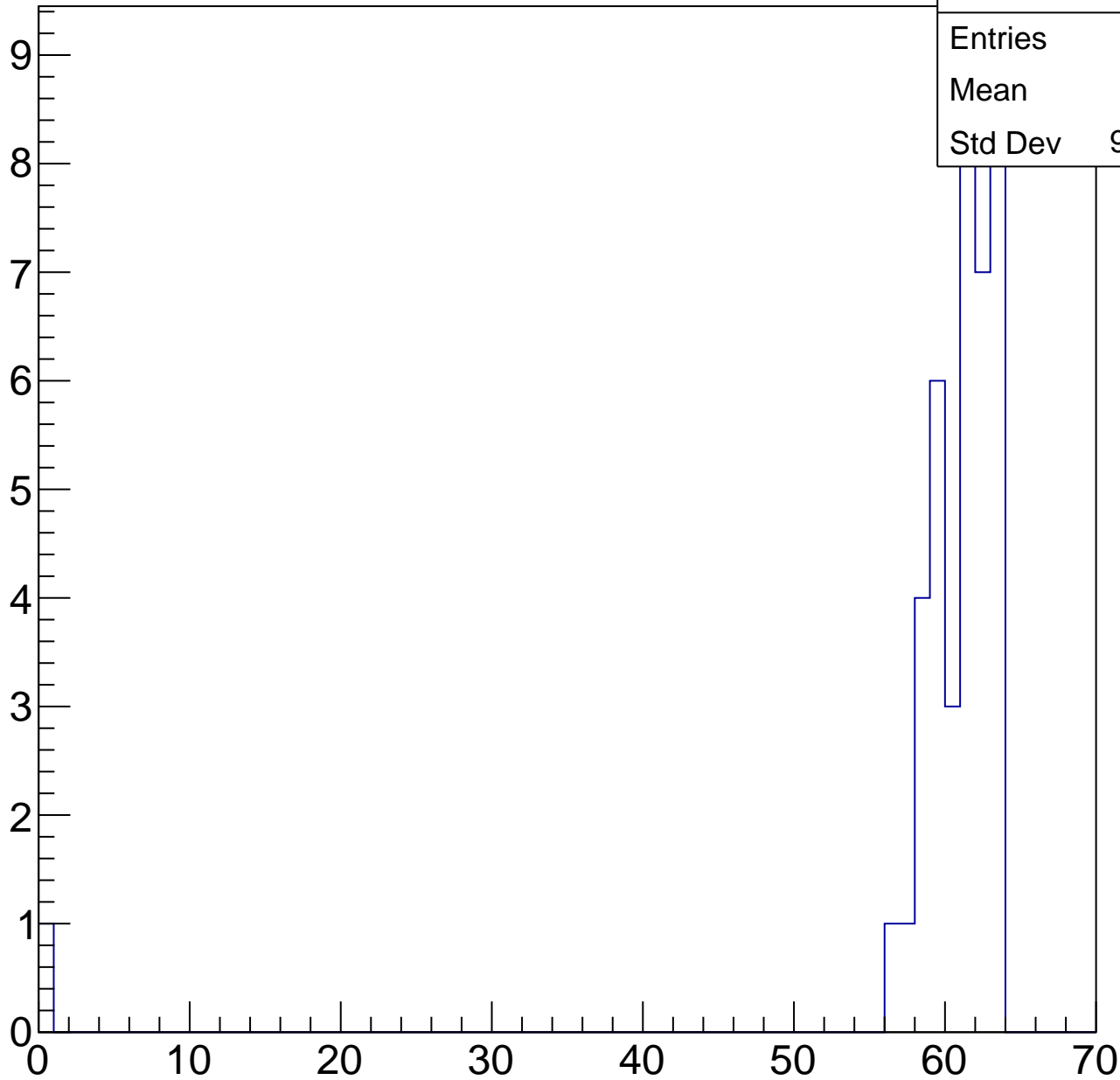
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.2
Std Dev	9.667

ampl



B0L002S, U7-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch28, adc0

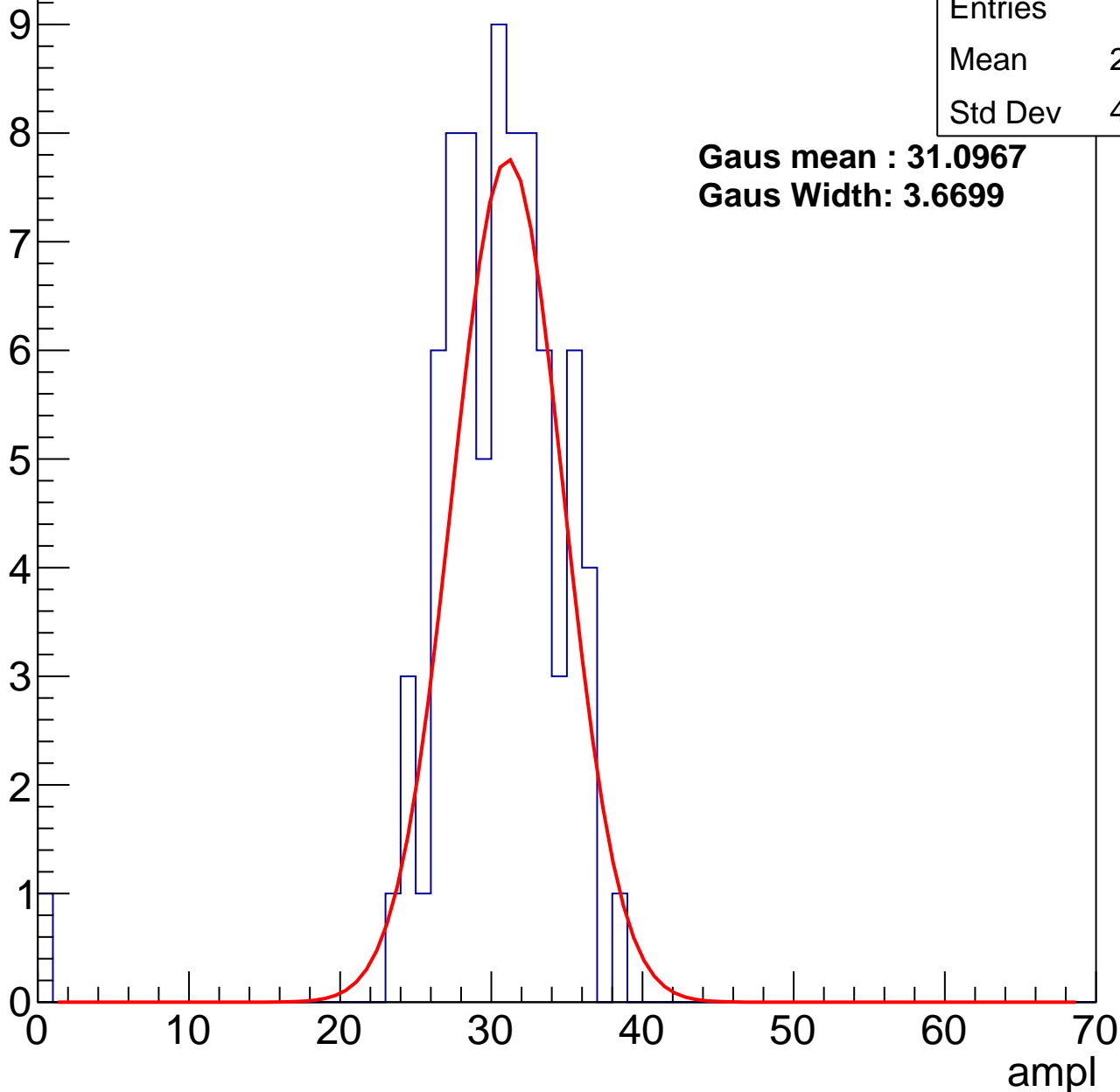
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.83
Std Dev	4.784

Gaus mean : 31.0967

Gaus Width: 3.6699



B0L002S, U7-ch28, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	38.16
Std Dev	3.539

Gaus mean : 38.7063

Gaus Width: 3.6831

10

8

6

4

2

0

0

10

20

30

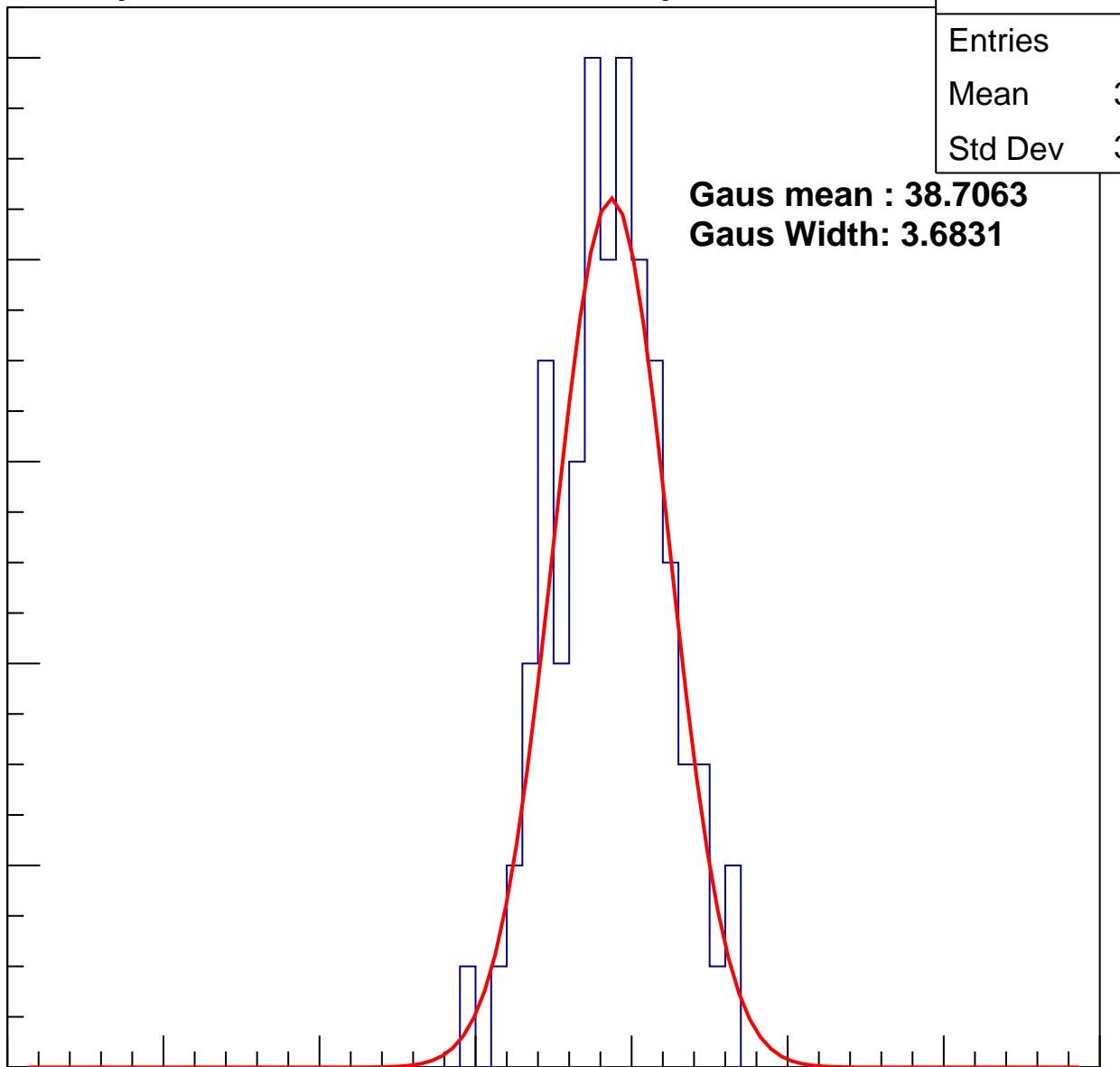
40

50

60

70

ampl



B0L002S, U7-ch28, adc2

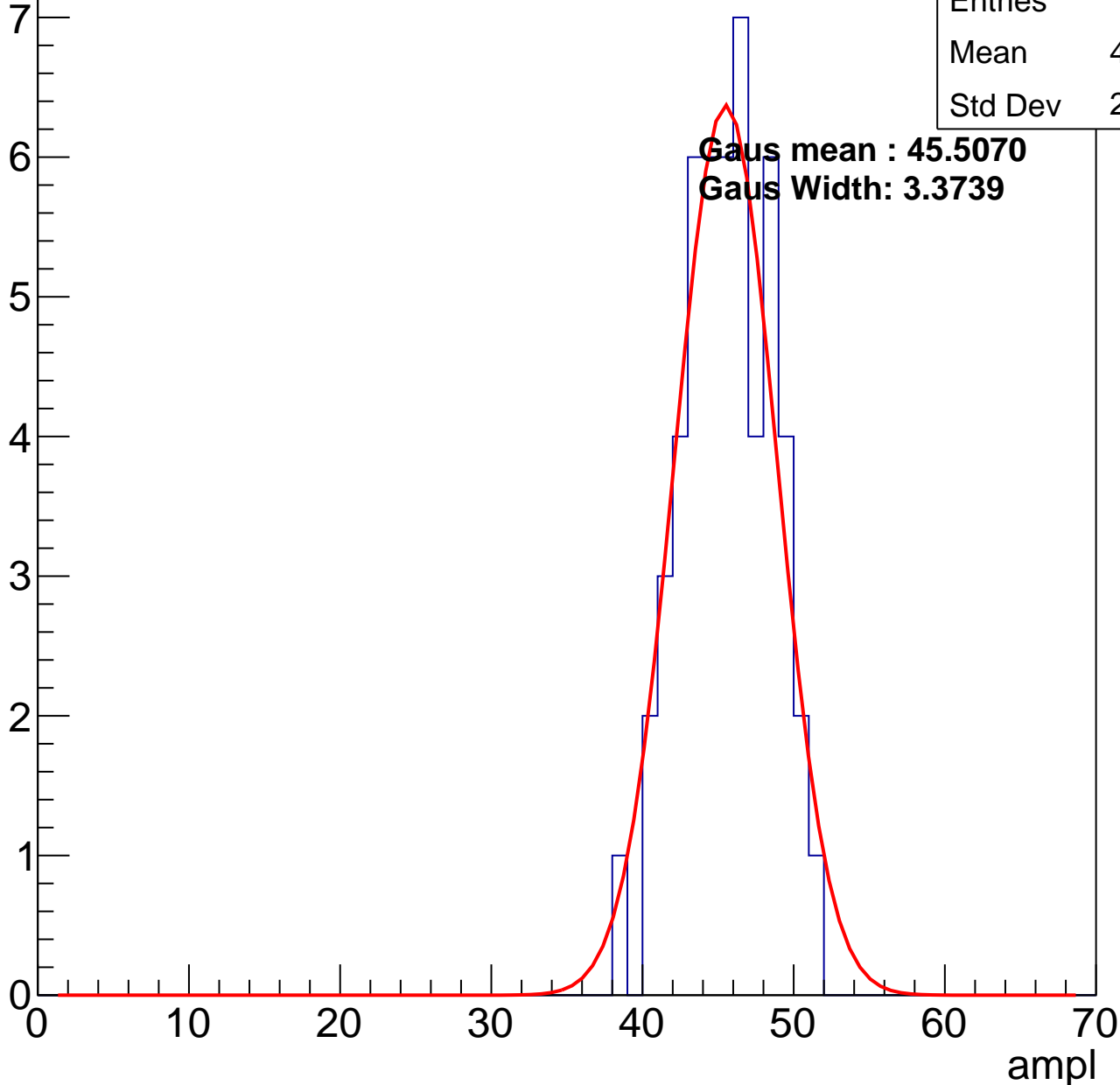
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	45.12
Std Dev	2.907

Gaus mean : 45.5070

Gaus Width: 3.3739

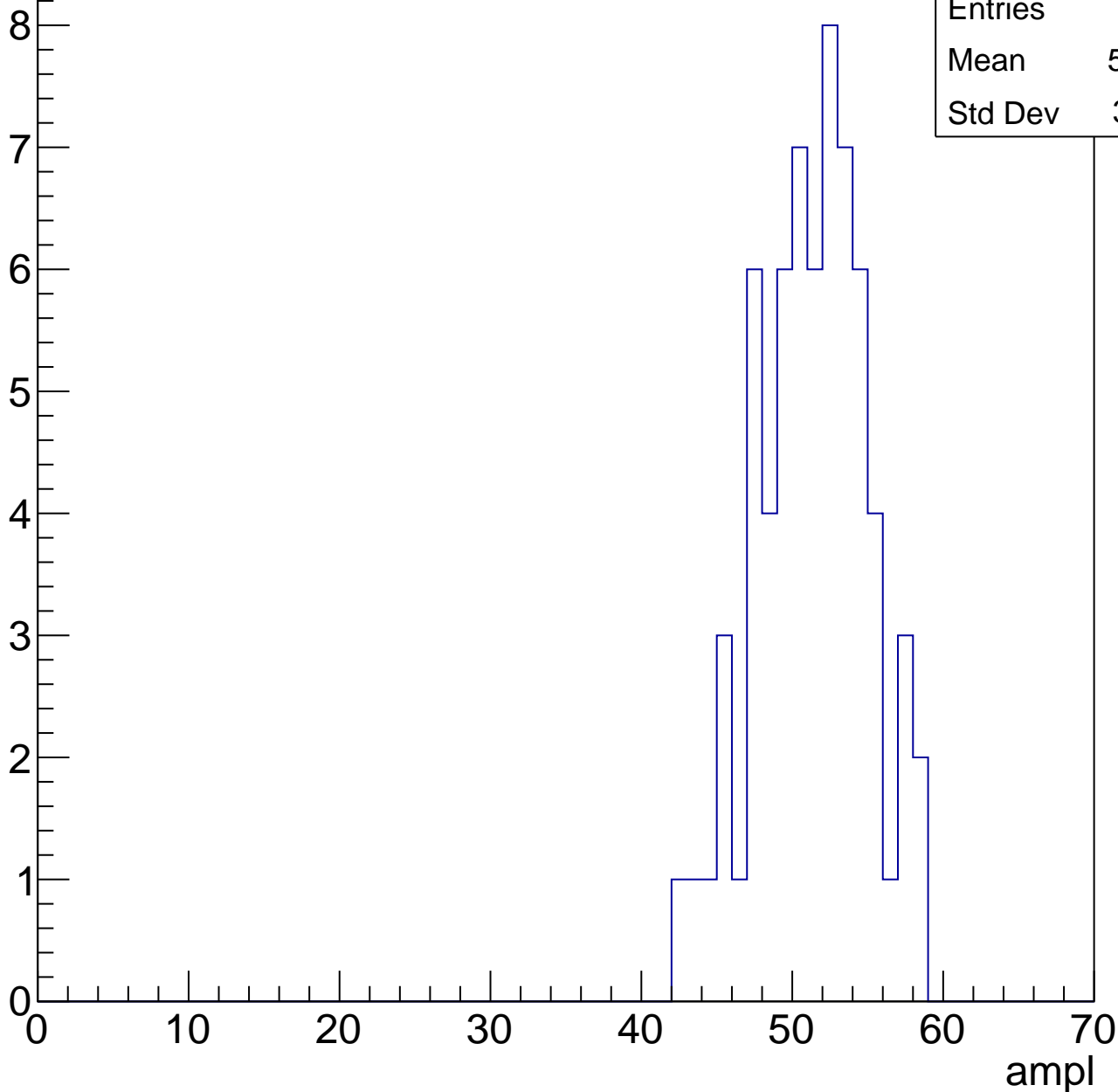


B0L002S, U7-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.87
Std Dev	3.611

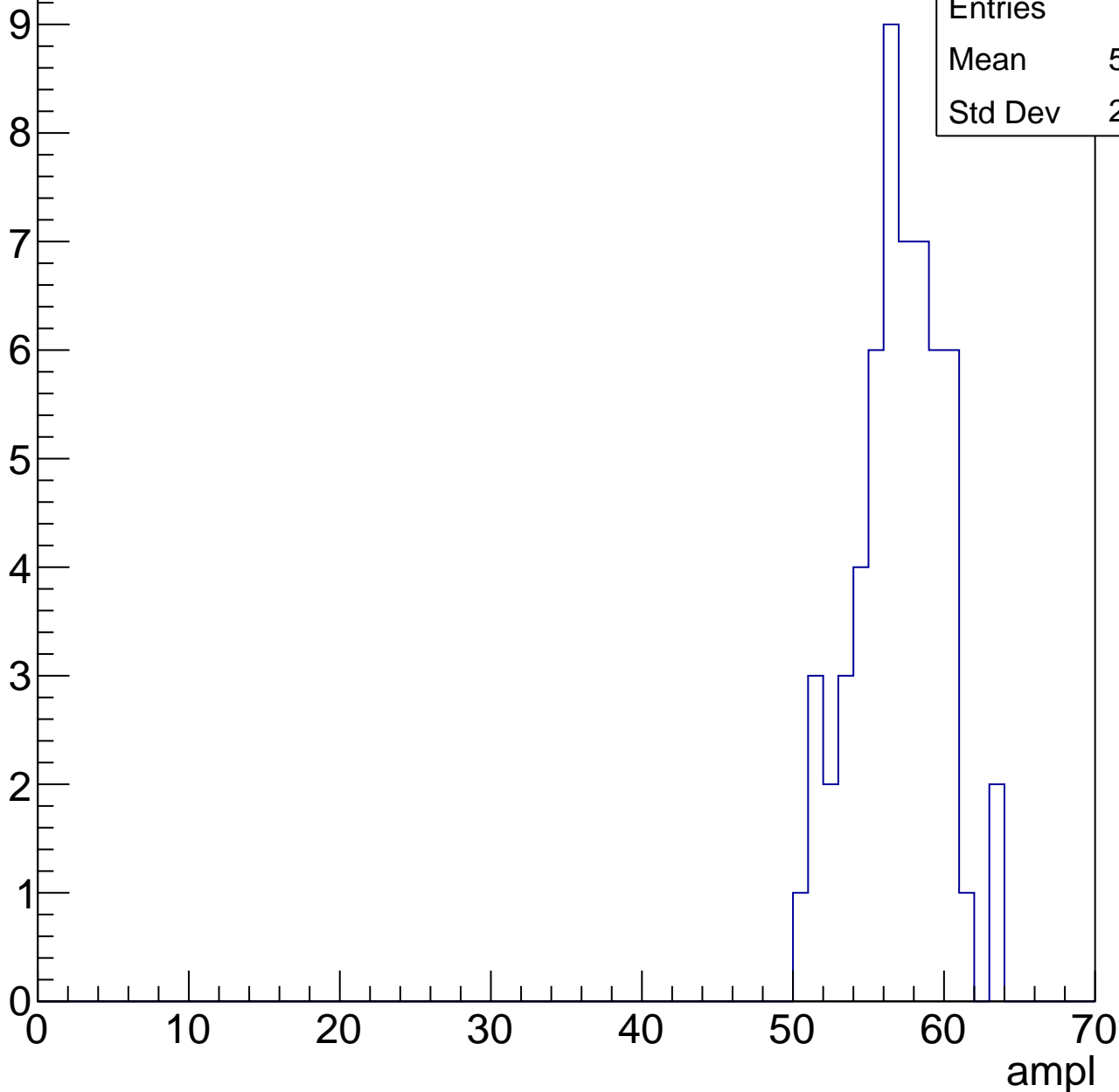


B0L002S, U7-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.53
Std Dev	2.915

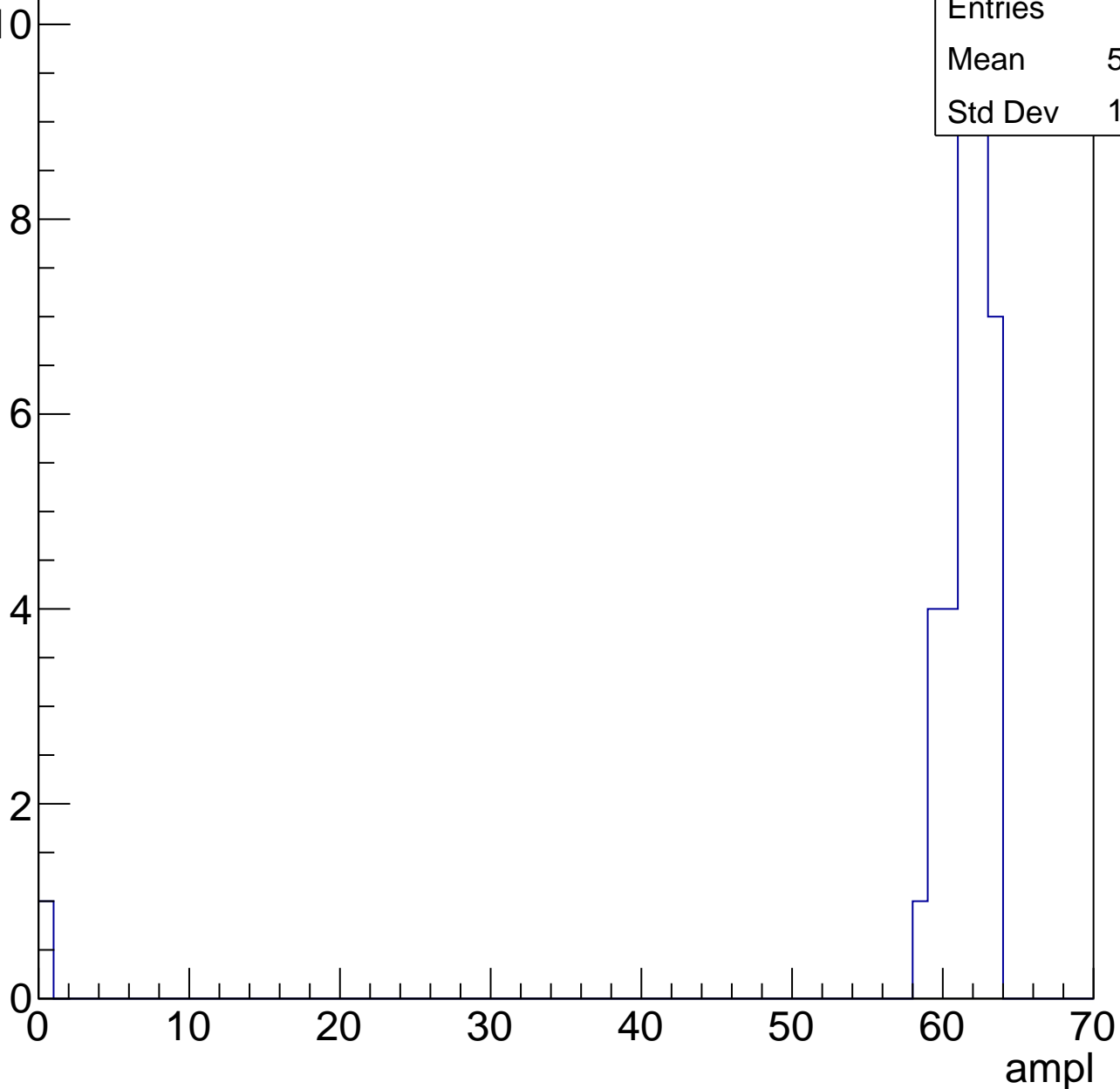


B0L002S, U7-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.56
Std Dev	10.16



B0L002S, U7-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch29, adc0

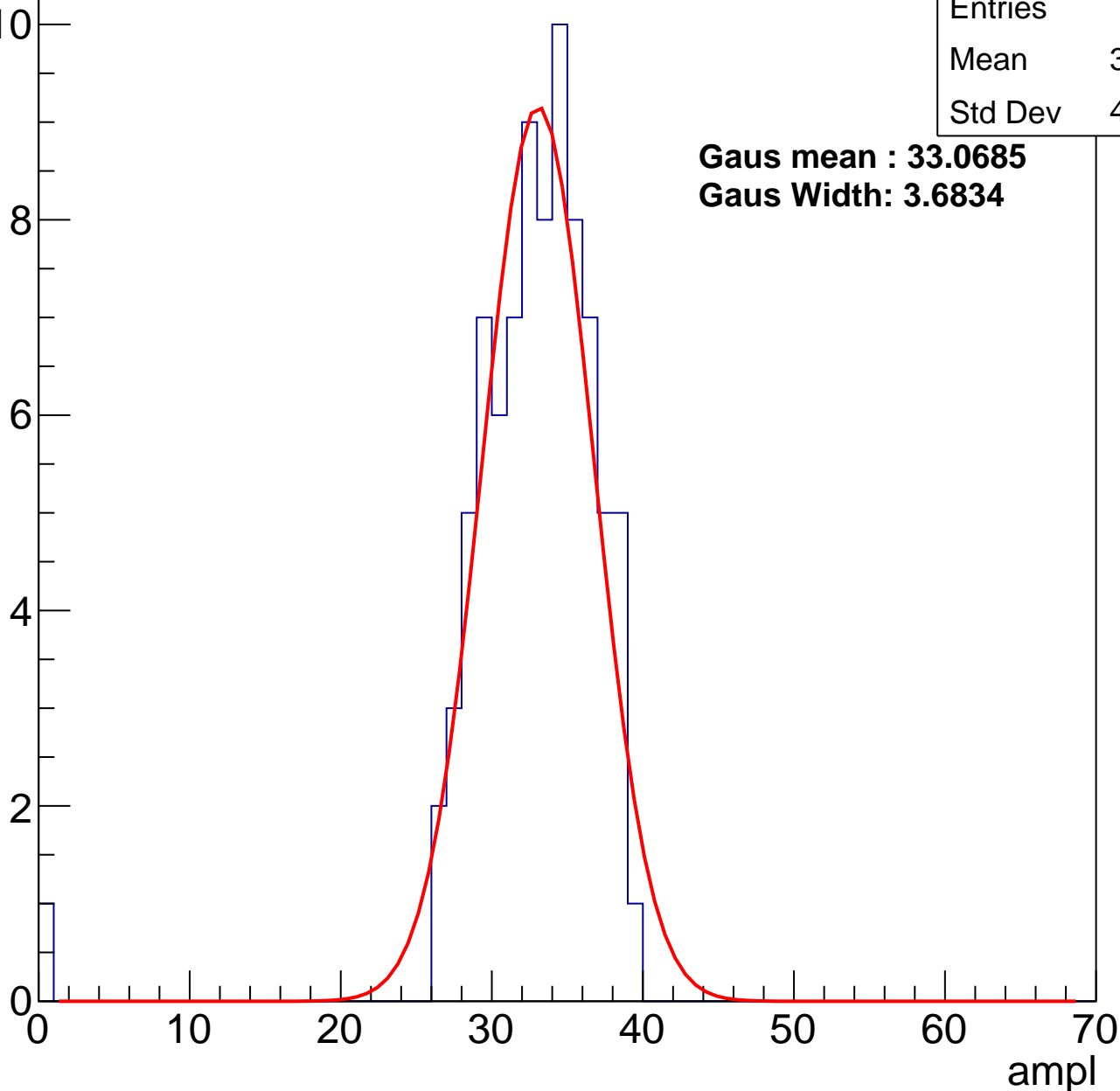
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	32.27
Std Dev	4.787

Gaus mean : 33.0685

Gaus Width: 3.6834



B0L002S, U7-ch29, adc1

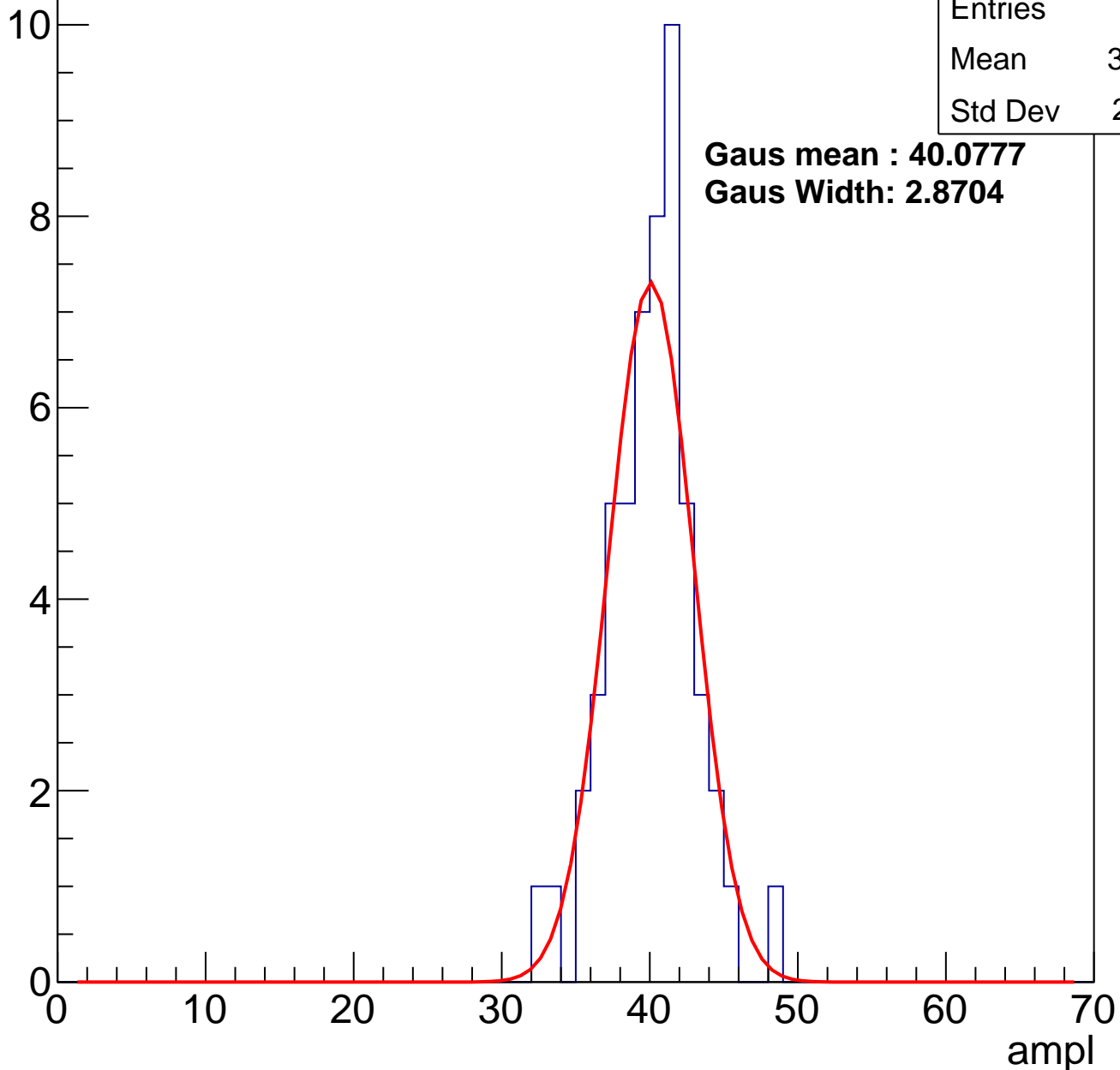
calib_packv5_042523_0143.root, FC#8, port C1

Entries	54
Mean	39.65
Std Dev	2.901

Gaus mean : 40.0777

Gaus Width: 2.8704

Entry



B0L002S, U7-ch29, adc2

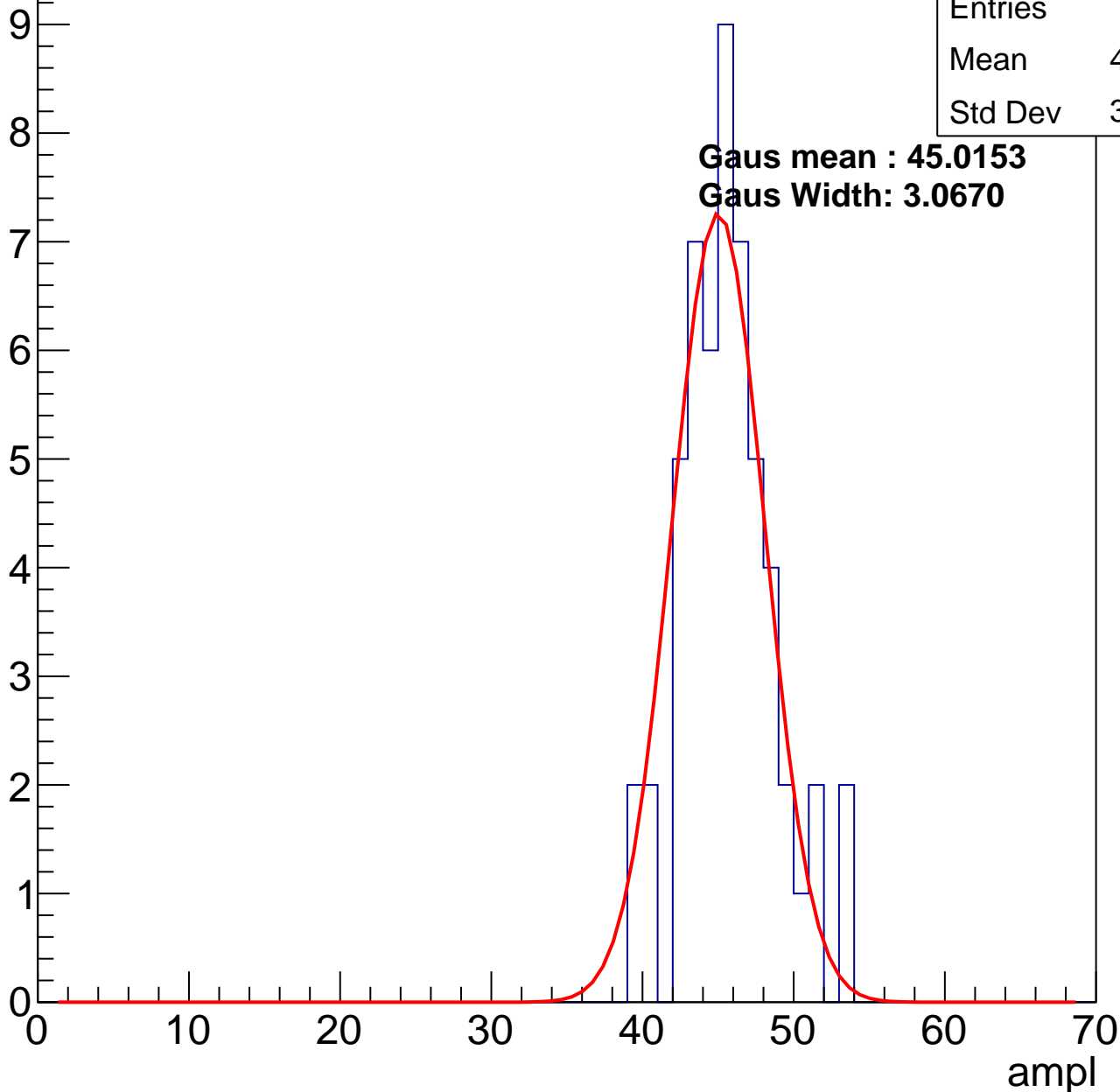
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	45.24
Std Dev	3.097

Gaus mean : 45.0153

Gaus Width: 3.0670

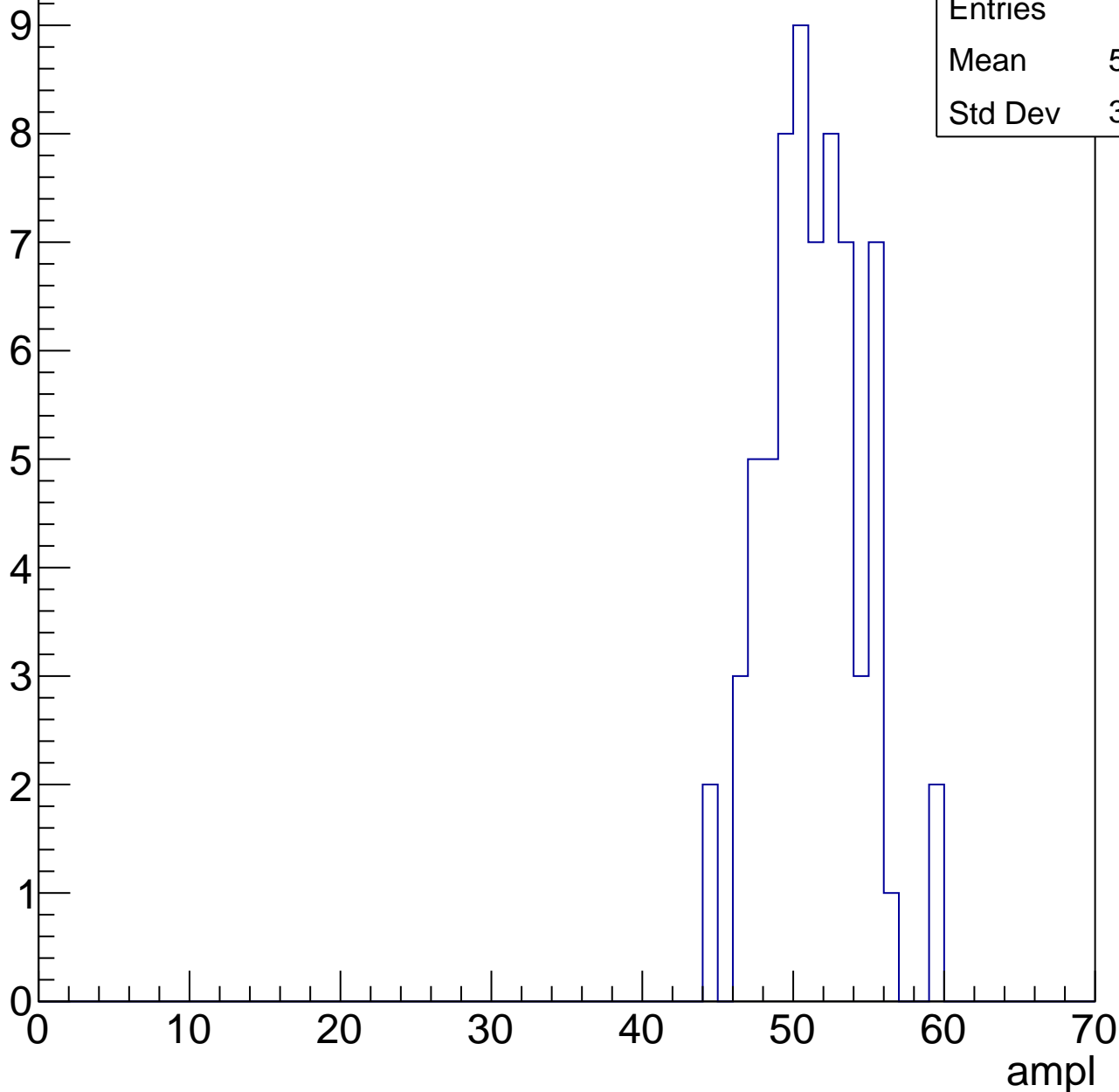


B0L002S, U7-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

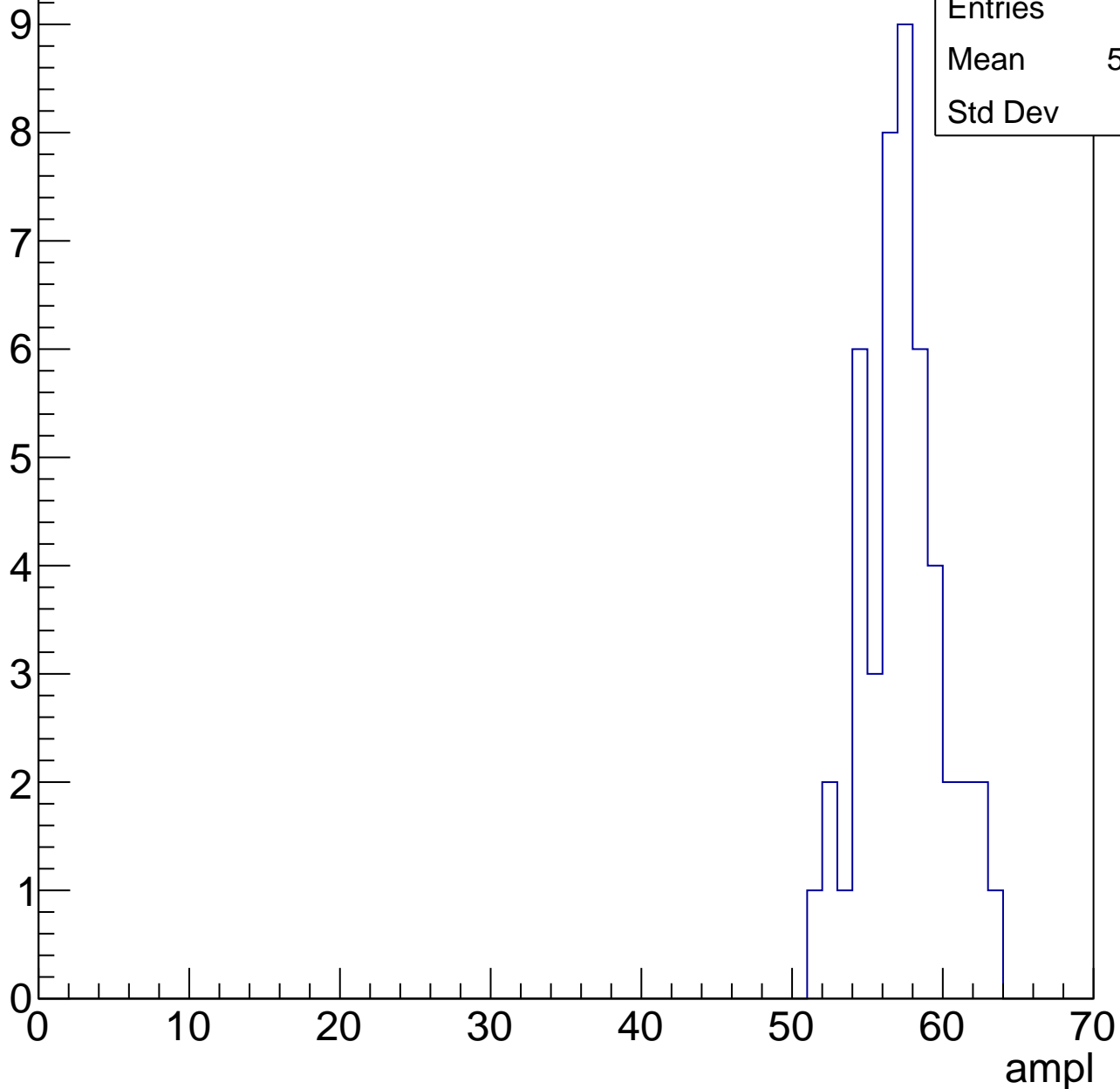
Entries	67
Mean	50.87
Std Dev	3.152



B0L002S, U7-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.52
Std Dev	2.073

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

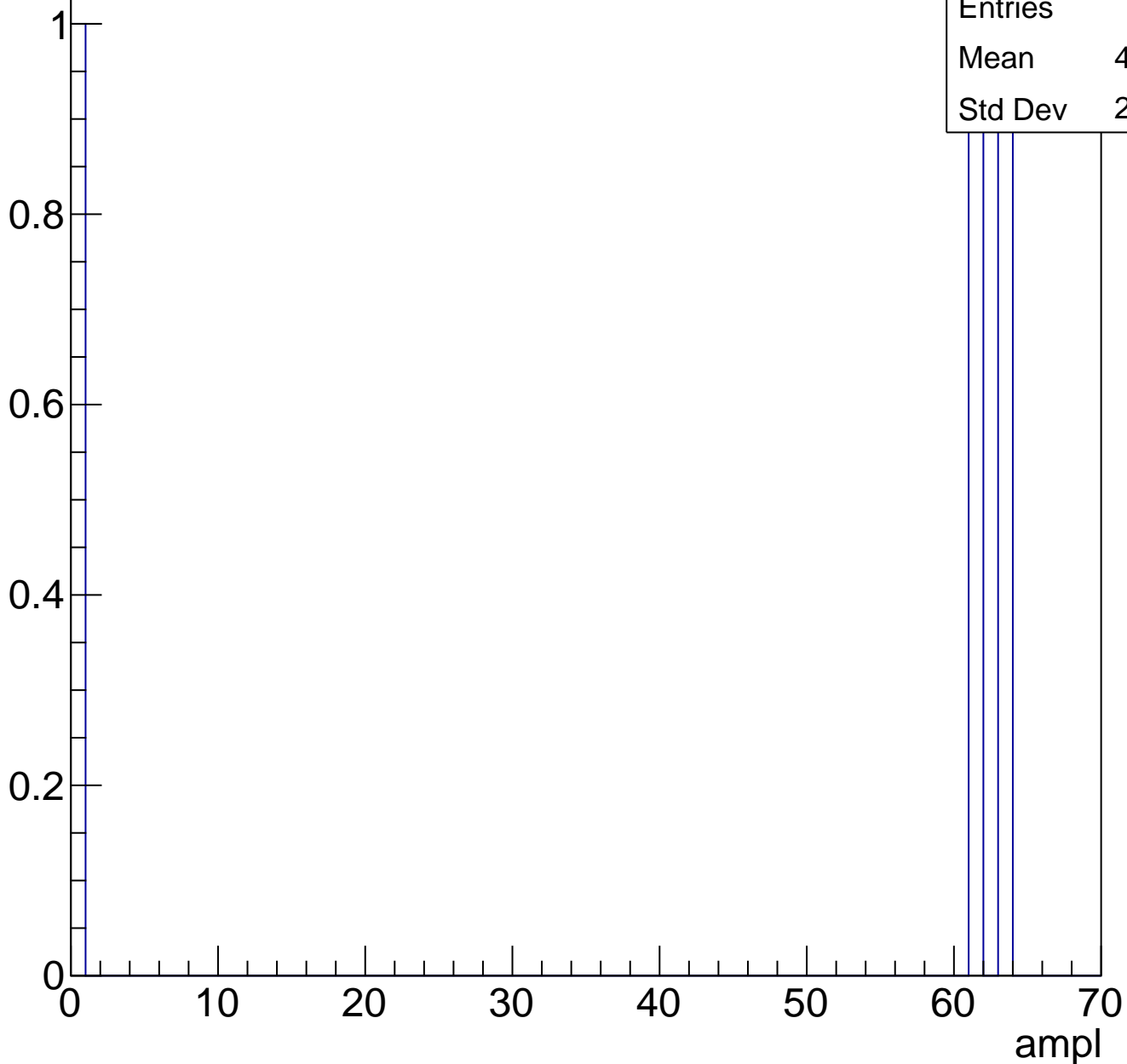
7

8

B0L002S, U7-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U7-ch30, adc0

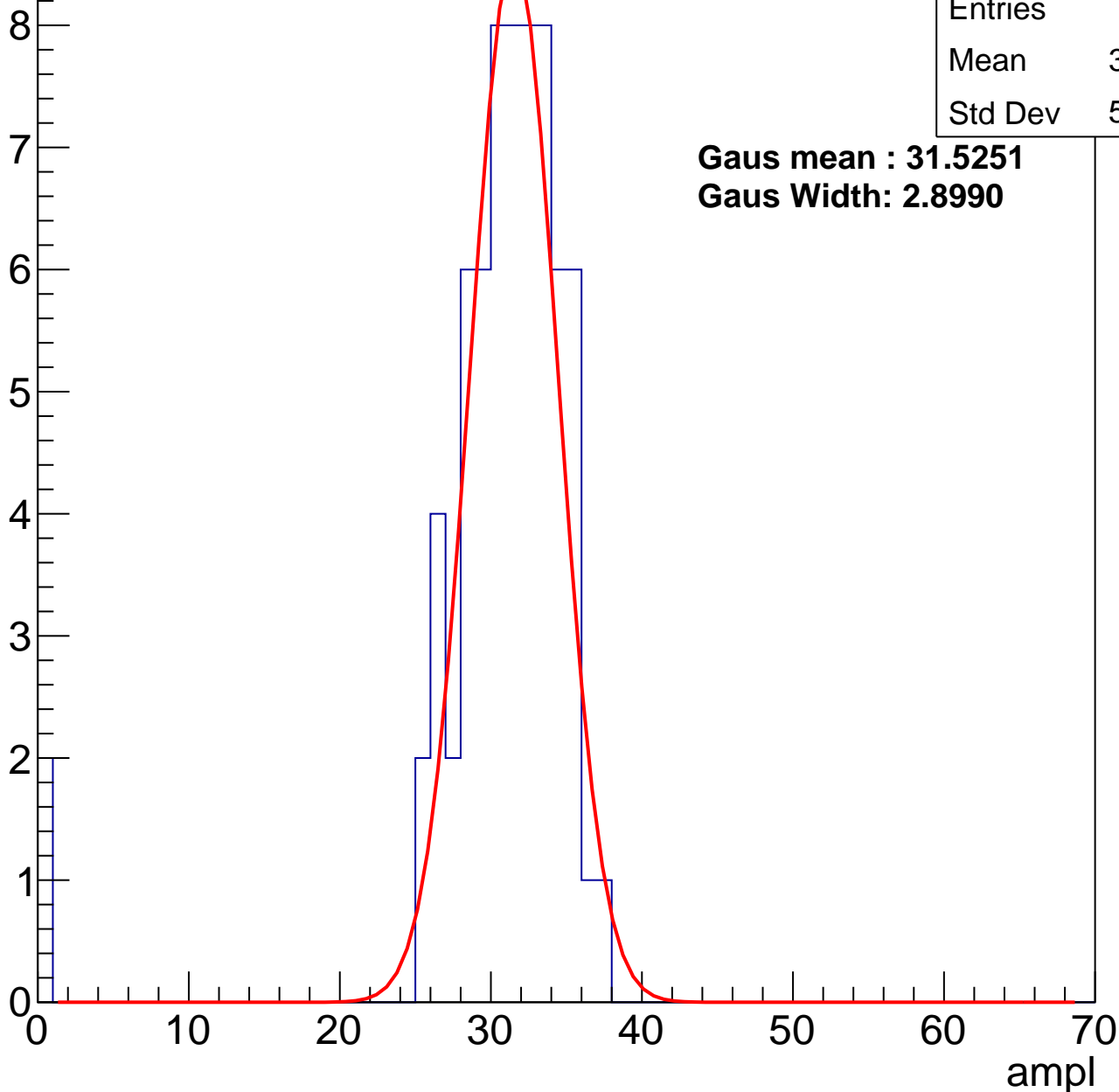
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.07
Std Dev	5.947

Gaus mean : 31.5251

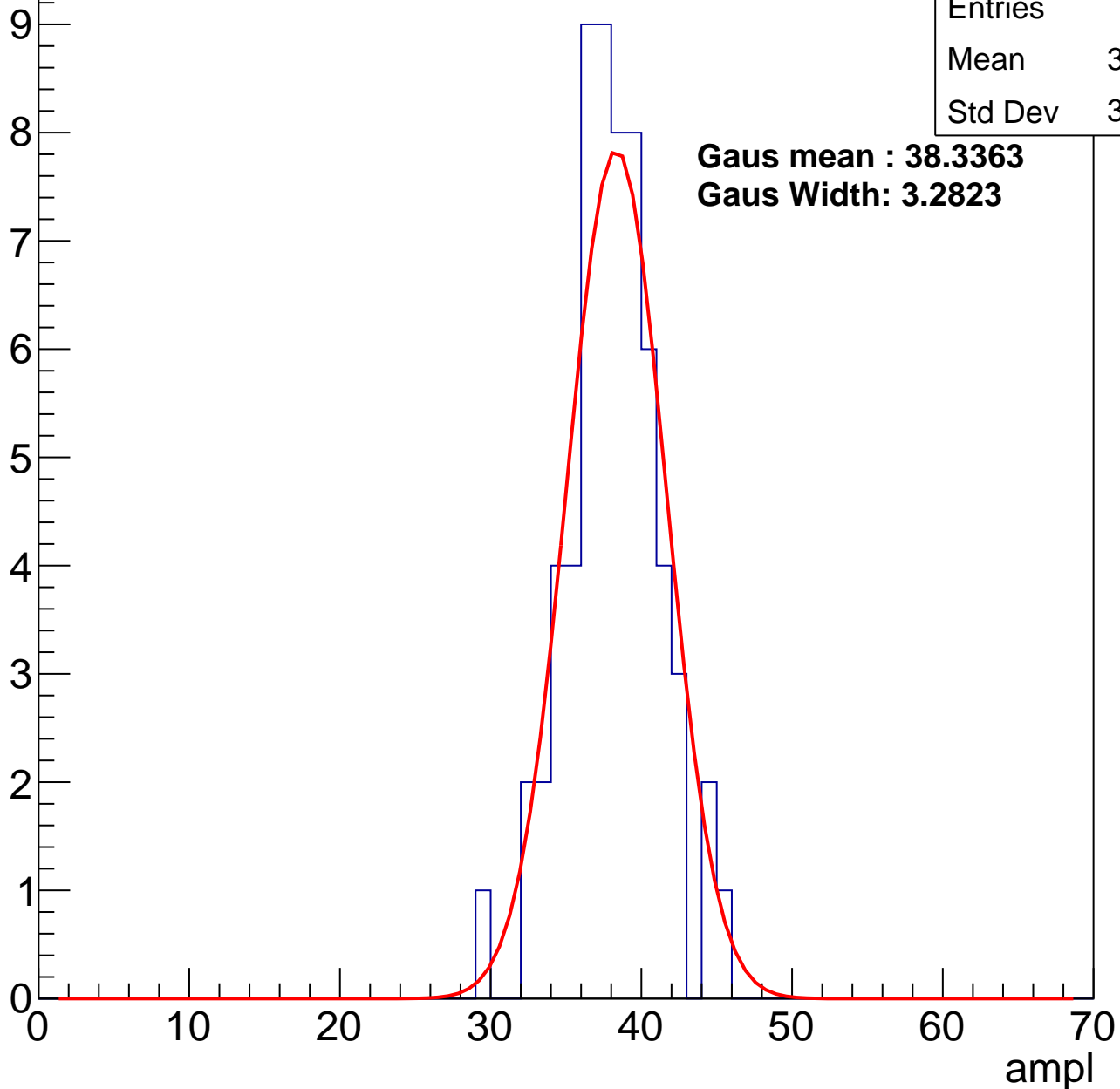
Gaus Width: 2.8990



B0L002S, U7-ch30, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch30, adc2

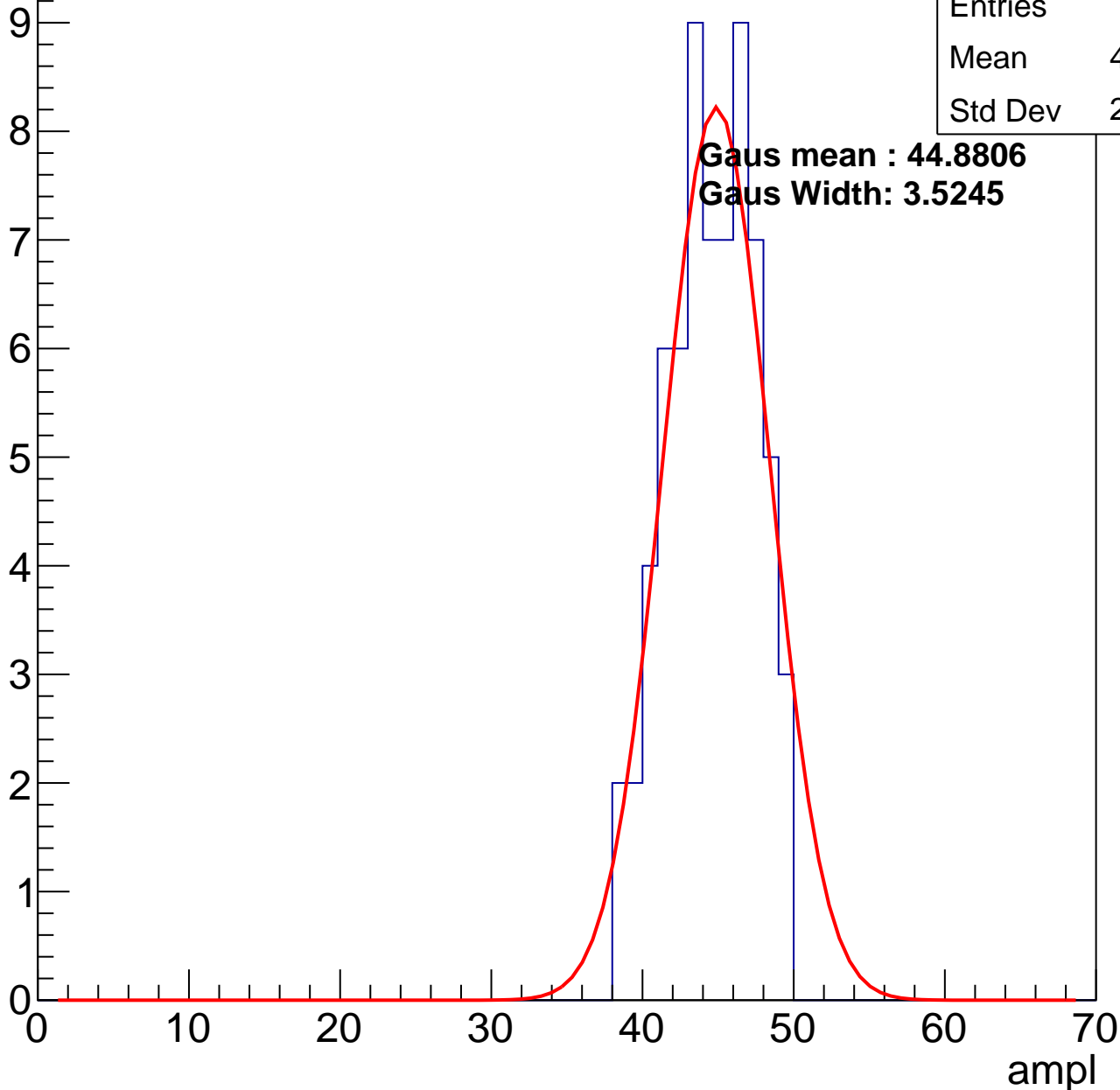
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.06
Std Dev	2.823

Gaus mean : 44.8806

Gaus Width: 3.5245



B0L002S, U7-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

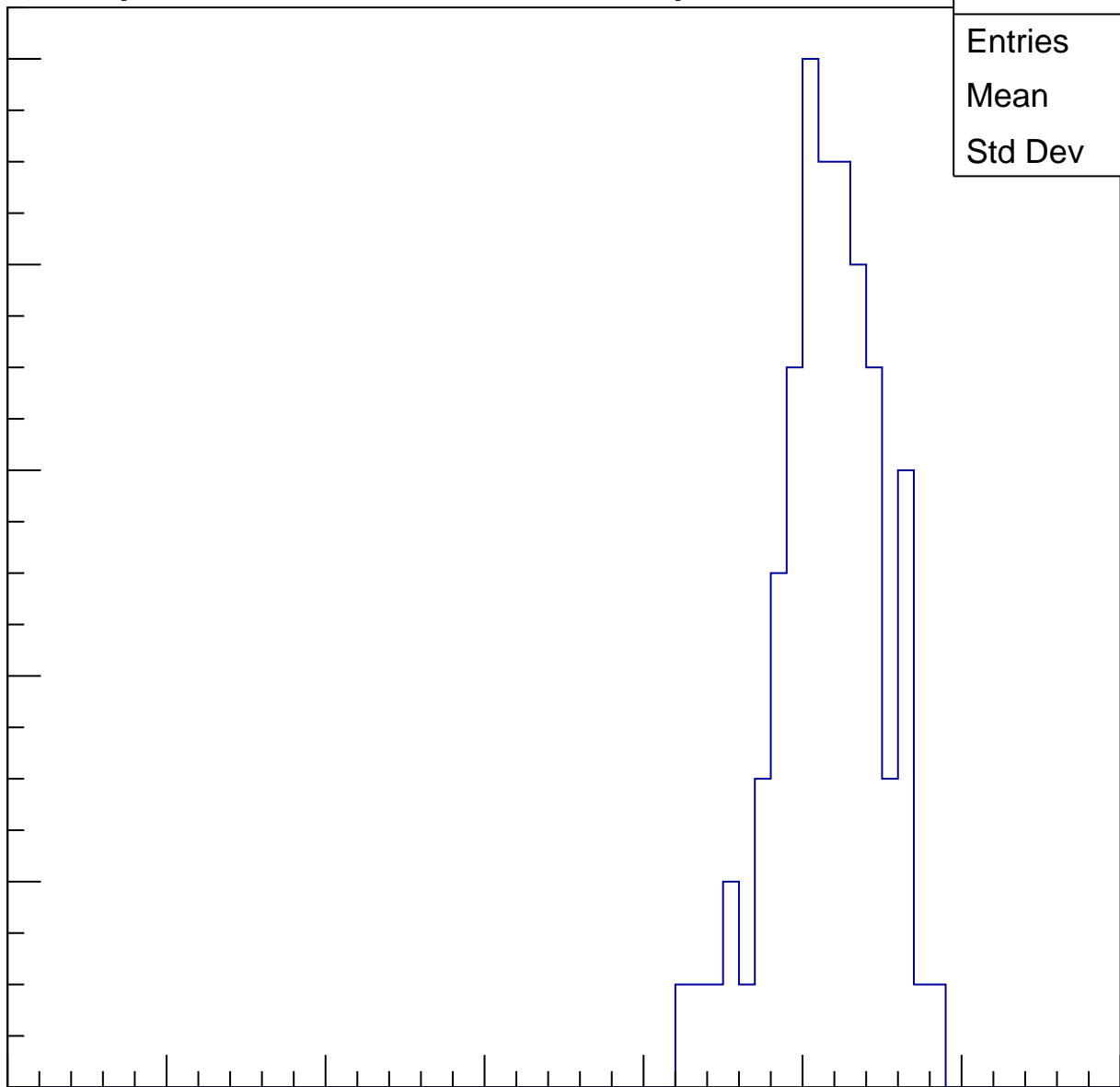
Entries	75
Mean	51.12
Std Dev	3.282

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch30, adc4

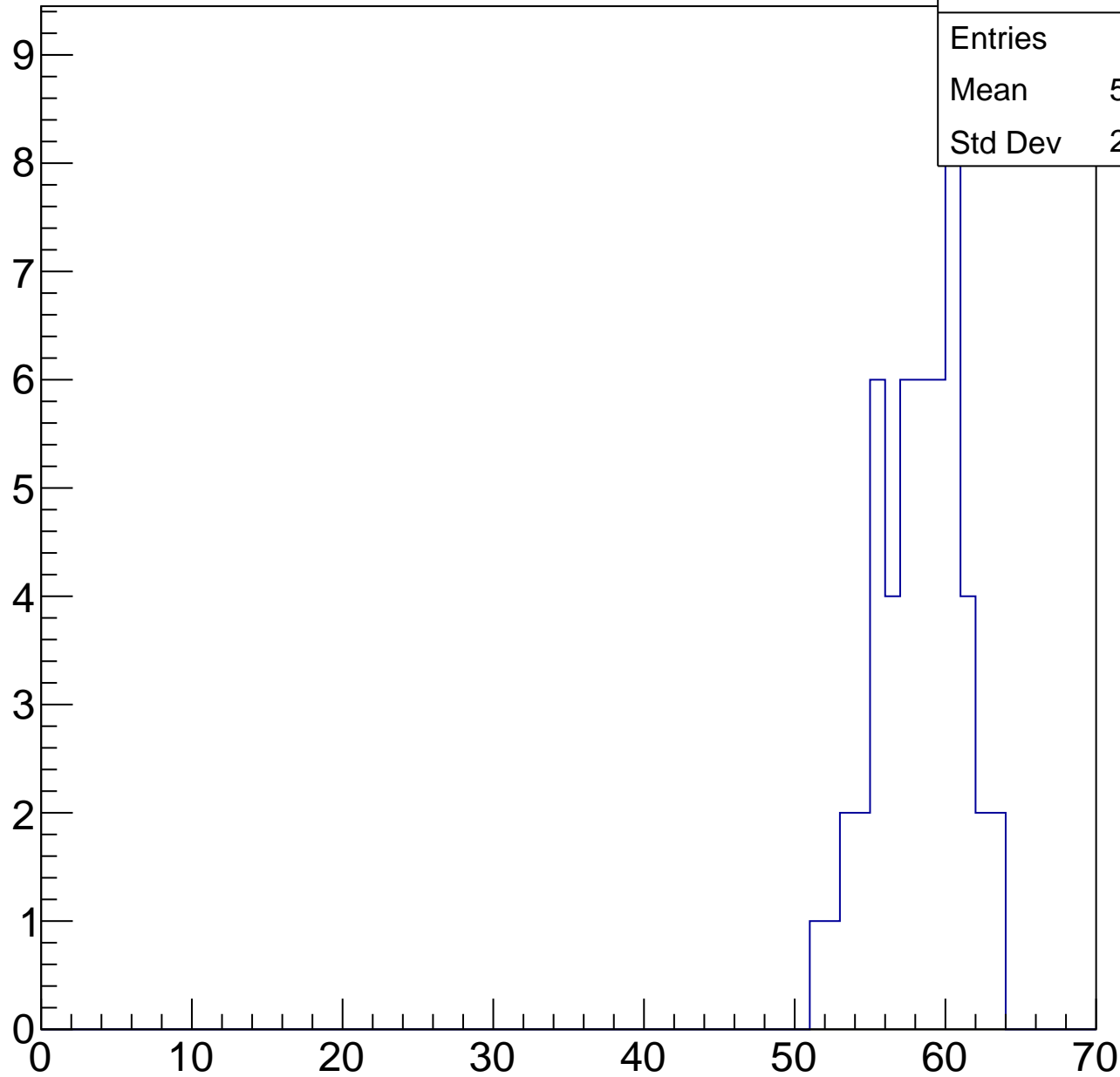
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	57.82
Std Dev	2.805

ampl

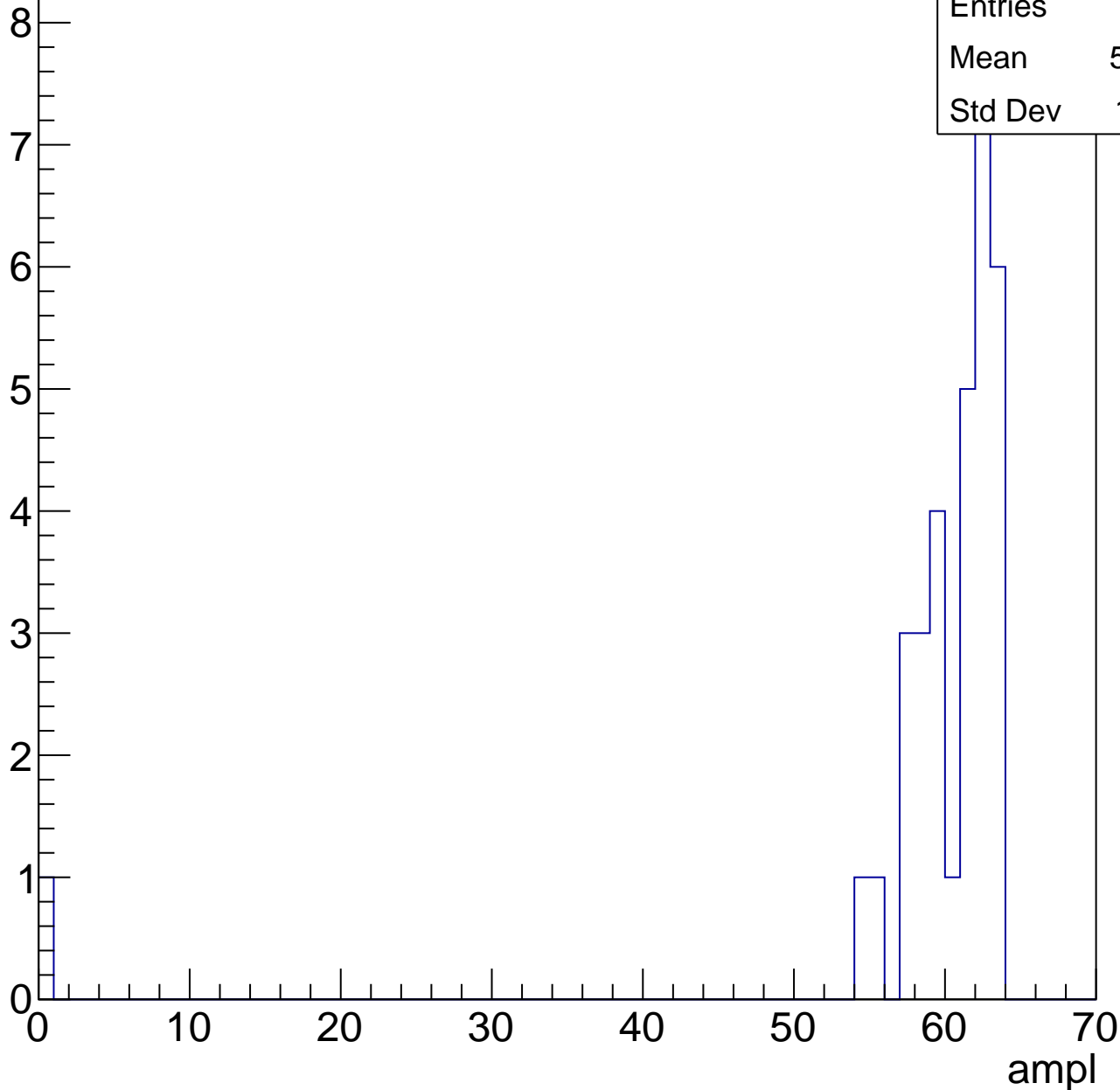


B0L002S, U7-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.45
Std Dev	10.61



B0L002S, U7-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch31, adc0

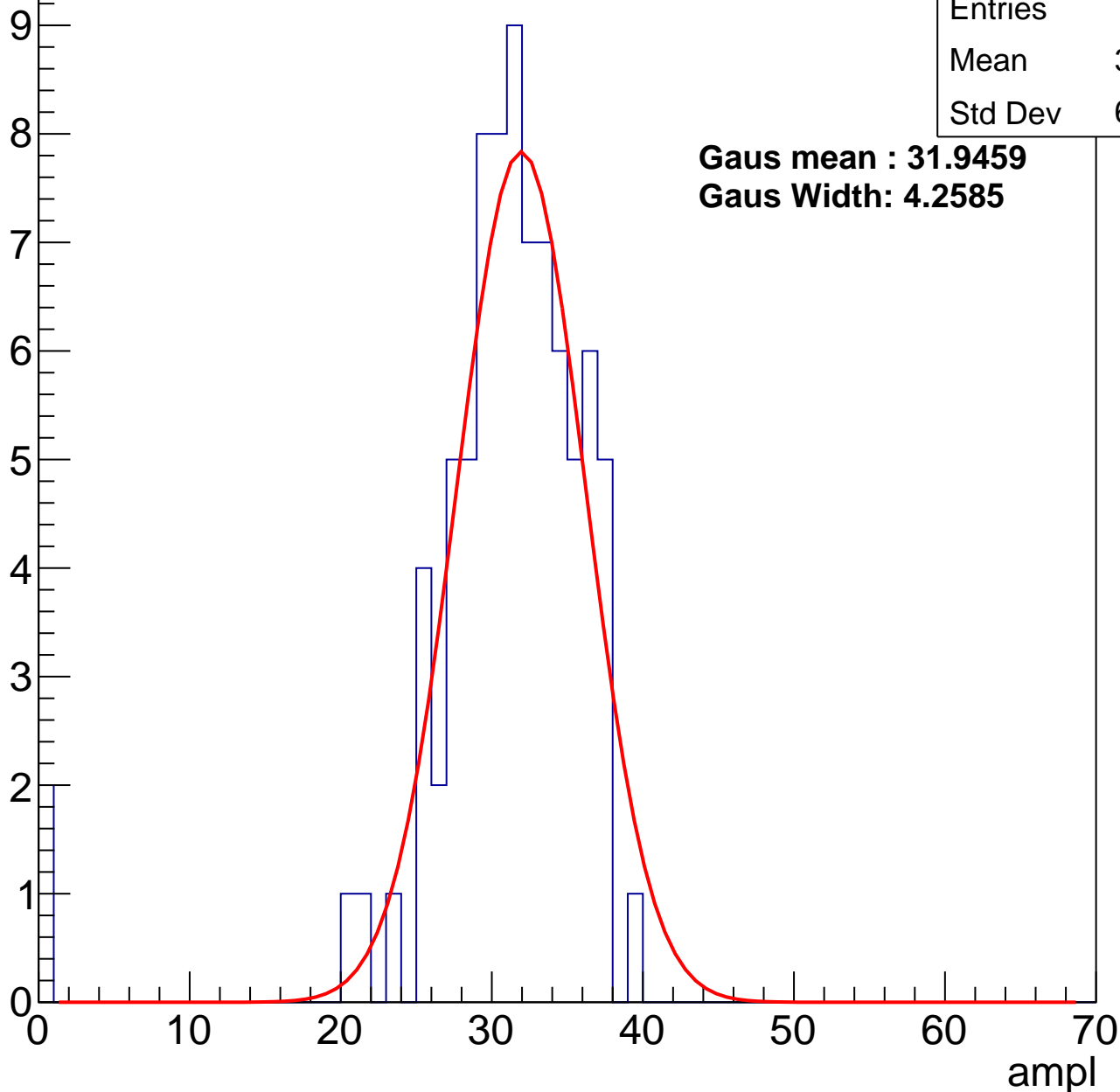
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.31
Std Dev	6.101

Gaus mean : 31.9459

Gaus Width: 4.2585



B0L002S, U7-ch31, adc1

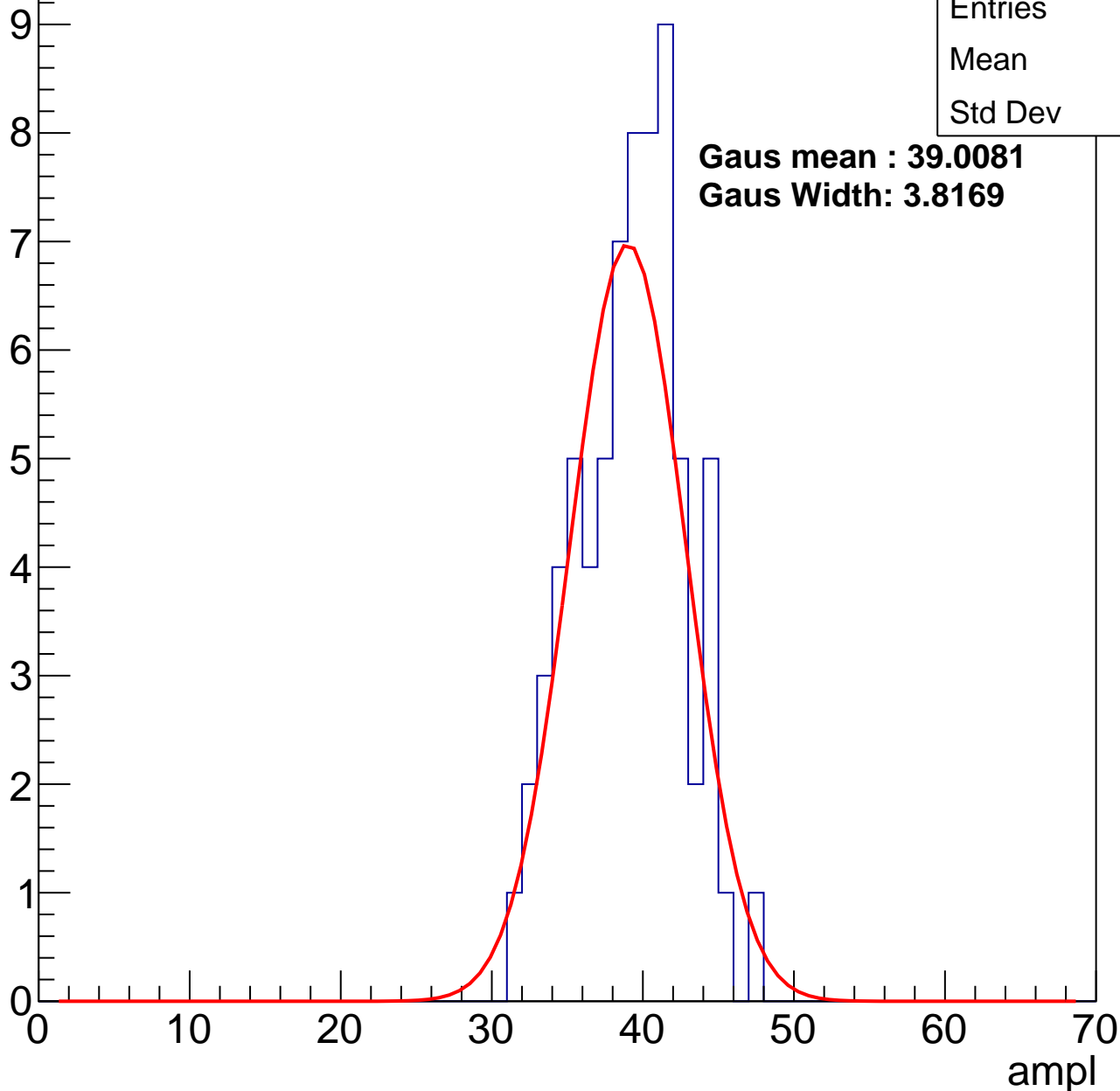
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.7
Std Dev	3.49

Gaus mean : 39.0081

Gaus Width: 3.8169



B0L002S, U7-ch31, adc2

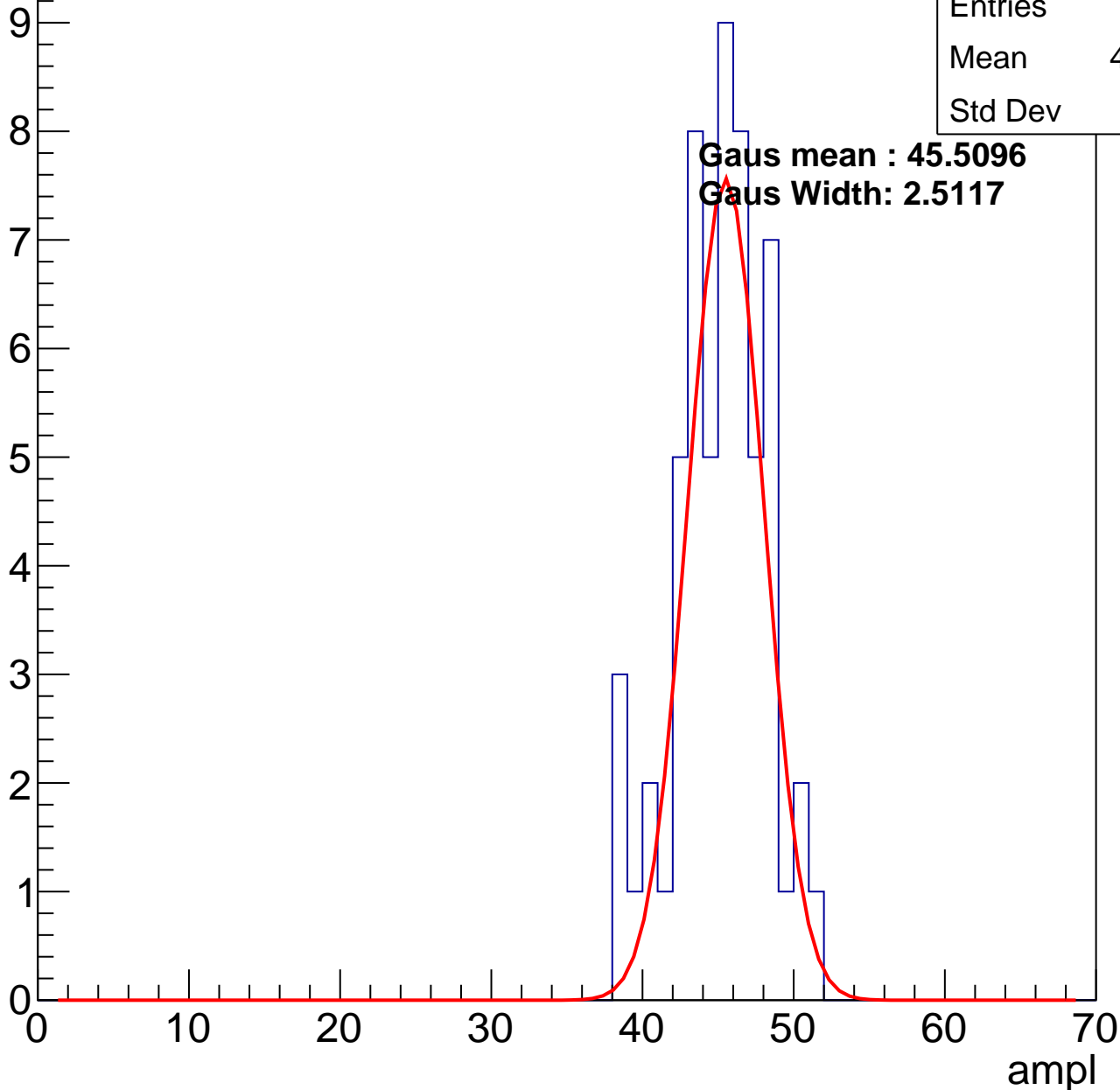
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.69
Std Dev	2.99

Gaus mean : 45.5096

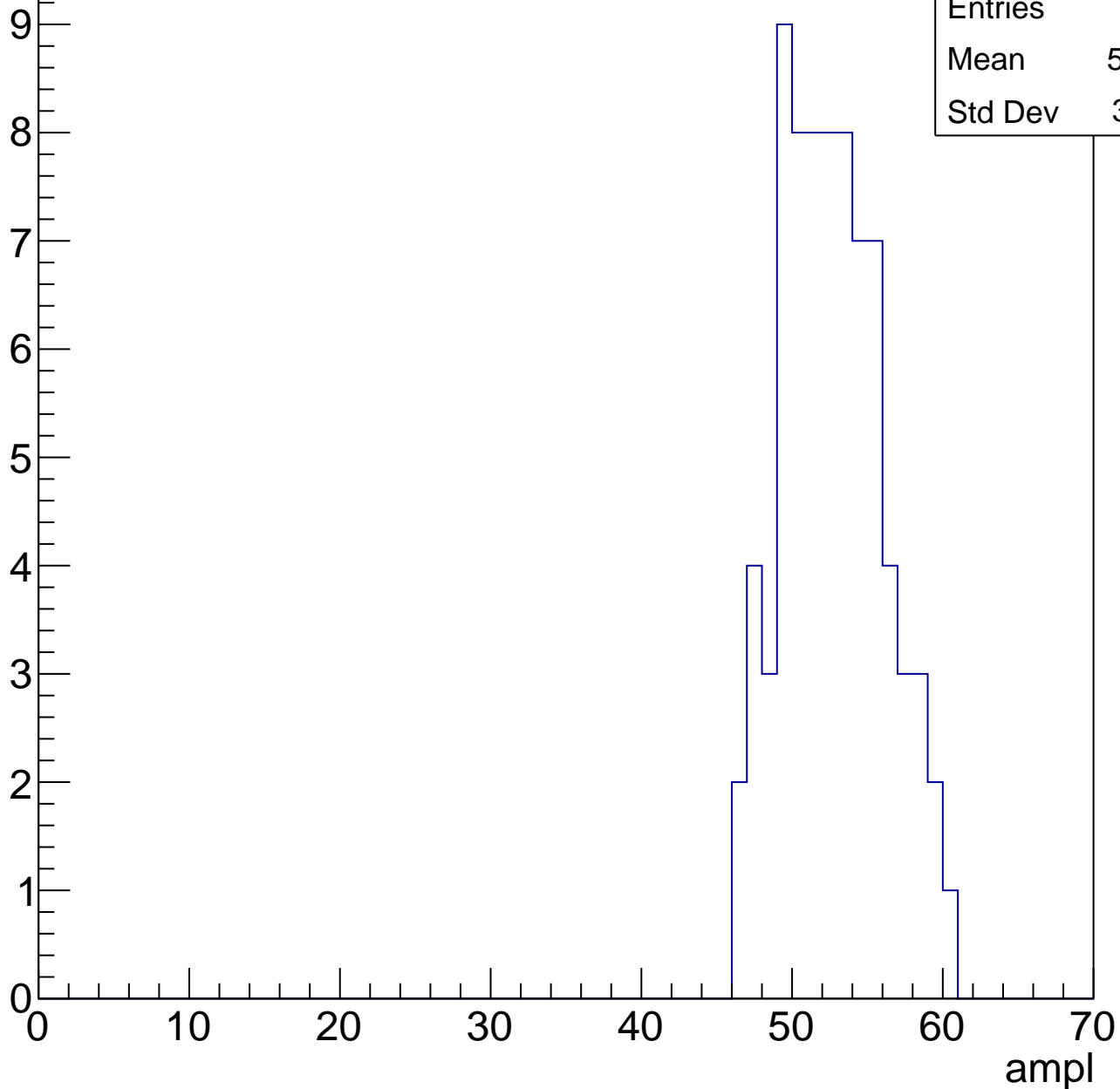
Gaus Width: 2.5117



B0L002S, U7-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

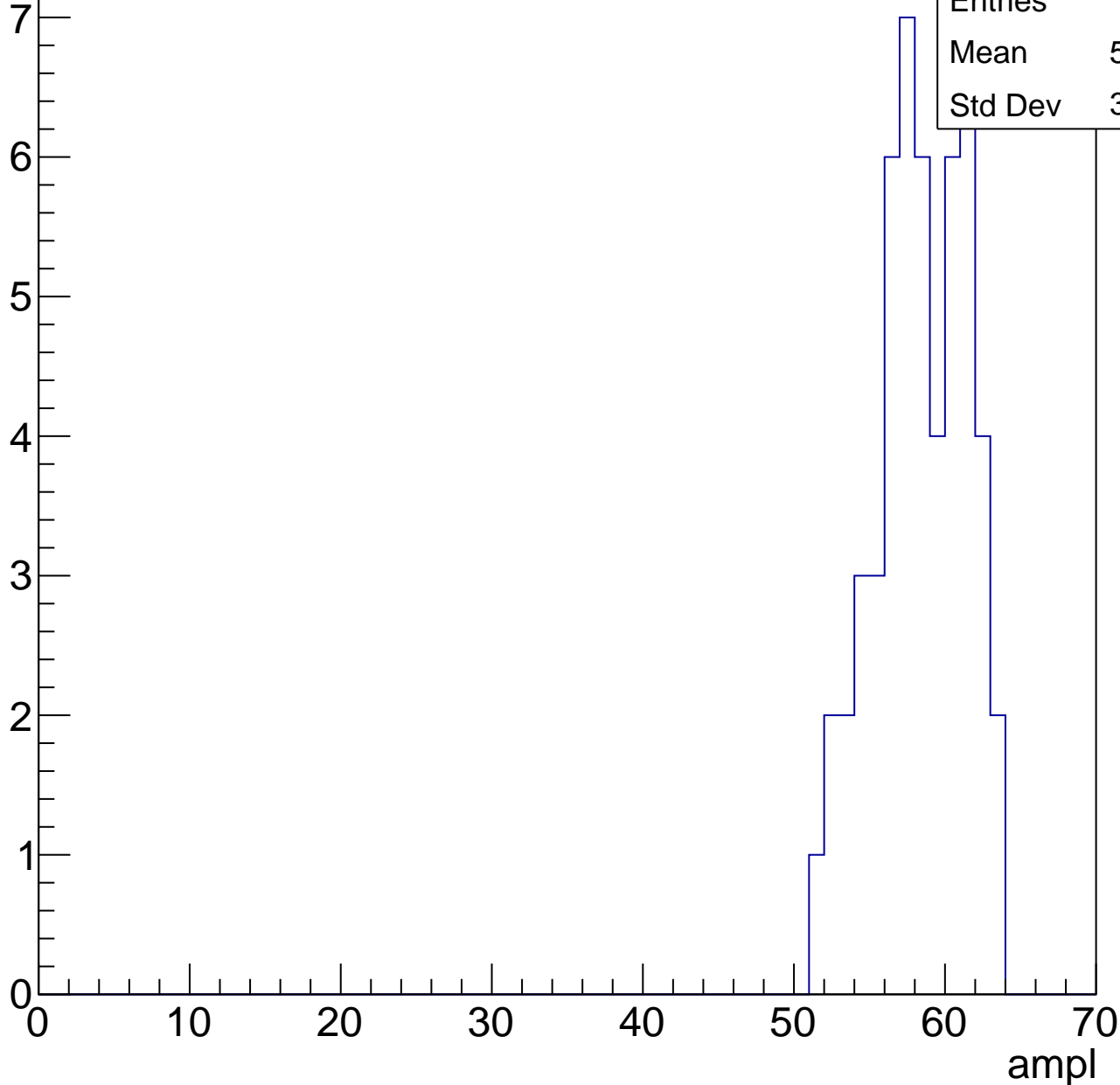


Entries	77
Mean	52.25
Std Dev	3.311

B0L002S, U7-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

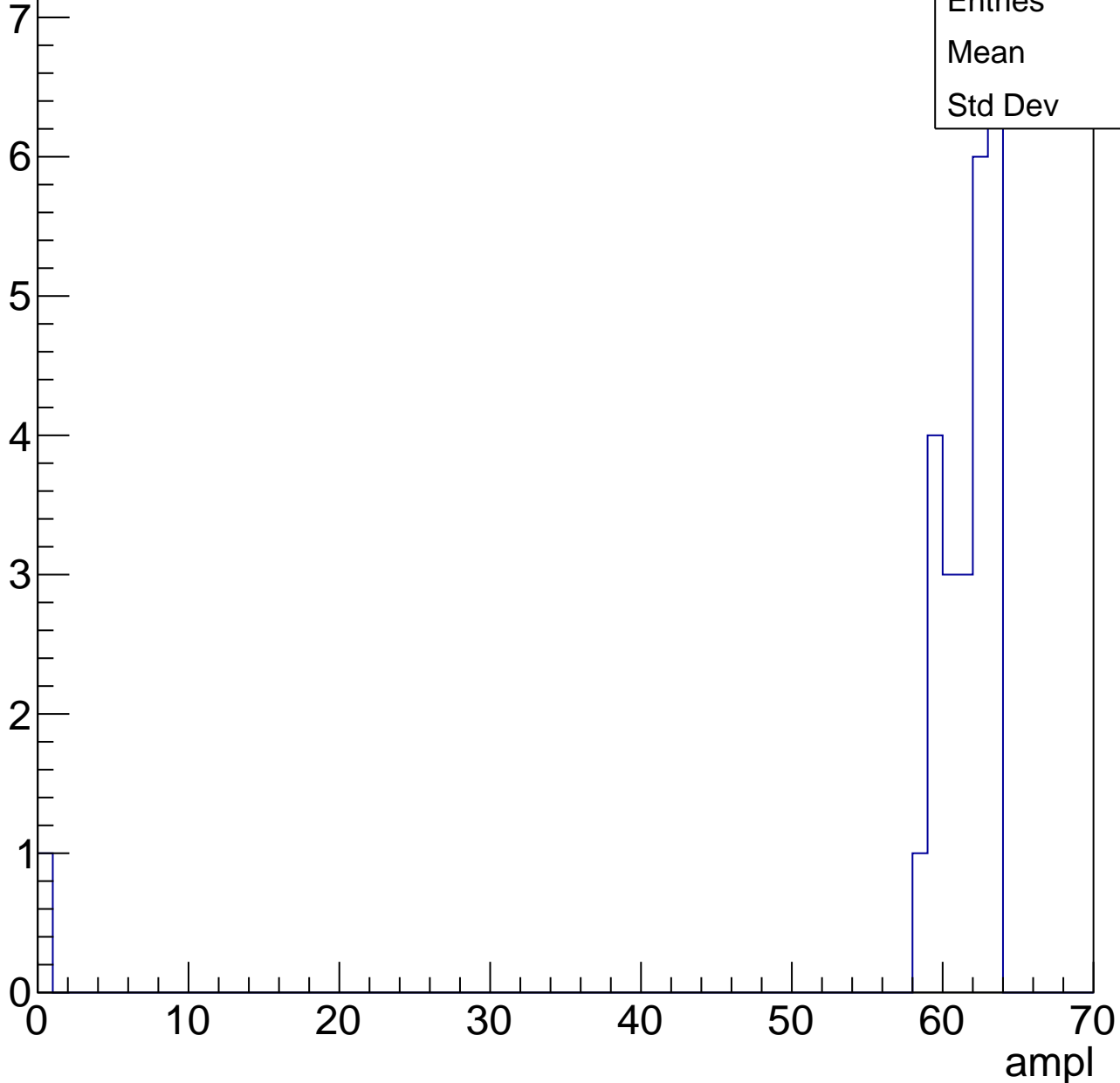


B0L002S, U7-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.8
Std Dev	12.1



B0L002S, U7-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch32, adc0

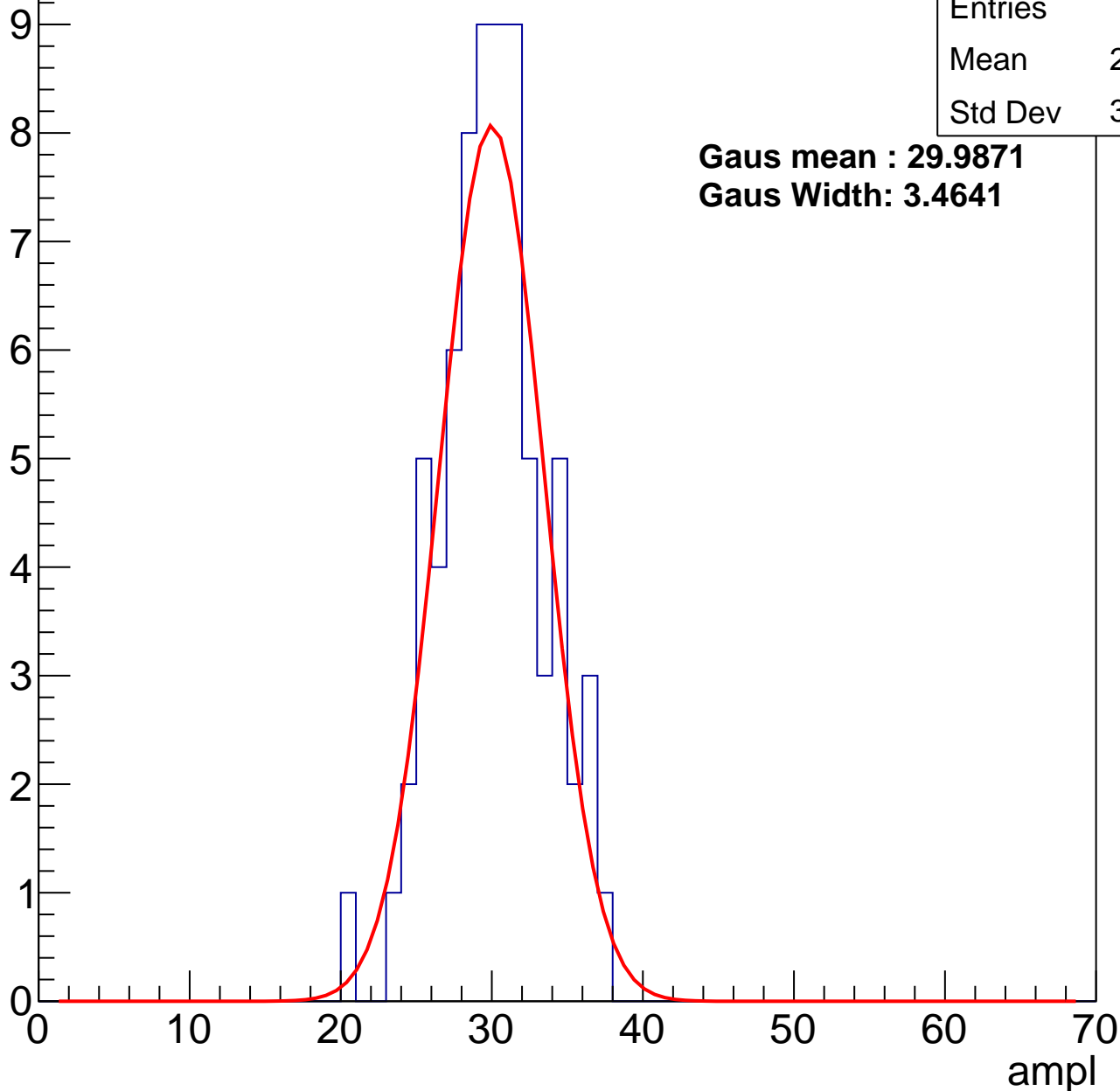
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.59
Std Dev	3.383

Gaus mean : 29.9871

Gaus Width: 3.4641



B0L002S, U7-ch32, adc1

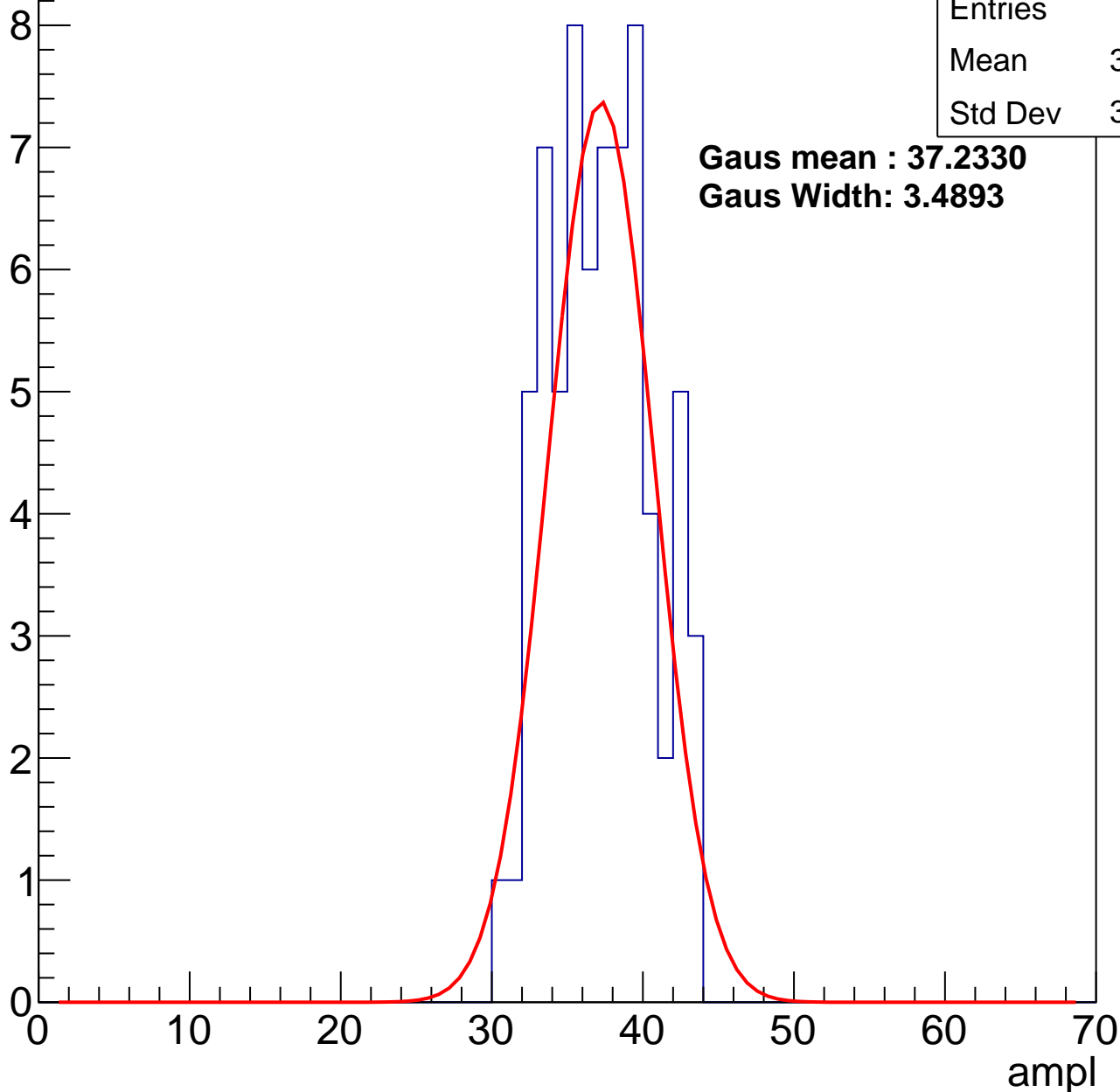
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.75
Std Dev	3.277

Gaus mean : 37.2330

Gaus Width: 3.4893



B0L002S, U7-ch32, adc2

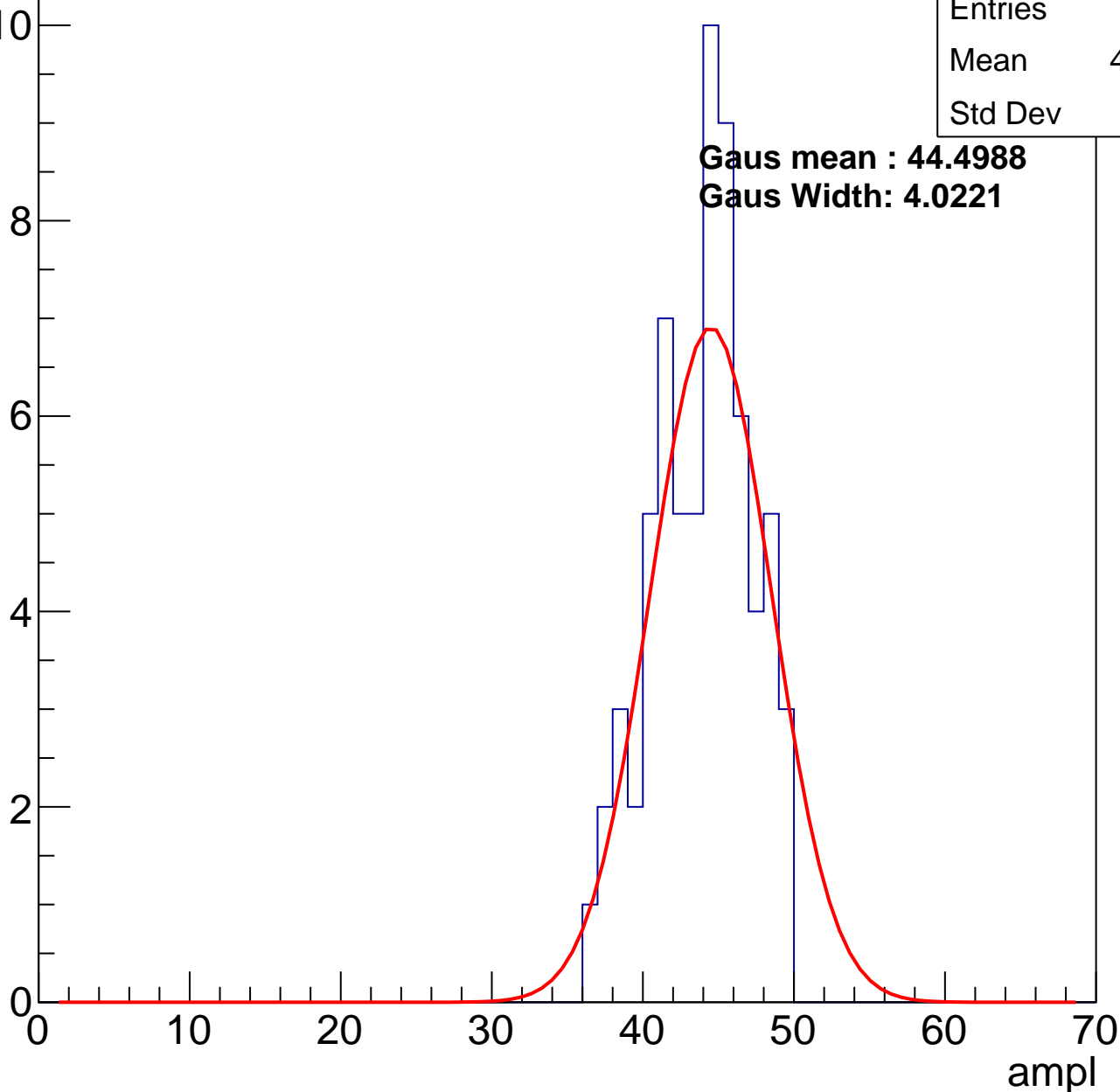
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.43
Std Dev	3.21

Gaus mean : 44.4988

Gaus Width: 4.0221

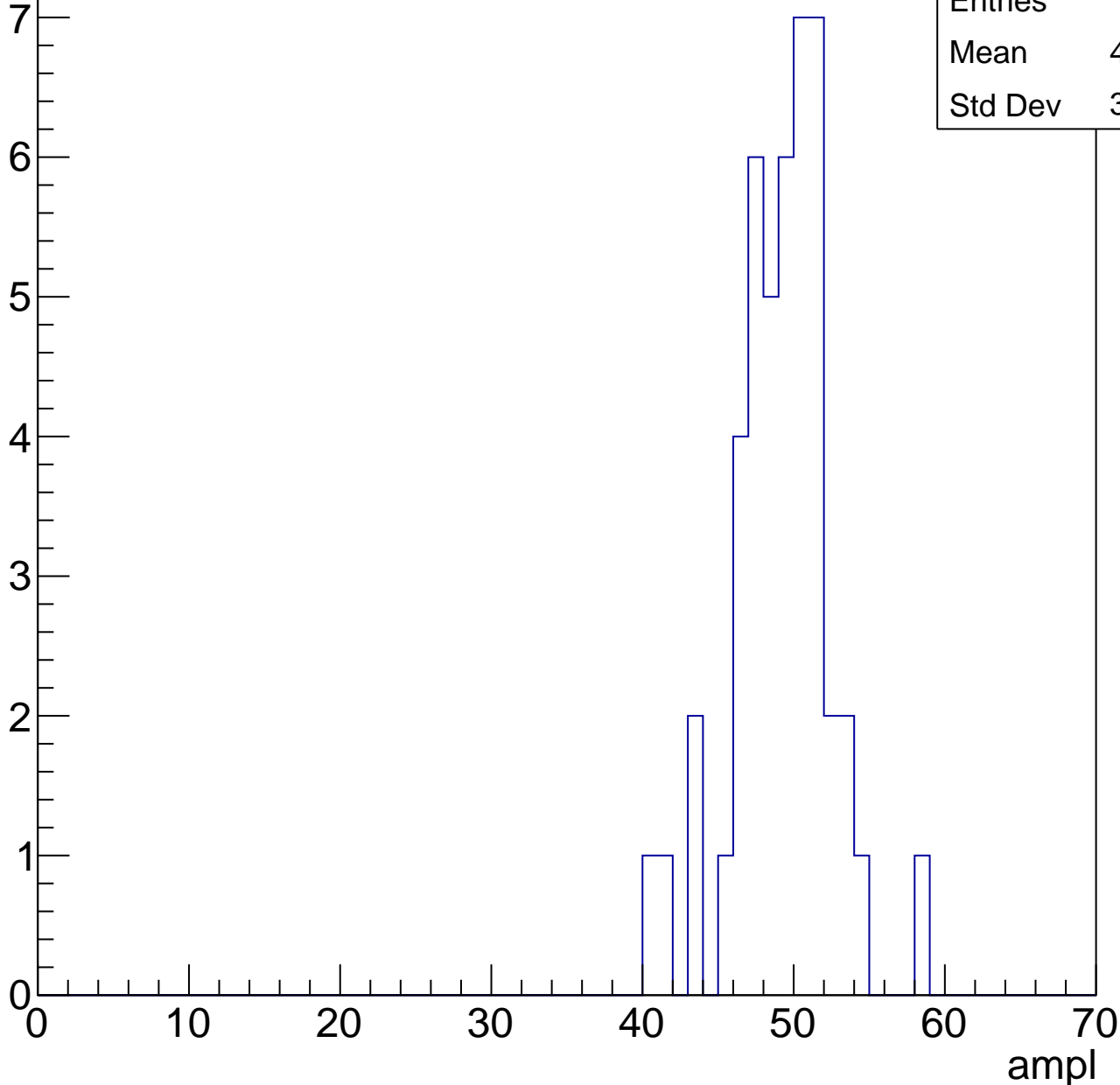


B0L002S, U7-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	48.72
Std Dev	3.248

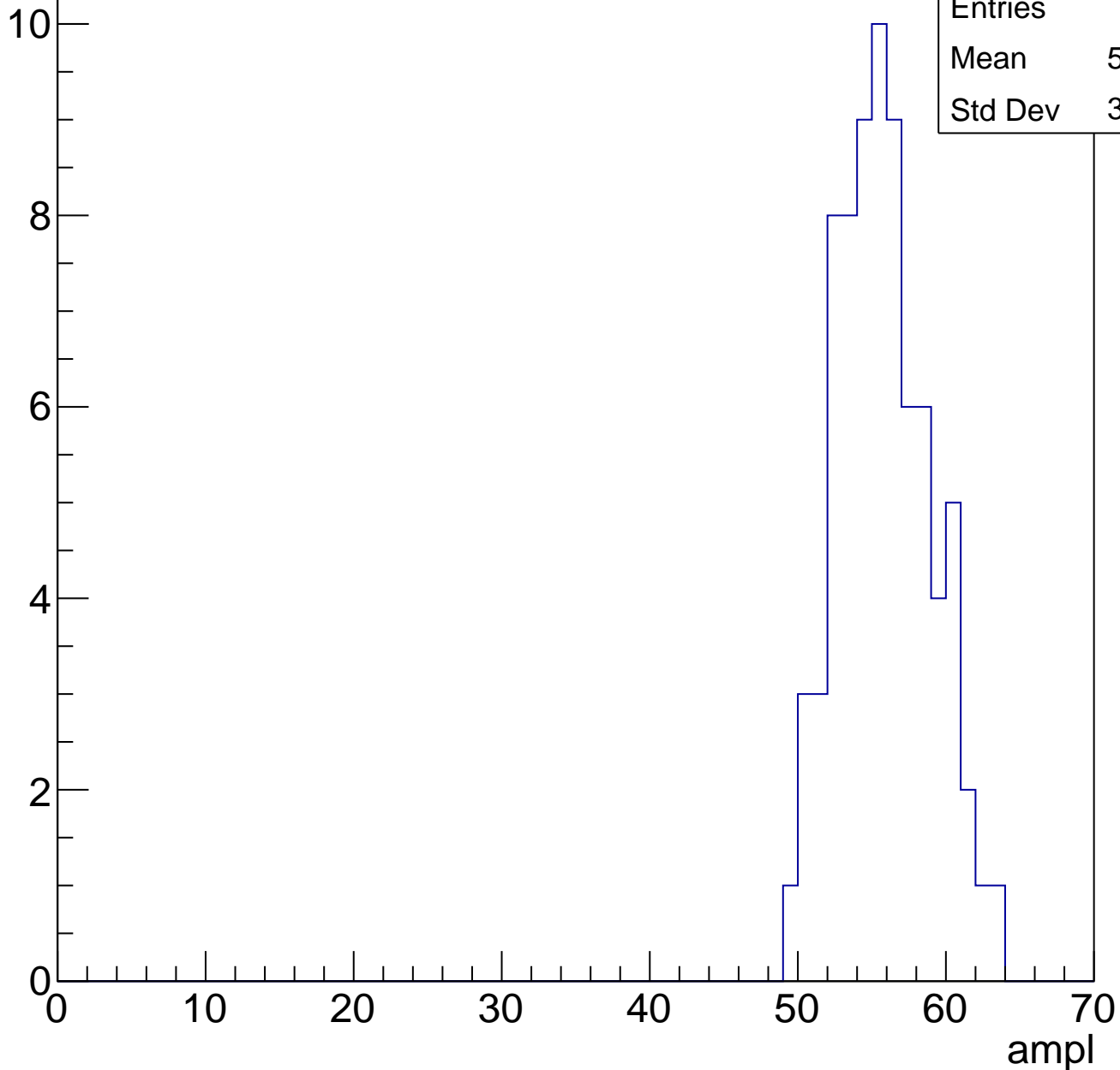


B0L002S, U7-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	55.33
Std Dev	3.088

Entry

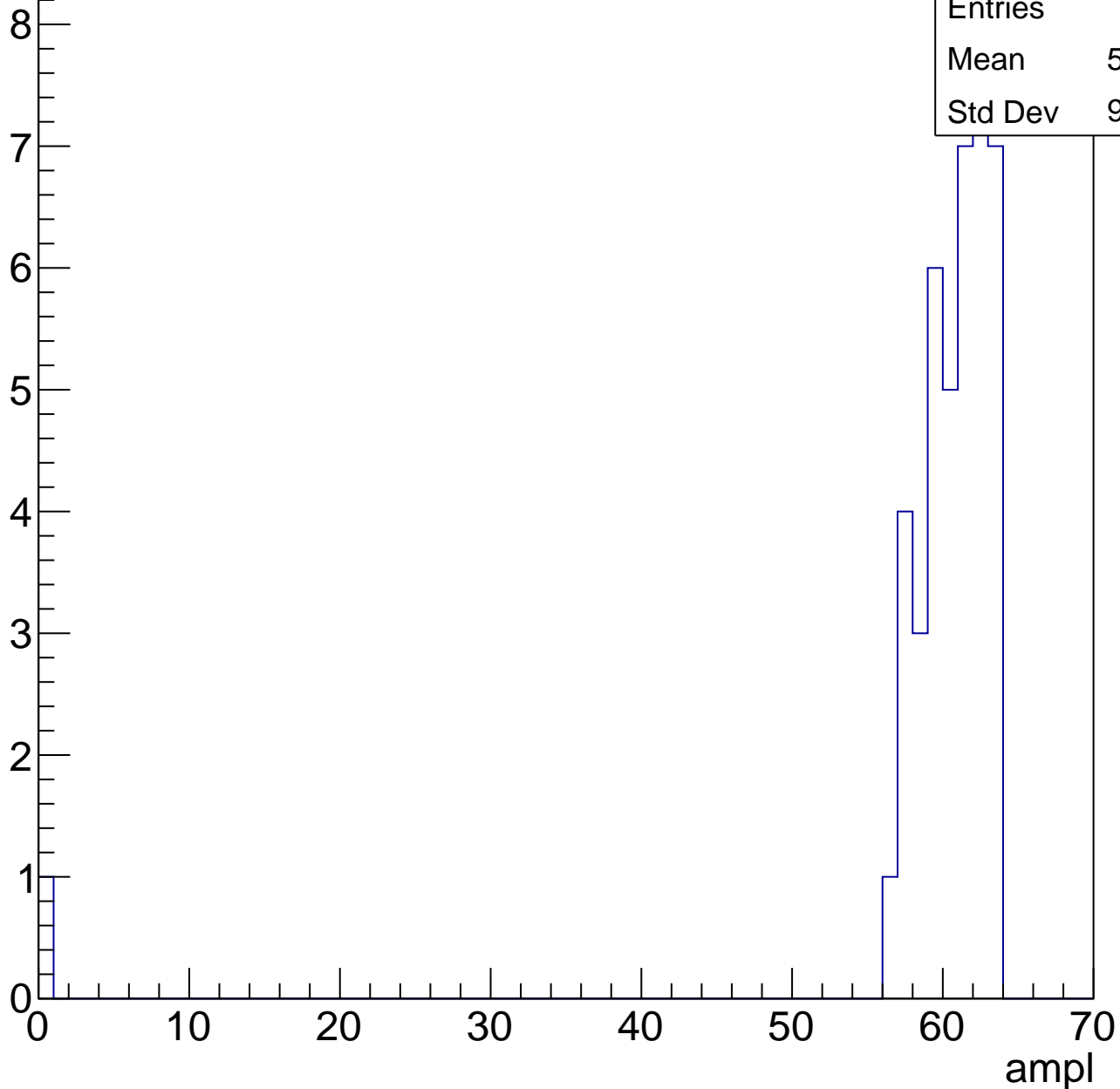


B0L002S, U7-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.95
Std Dev	9.419



B0L002S, U7-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B0L002S, U7-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch33, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	28.31
Std Dev	5.621

Gaus mean : 29.4783

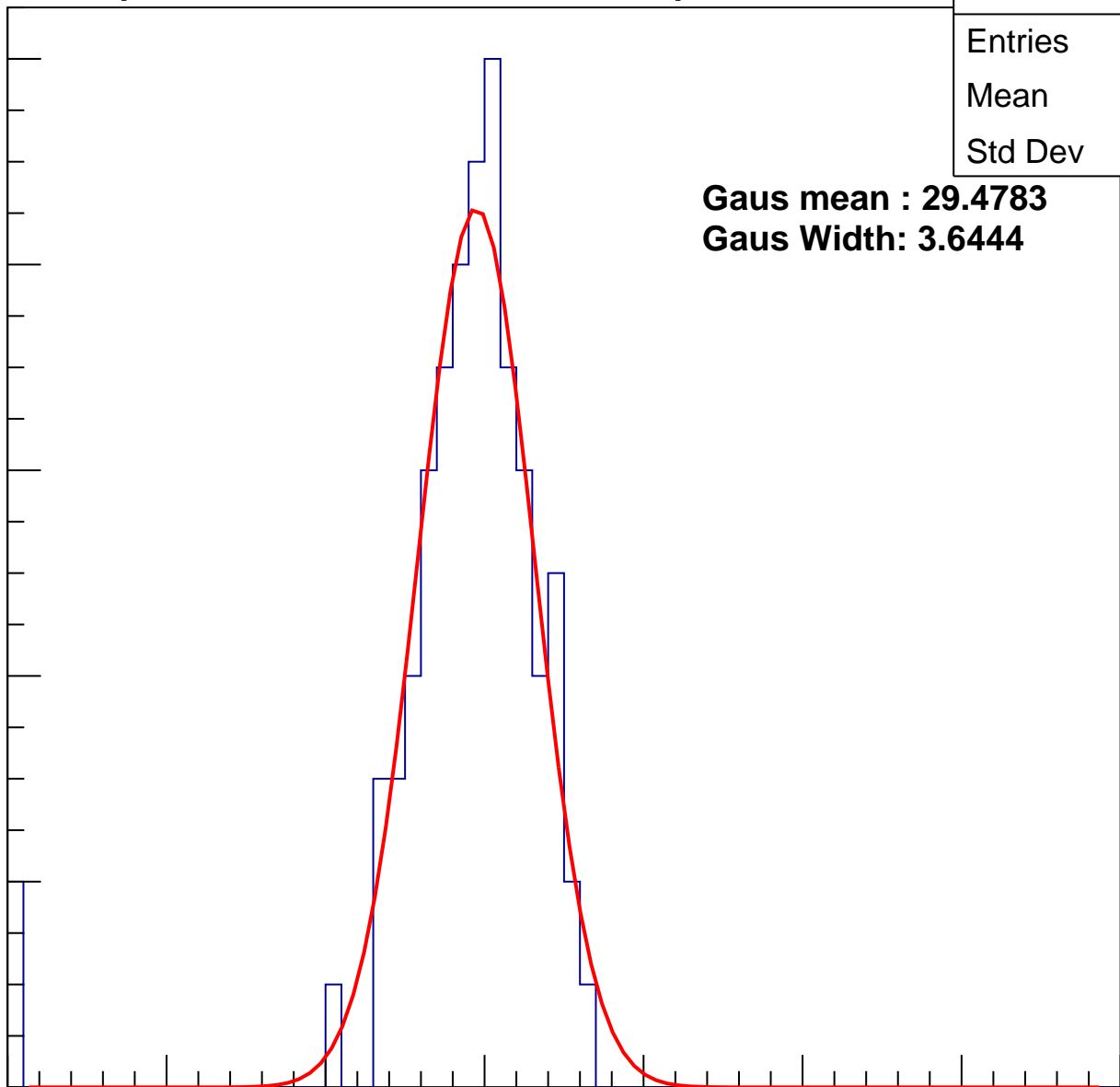
Gaus Width: 3.6444

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch33, adc1

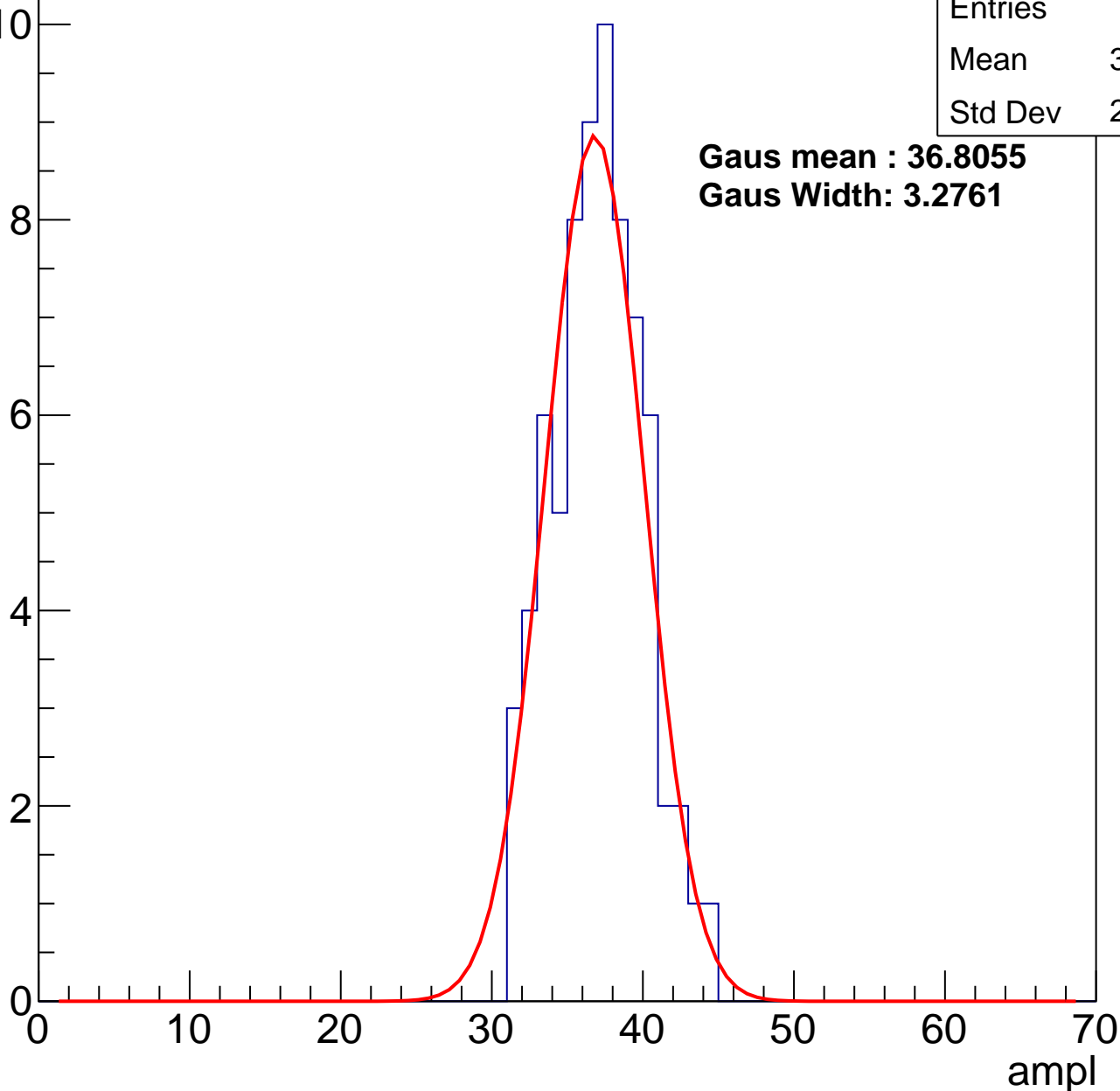
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.57
Std Dev	2.967

Gaus mean : 36.8055

Gaus Width: 3.2761



B0L002S, U7-ch33, adc2

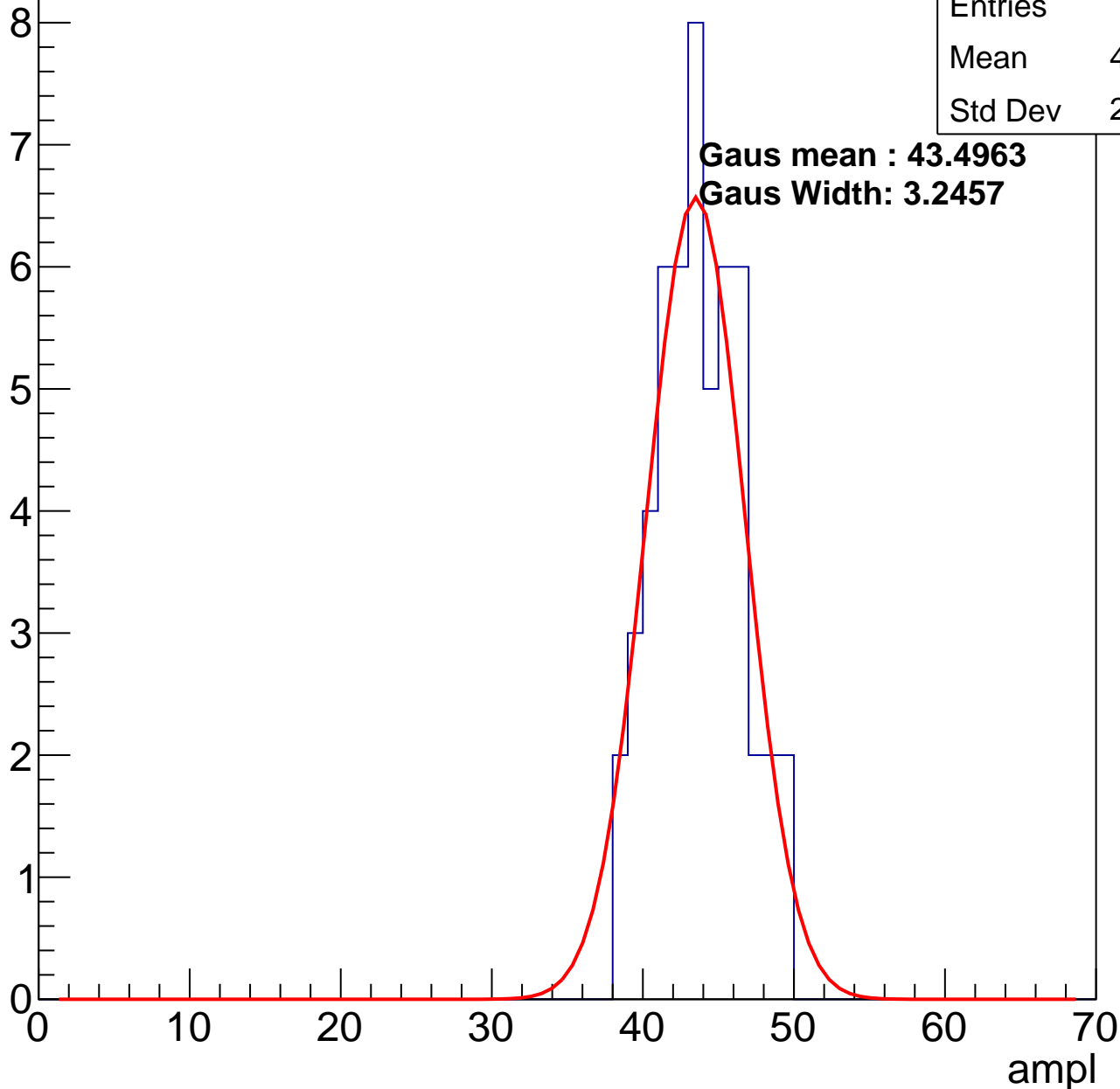
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	43.25
Std Dev	2.766

Gaus mean : 43.4963

Gaus Width: 3.2457

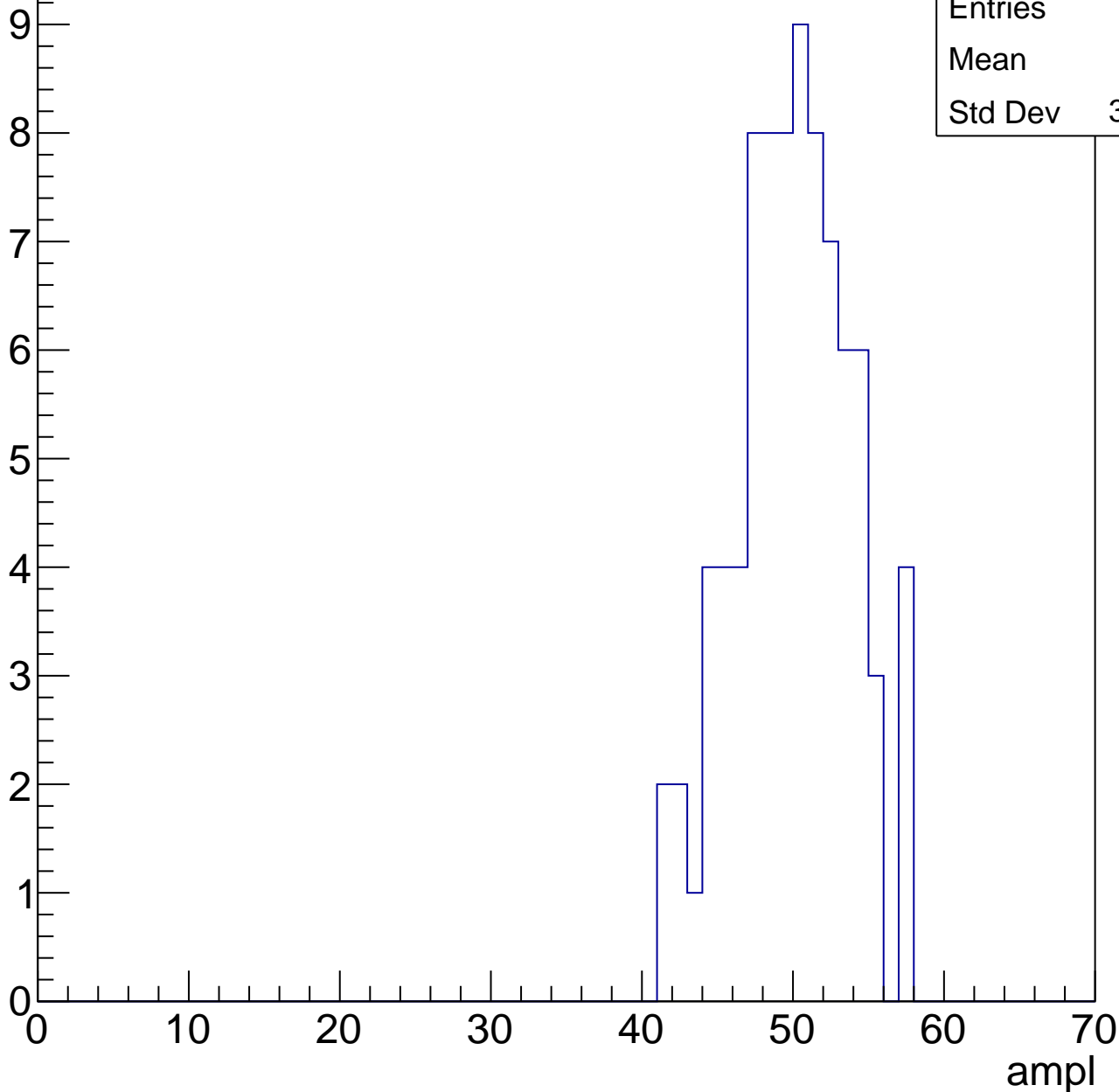


B0L002S, U7-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	49.5
Std Dev	3.772

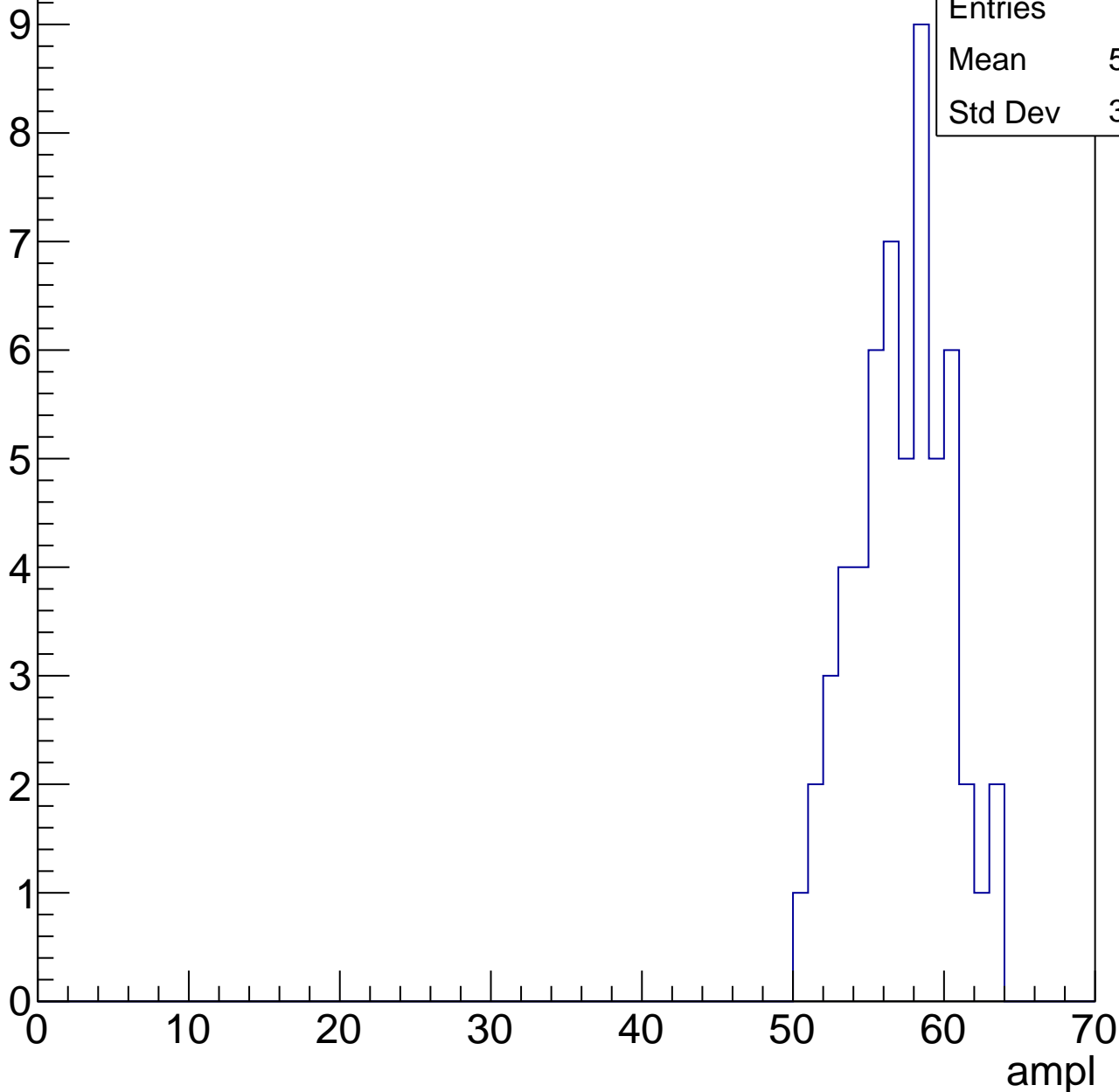


B0L002S, U7-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.67
Std Dev	3.057

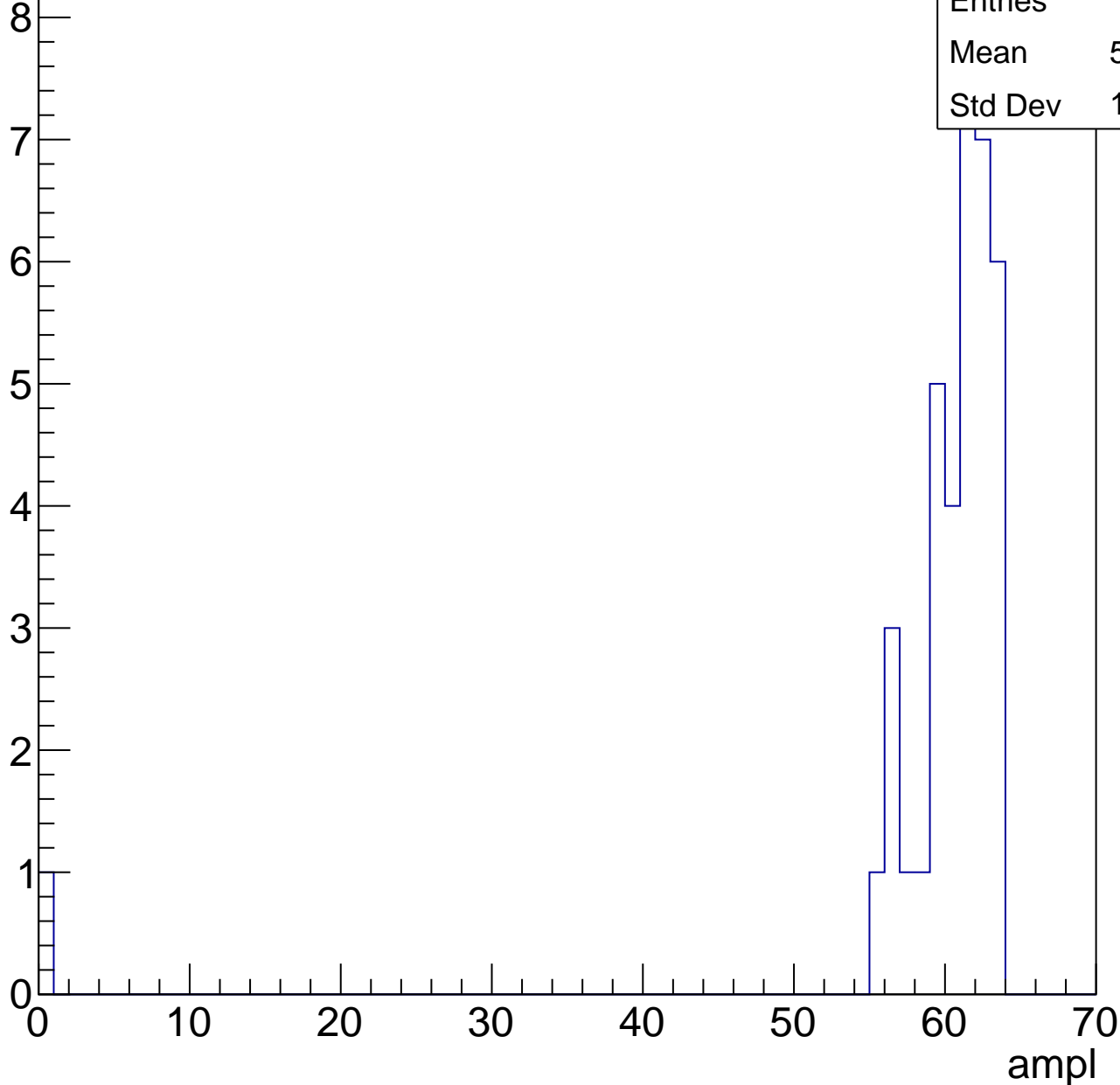


B0L002S, U7-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.73
Std Dev	10.03



B0L002S, U7-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.5
Std Dev	0.5

B0L002S, U7-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

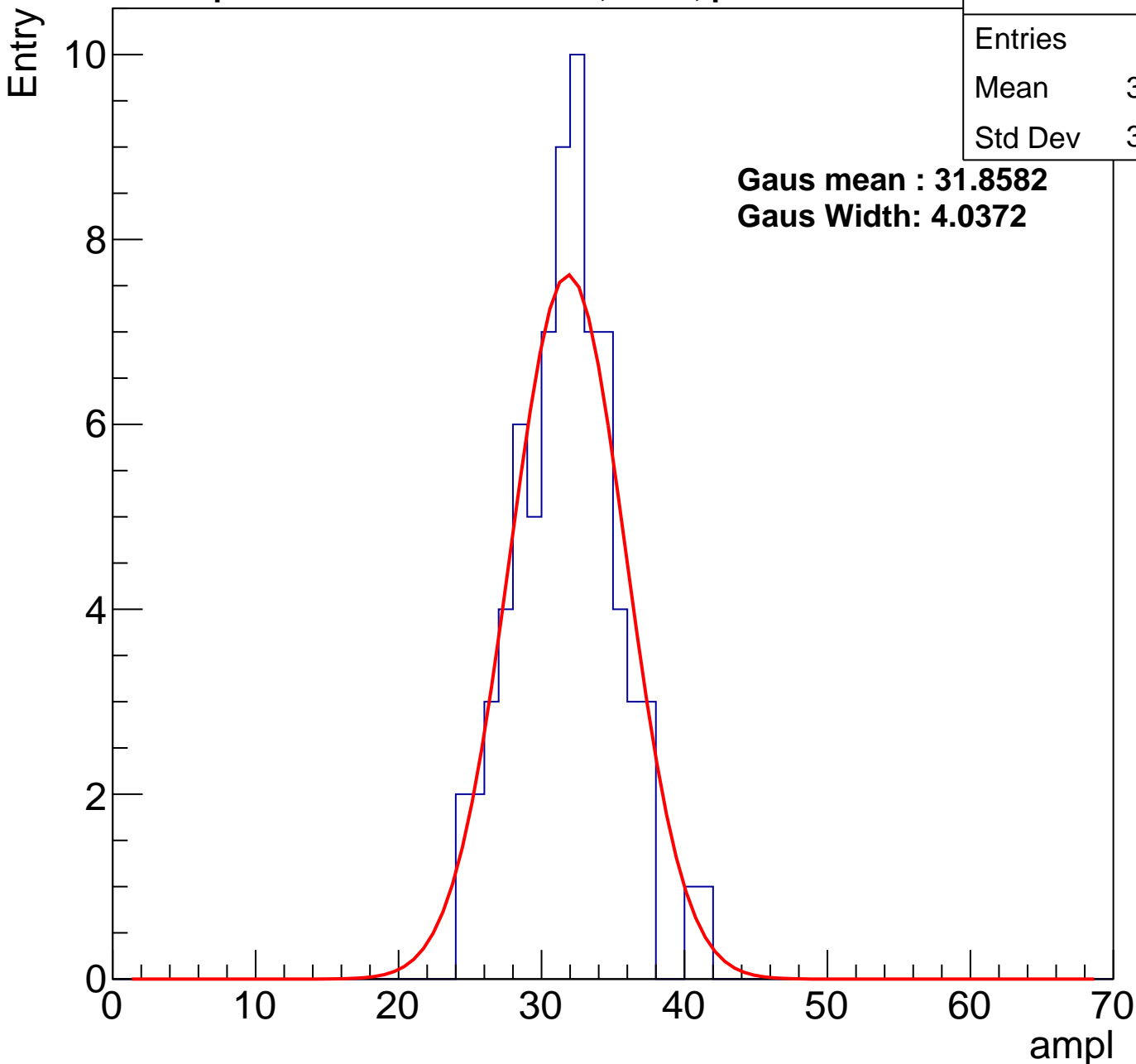
B0L002S, U7-ch34, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	31.28
Std Dev	3.513

Gaus mean : 31.8582

Gaus Width: 4.0372



B0L002S, U7-ch34, adc1

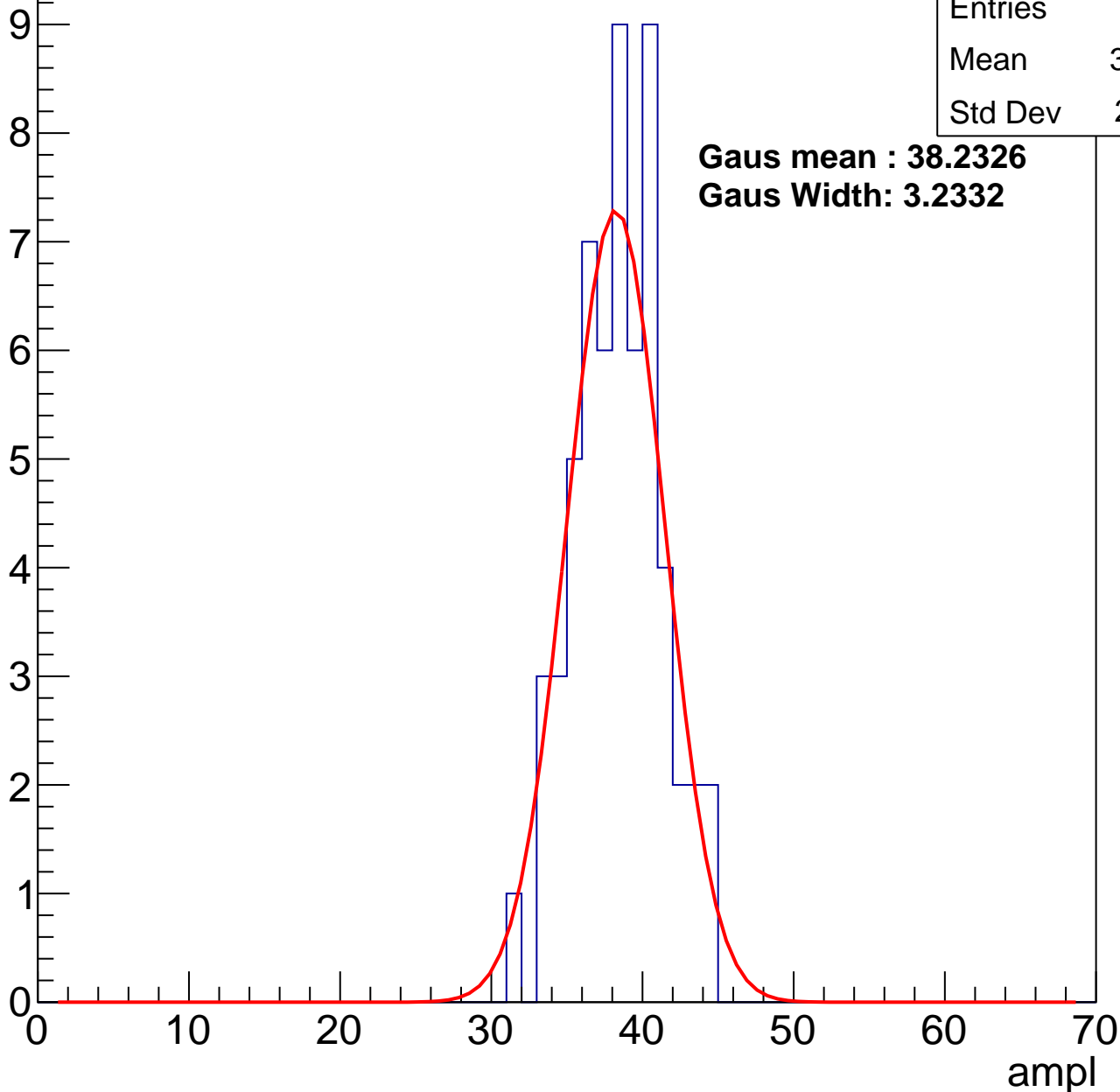
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.95
Std Dev	2.861

Gaus mean : 38.2326

Gaus Width: 3.2332



B0L002S, U7-ch34, adc2

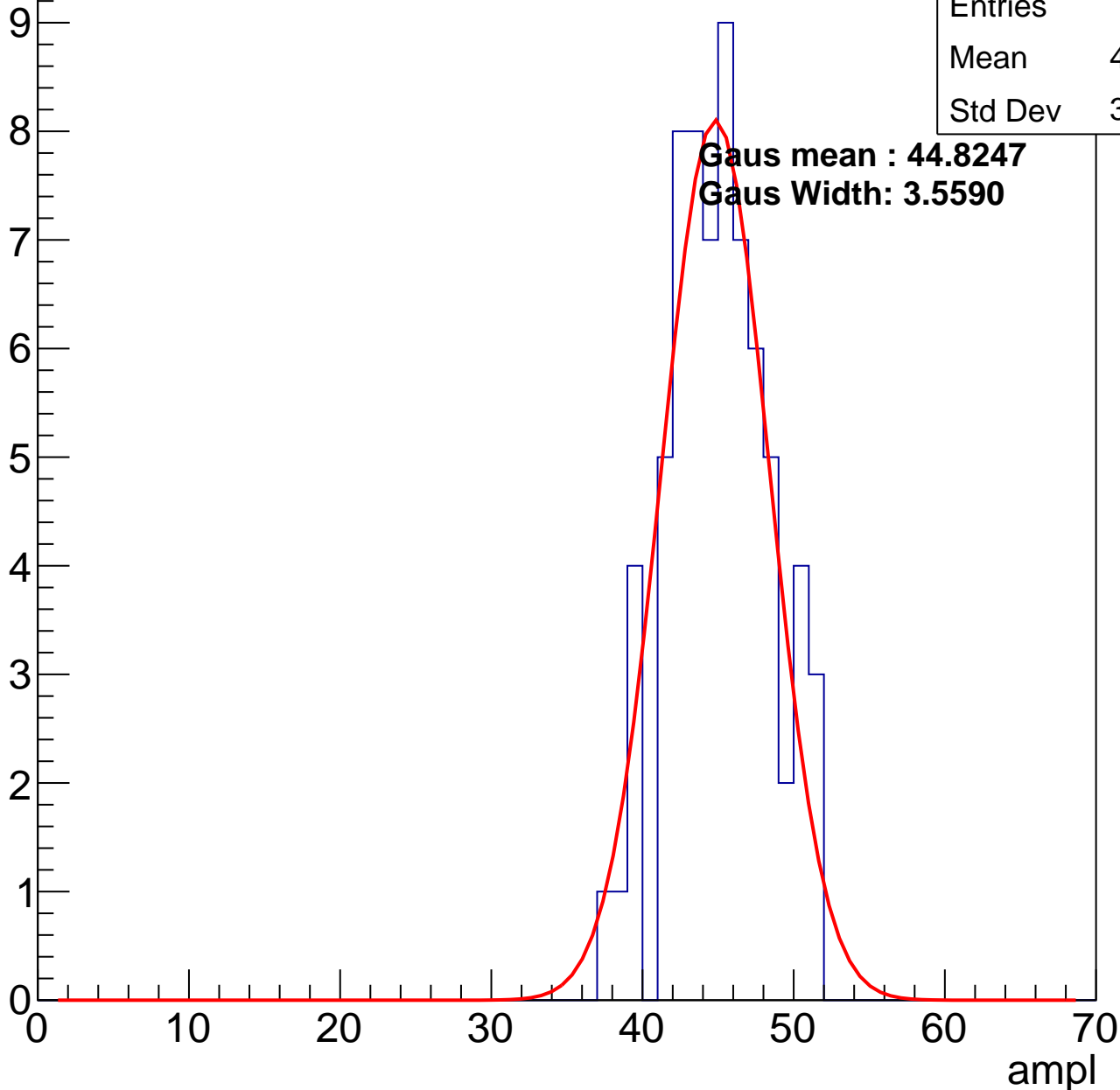
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.63
Std Dev	3.283

Gaus mean : 44.8247

Gaus Width: 3.5590



B0L002S, U7-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

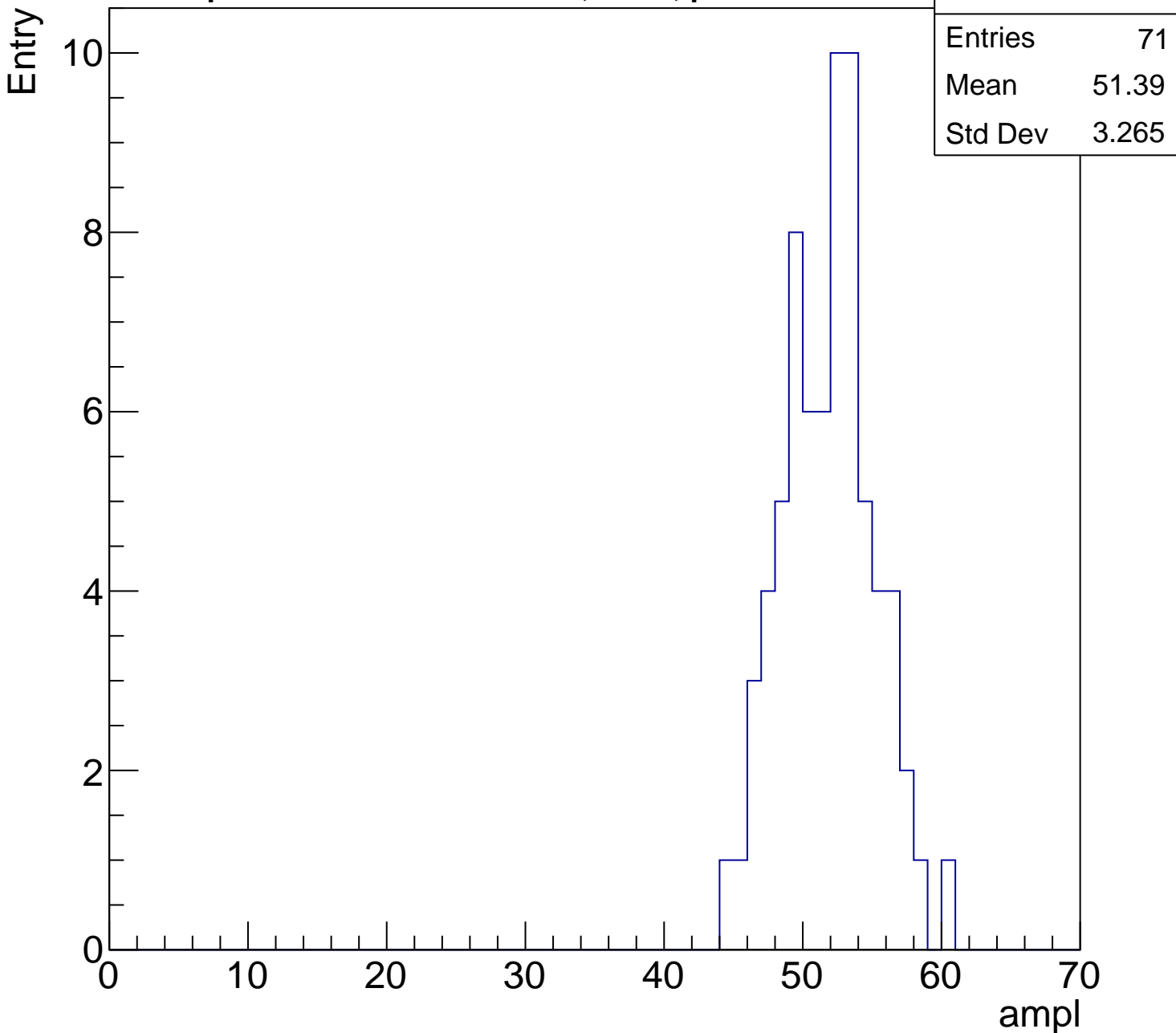
Entries	71
Mean	51.39
Std Dev	3.265

Entry

10
8
6
4
2
0

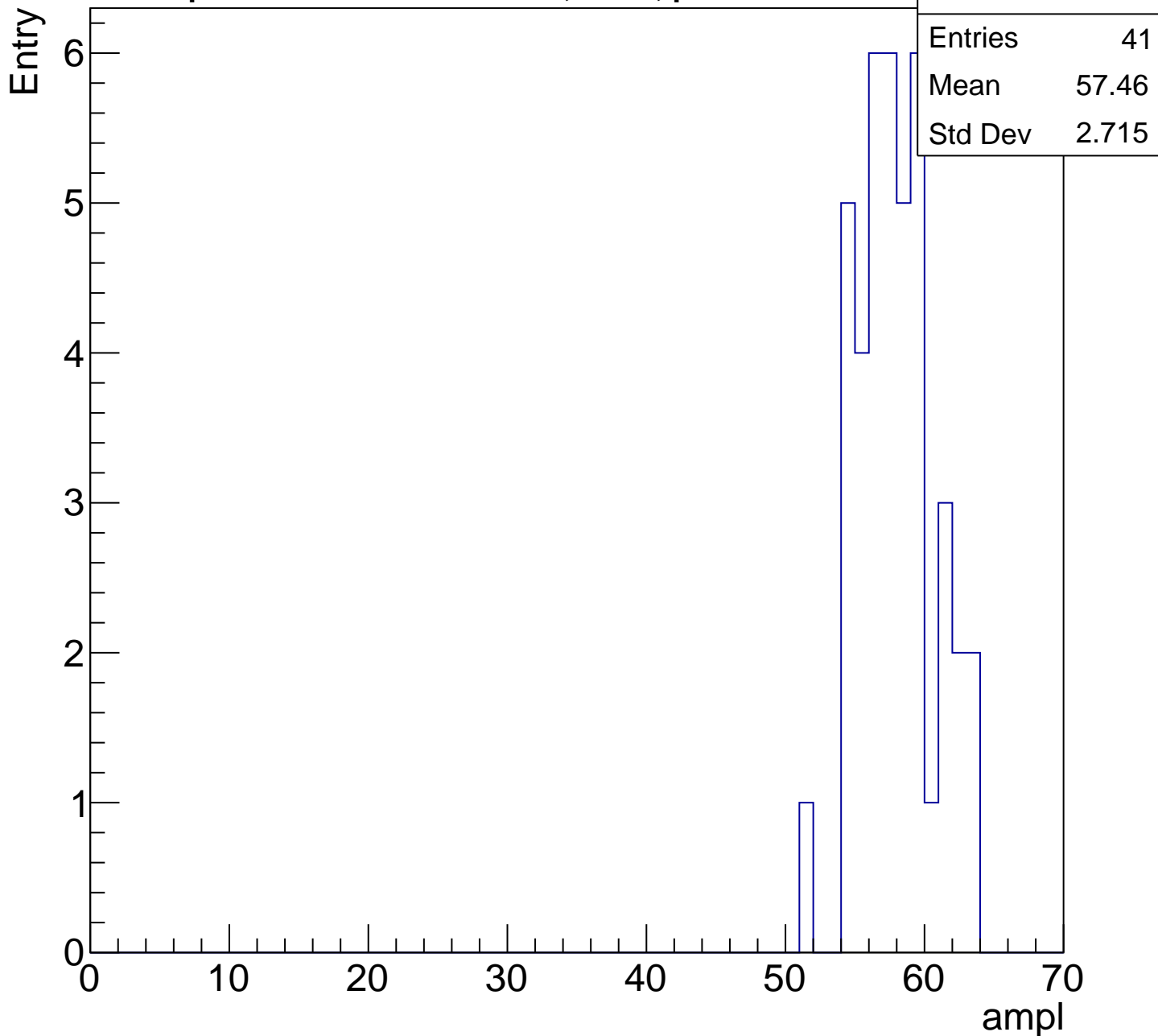
0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

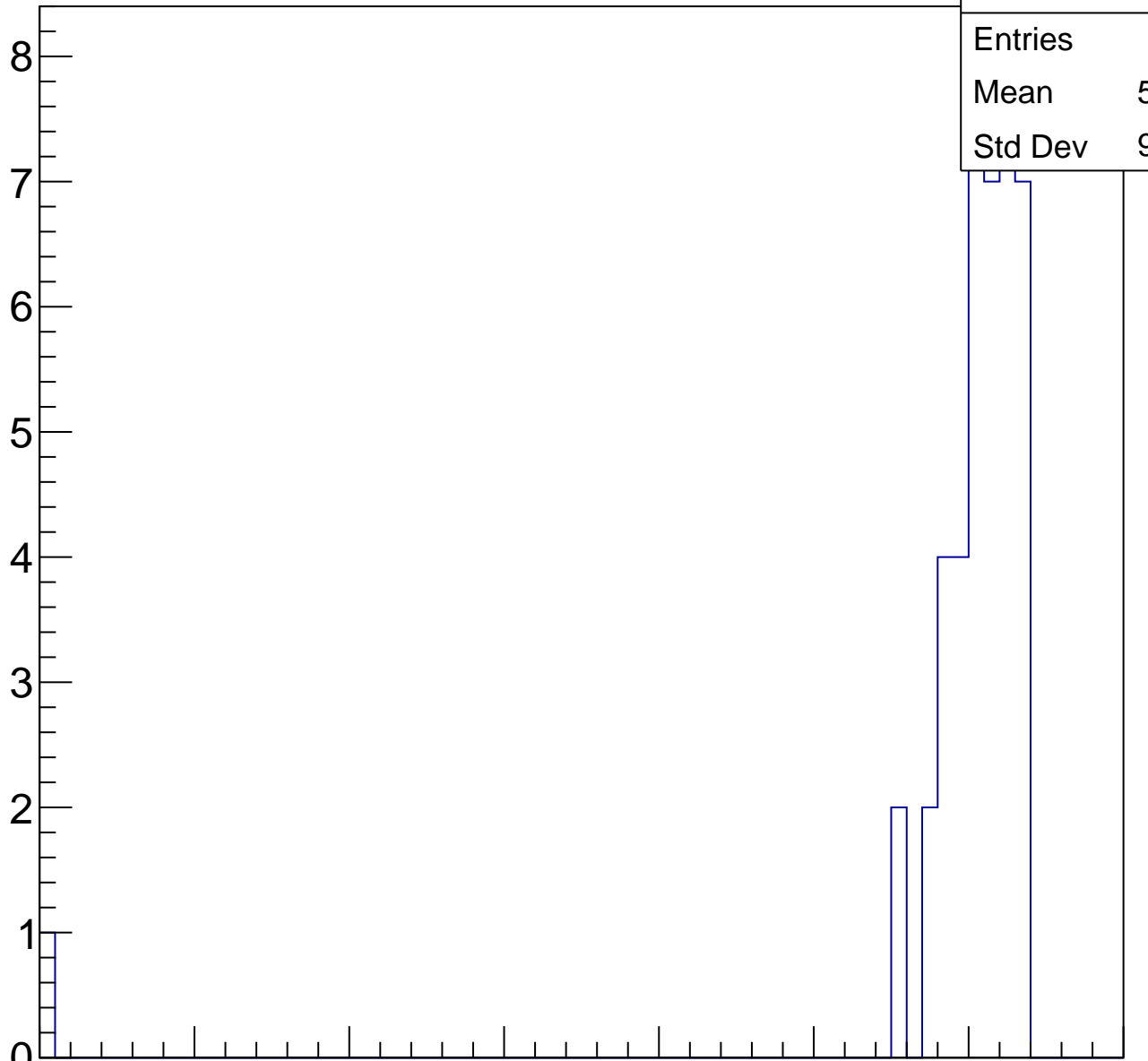
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.98
Std Dev	9.332

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U7-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch35, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	31.13
Std Dev	3.021

Gaus mean : 31.8938

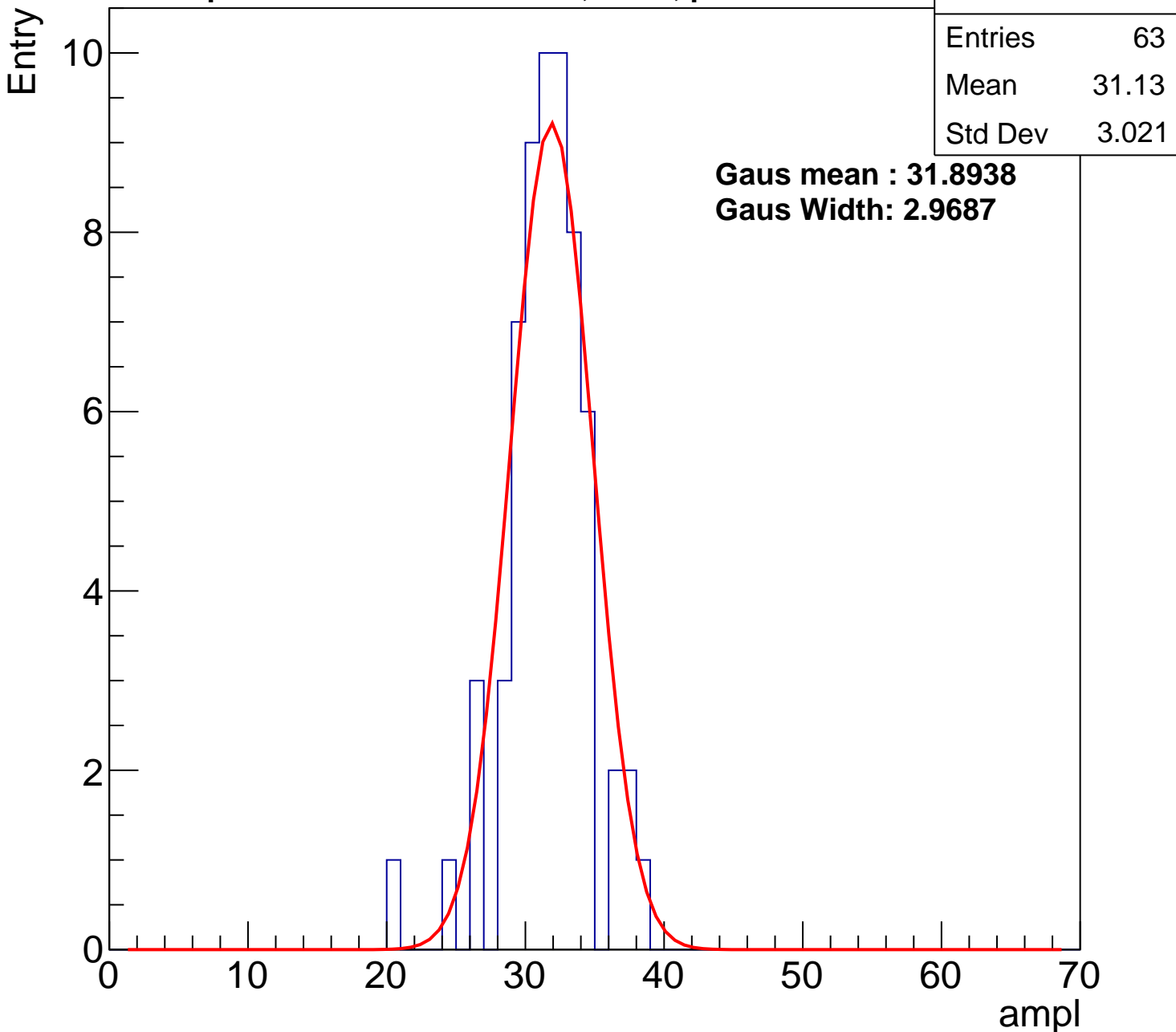
Gaus Width: 2.9687

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch35, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	50
Mean	37.54
Std Dev	2.801

Gaus mean : 37.6712

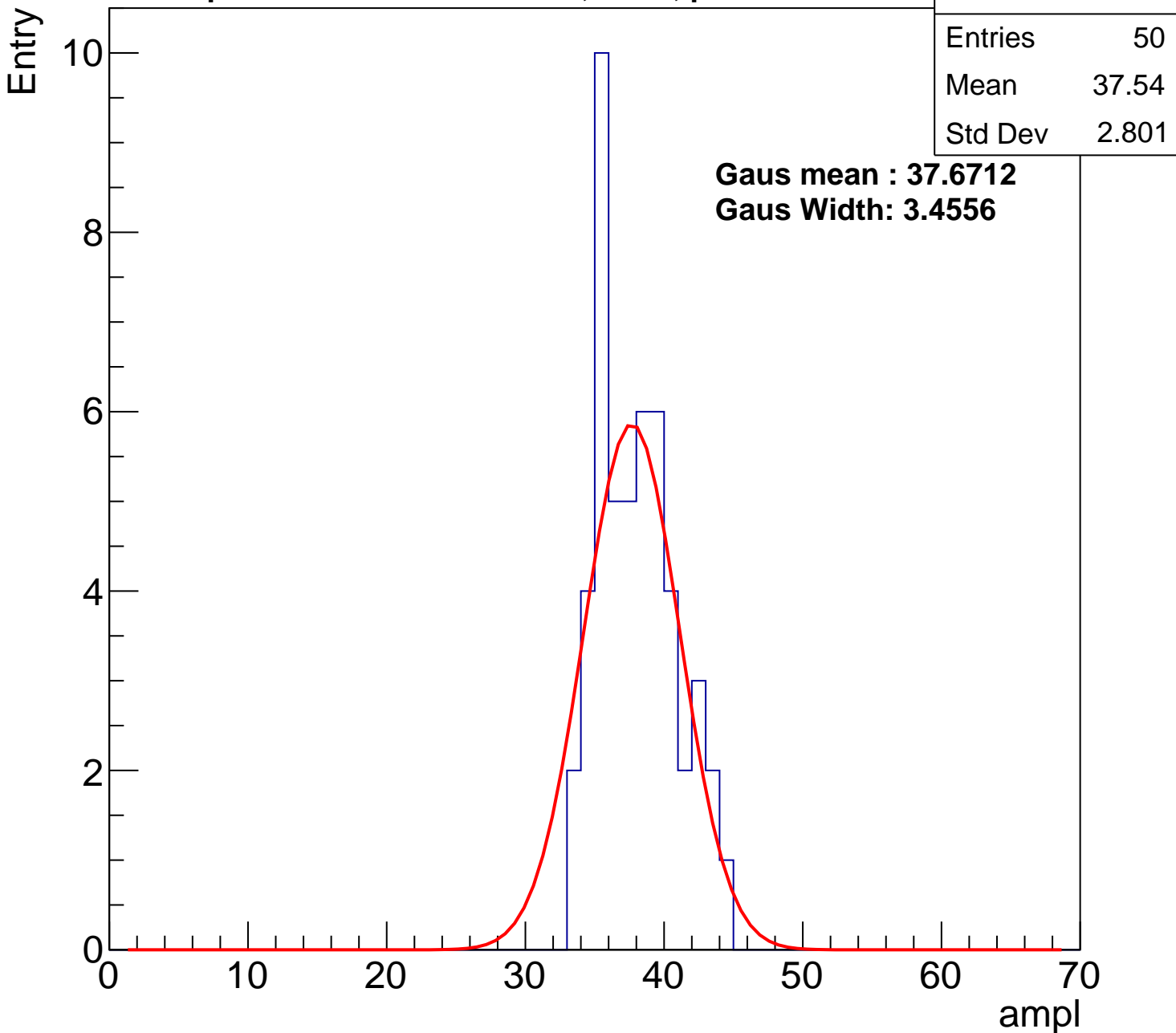
Gaus Width: 3.4556

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch35, adc2

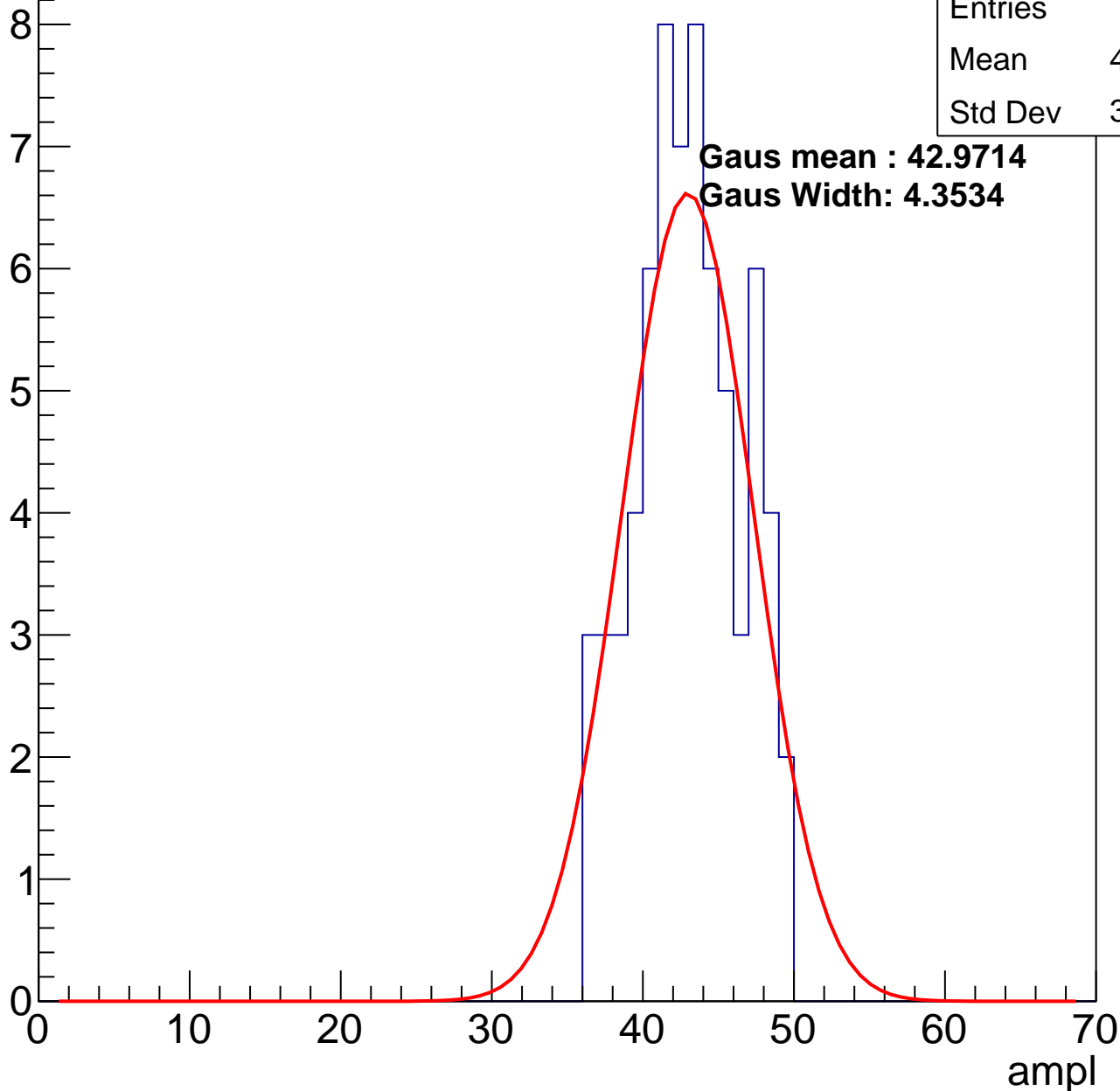
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.56
Std Dev	3.419

Gaus mean : 42.9714

Gaus Width: 4.3534

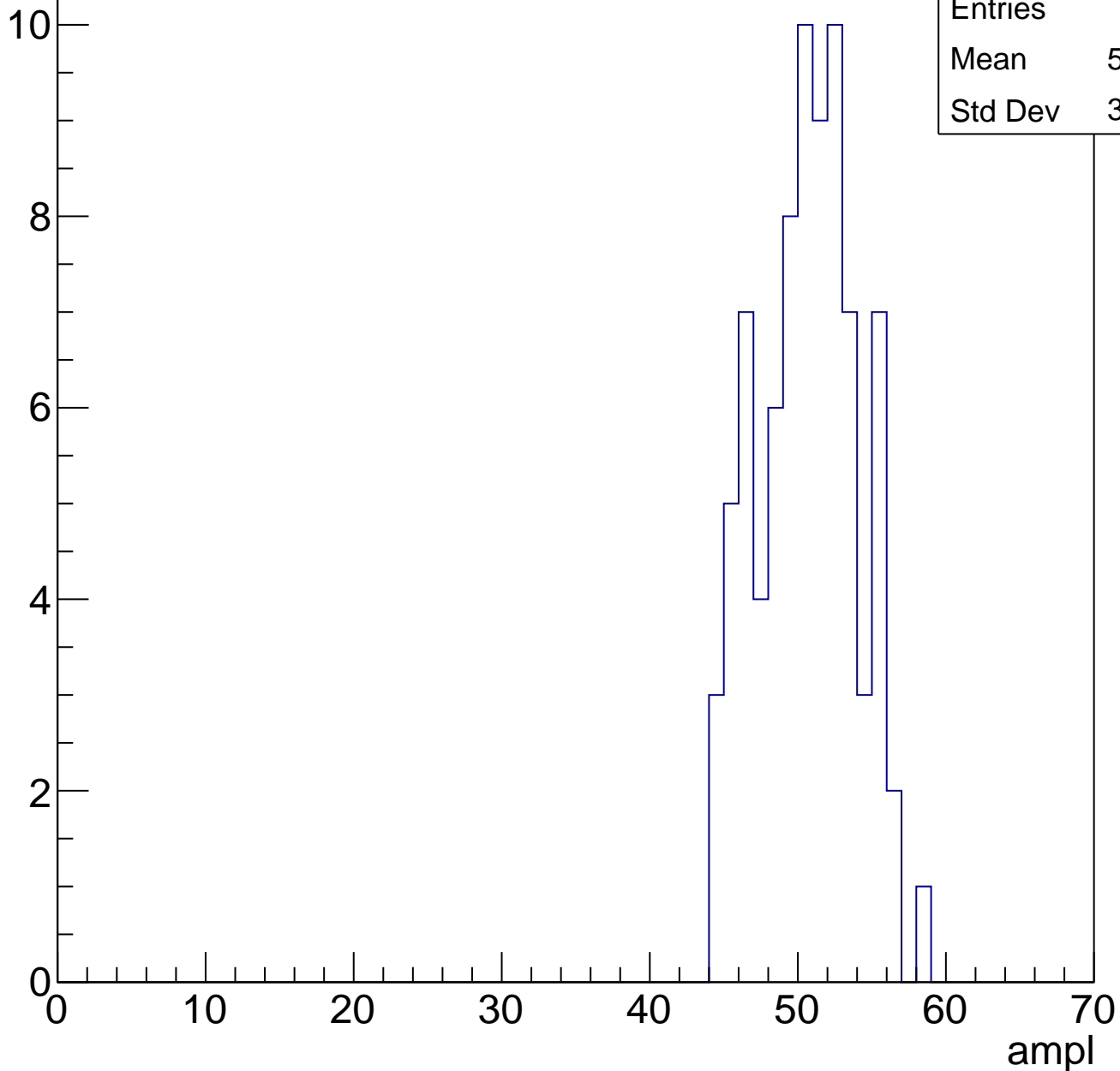


B0L002S, U7-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	50.17
Std Dev	3.279

Entry

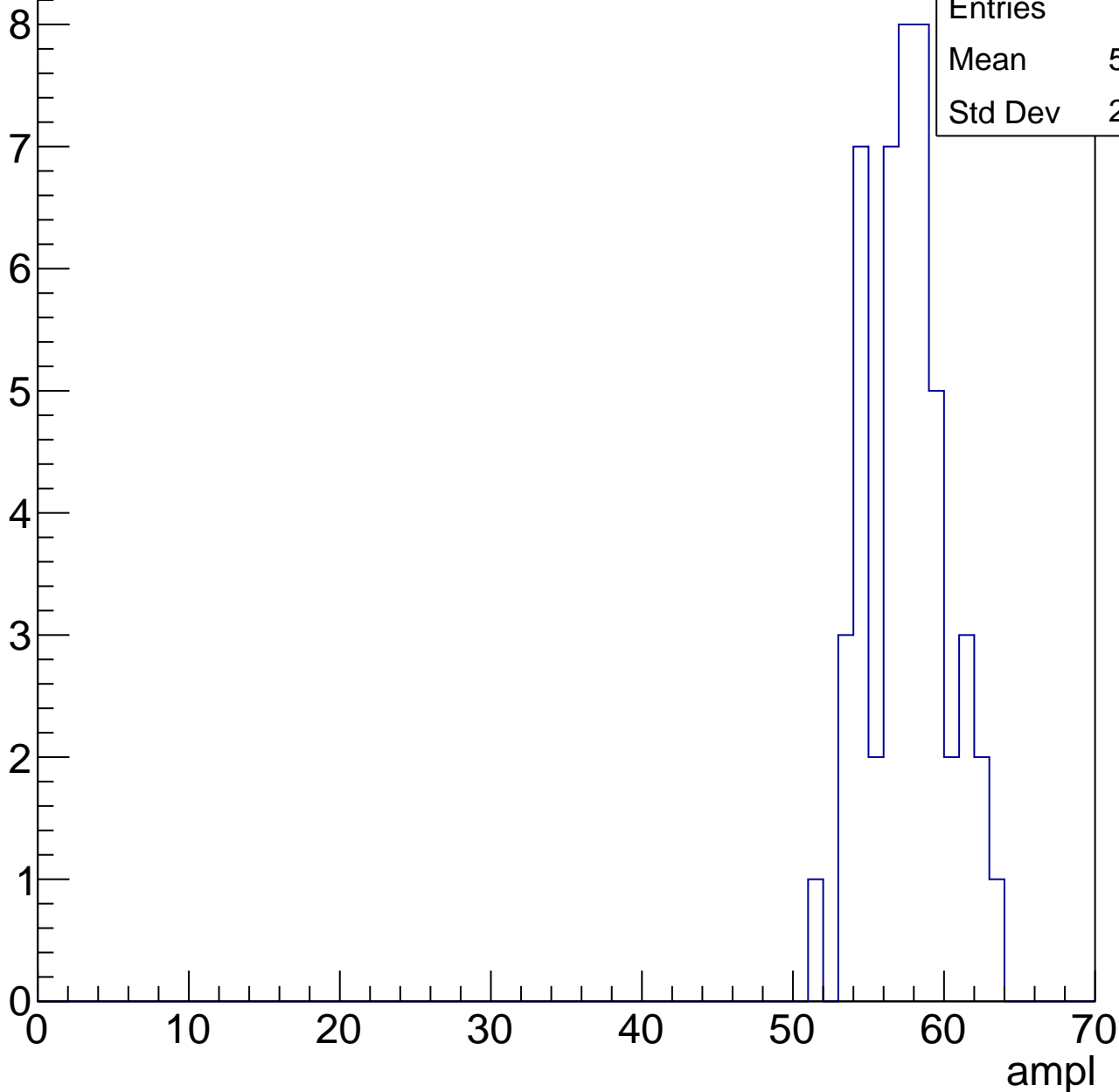


B0L002S, U7-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	57.04
Std Dev	2.642



B0L002S, U7-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 35

Mean 58.71

Std Dev 10.27

ampl

0

10

20

30

40

50

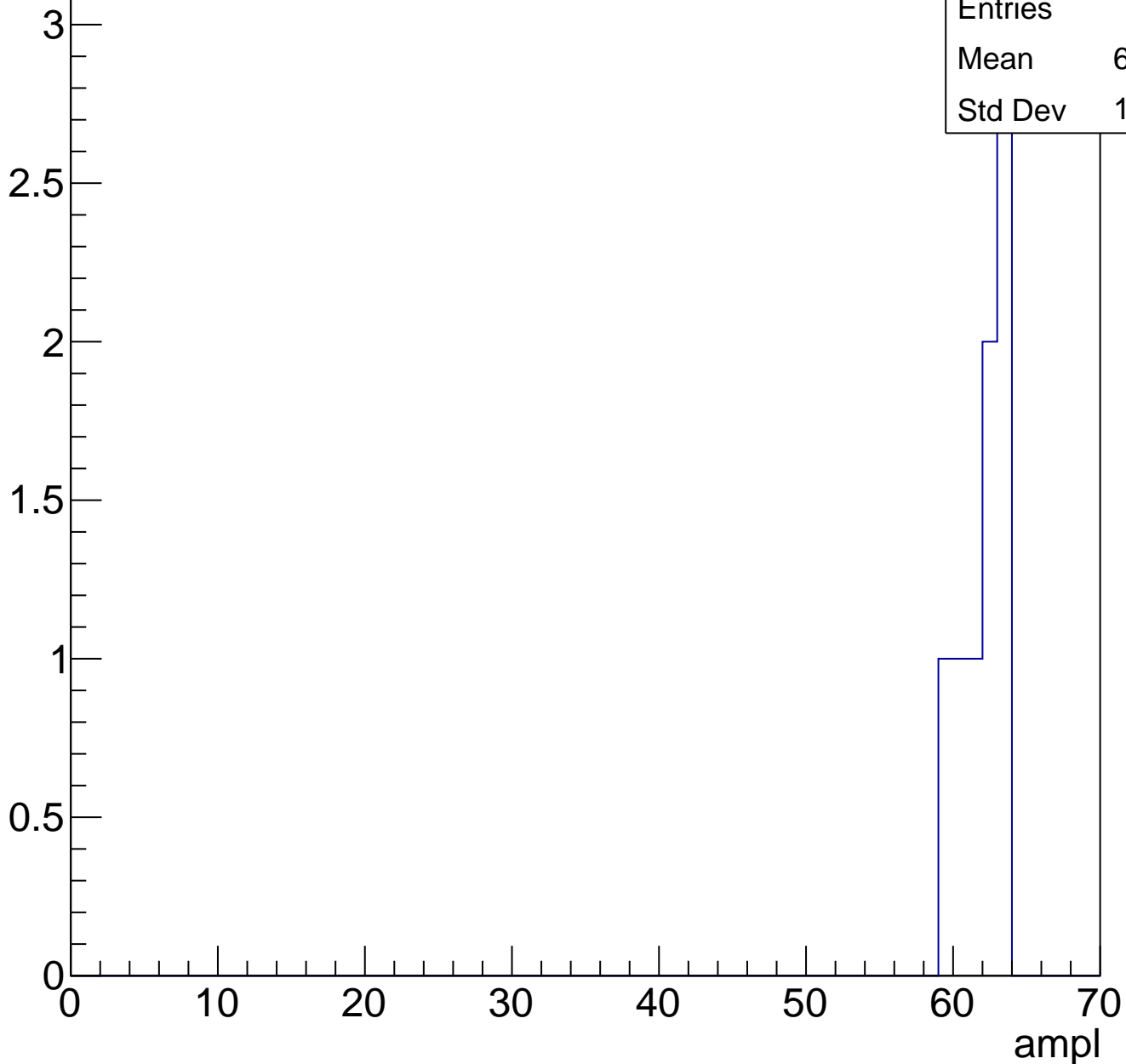
60

70

B0L002S, U7-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

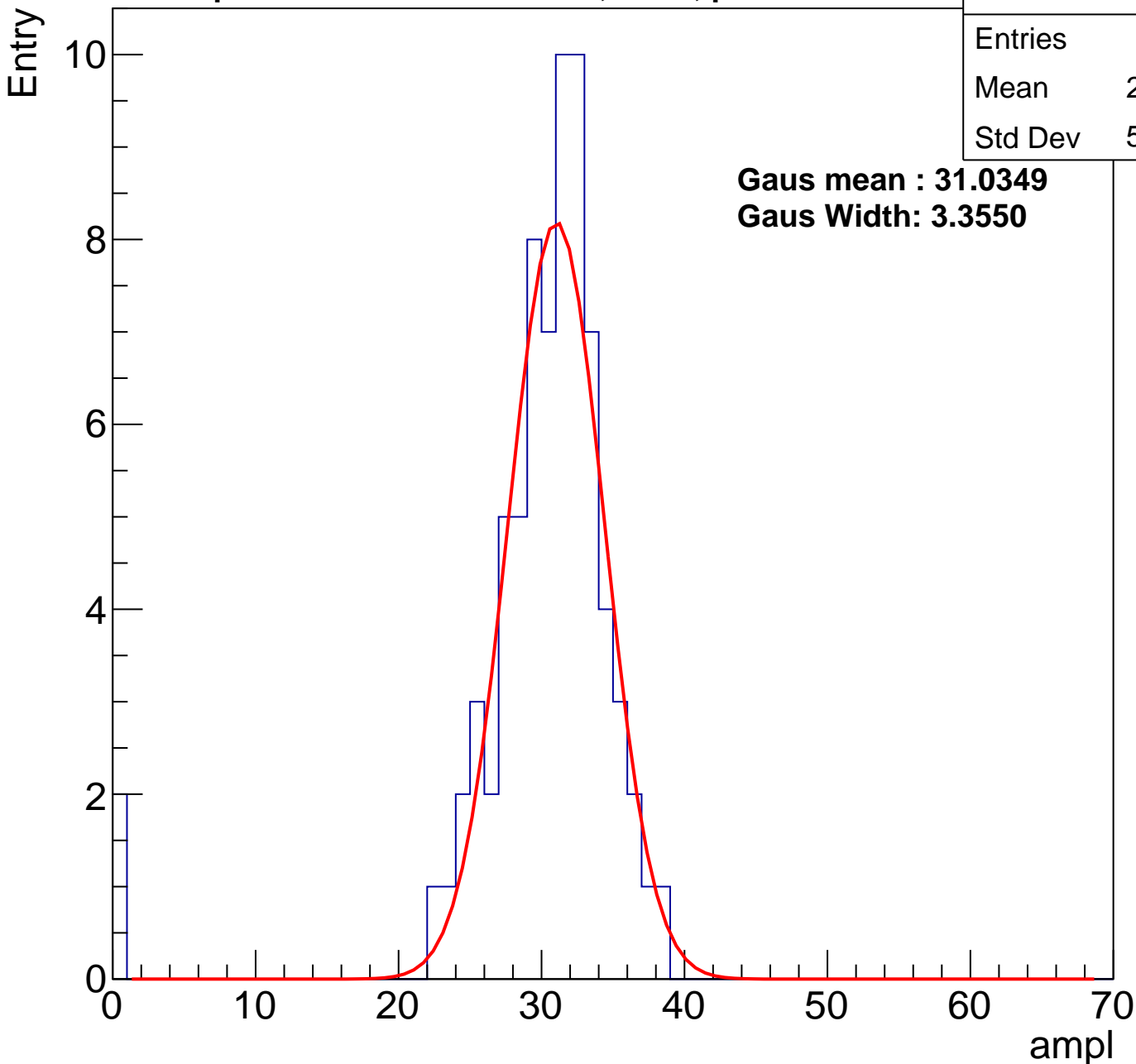
B0L002S, U7-ch36, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	29.54
Std Dev	5.912

Gaus mean : 31.0349

Gaus Width: 3.3550



B0L002S, U7-ch36, adc1

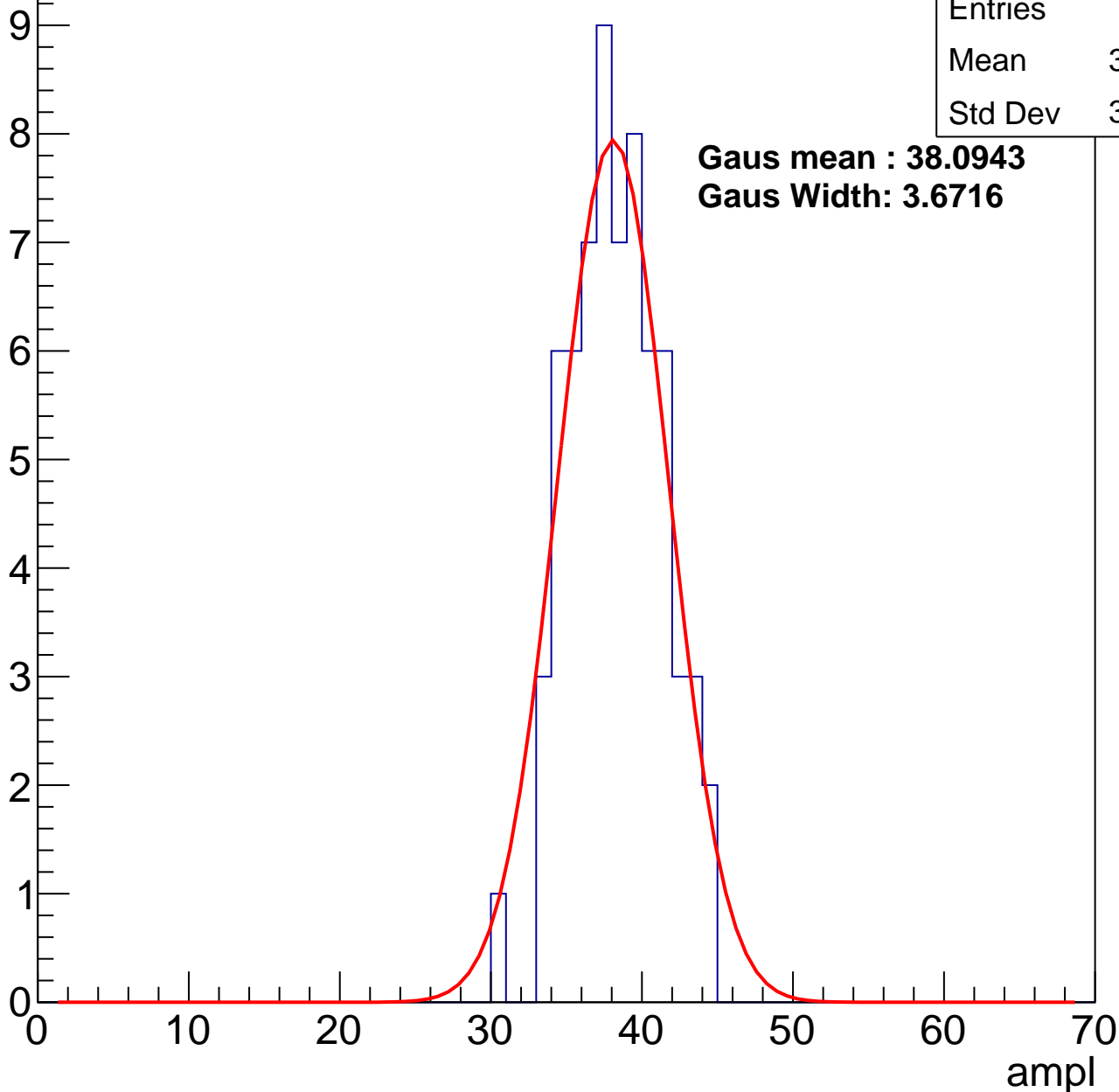
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.84
Std Dev	3.005

Gaus mean : 38.0943

Gaus Width: 3.6716



B0L002S, U7-ch36, adc2

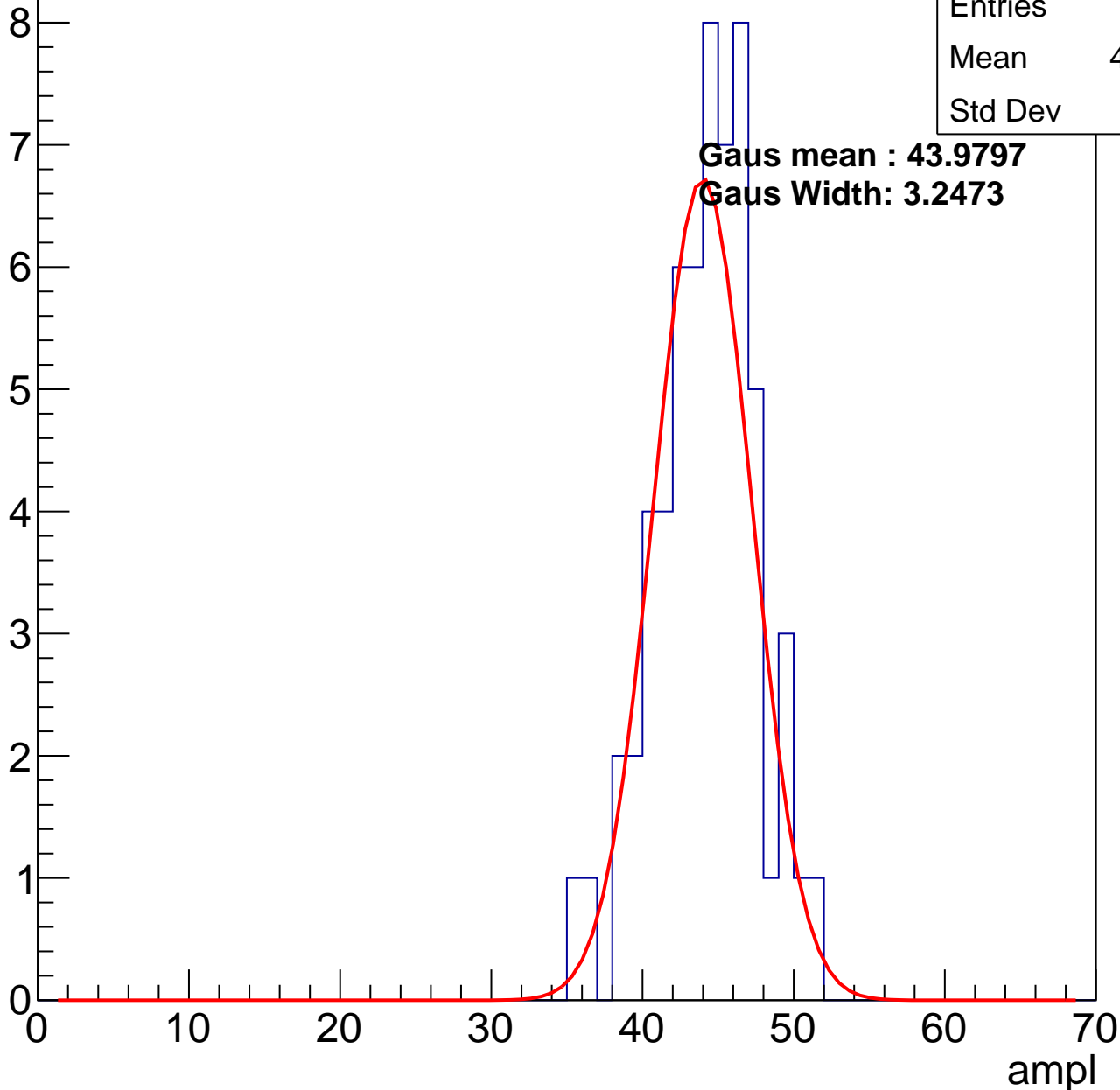
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.75
Std Dev	3.3

Gaus mean : 43.9797

Gaus Width: 3.2473

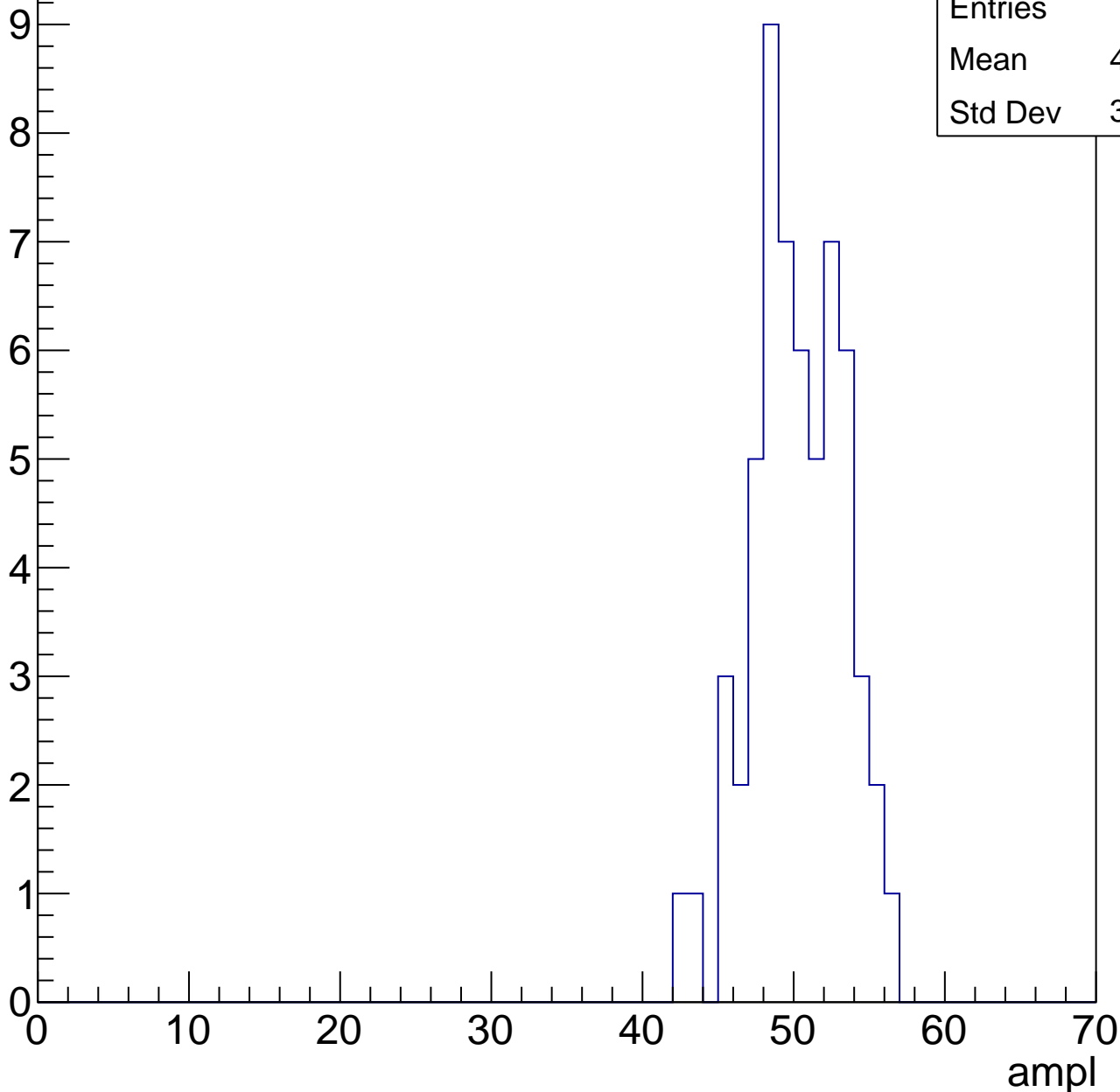


B0L002S, U7-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

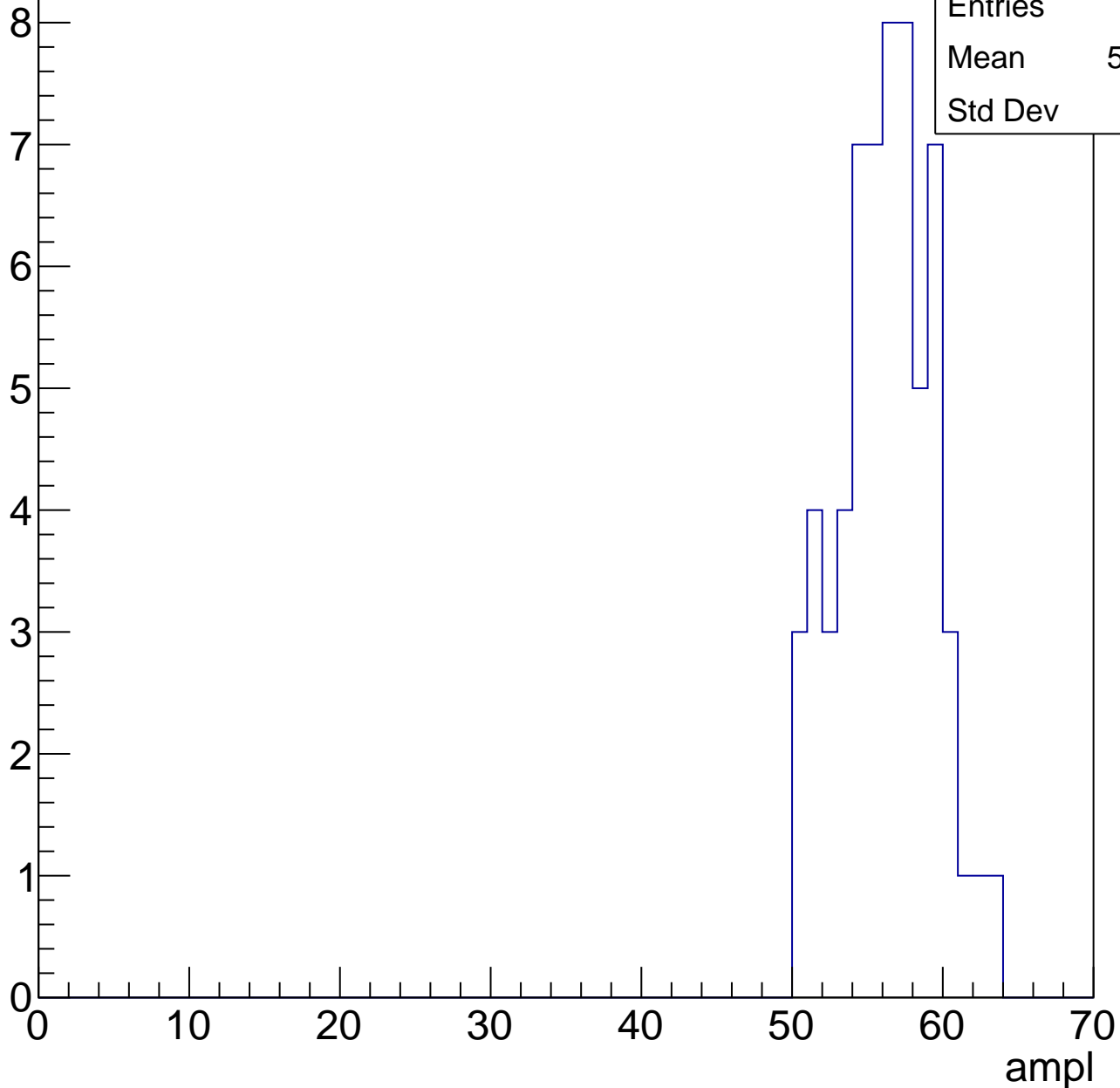
Entries	58
Mean	49.78
Std Dev	3.012



B0L002S, U7-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	62
Mean	55.77
Std Dev	3.04

B0L002S, U7-ch36, adc5

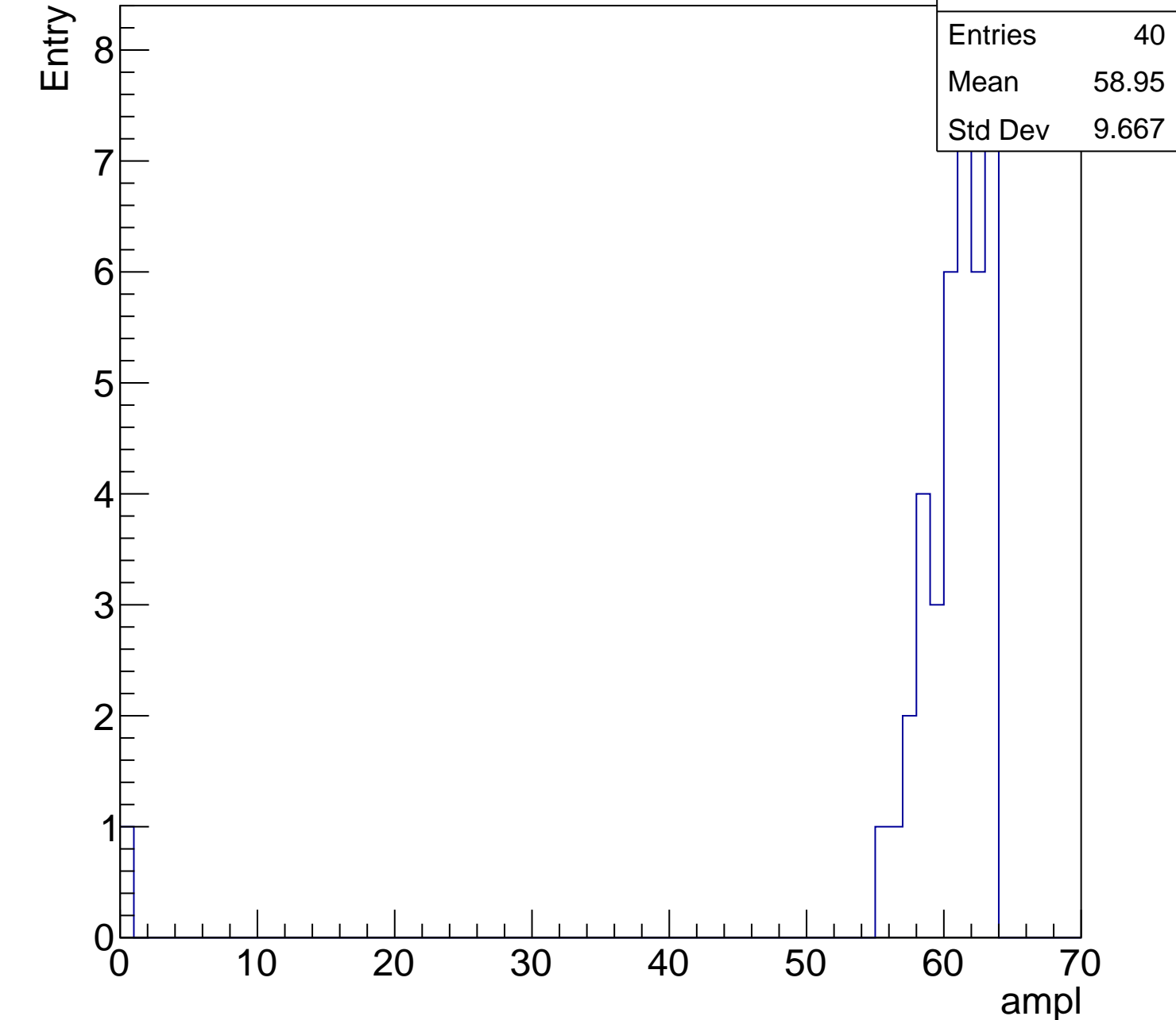
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.95
Std Dev	9.667

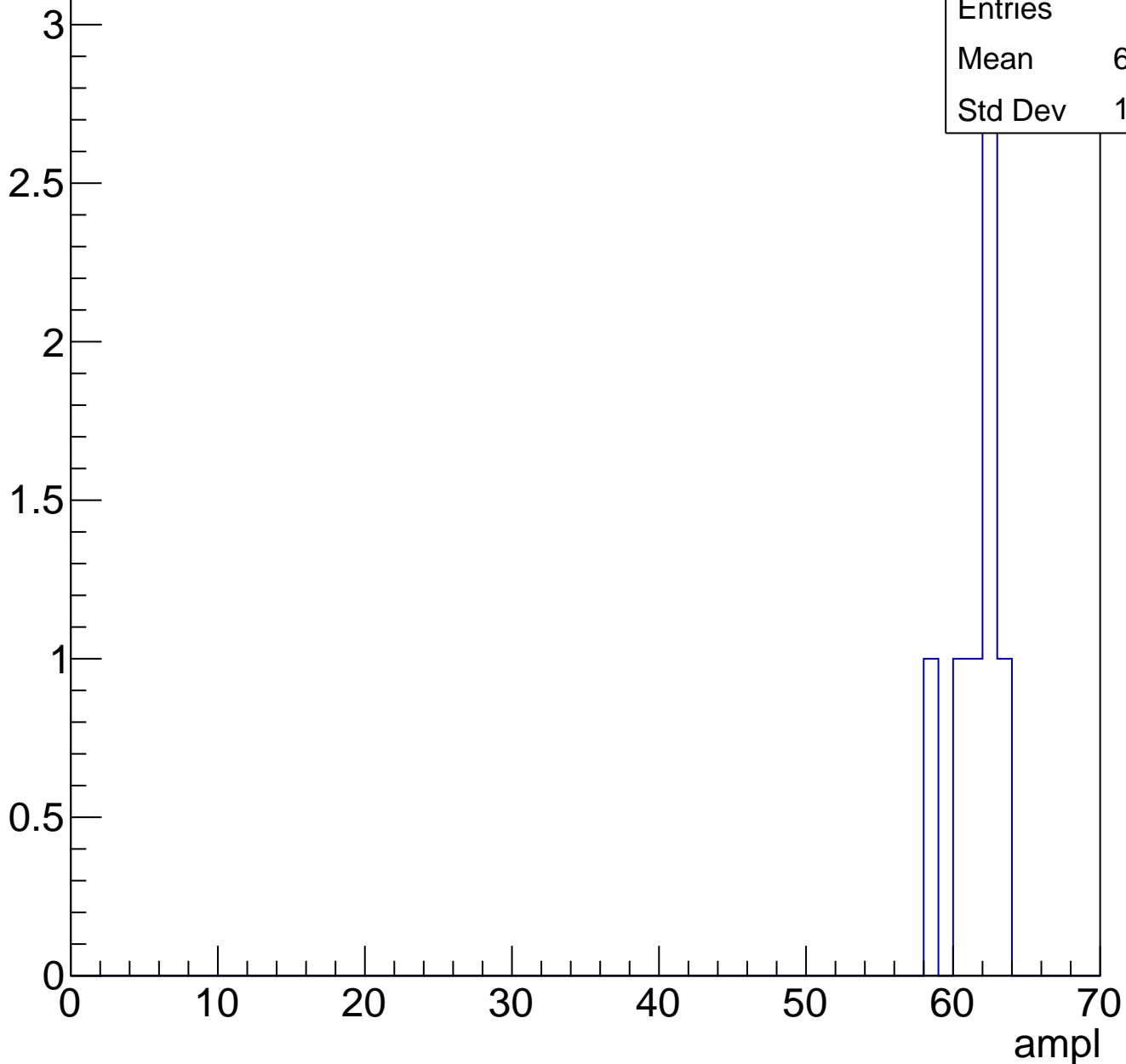
ampl



B0L002S, U7-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch37, adc0

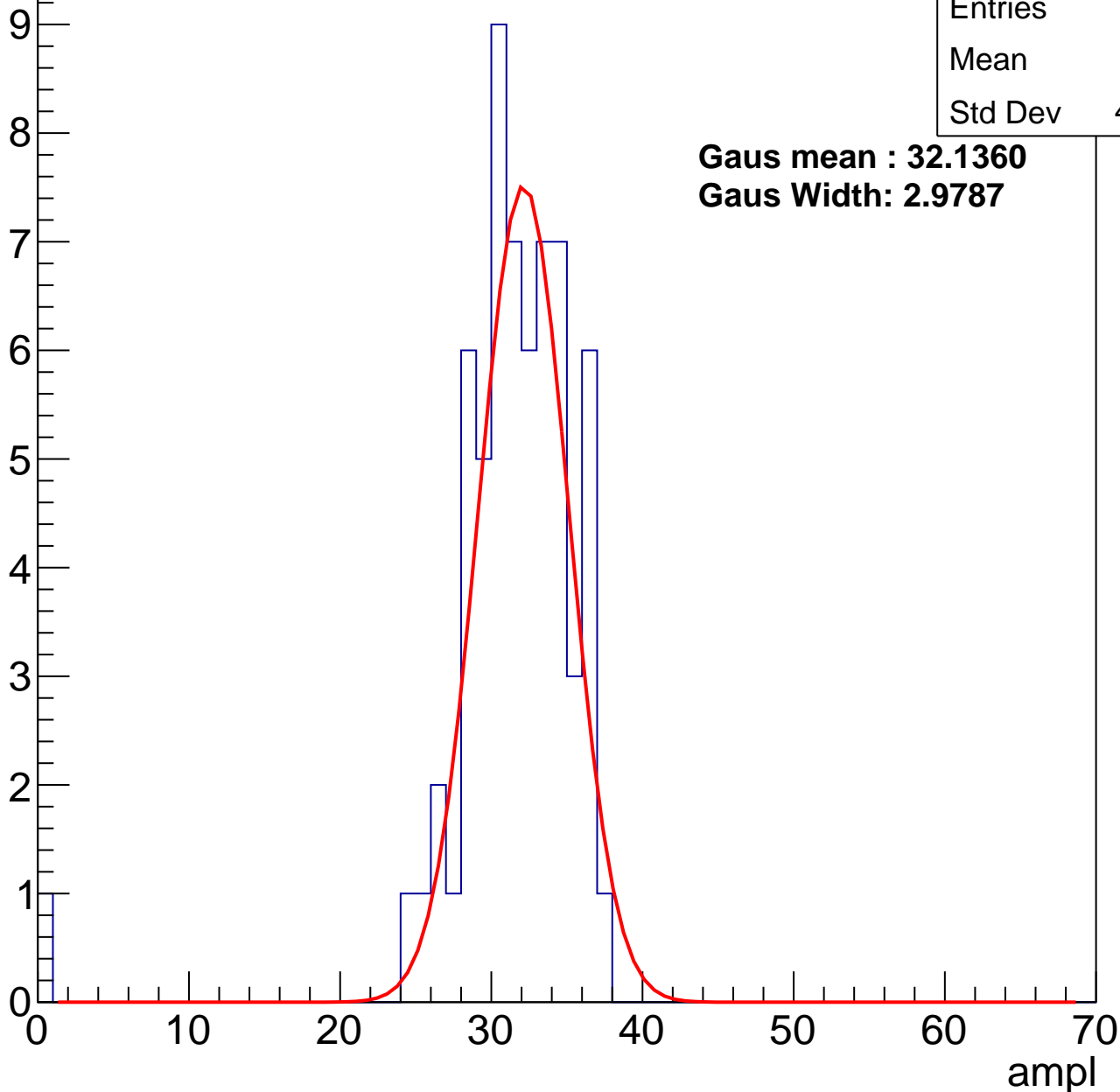
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.9
Std Dev	4.921

Gaus mean : 32.1360

Gaus Width: 2.9787



B0L002S, U7-ch37, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	38.57
Std Dev	3.707

Gaus mean : 39.2208

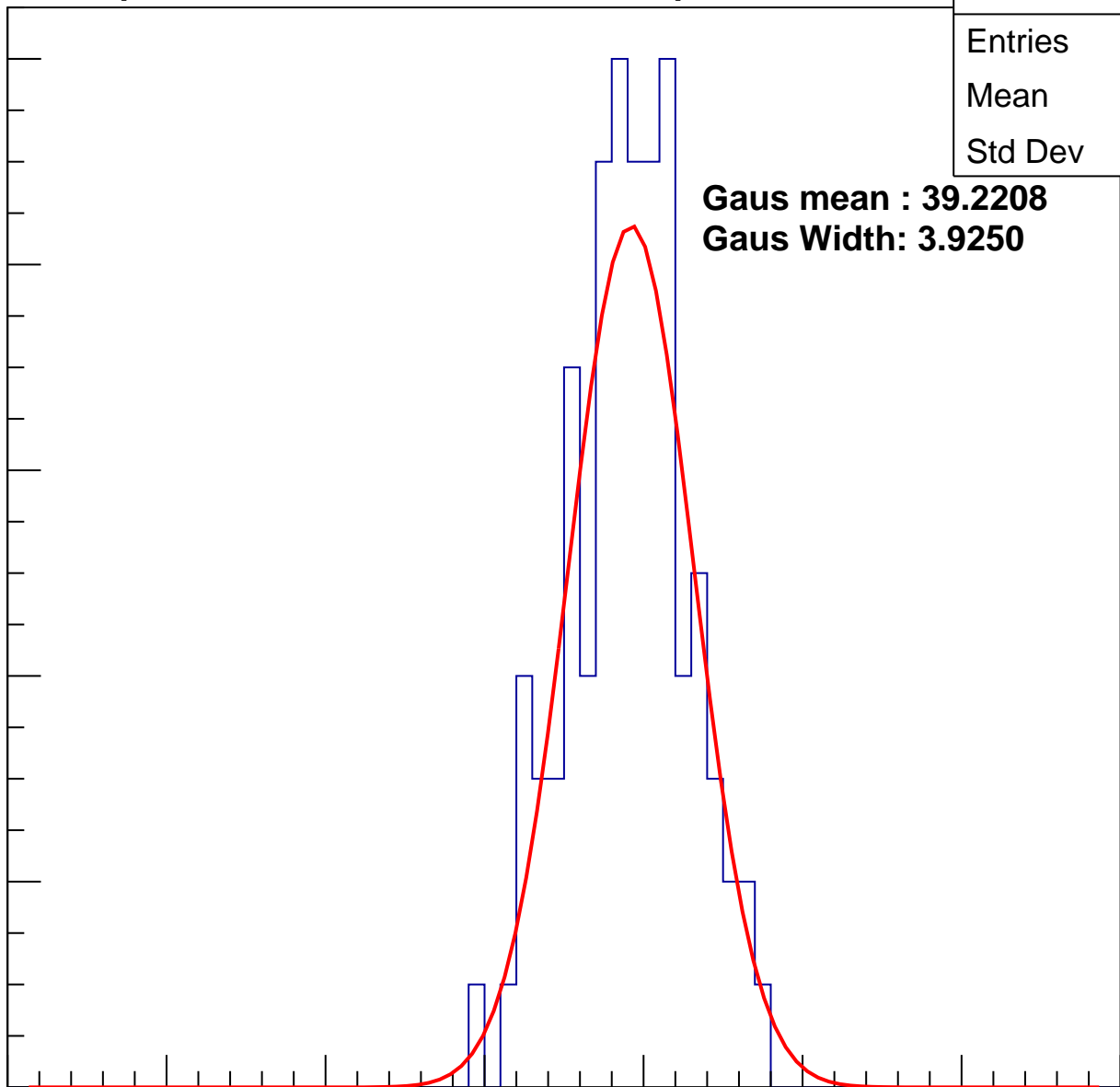
Gaus Width: 3.9250

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch37, adc2

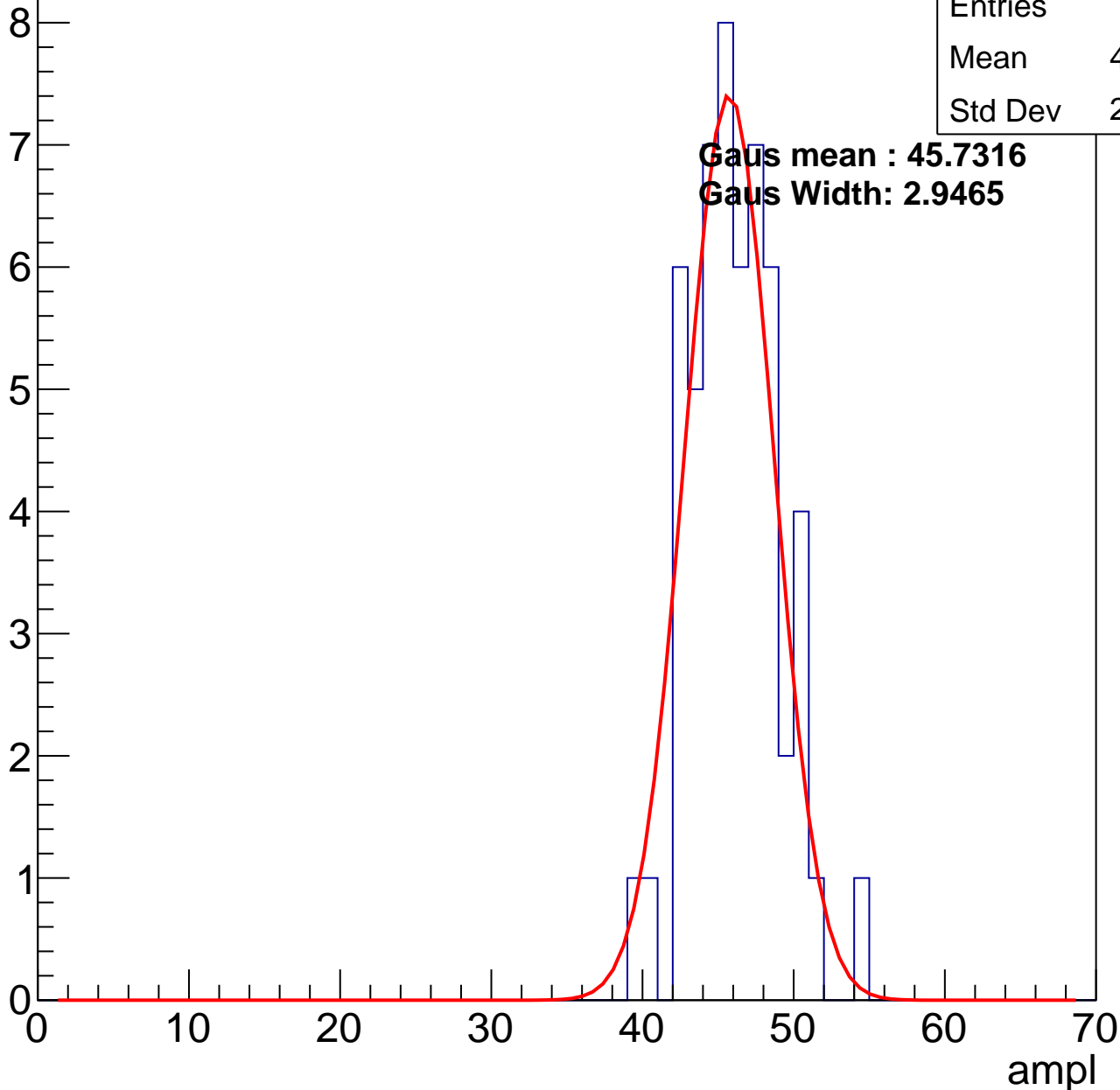
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.64
Std Dev	2.882

Gaus mean : 45.7316

Gaus Width: 2.9465

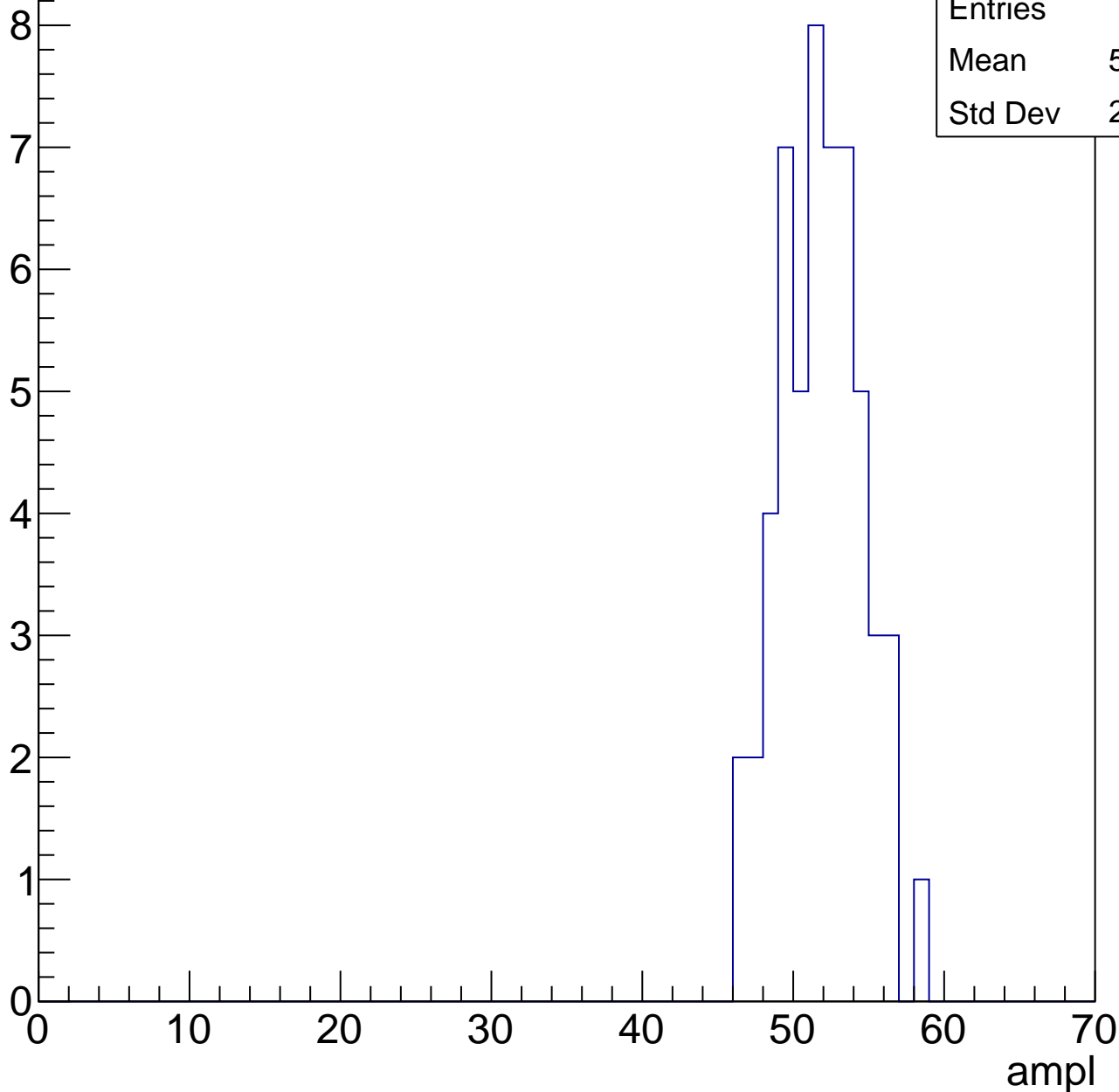


B0L002S, U7-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

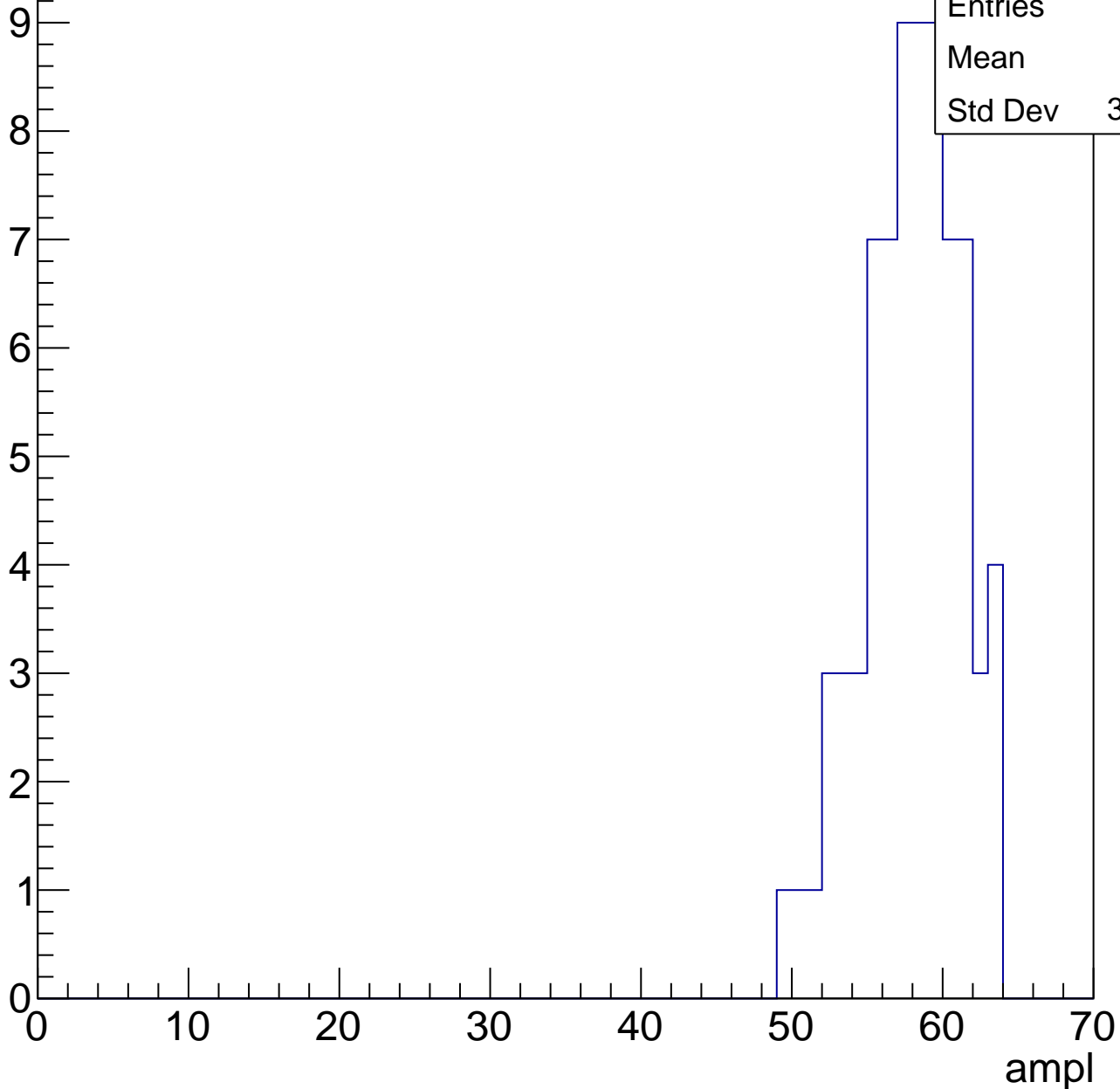
Entries	54
Mean	51.39
Std Dev	2.704



B0L002S, U7-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



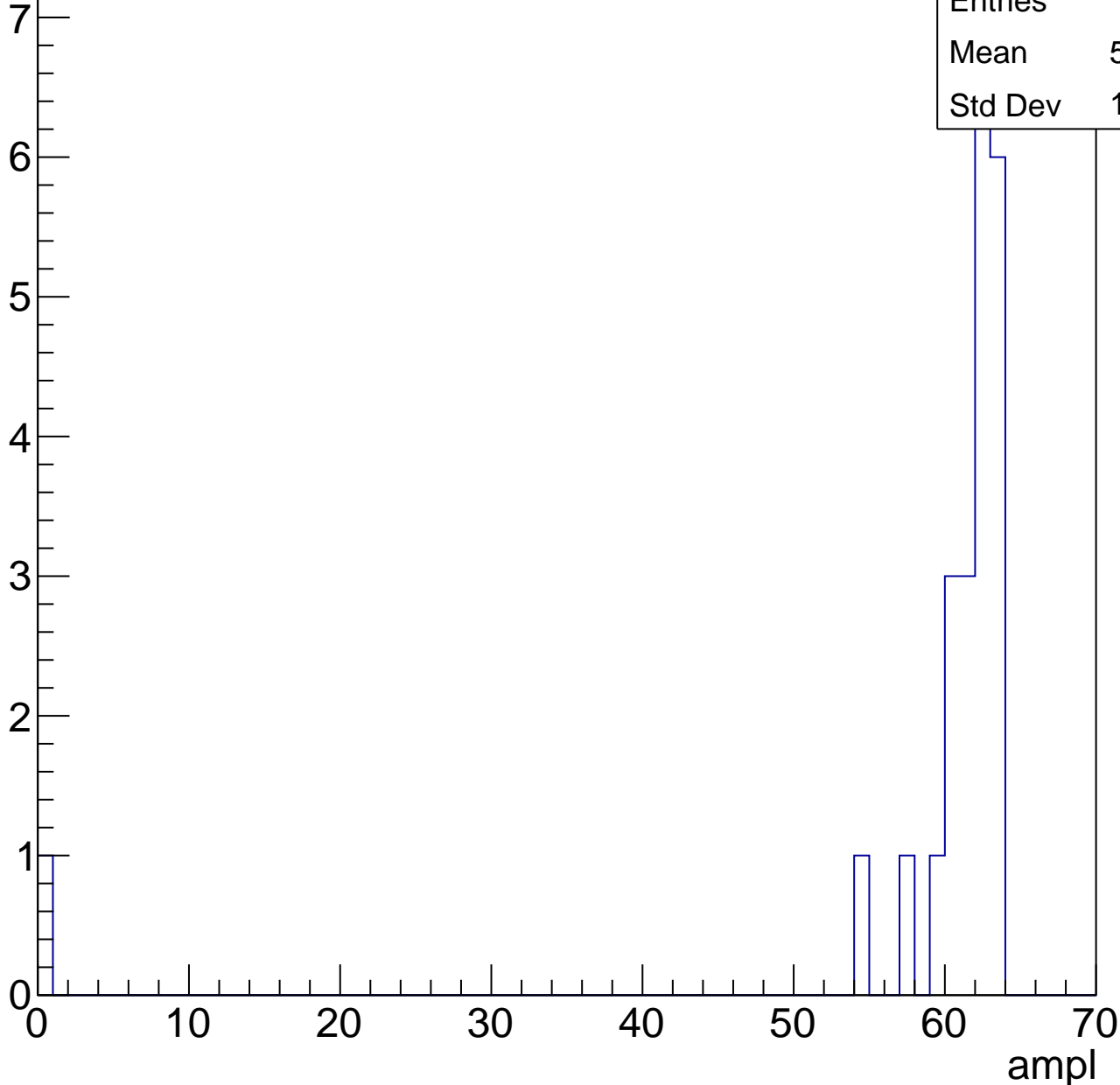
Entries	74
Mean	57.5
Std Dev	3.193

B0L002S, U7-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.48
Std Dev	12.65



B0L002S, U7-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U7-ch38, adc0

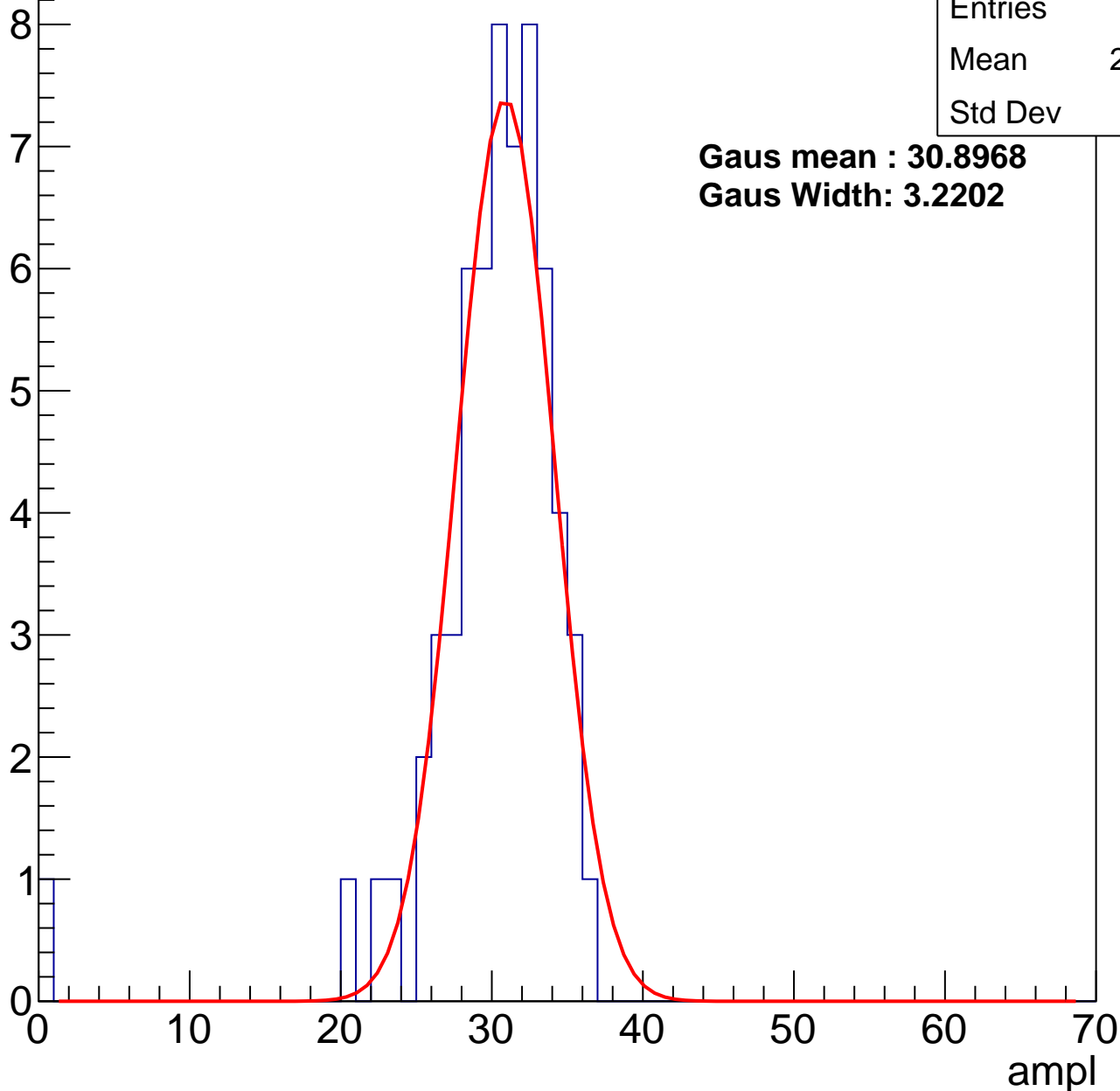
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.57
Std Dev	5

Gaus mean : 30.8968

Gaus Width: 3.2202



B0L002S, U7-ch38, adc1

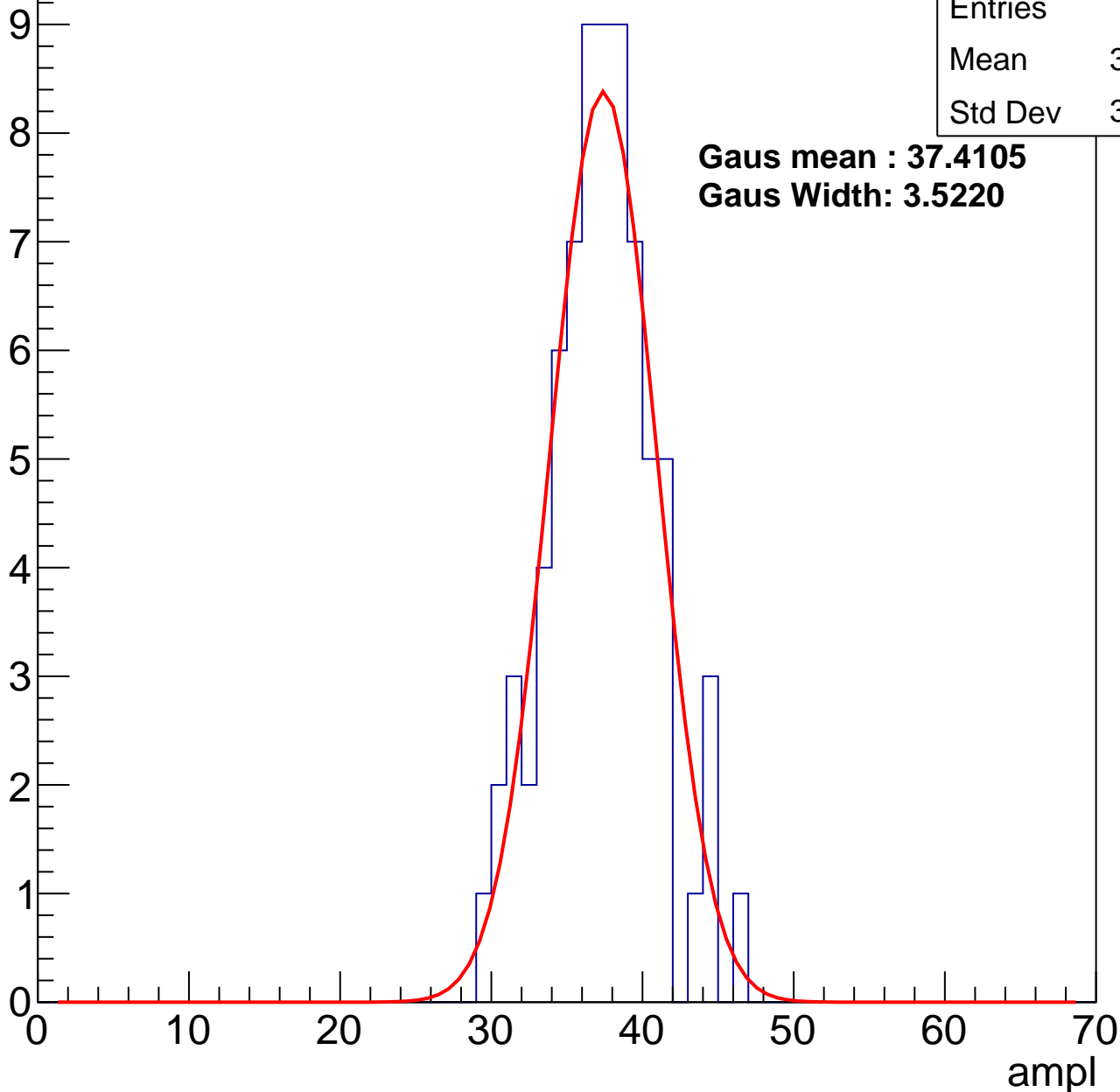
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.82
Std Dev	3.485

Gaus mean : 37.4105

Gaus Width: 3.5220



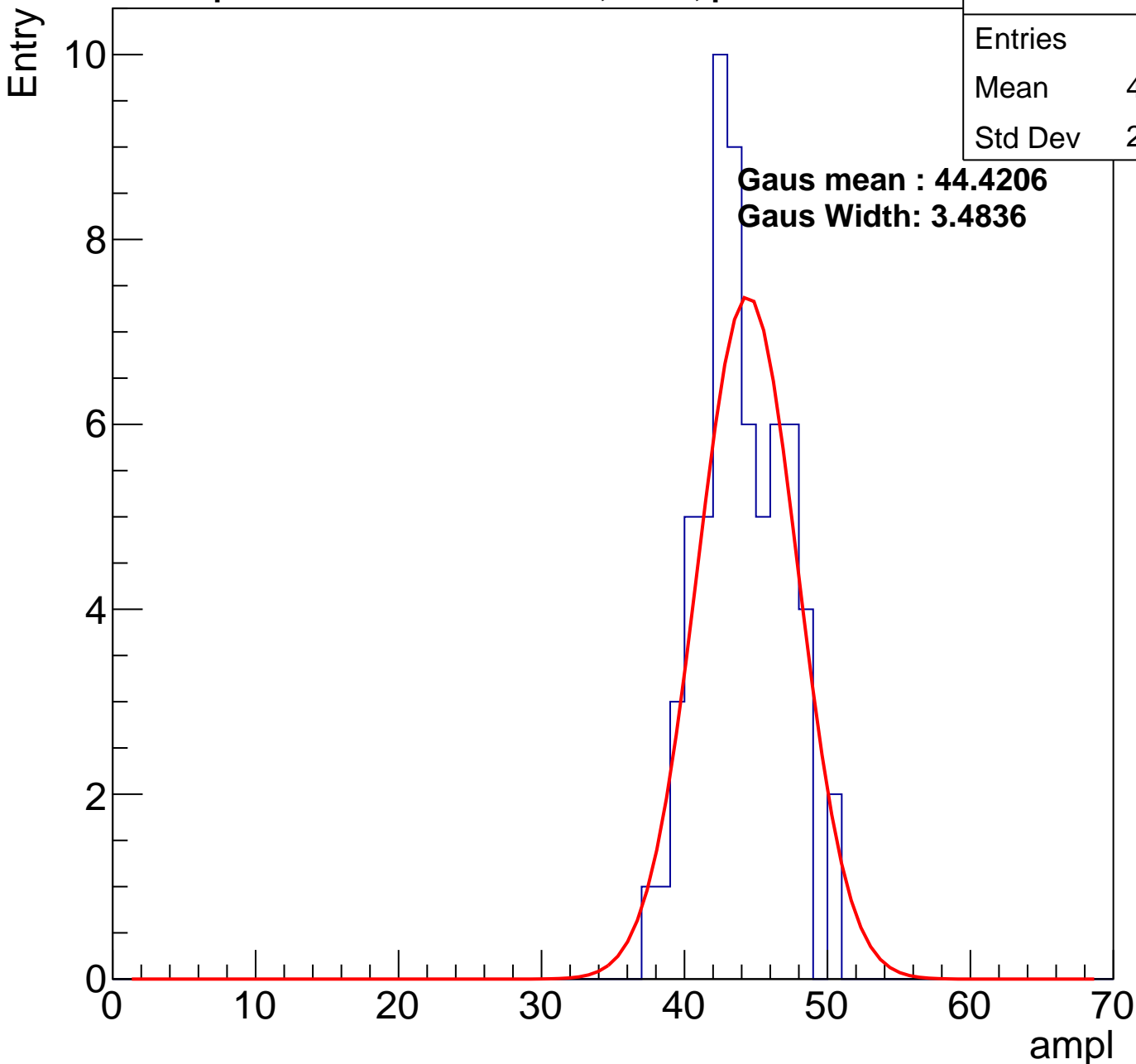
B0L002S, U7-ch38, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	43.54
Std Dev	2.927

Gaus mean : 44.4206

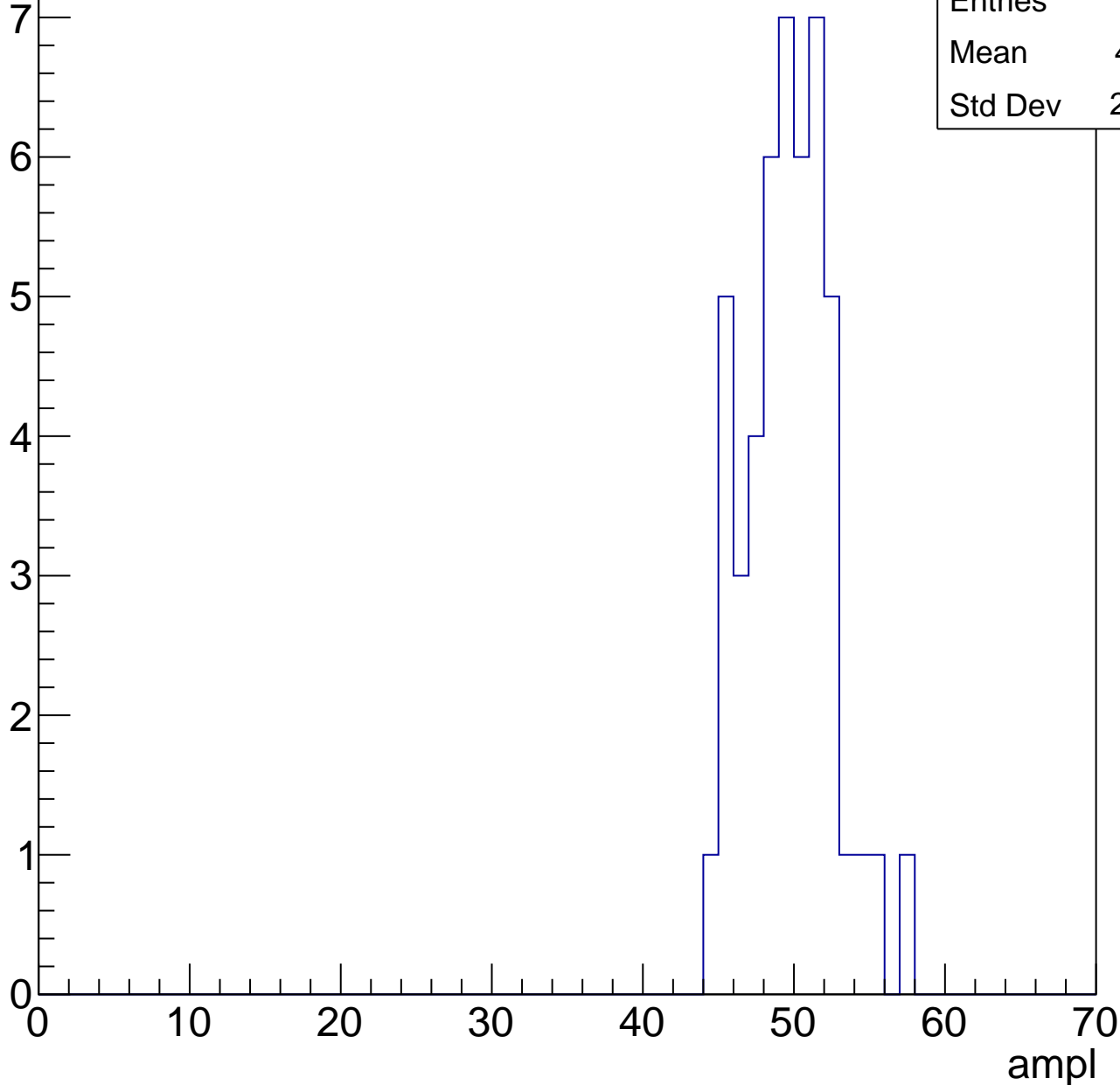
Gaus Width: 3.4836



B0L002S, U7-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	48
Mean	49.21
Std Dev	2.784

B0L002S, U7-ch38, adc4

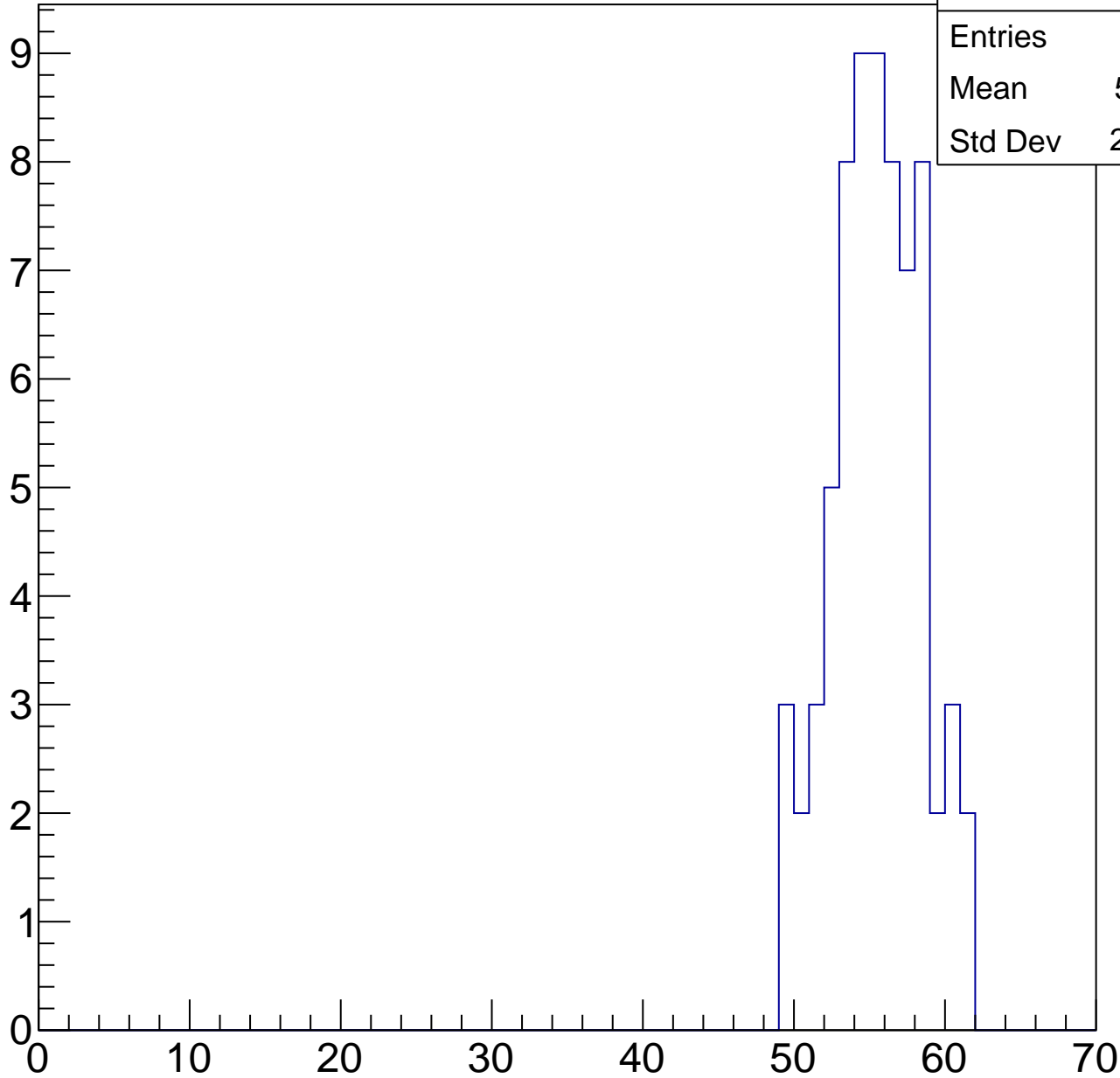
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	55.01
Std Dev	2.897

ampl

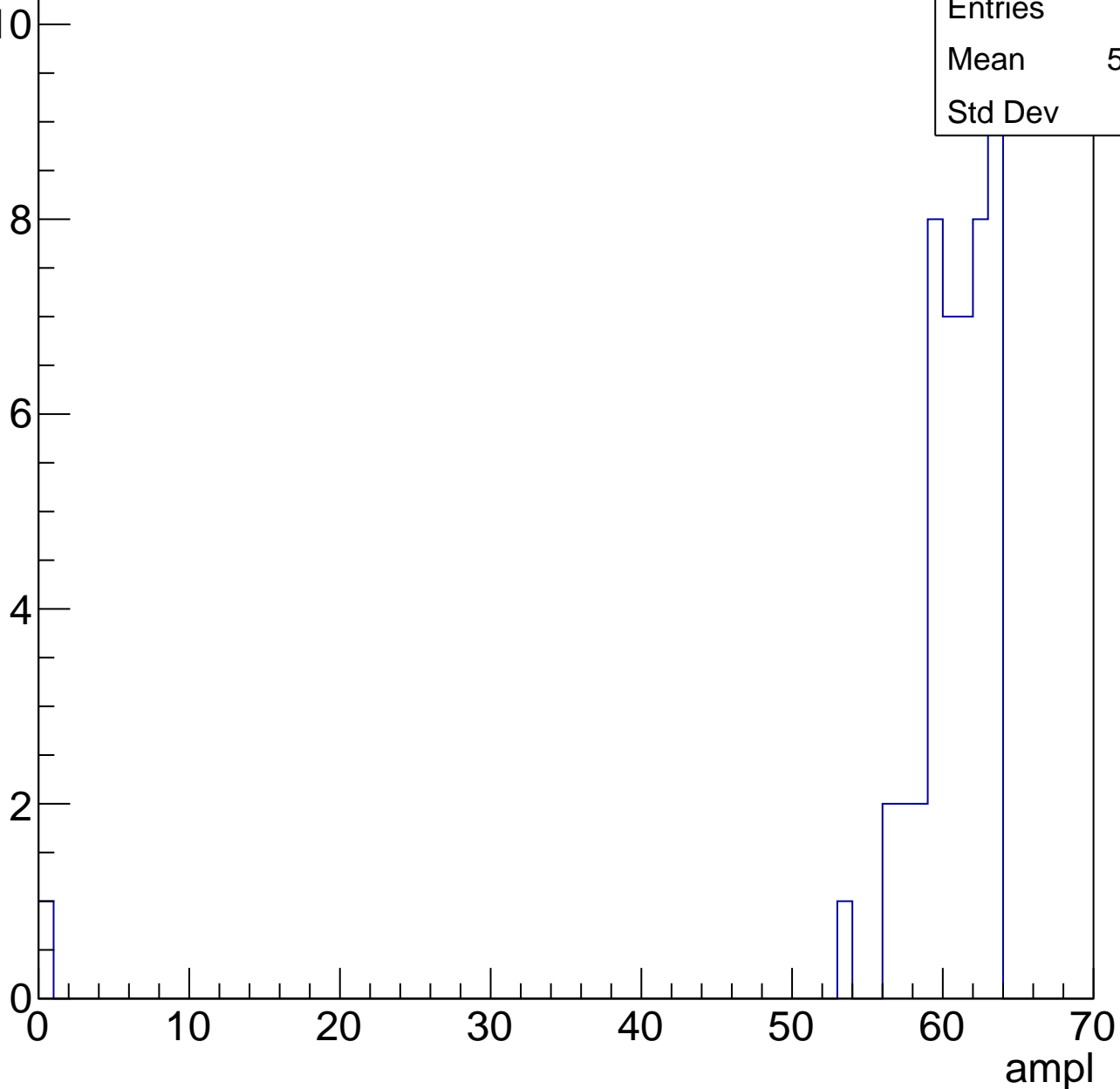


B0L002S, U7-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	59.17
Std Dev	8.91



B0L002S, U7-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U7-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch39, adc0

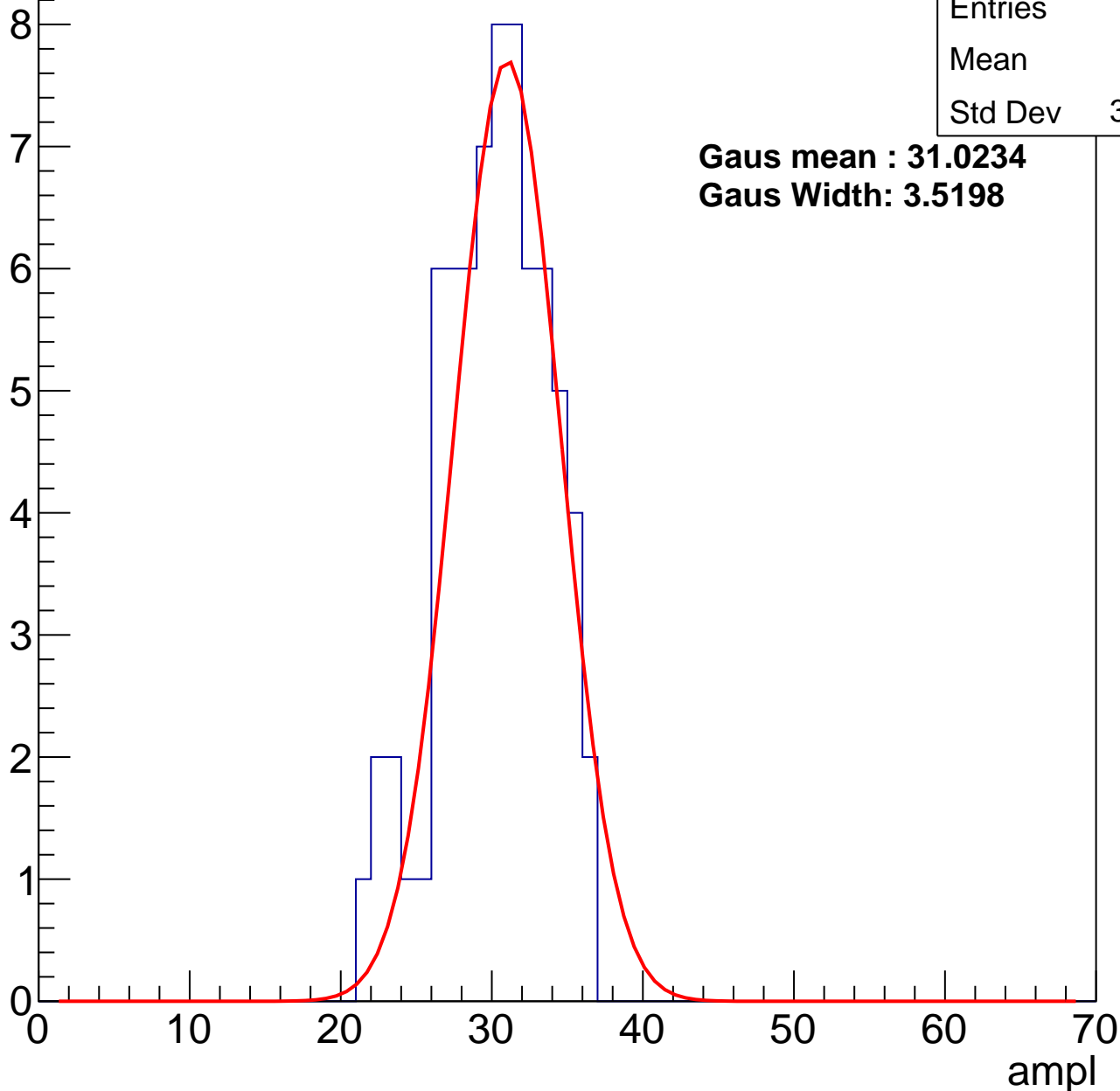
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.7
Std Dev	3.522

Gaus mean : 31.0234

Gaus Width: 3.5198



B0L002S, U7-ch39, adc1

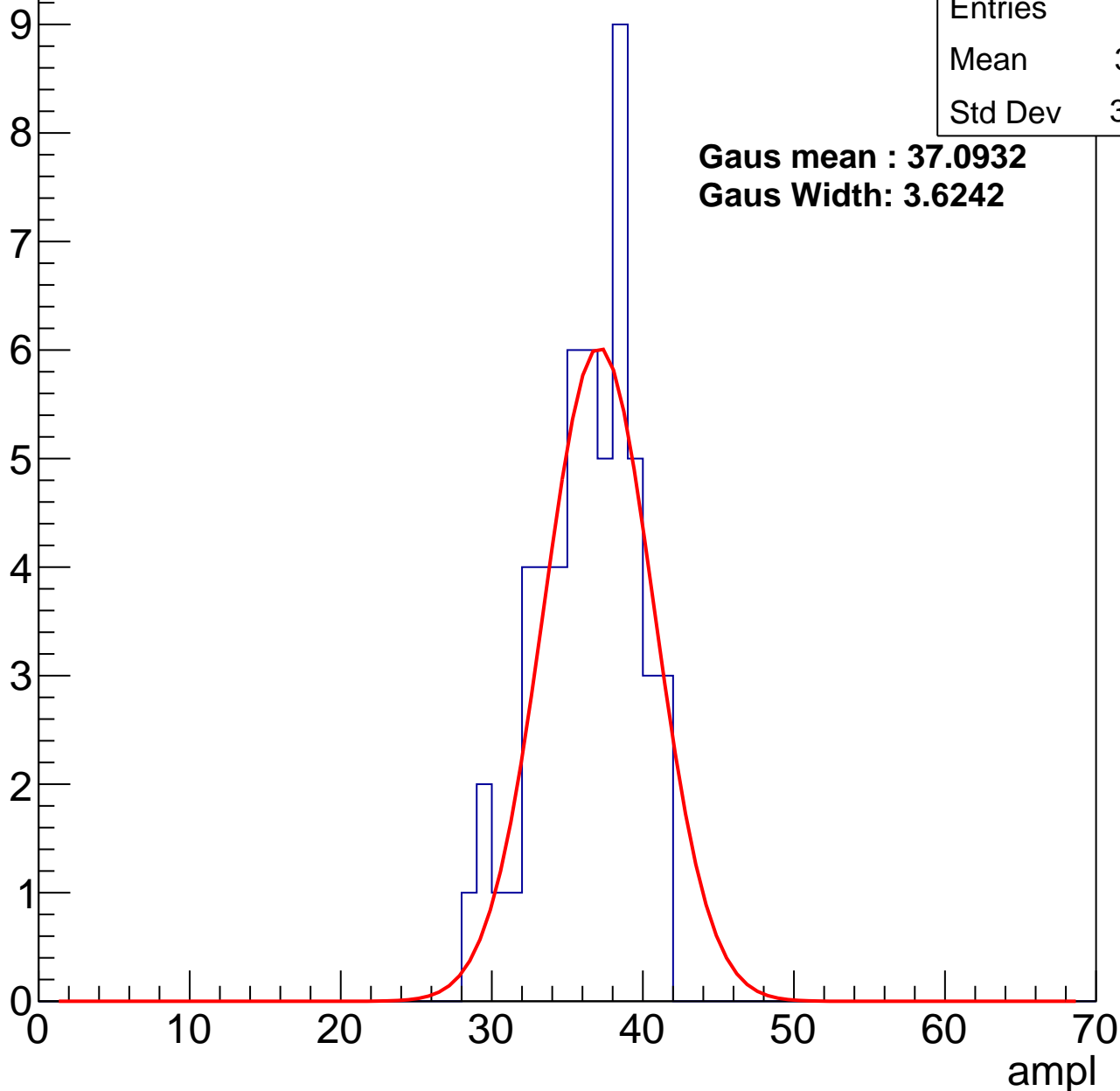
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	35.81
Std Dev	3.198

Gaus mean : 37.0932

Gaus Width: 3.6242



B0L002S, U7-ch39, adc2

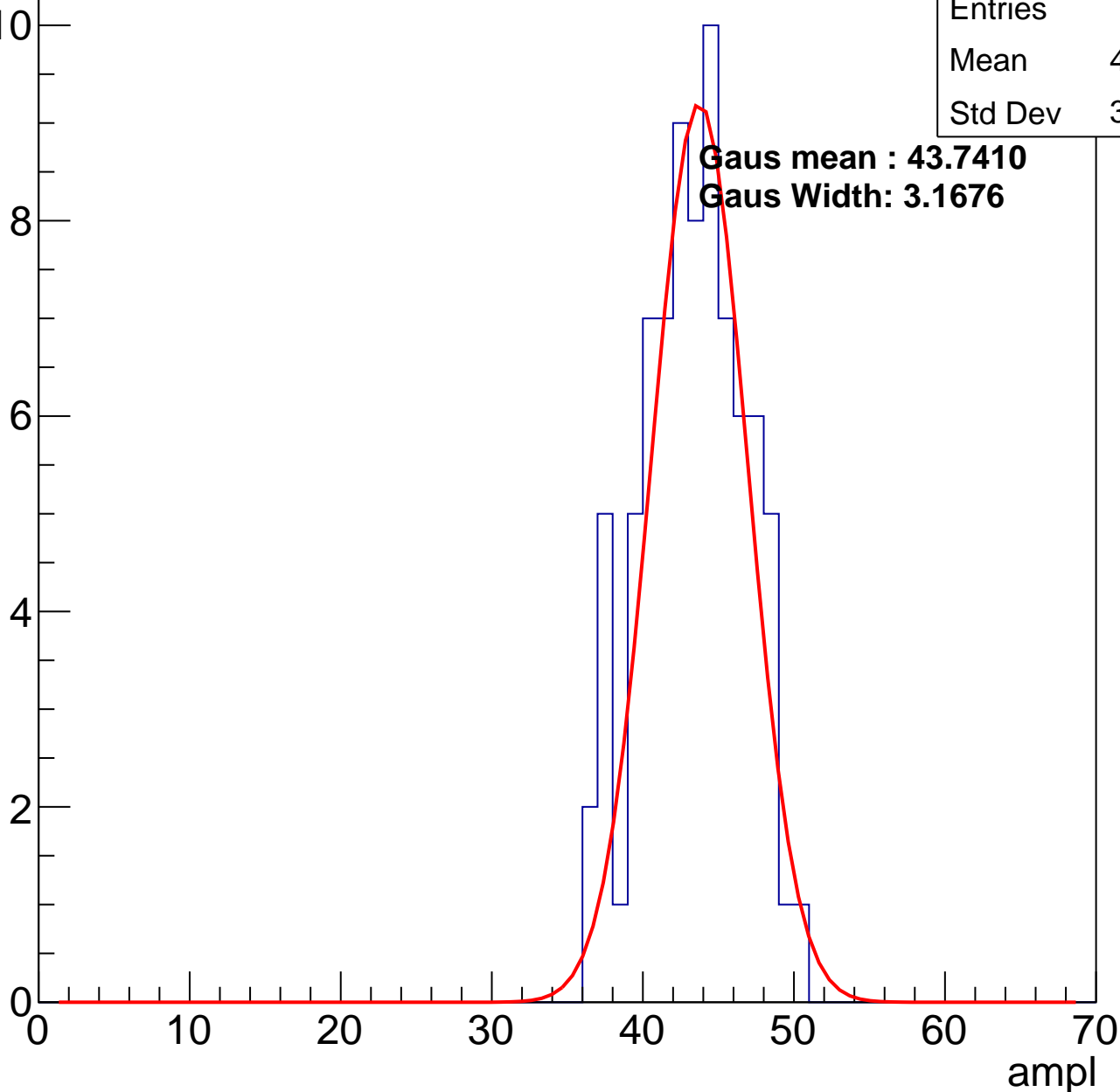
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	42.89
Std Dev	3.317

Gaus mean : 43.7410

Gaus Width: 3.1676

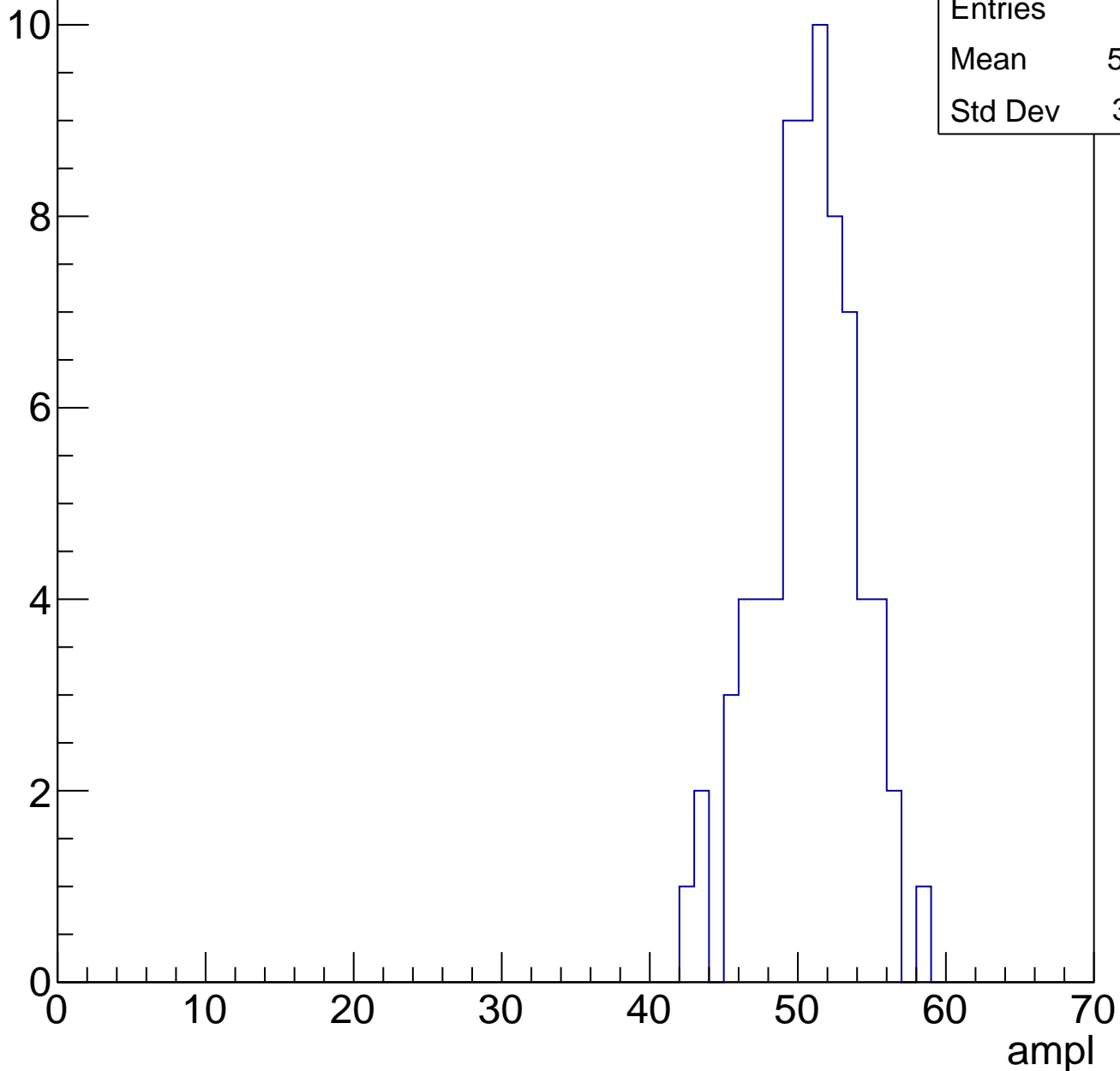


B0L002S, U7-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	50.29
Std Dev	3.251

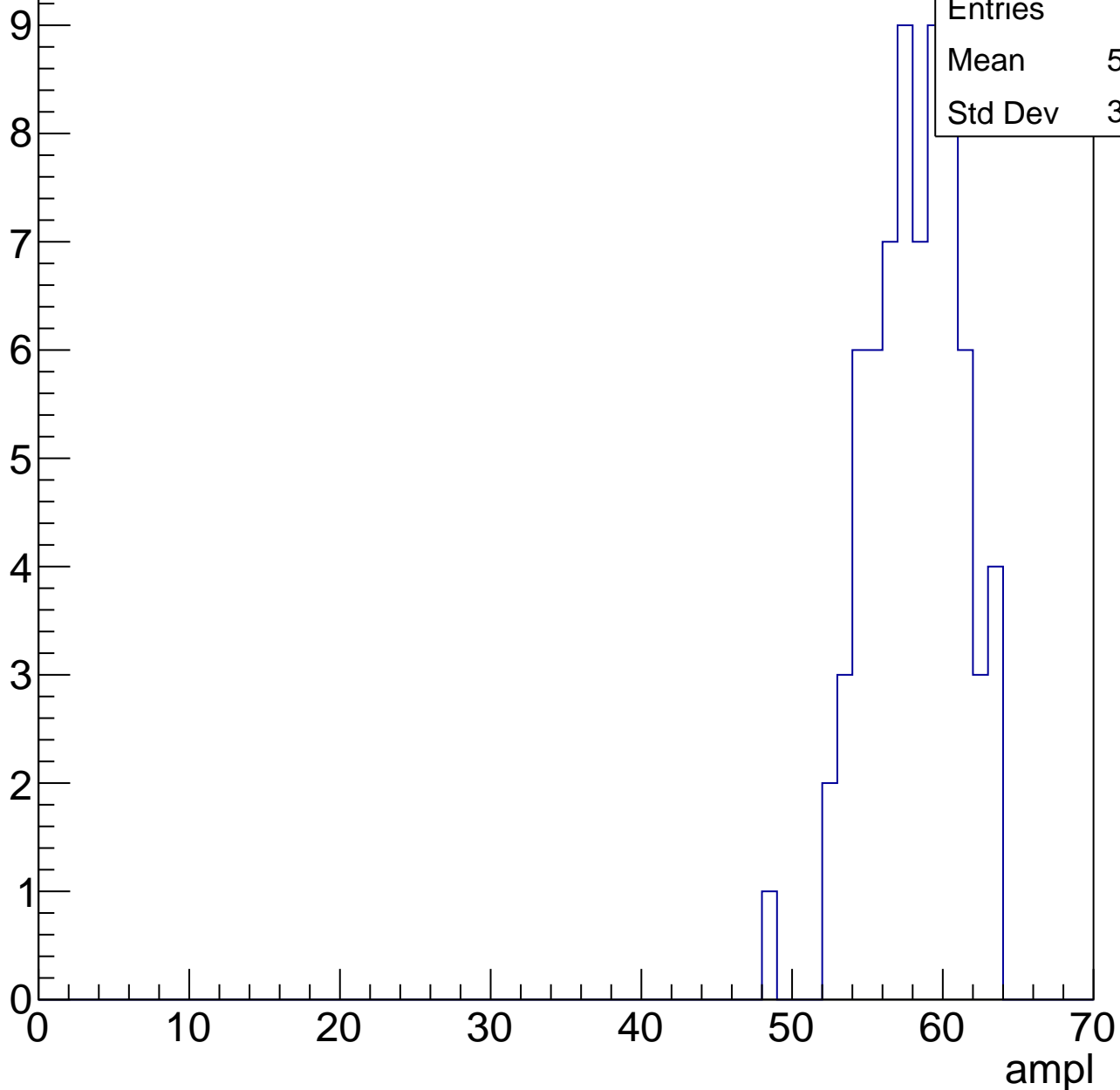
Entry



B0L002S, U7-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

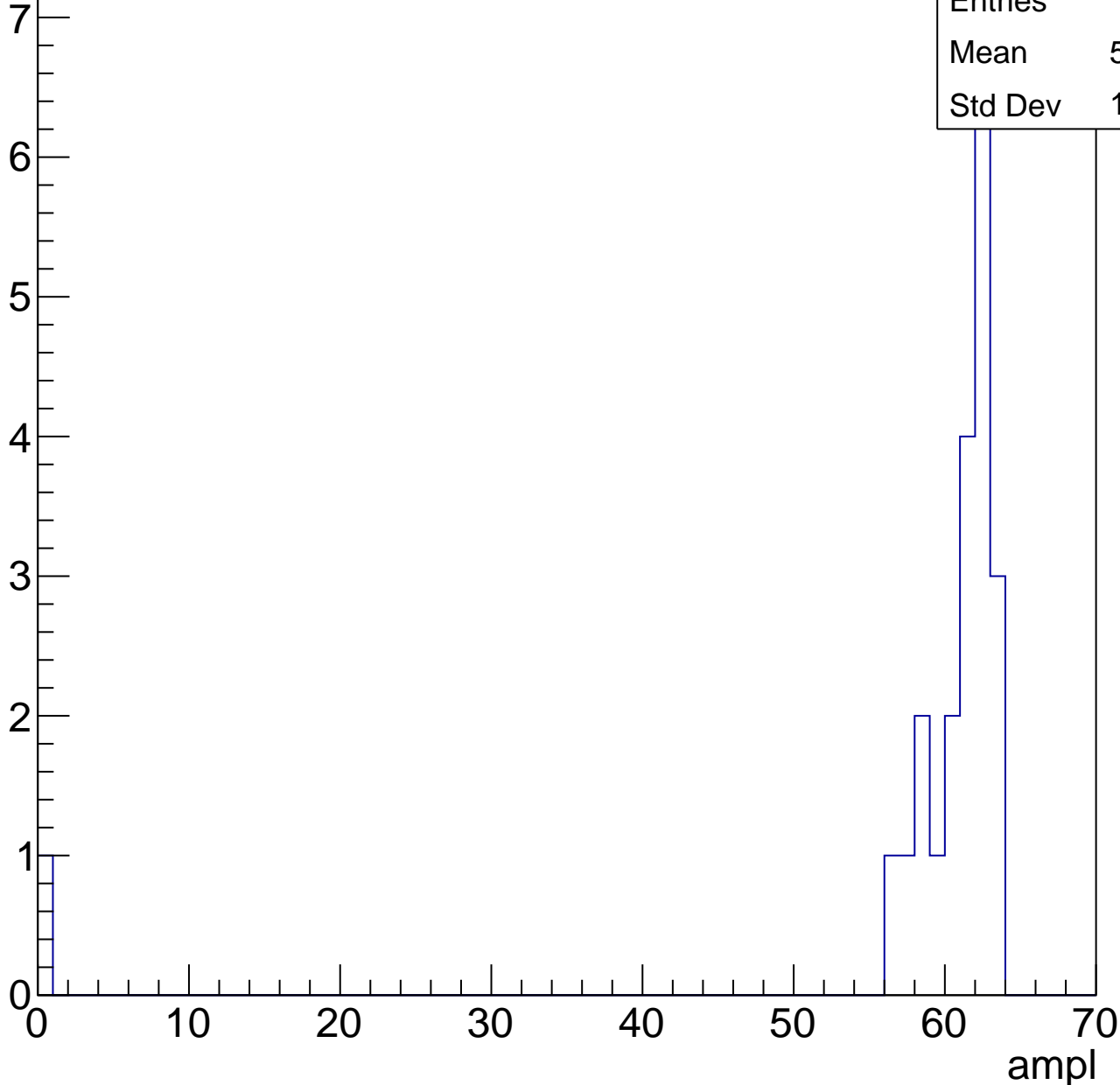


B0L002S, U7-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

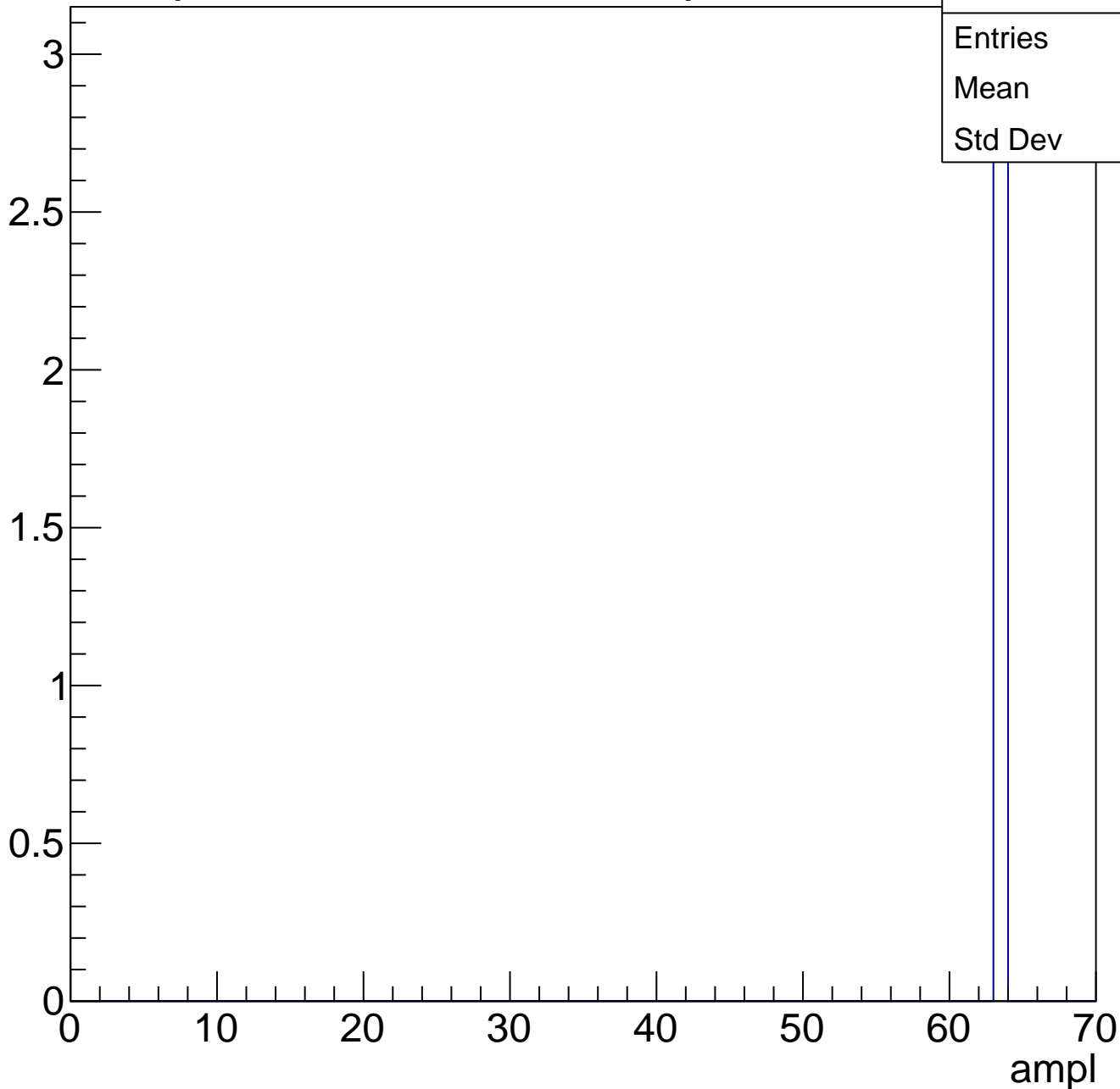
Entries	22
Mean	57.95
Std Dev	12.79



B0L002S, U7-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch40, adc0

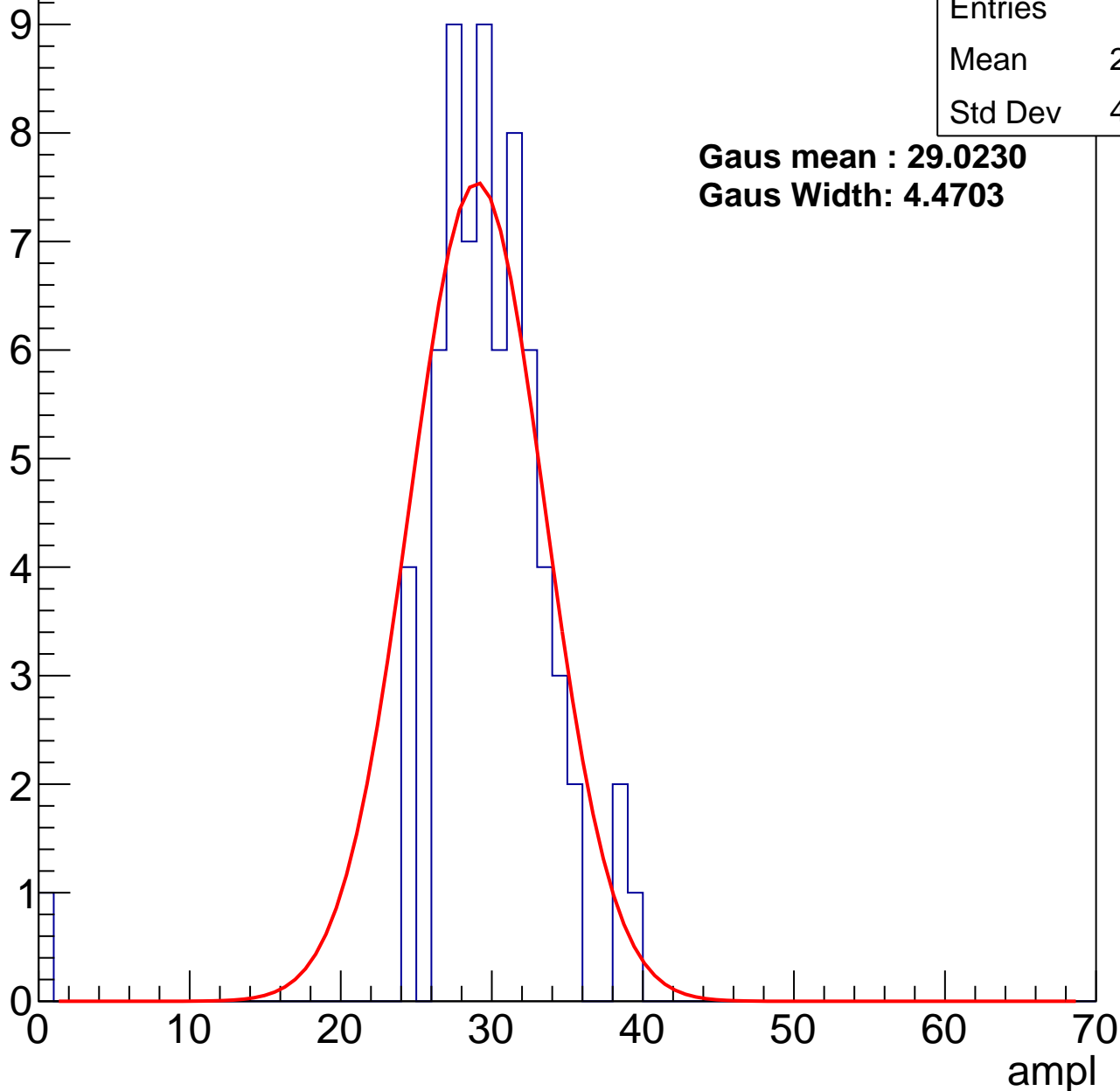
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.28
Std Dev	4.847

Gaus mean : 29.0230

Gaus Width: 4.4703



B0L002S, U7-ch40, adc1

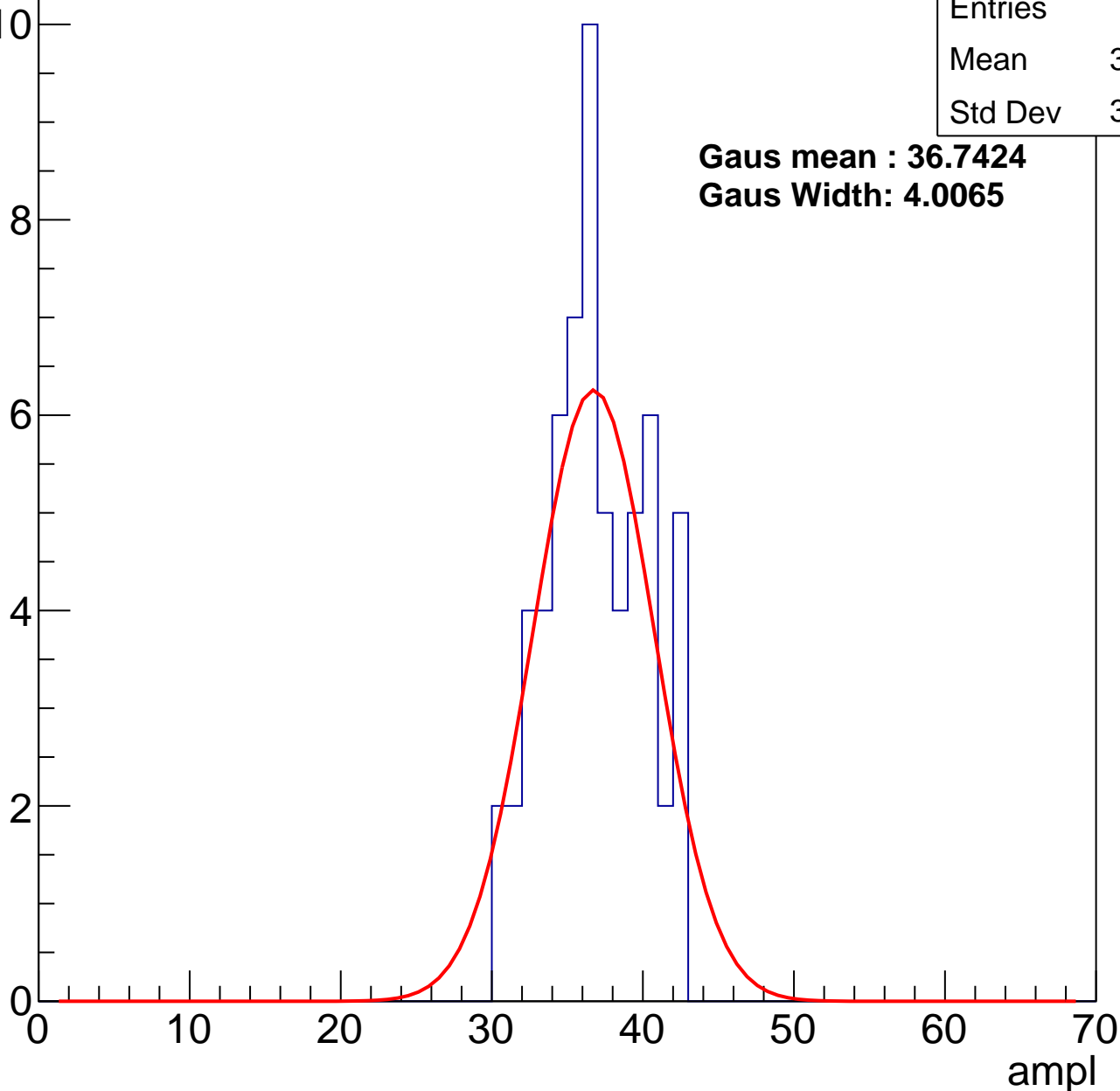
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.37
Std Dev	3.204

Gaus mean : 36.7424

Gaus Width: 4.0065



B0L002S, U7-ch40, adc2

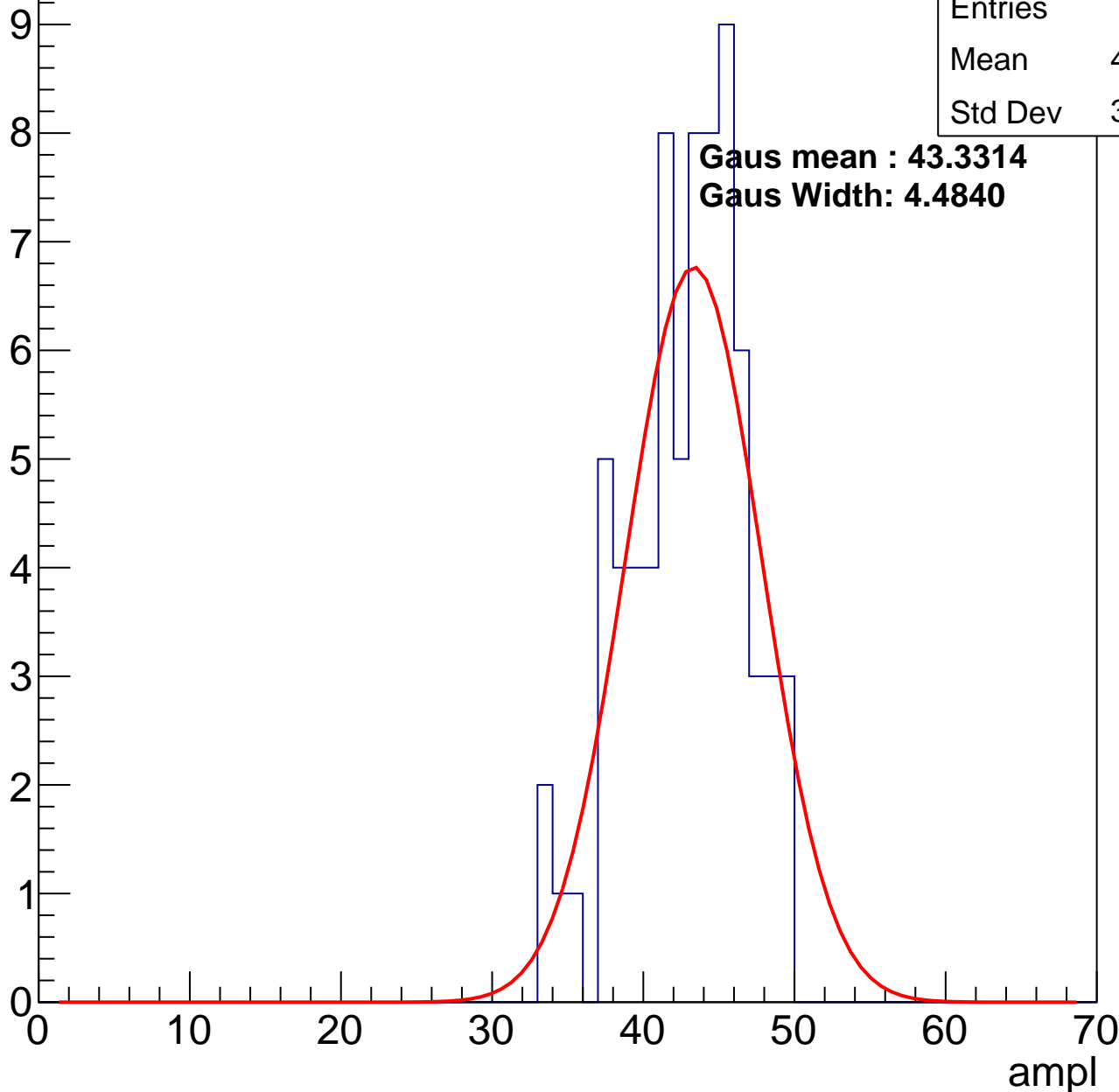
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	42.36
Std Dev	3.787

Gaus mean : 43.3314

Gaus Width: 4.4840

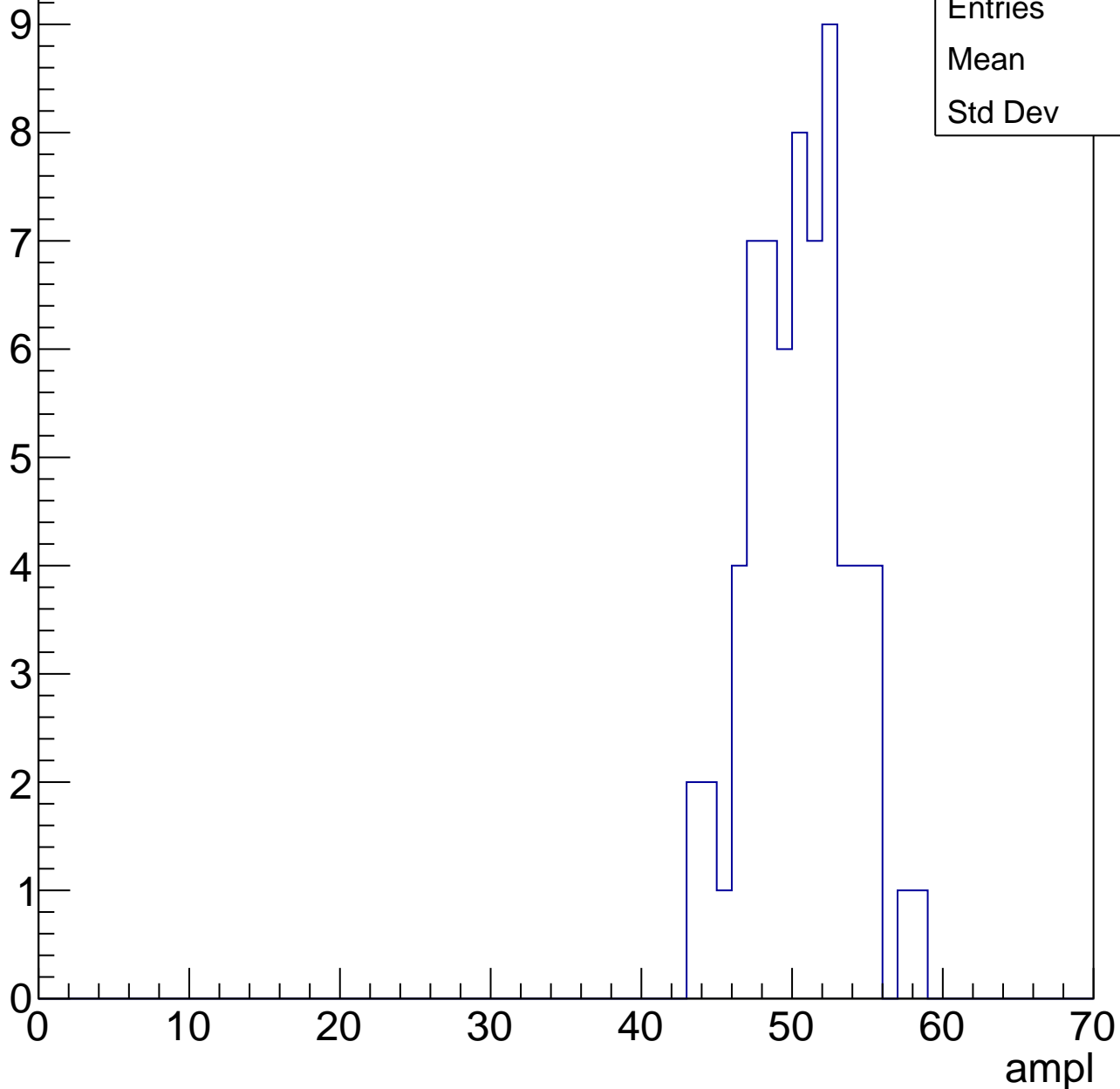


B0L002S, U7-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50
Std Dev	3.26

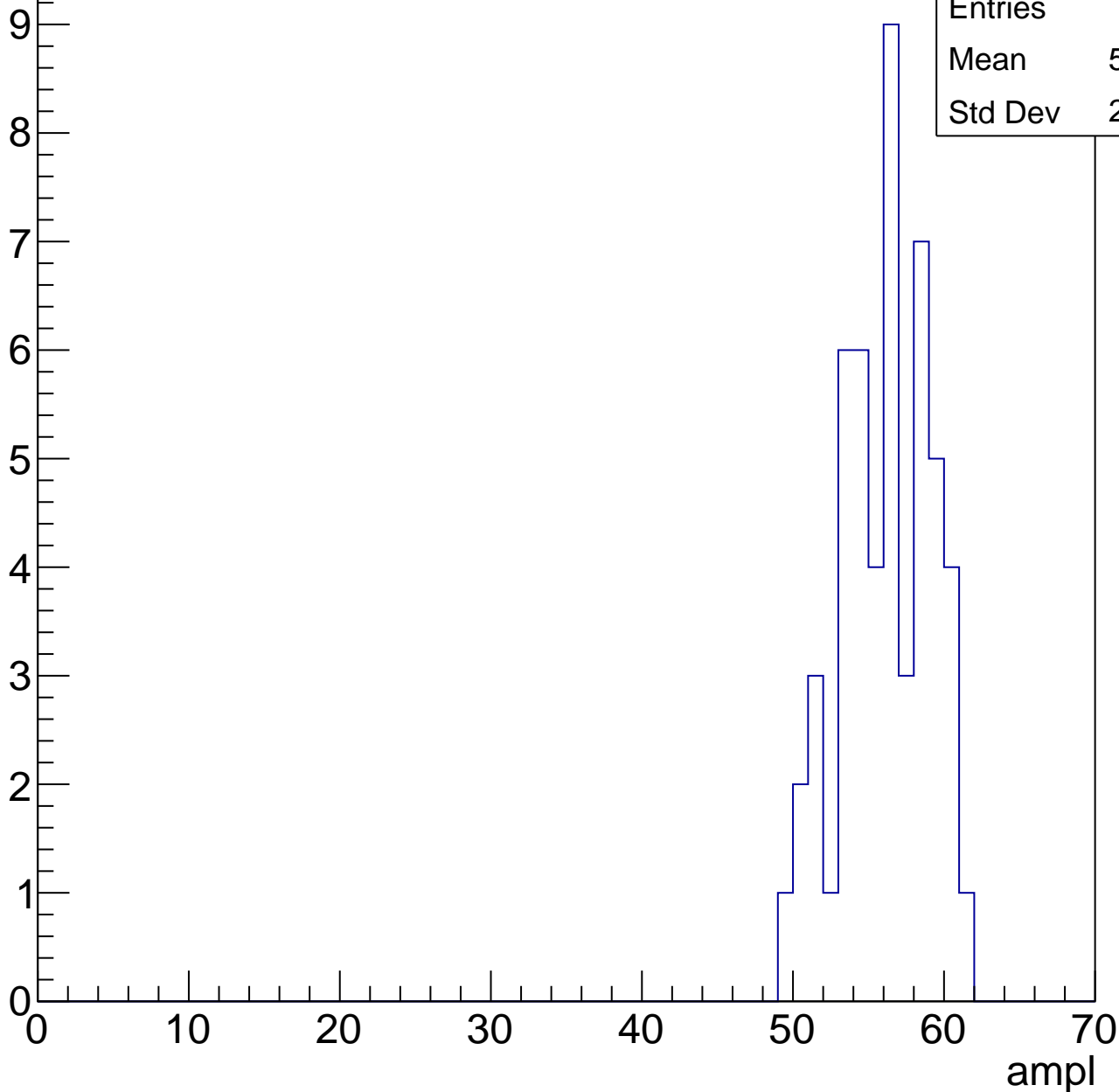


B0L002S, U7-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	55.63
Std Dev	2.948

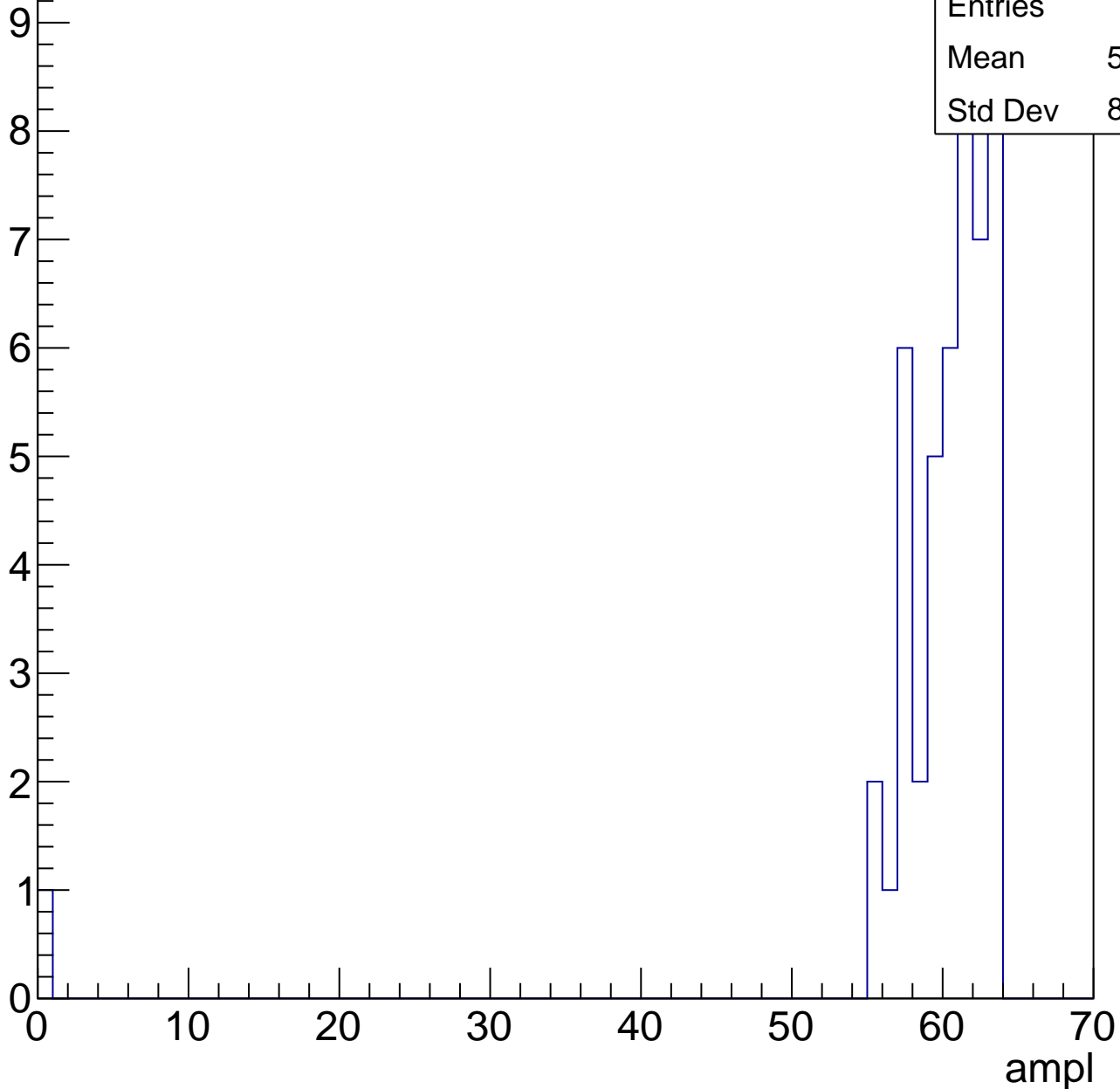


B0L002S, U7-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

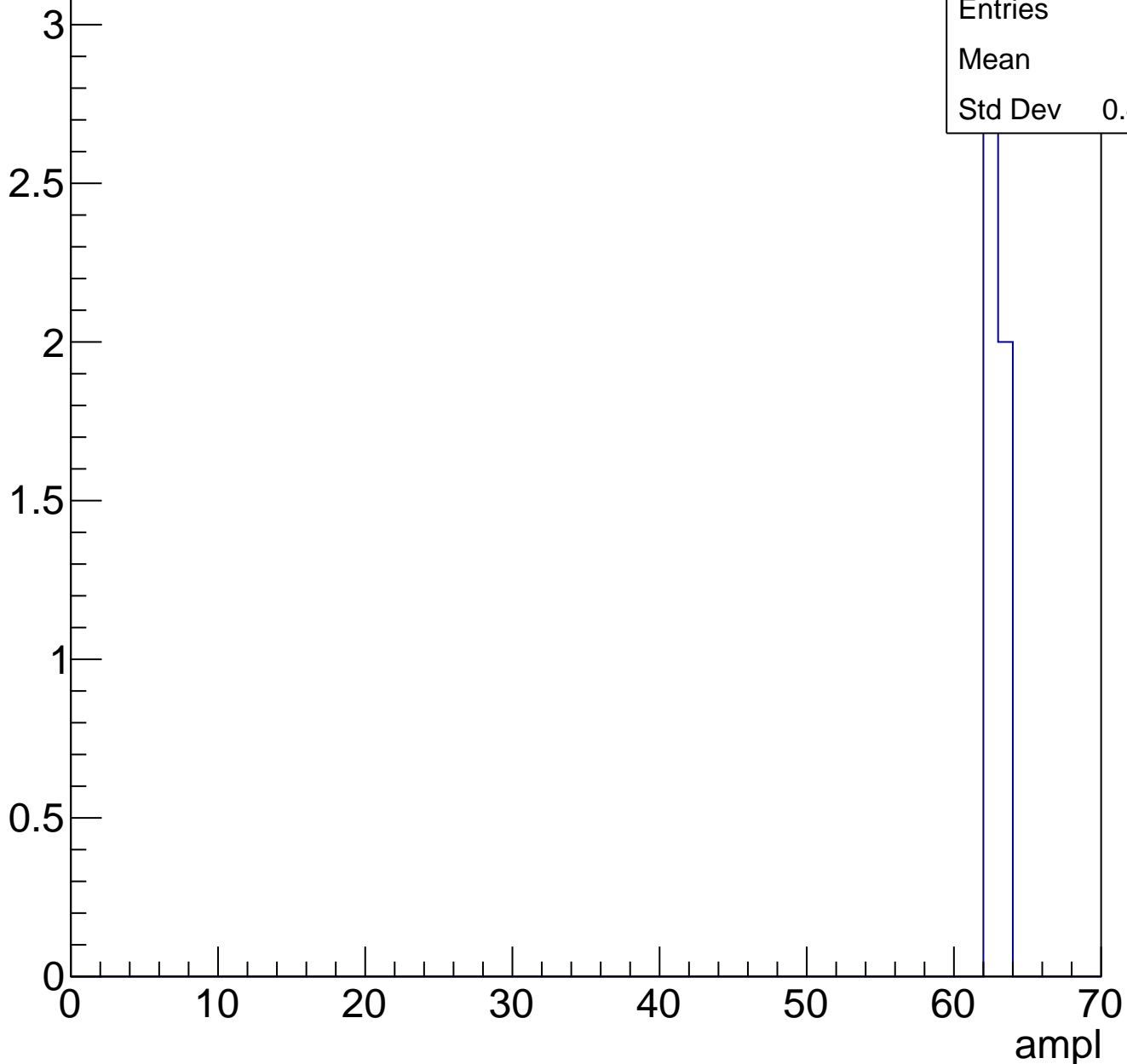
Entries	47
Mean	58.85
Std Dev	8.968



B0L002S, U7-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

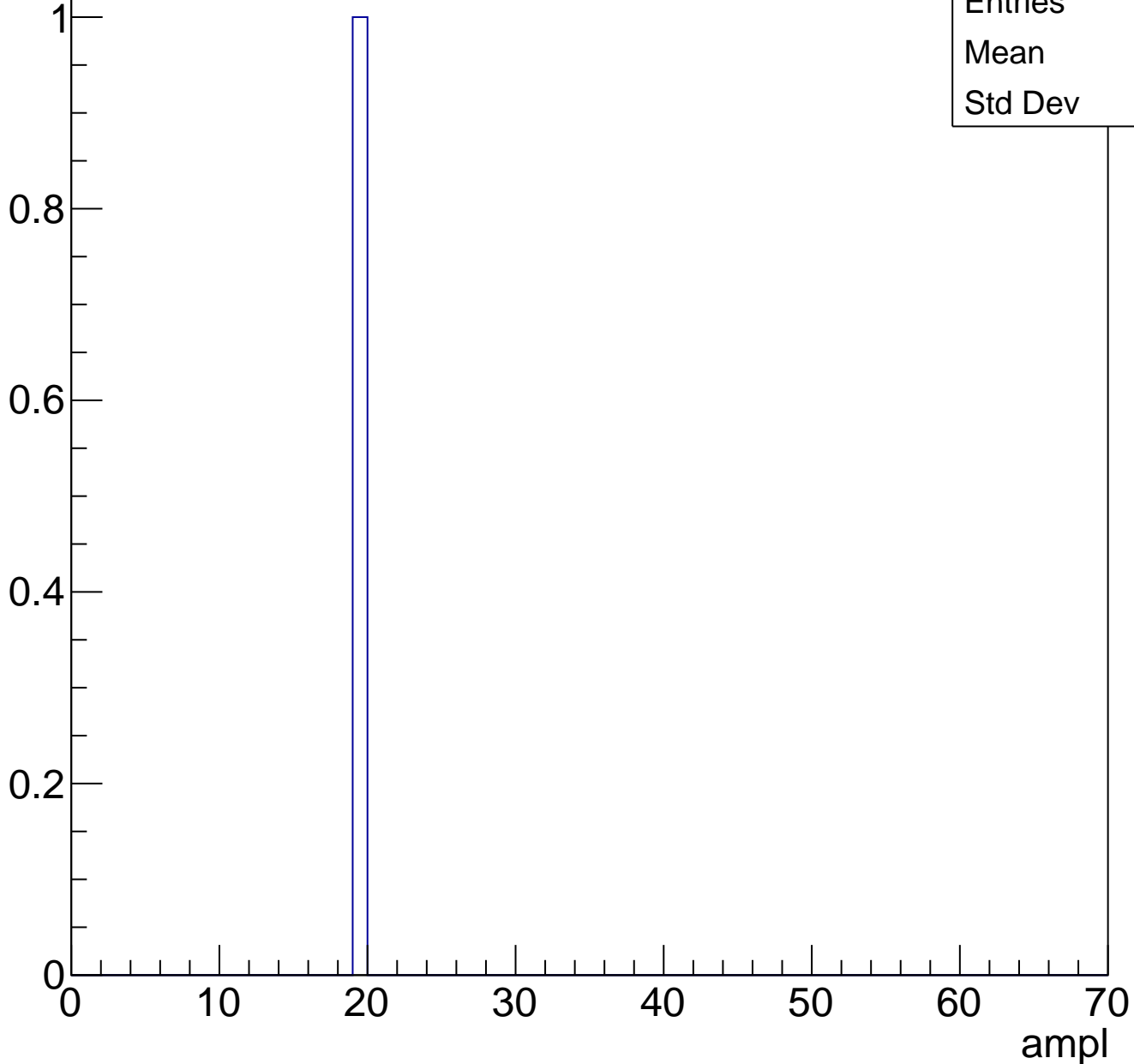
Entry



B0L002S, U7-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	19
Std Dev	0

B0L002S, U7-ch41, adc0

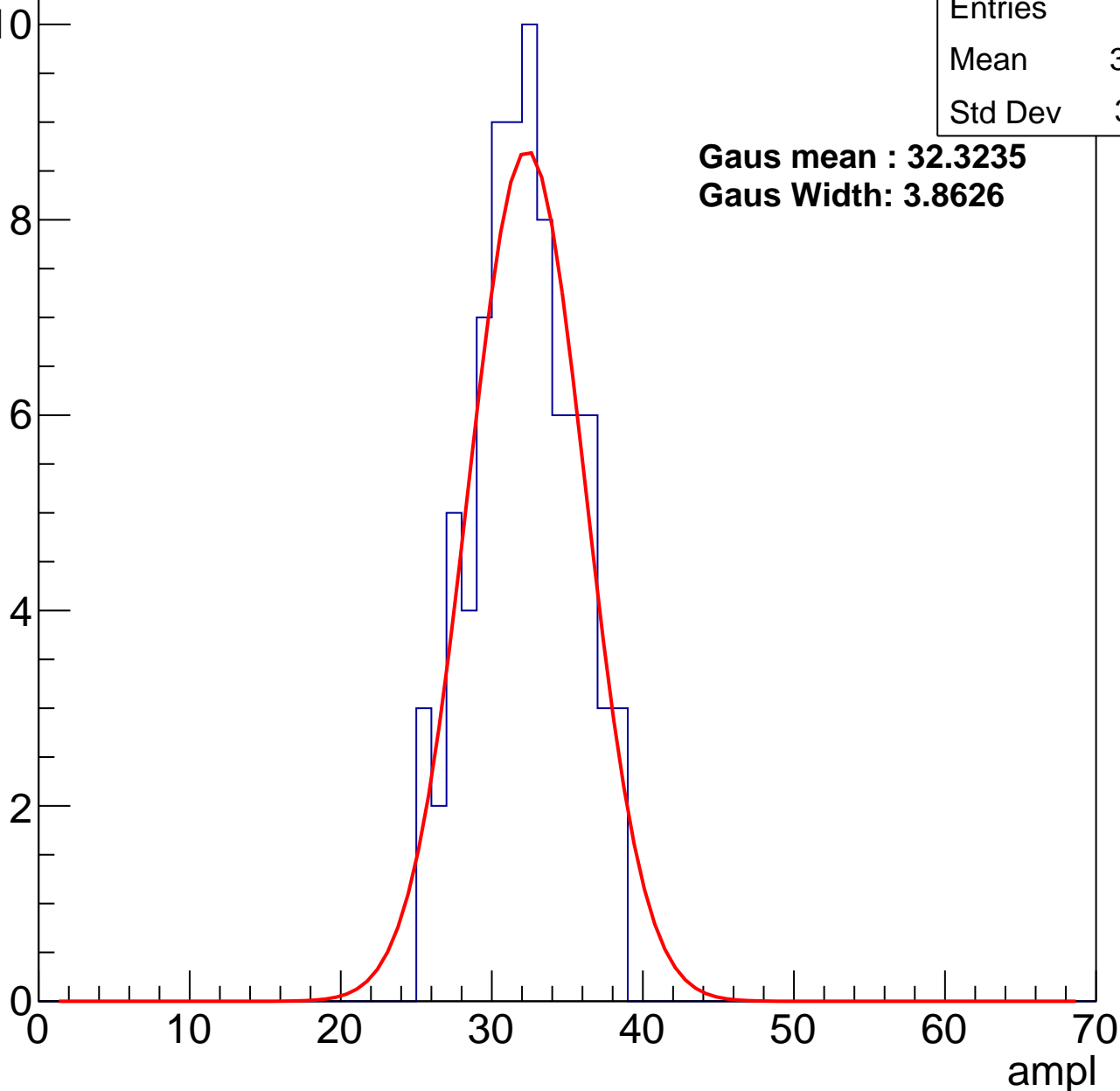
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.67
Std Dev	3.281

Gaus mean : 32.3235

Gaus Width: 3.8626



B0L002S, U7-ch41, adc1

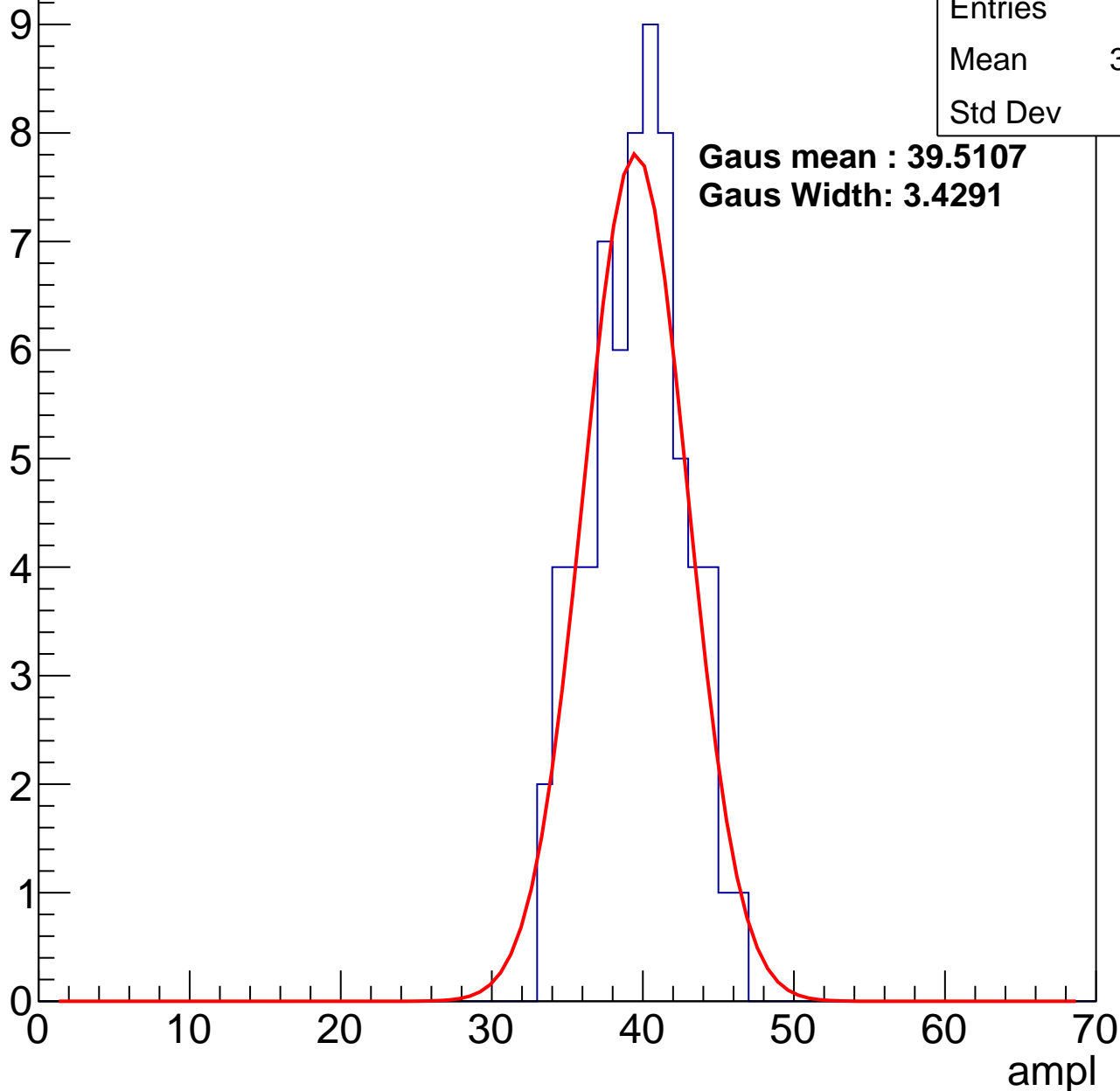
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	39.13
Std Dev	3.09

Gaus mean : 39.5107

Gaus Width: 3.4291



B0L002S, U7-ch41, adc2

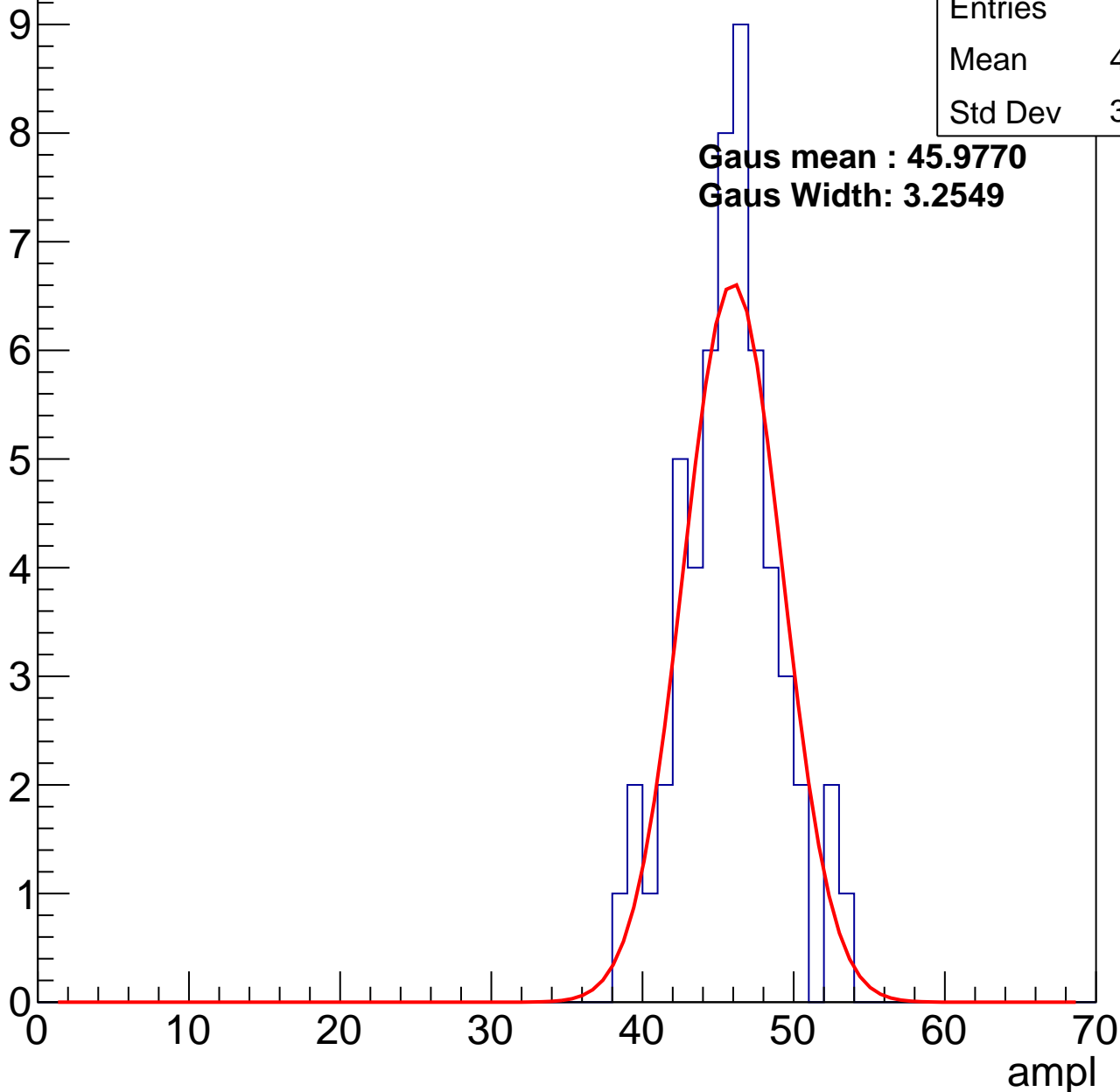
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	45.29
Std Dev	3.189

Gaus mean : 45.9770

Gaus Width: 3.2549

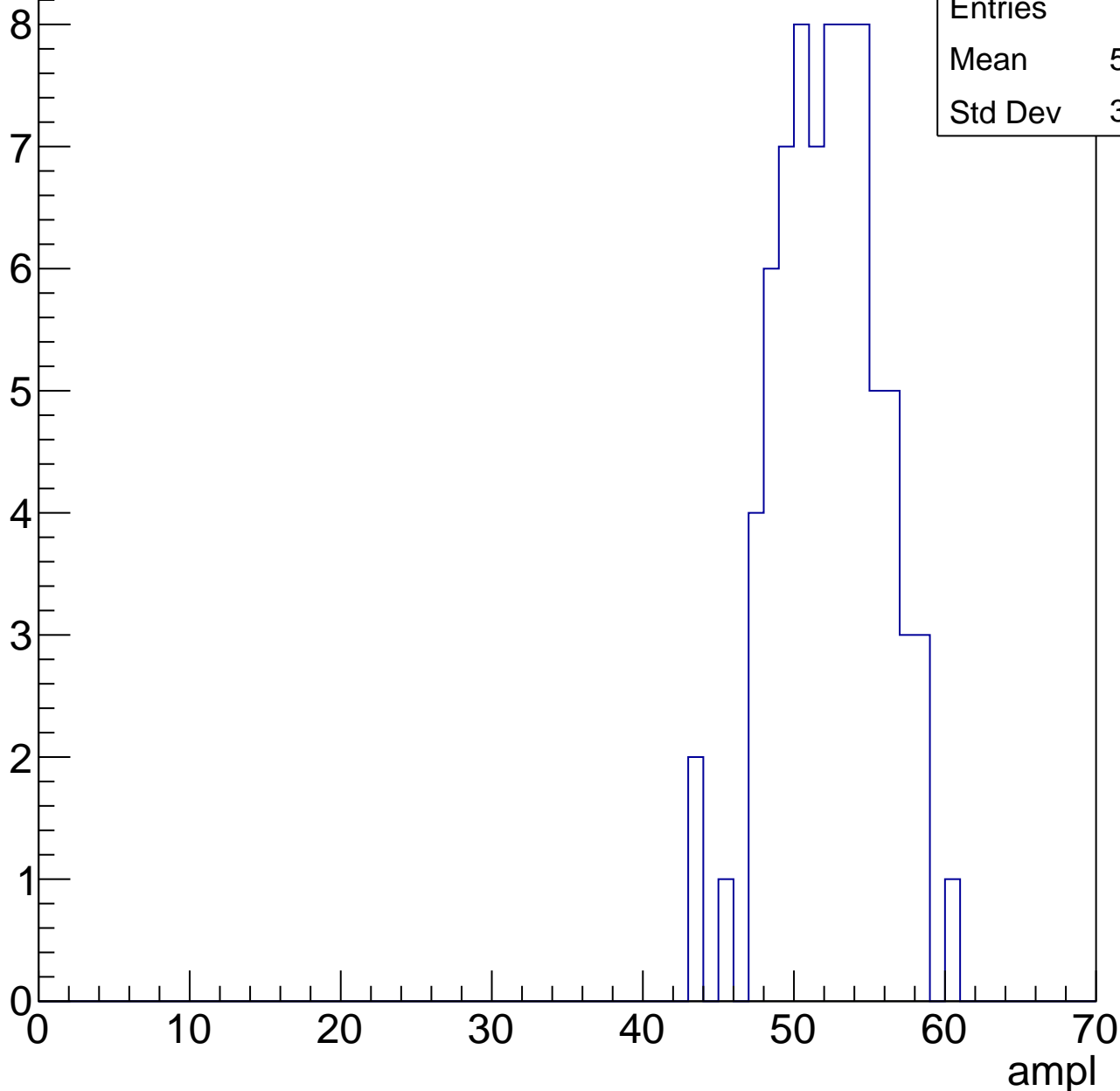


B0L002S, U7-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	51.83
Std Dev	3.477



B0L002S, U7-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

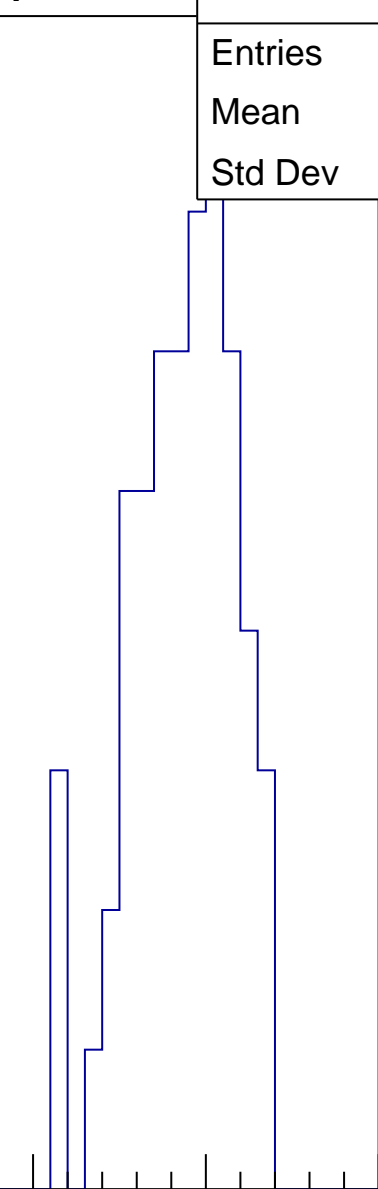
Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.12
Std Dev	3.006

ampl

0 10 20 30 40 50 60 70

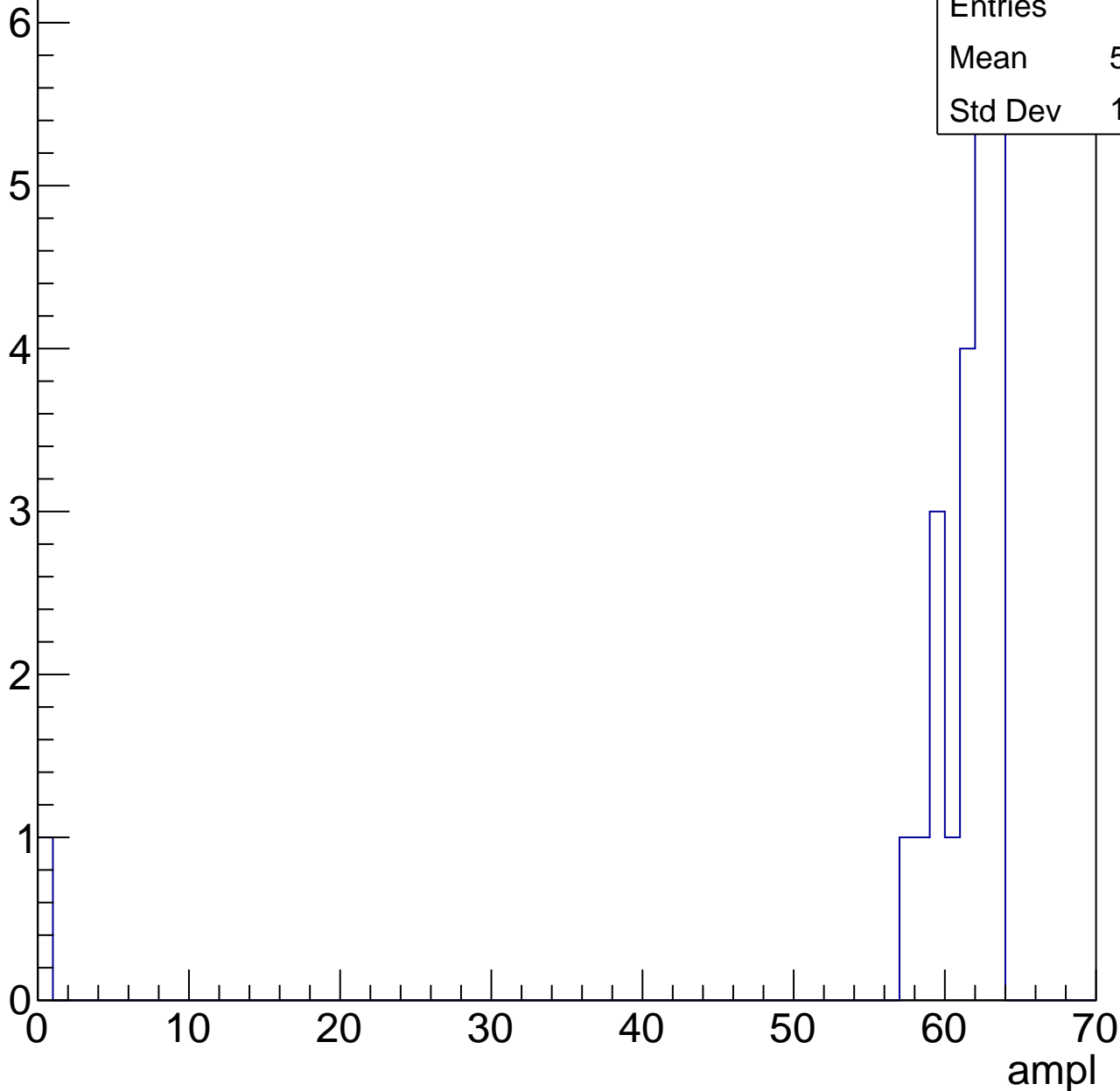


B0L002S, U7-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.52
Std Dev	12.59



B0L002S, U7-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch42, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	30.6
Std Dev	4.71

Gaus mean : 31.8051

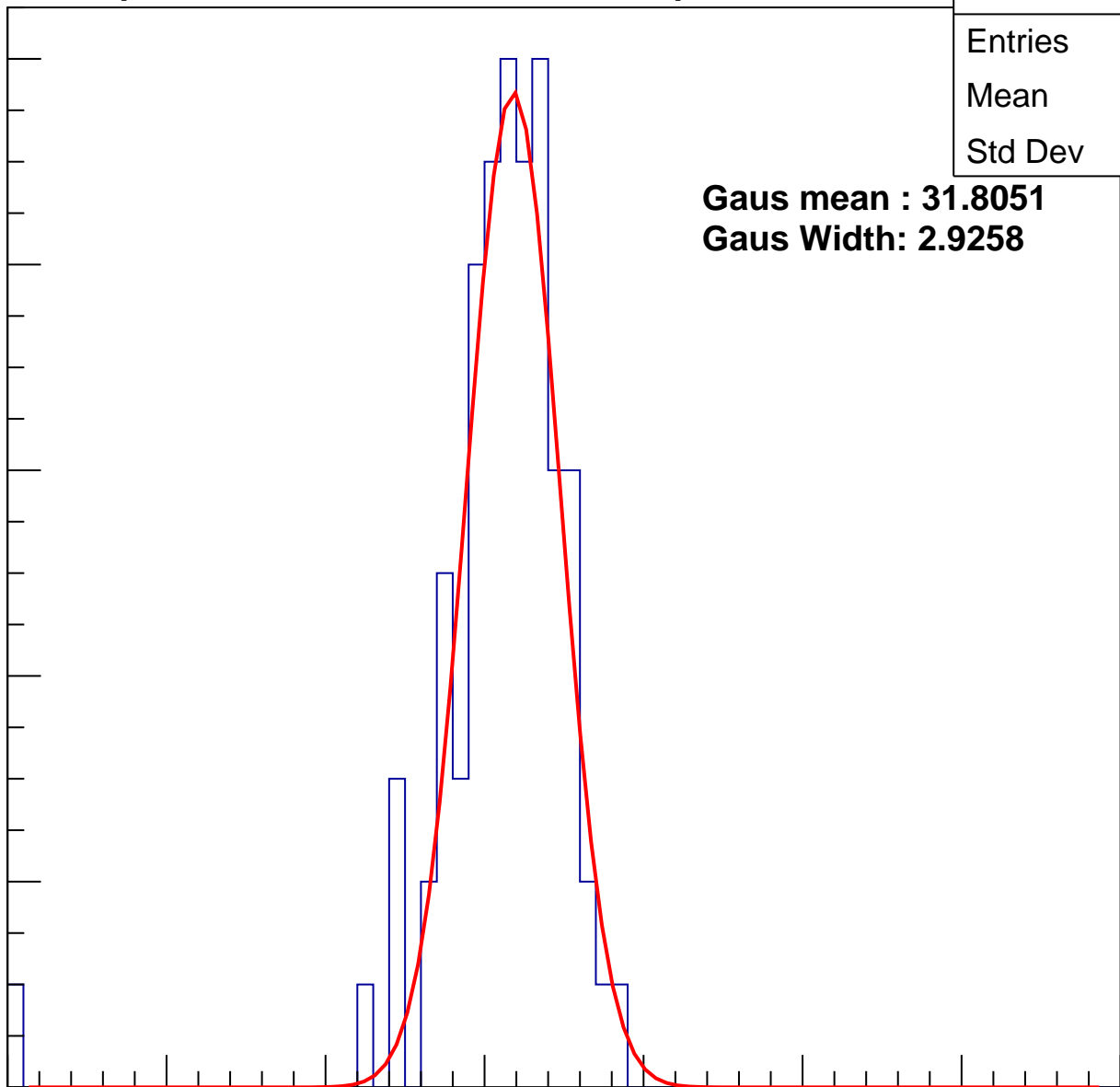
Gaus Width: 2.9258

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch42, adc1

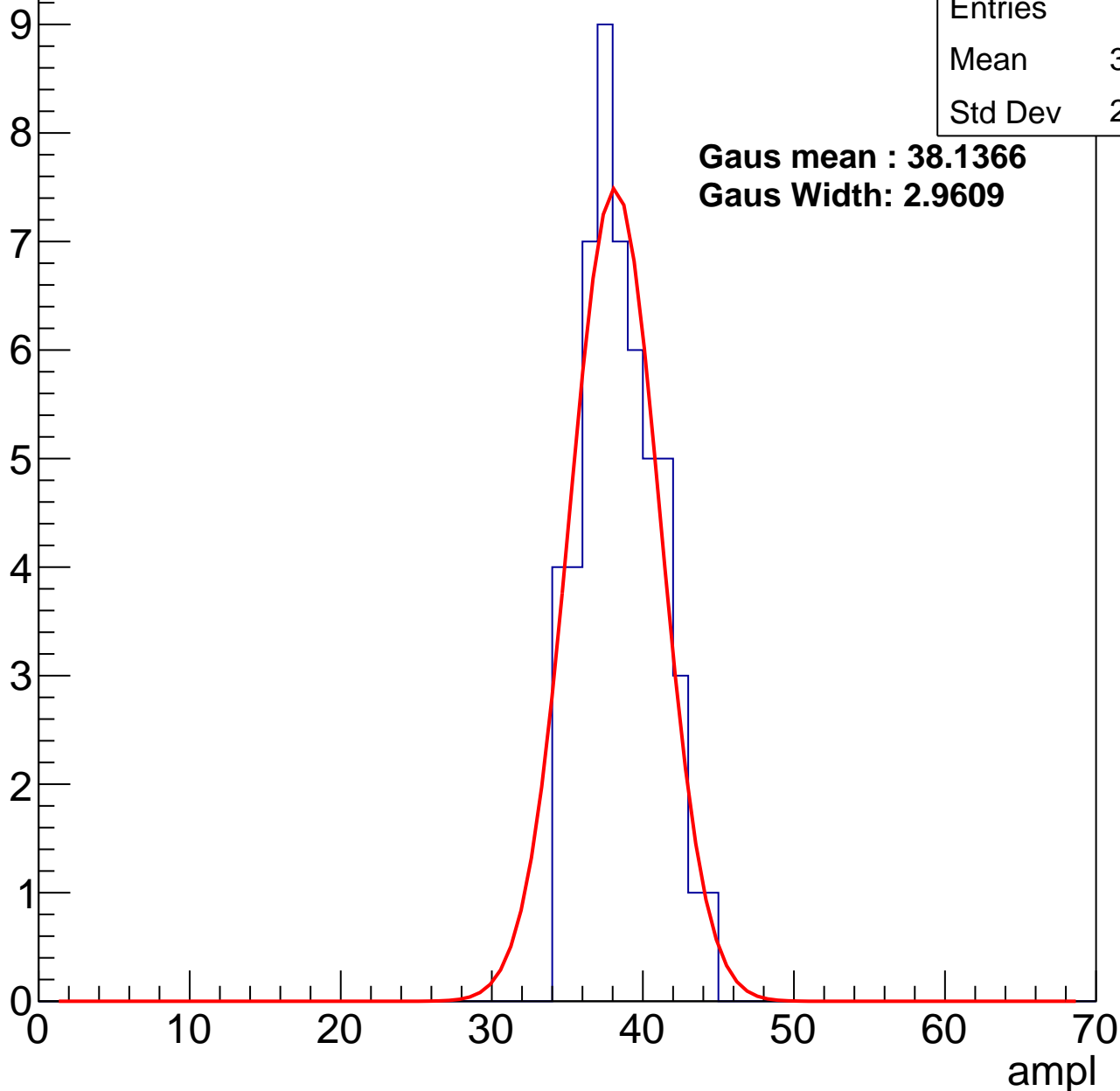
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	38.06
Std Dev	2.468

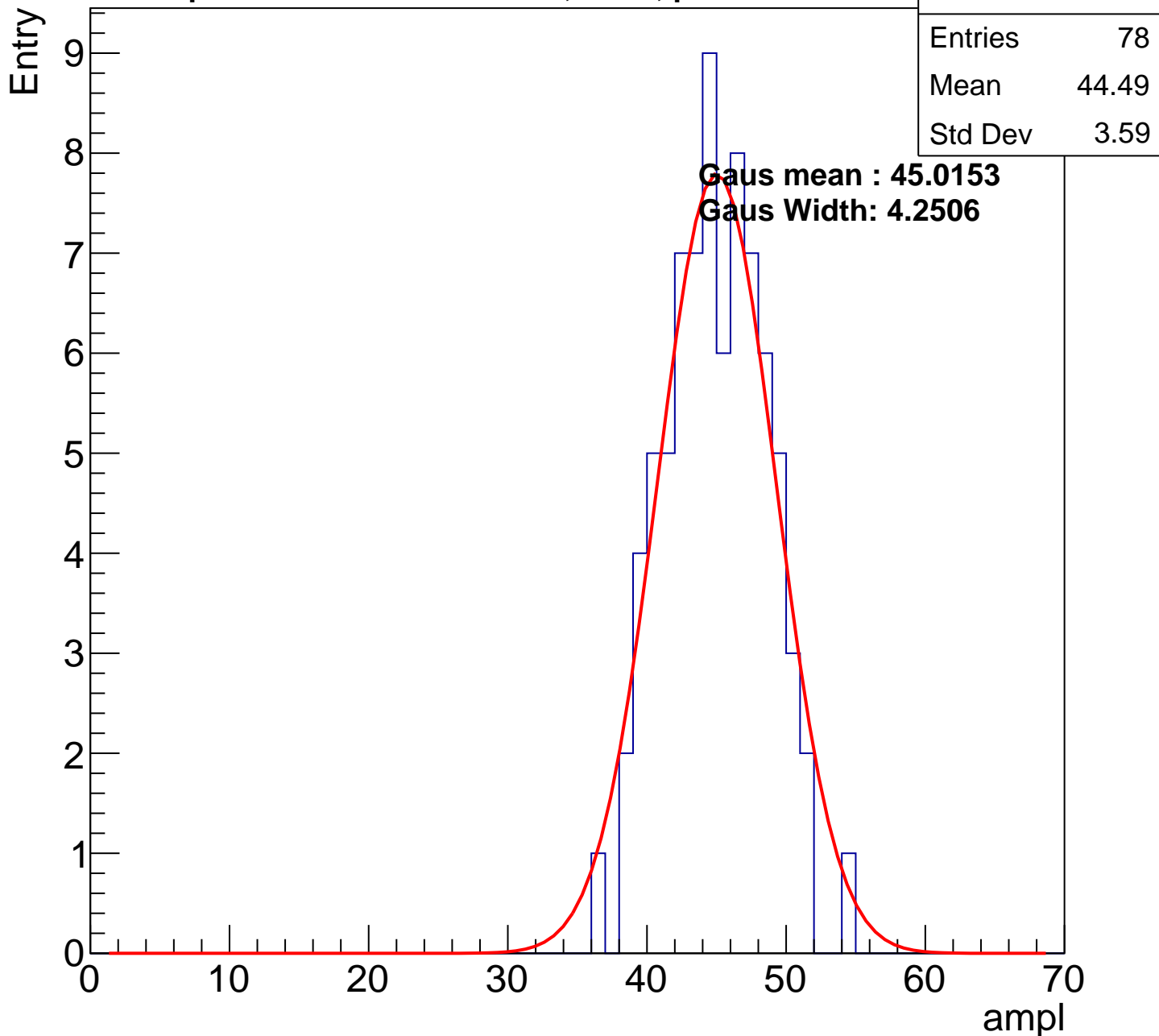
Gaus mean : 38.1366

Gaus Width: 2.9609



B0L002S, U7-ch42, adc2

calib_packv5_042523_0143.root, FC#8, port C1

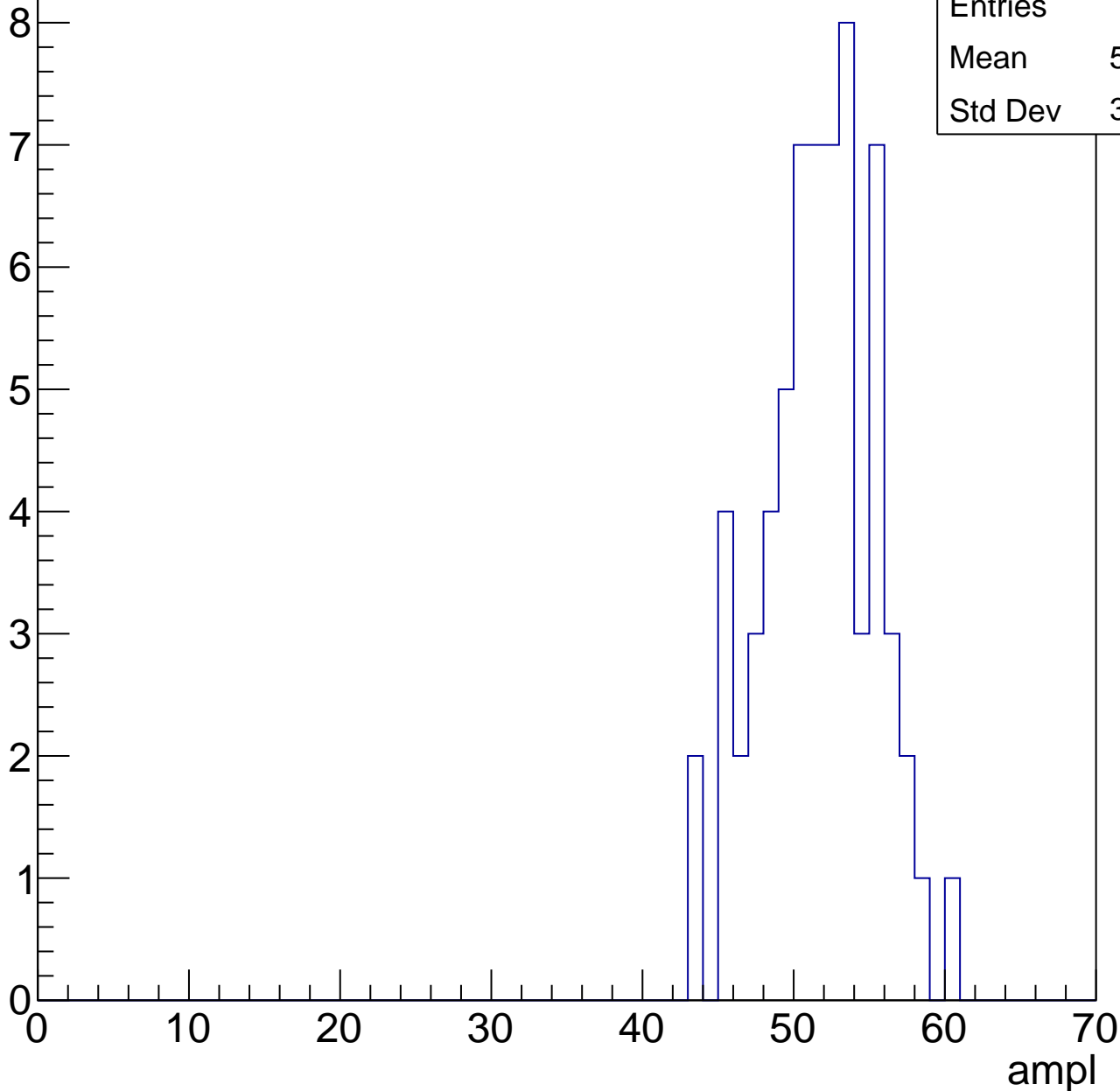


B0L002S, U7-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

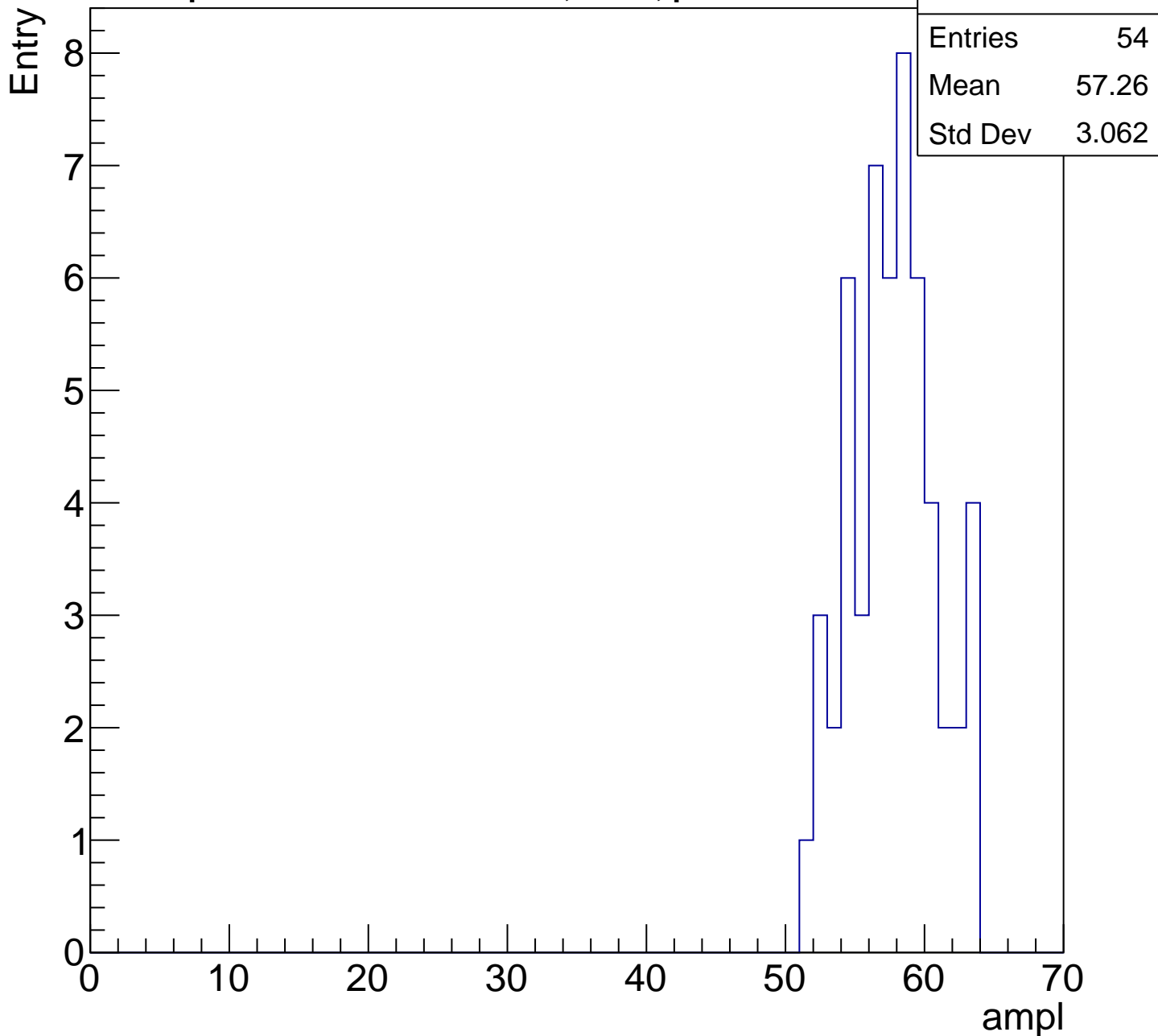
Entry

Entries	66
Mean	51.18
Std Dev	3.664



B0L002S, U7-ch42, adc4

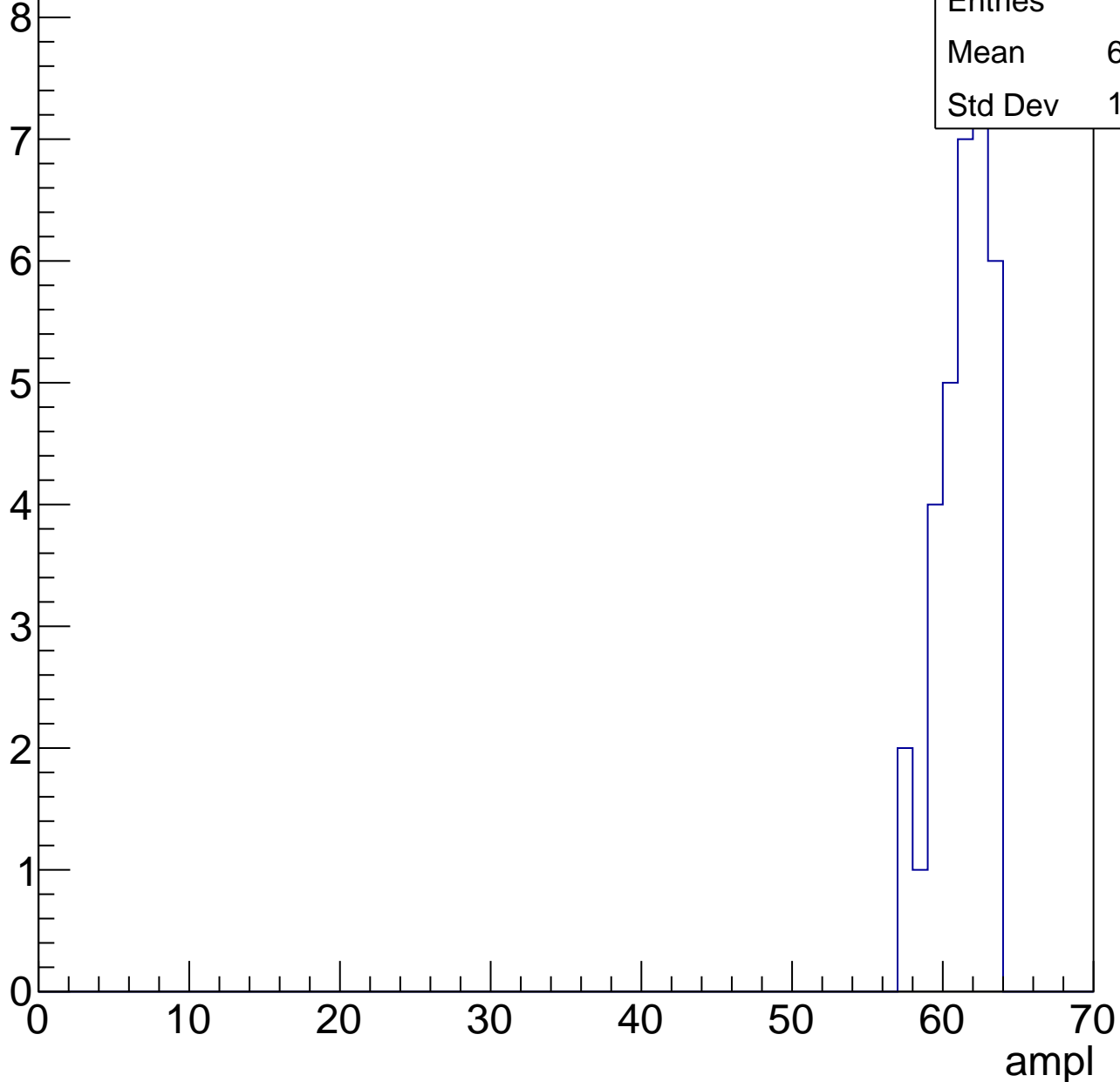
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

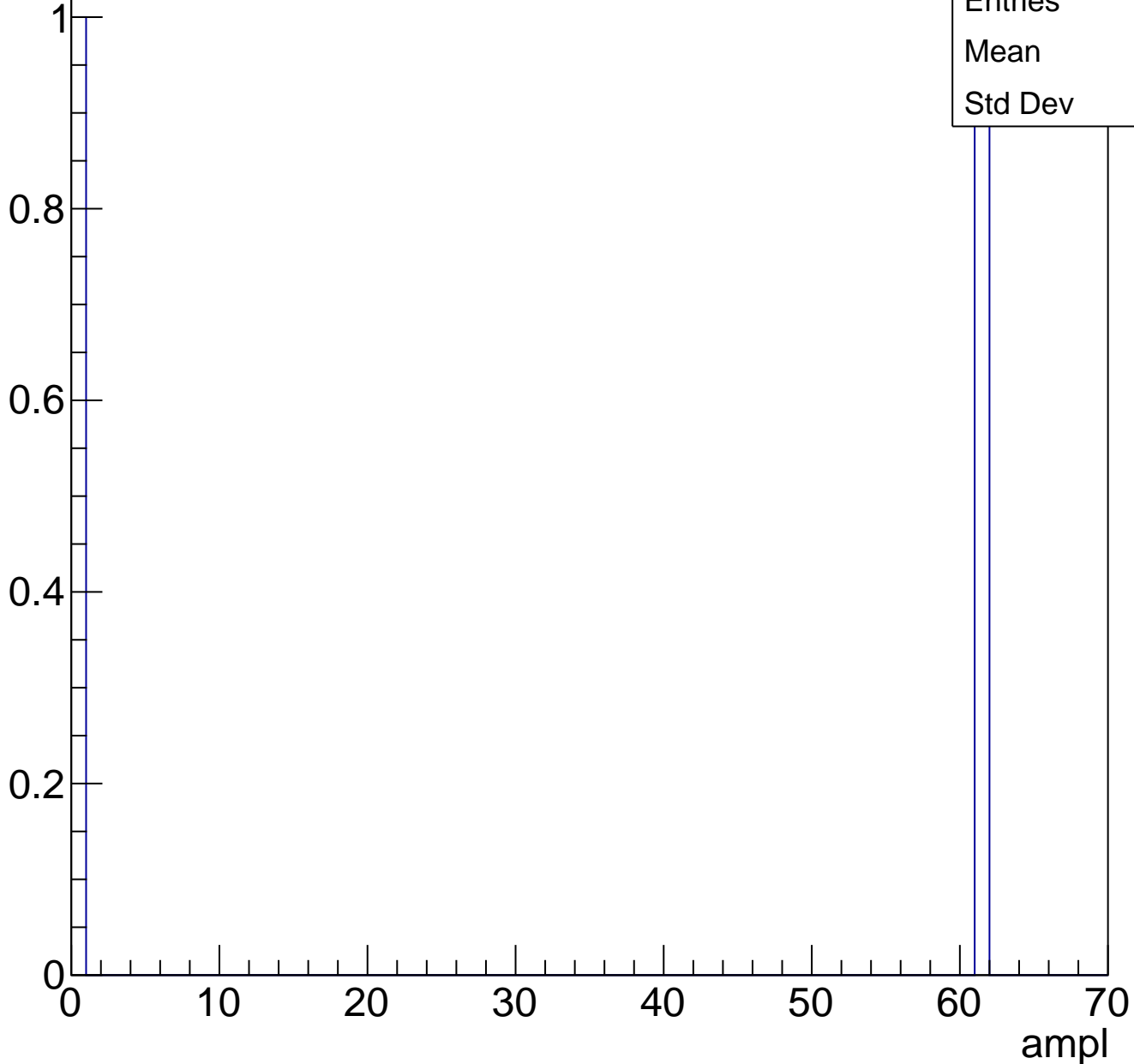
Entry



B0L002S, U7-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch43, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	31.01
Std Dev	3.553

Gaus mean : 31.9264

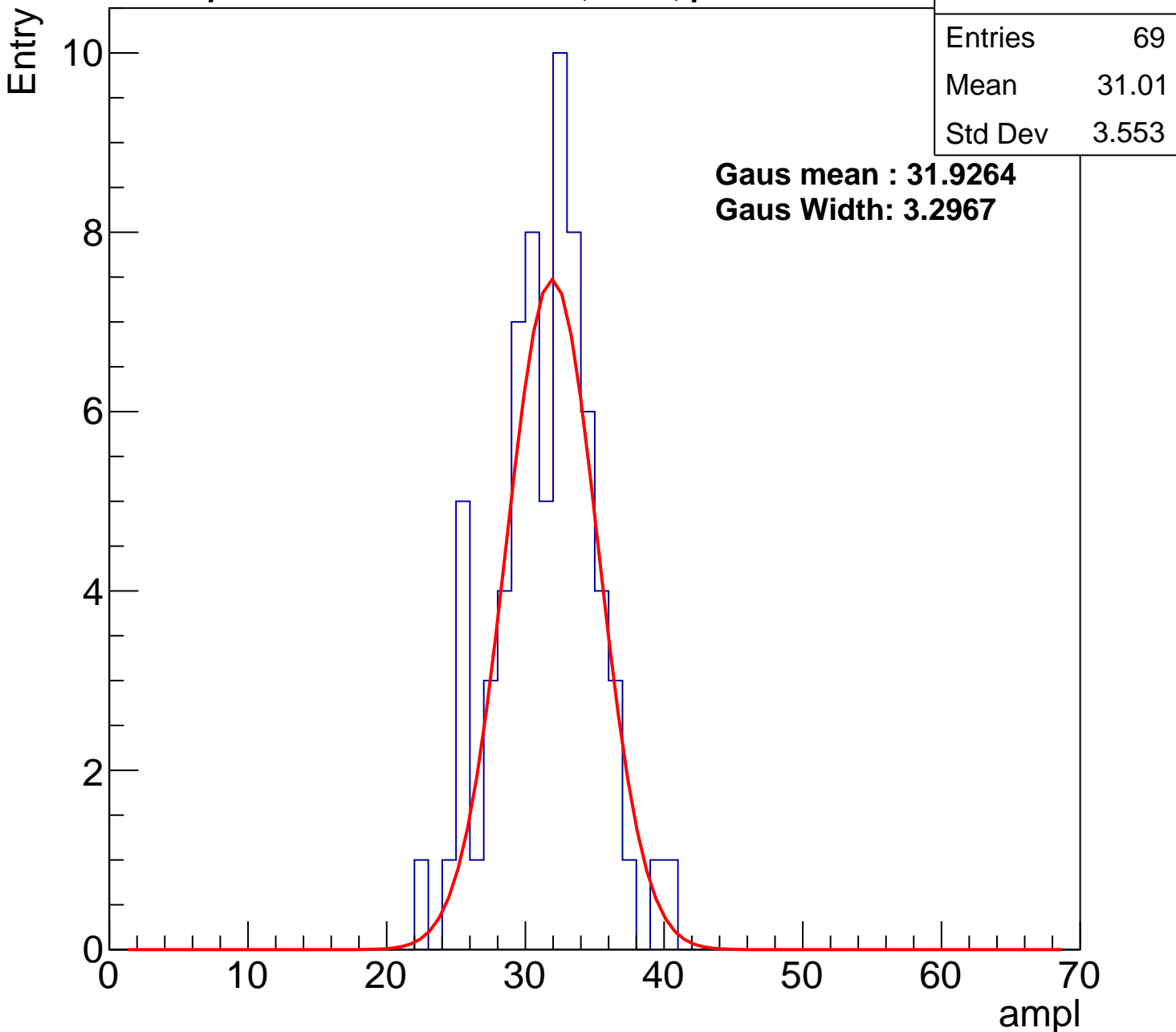
Gaus Width: 3.2967

Entry

10
8
6
4
2
0

ampl

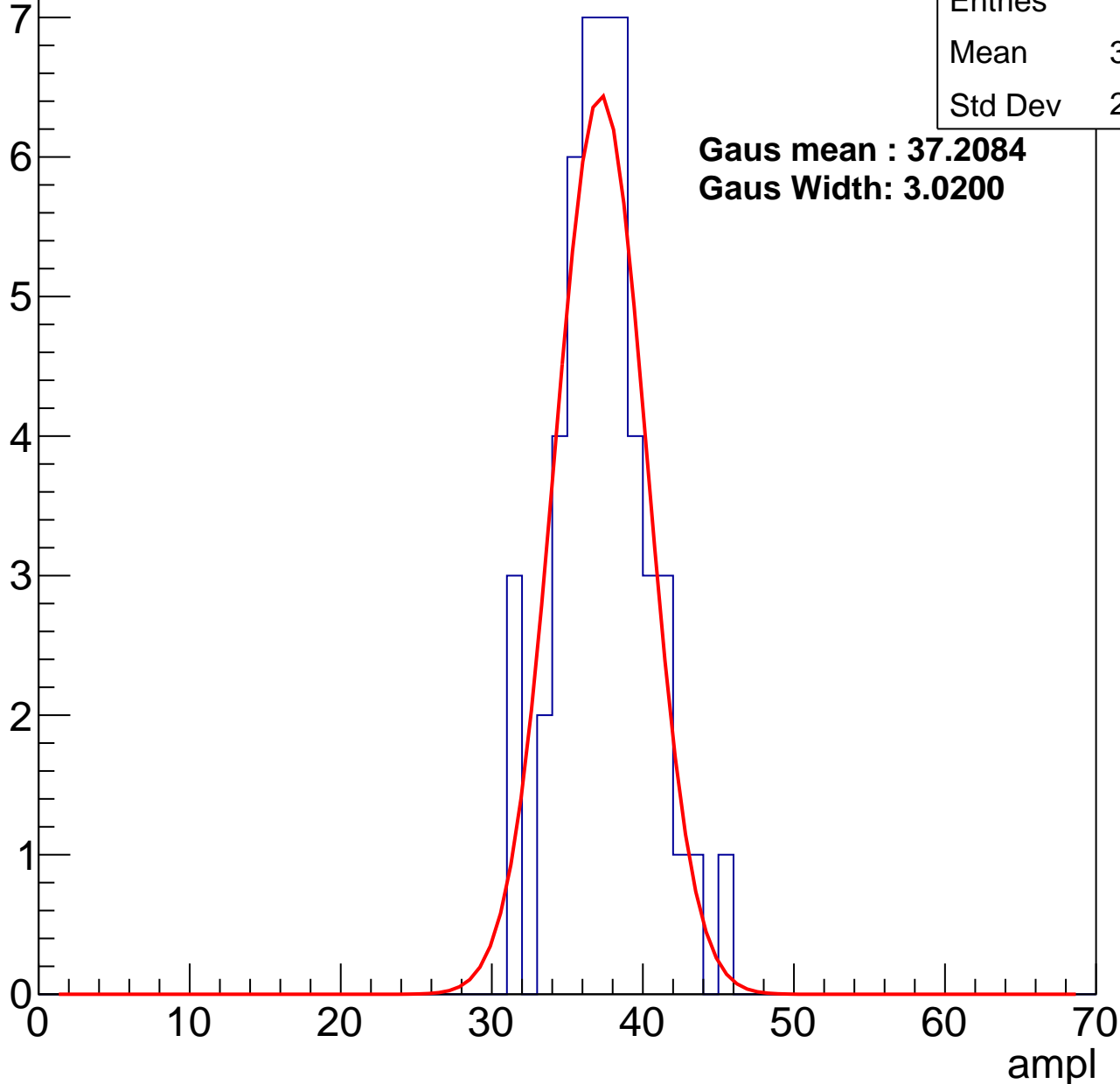
0 10 20 30 40 50 60 70



B0L002S, U7-ch43, adc1

calib_packv5_042523_0143.root, FC#8, port C1

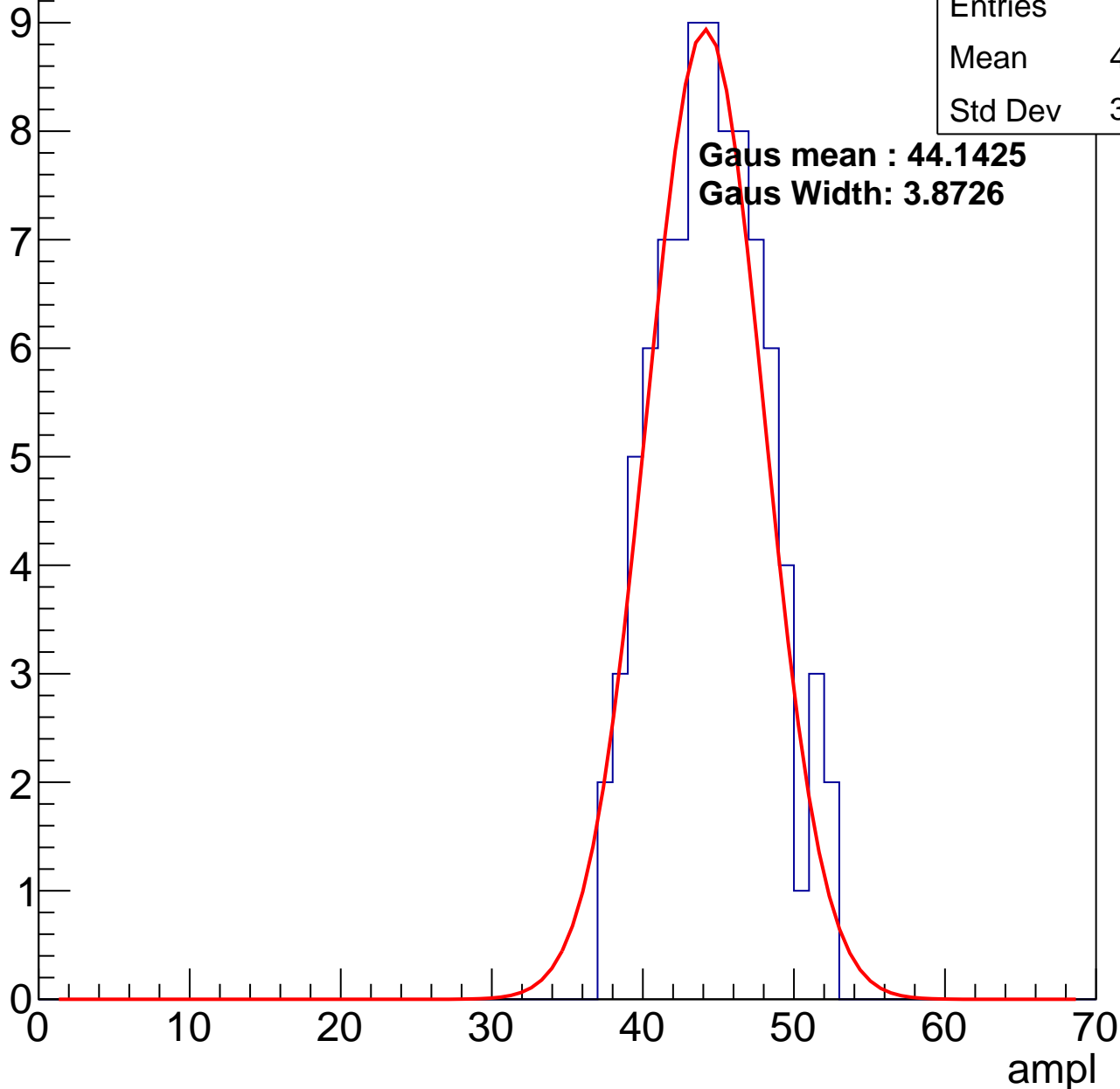
Entry



B0L002S, U7-ch43, adc2

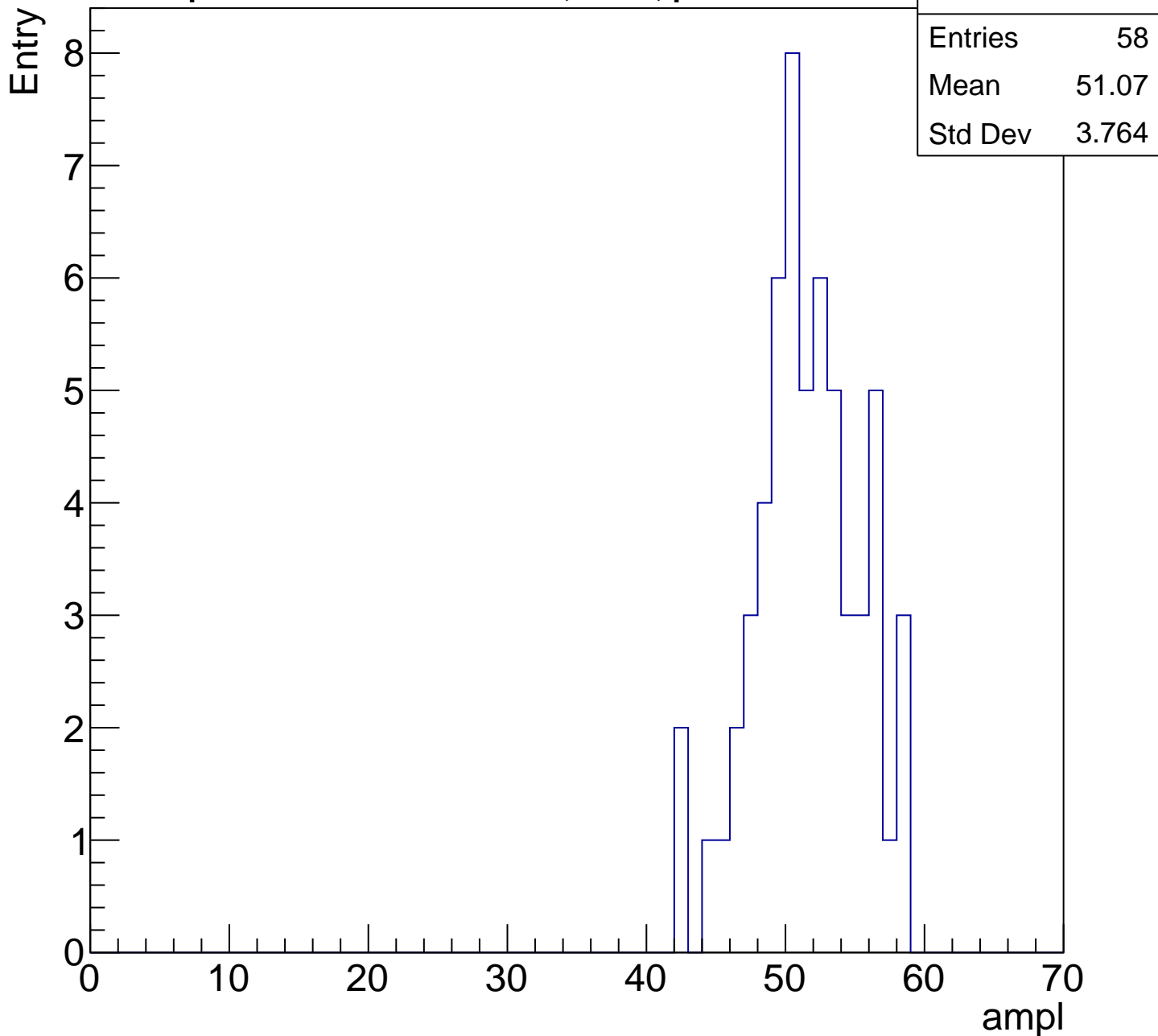
calib_packv5_042523_0143.root, FC#8, port C1

Entry



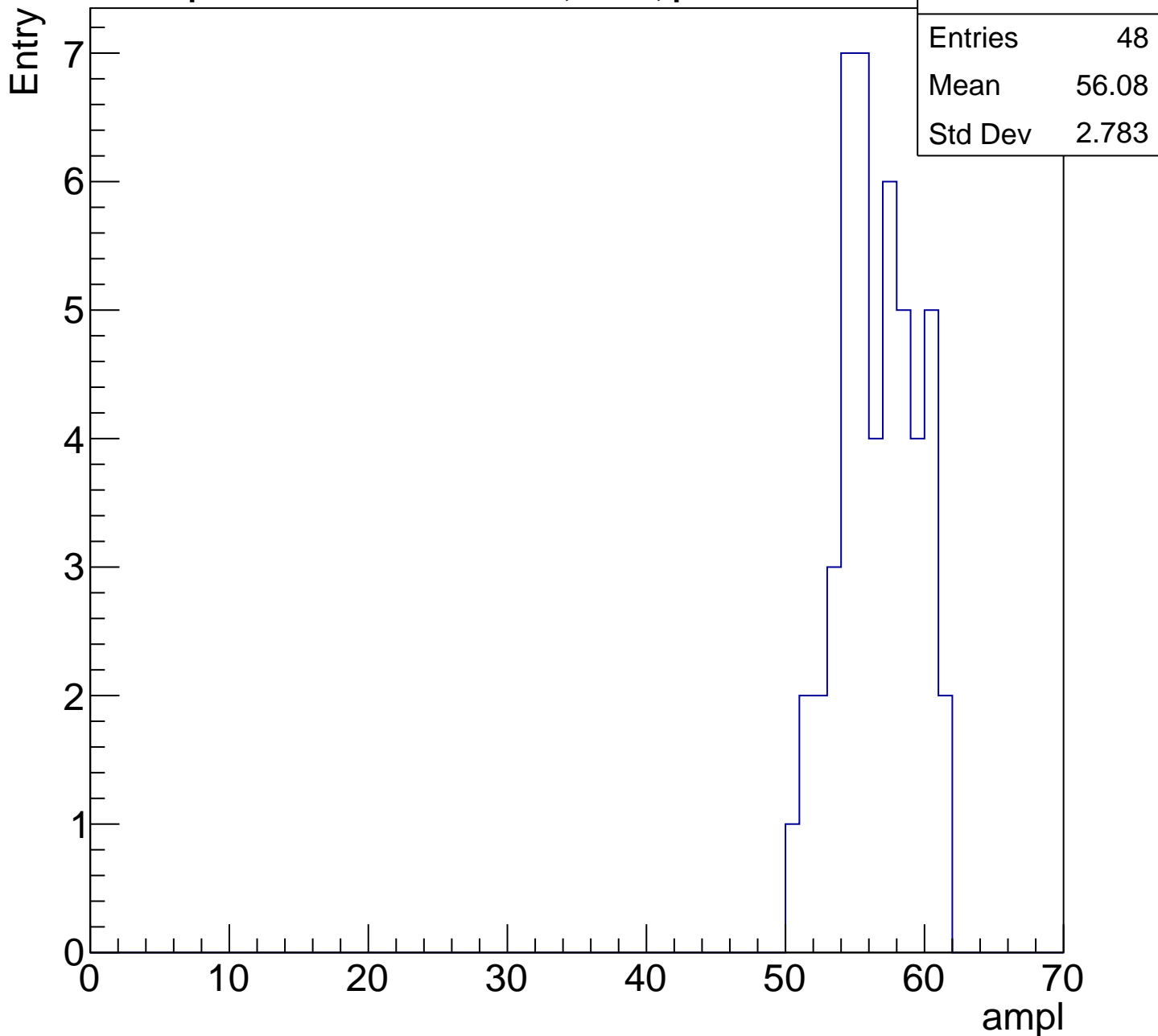
B0L002S, U7-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

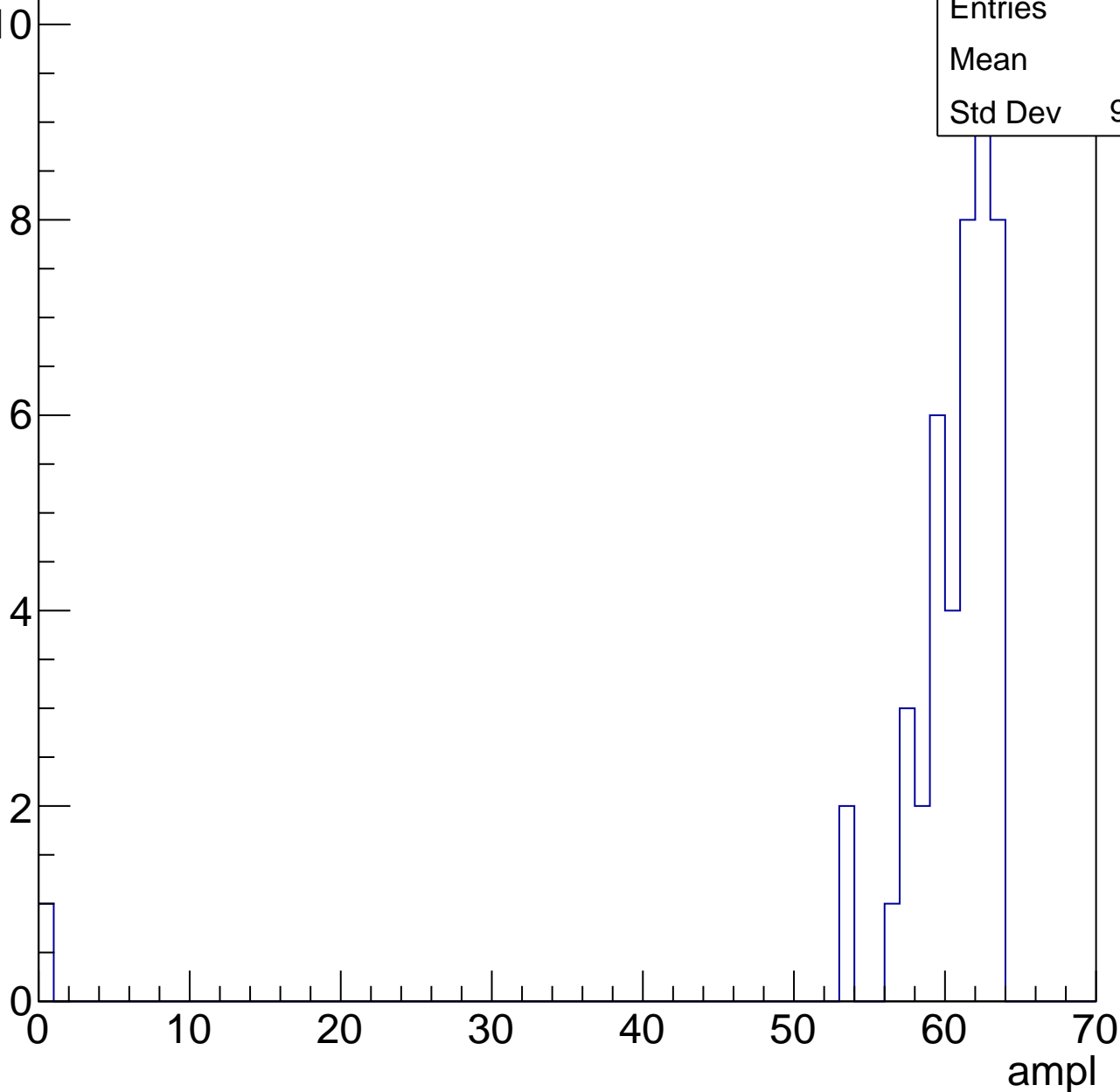


B0L002S, U7-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59
Std Dev	9.226



B0L002S, U7-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch44, adc0

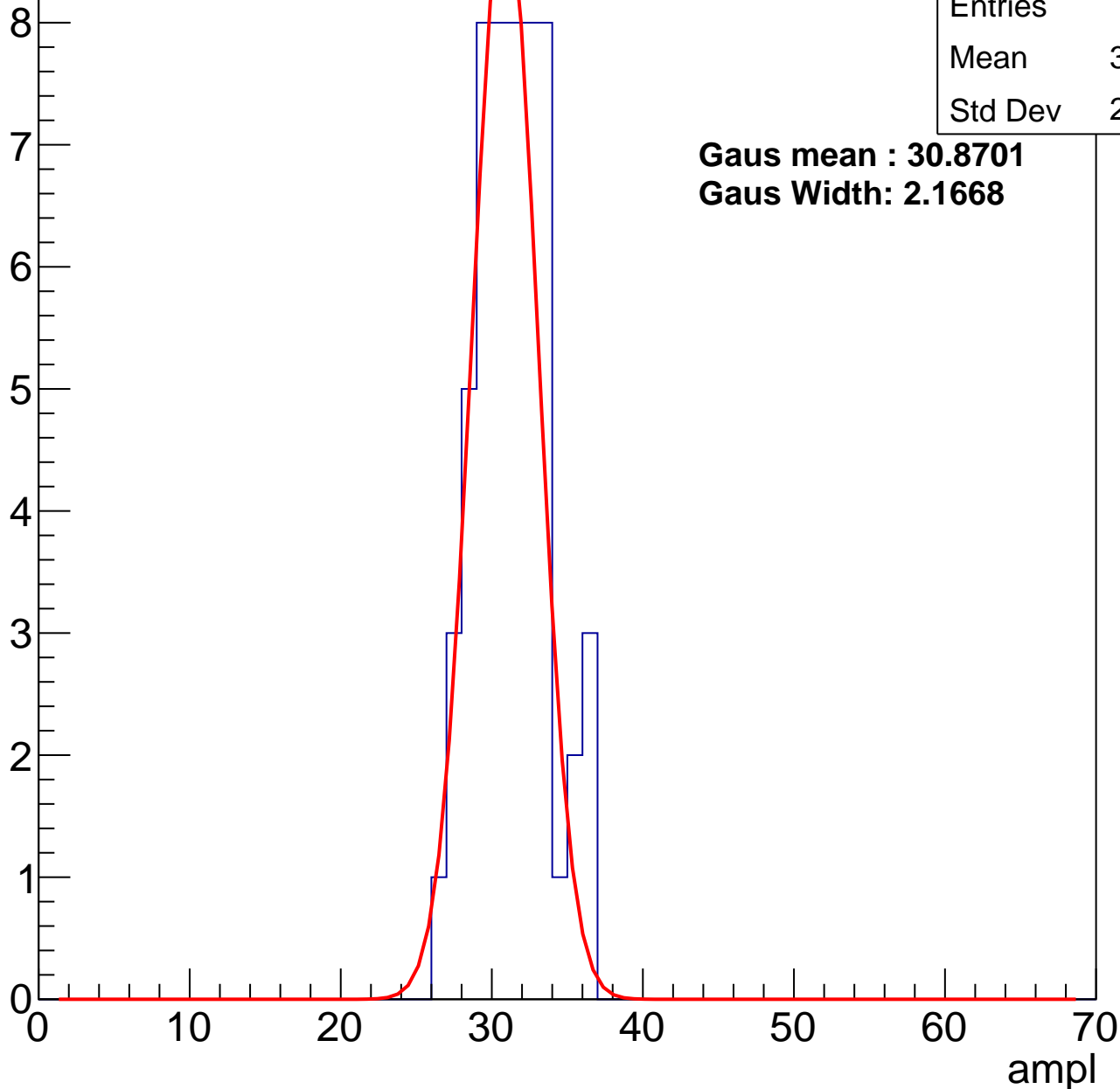
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	30.89
Std Dev	2.387

Gaus mean : 30.8701

Gaus Width: 2.1668



B0L002S, U7-ch44, adc1

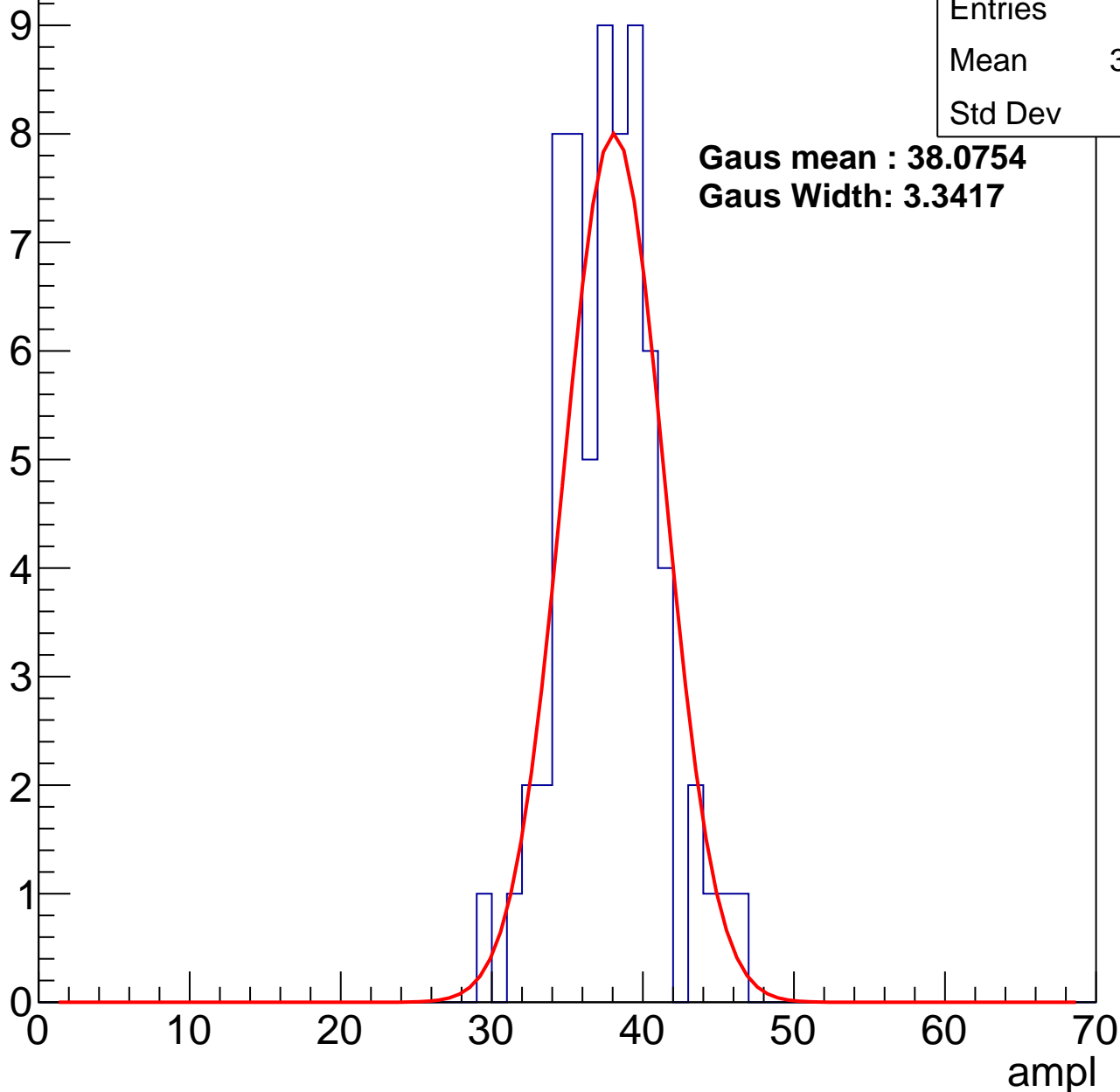
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.28
Std Dev	3.24

Gaus mean : 38.0754

Gaus Width: 3.3417



B0L002S, U7-ch44, adc2

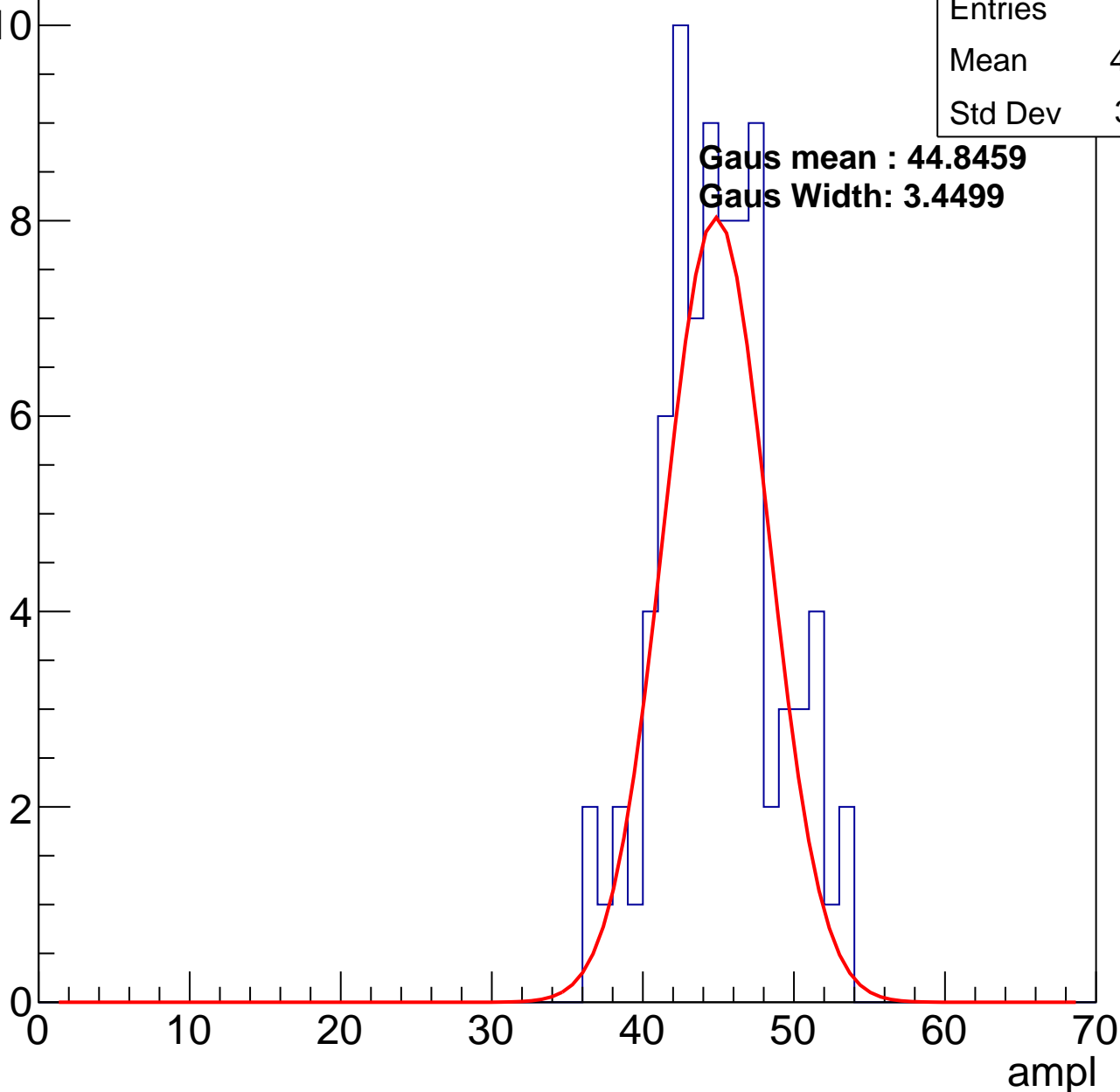
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	44.55
Std Dev	3.781

Gaus mean : 44.8459

Gaus Width: 3.4499

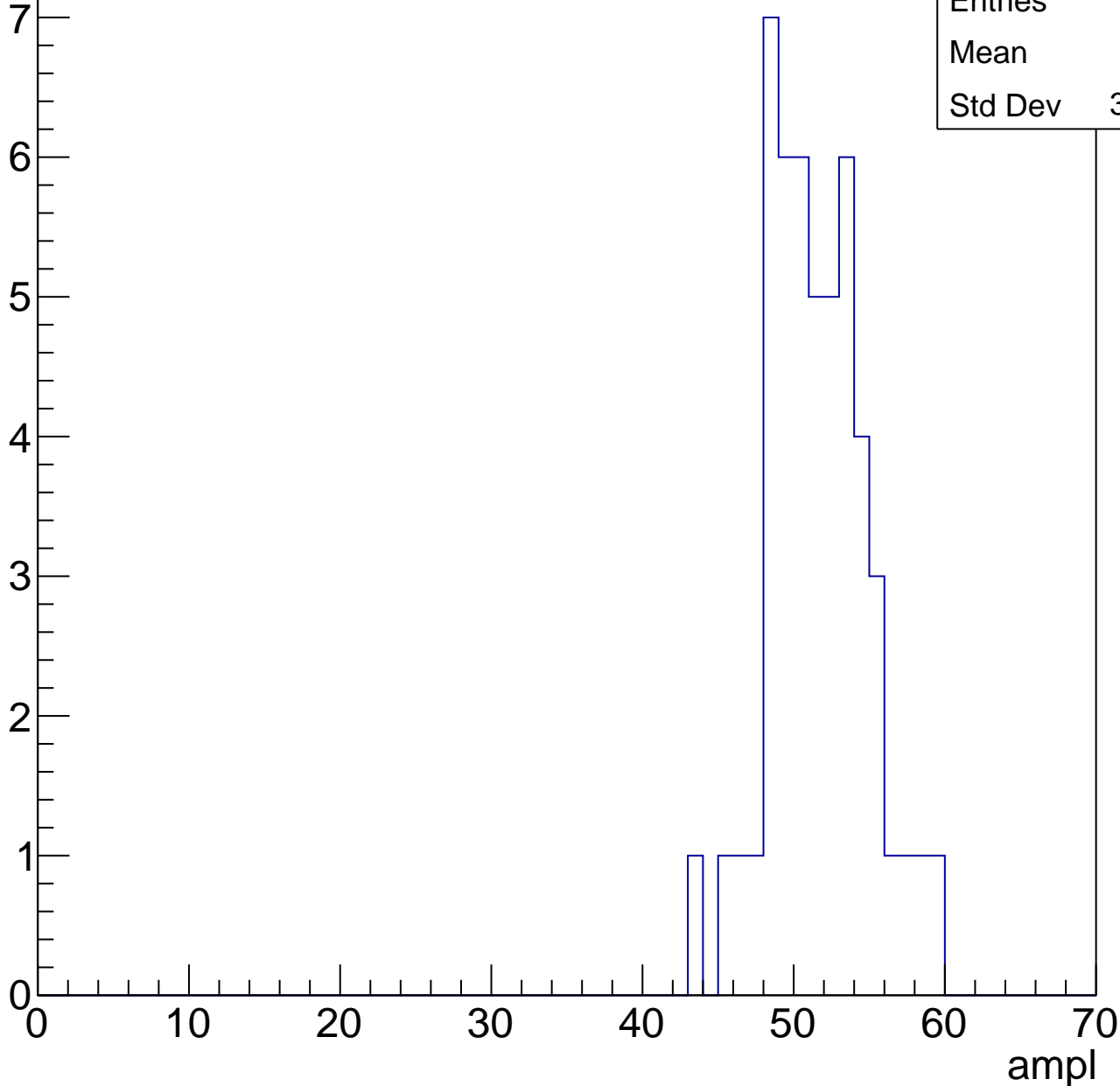


B0L002S, U7-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

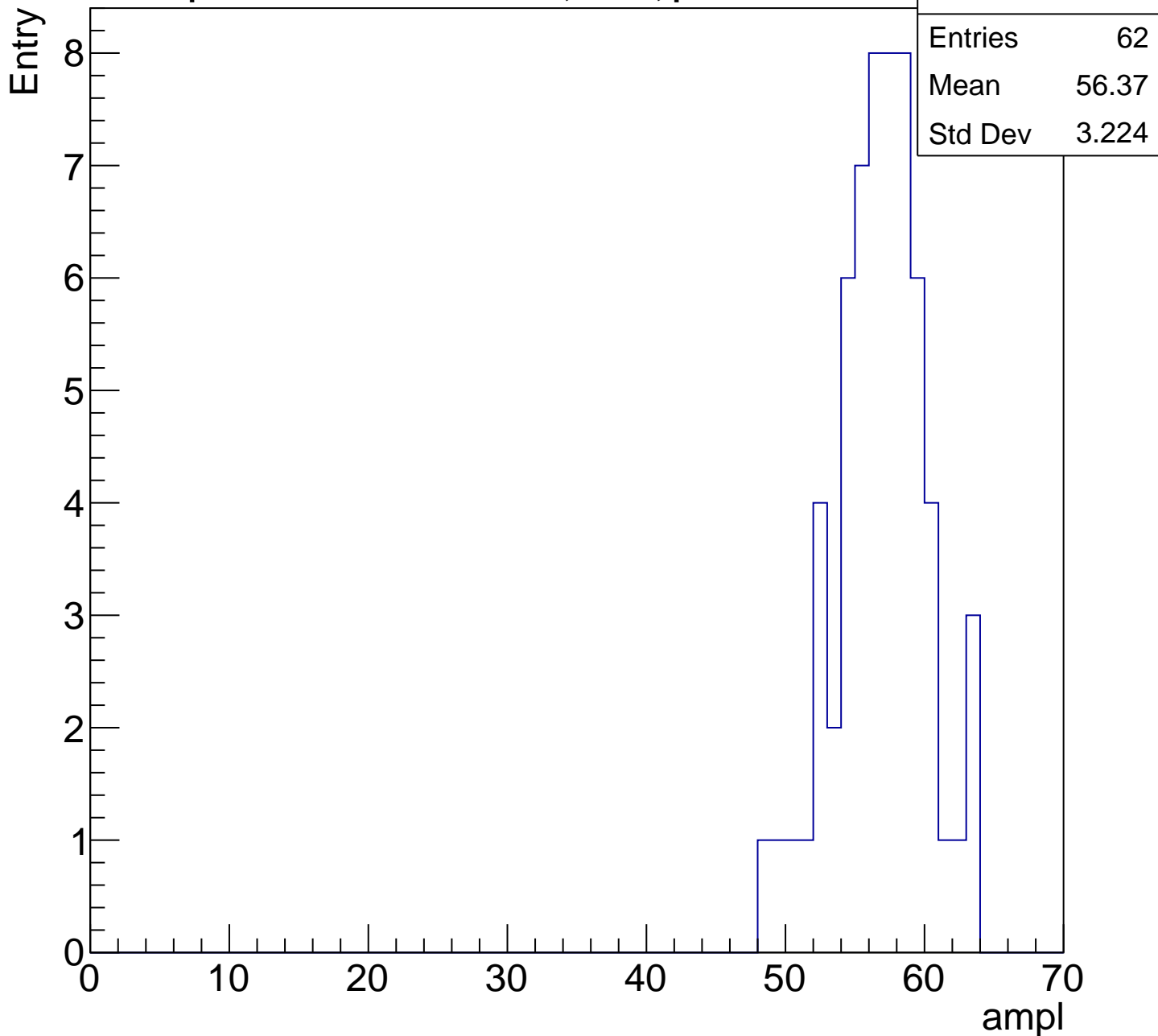
Entry

Entries	50
Mean	51.1
Std Dev	3.226



B0L002S, U7-ch44, adc4

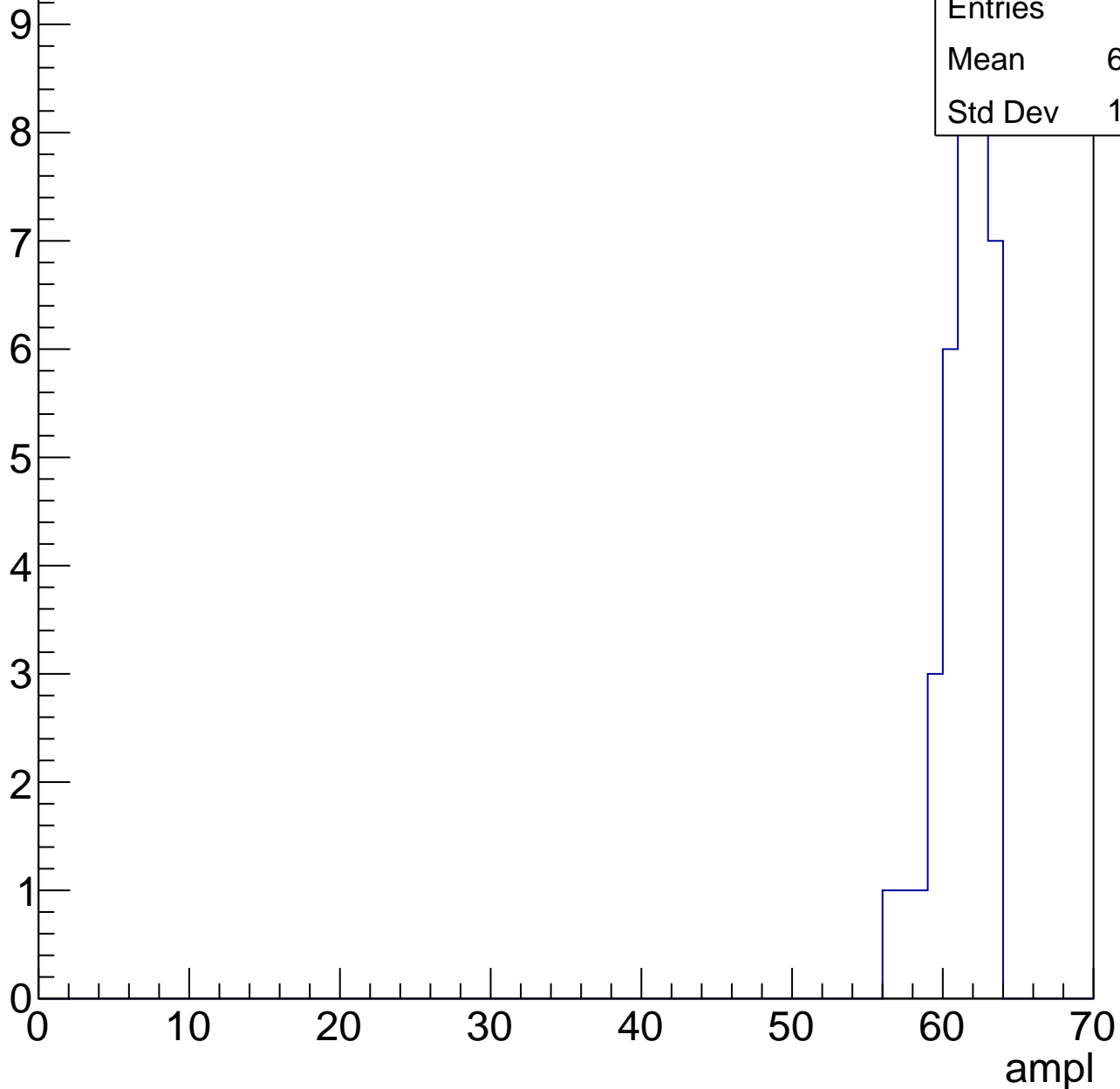
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch45, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	31.24
Std Dev	4.774

Gaus mean : 32.3323

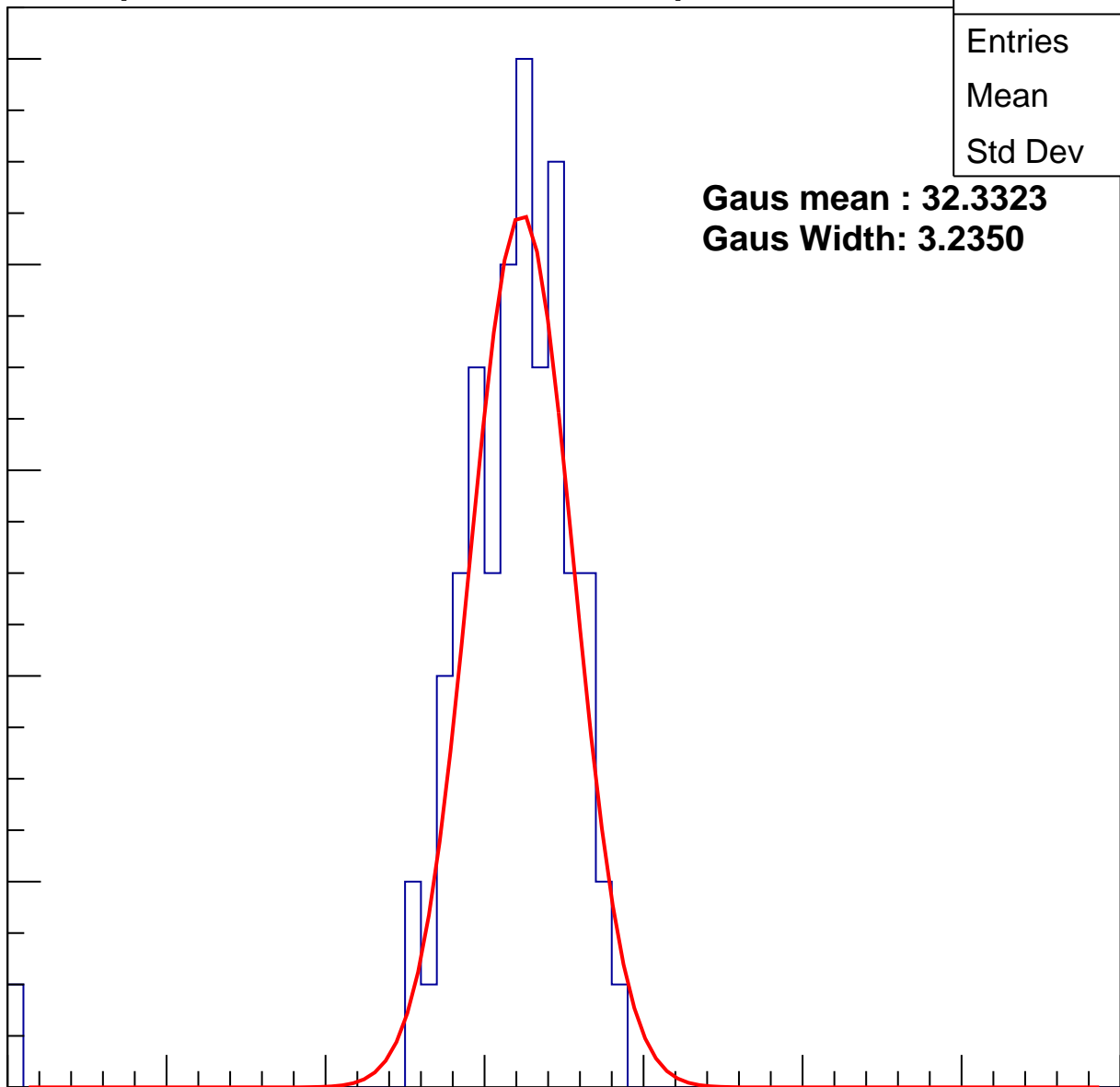
Gaus Width: 3.2350

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch45, adc1

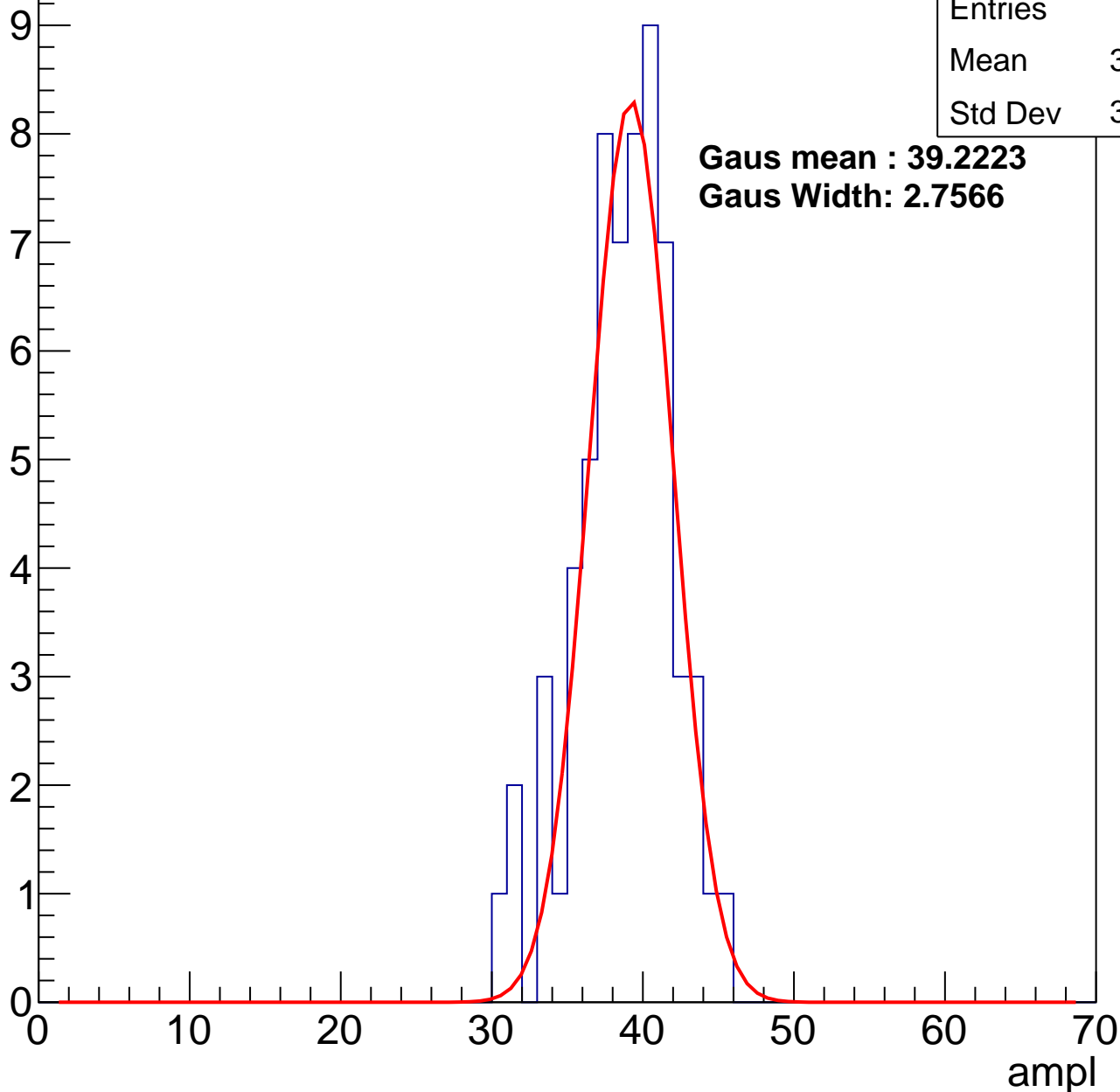
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.25
Std Dev	3.157

Gaus mean : 39.2223

Gaus Width: 2.7566



B0L002S, U7-ch45, adc2

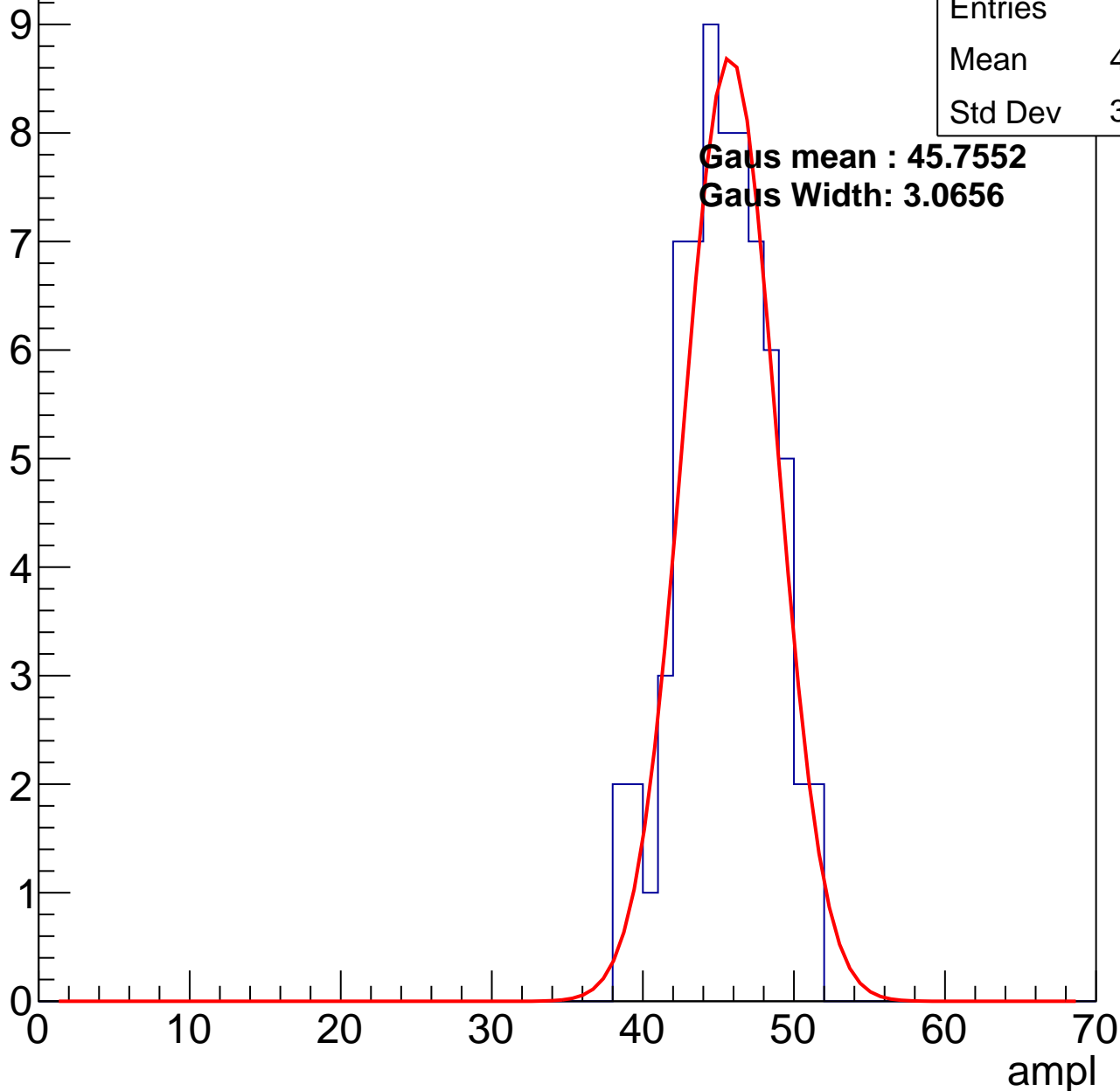
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.93
Std Dev	3.033

Gaus mean : 45.7552

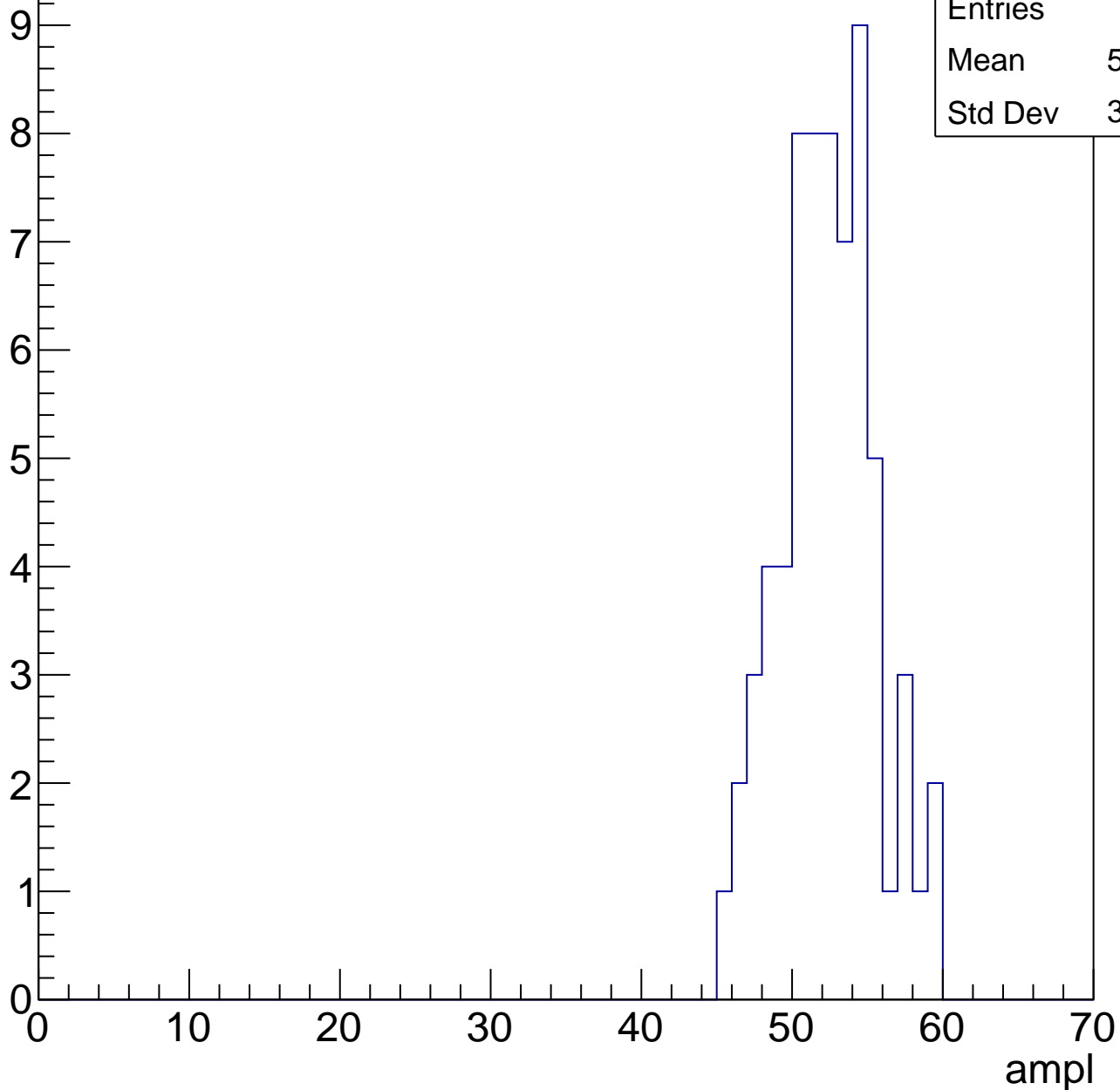
Gaus Width: 3.0656



B0L002S, U7-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

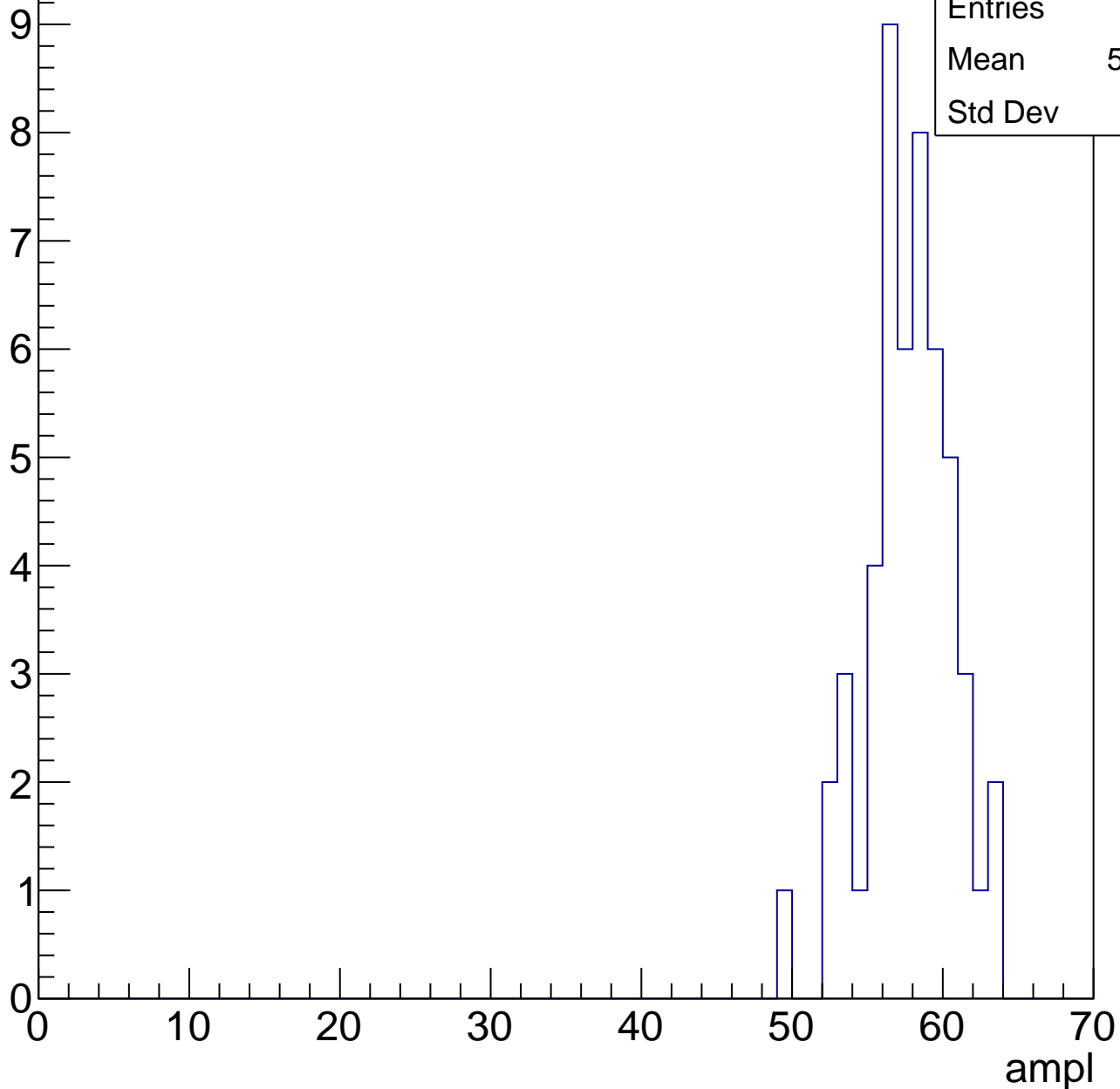
Entry



B0L002S, U7-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

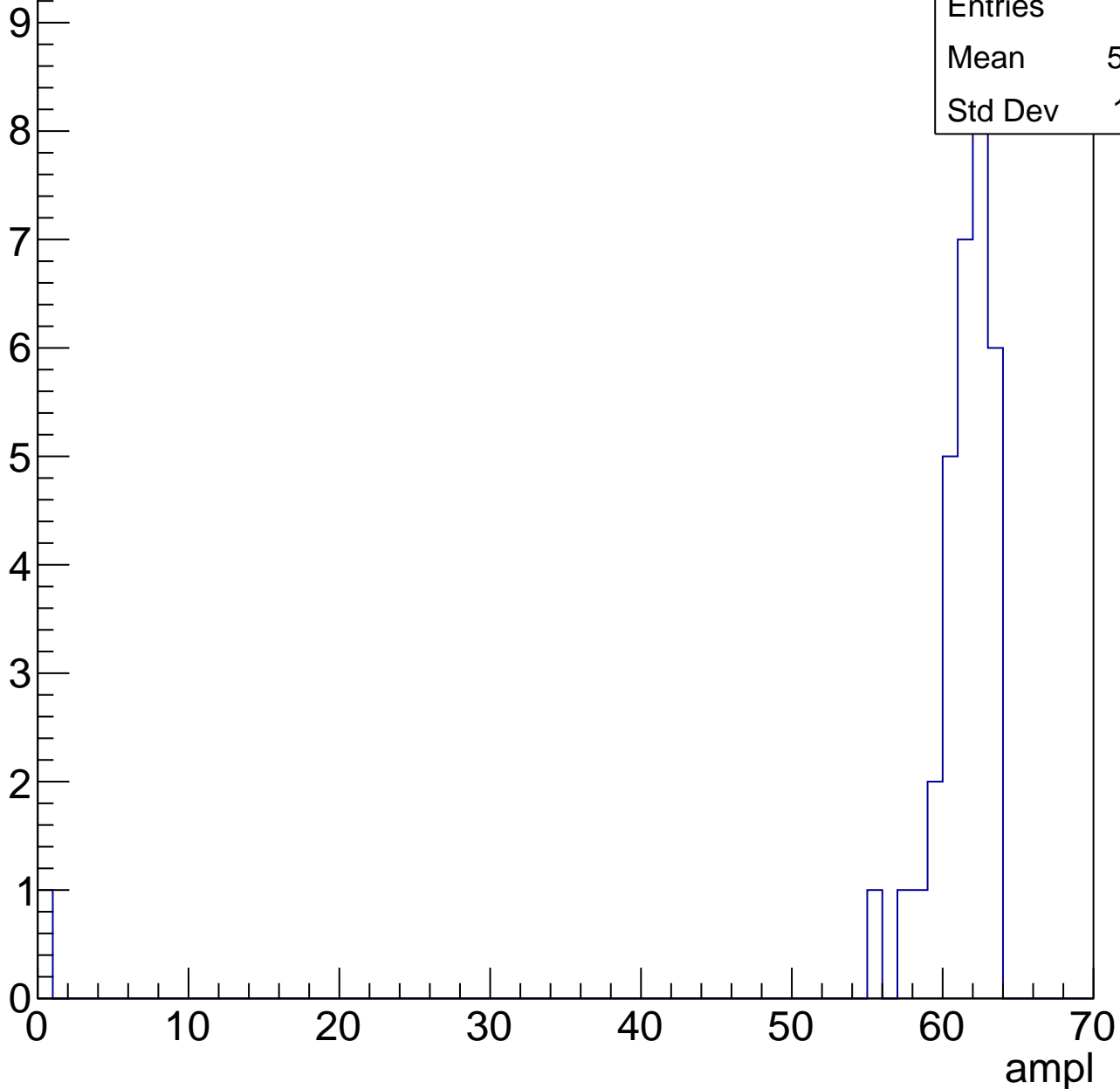


B0L002S, U7-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59.12
Std Dev	10.61



B0L002S, U7-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch46, adc0

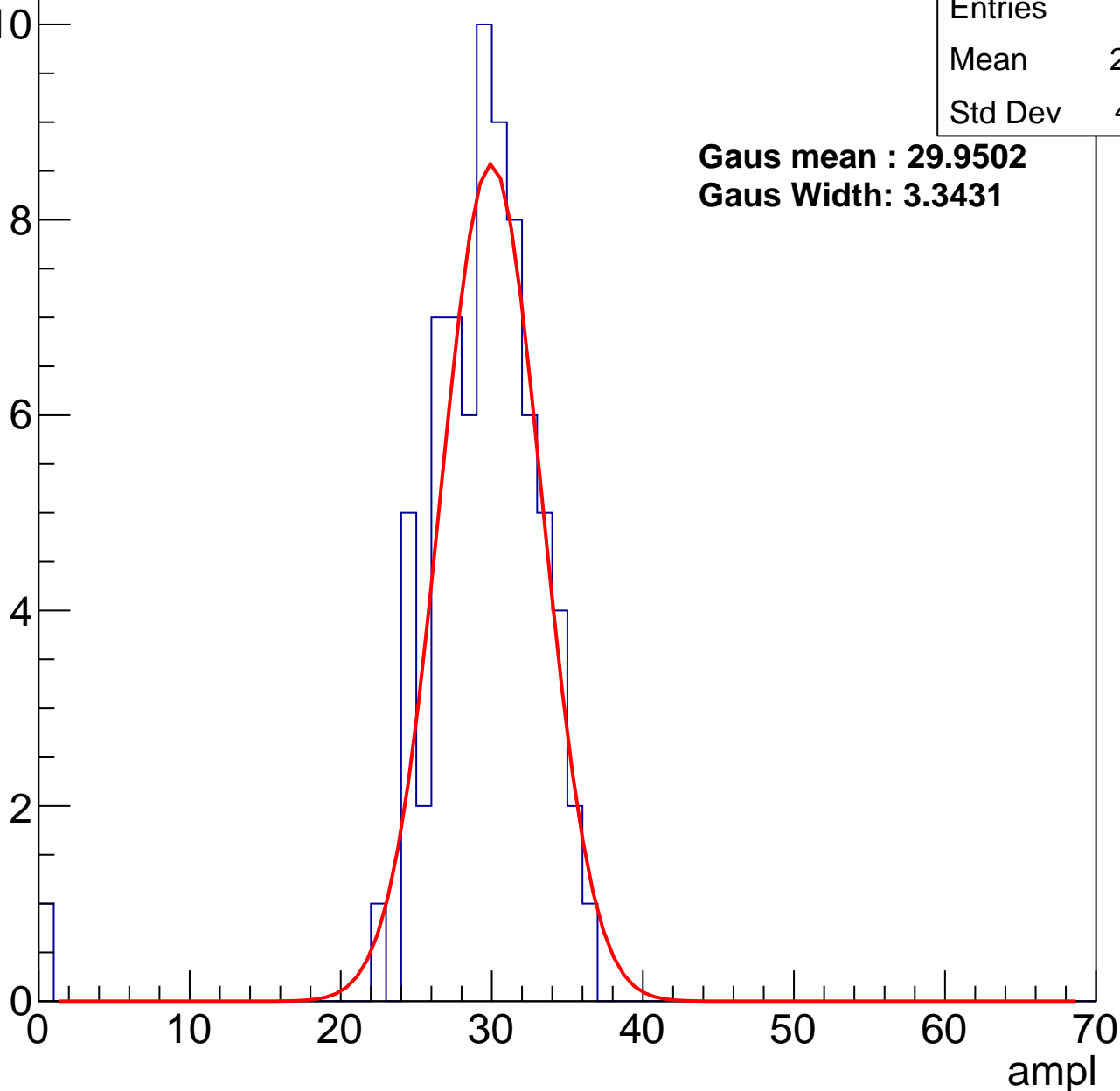
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	28.89
Std Dev	4.561

Gaus mean : 29.9502

Gaus Width: 3.3431



B0L002S, U7-ch46, adc1

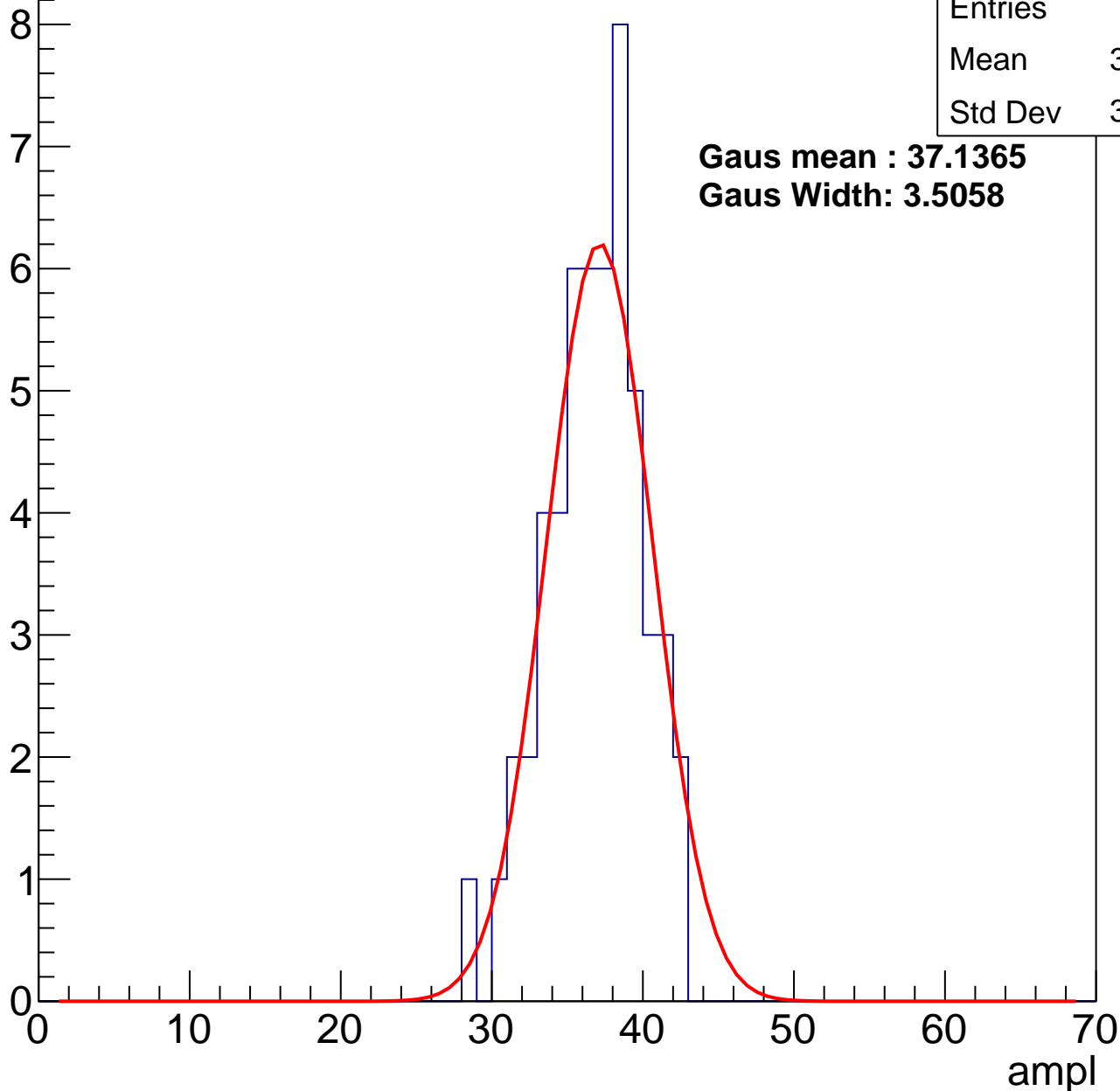
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	36.34
Std Dev	3.108

Gaus mean : 37.1365

Gaus Width: 3.5058



B0L002S, U7-ch46, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

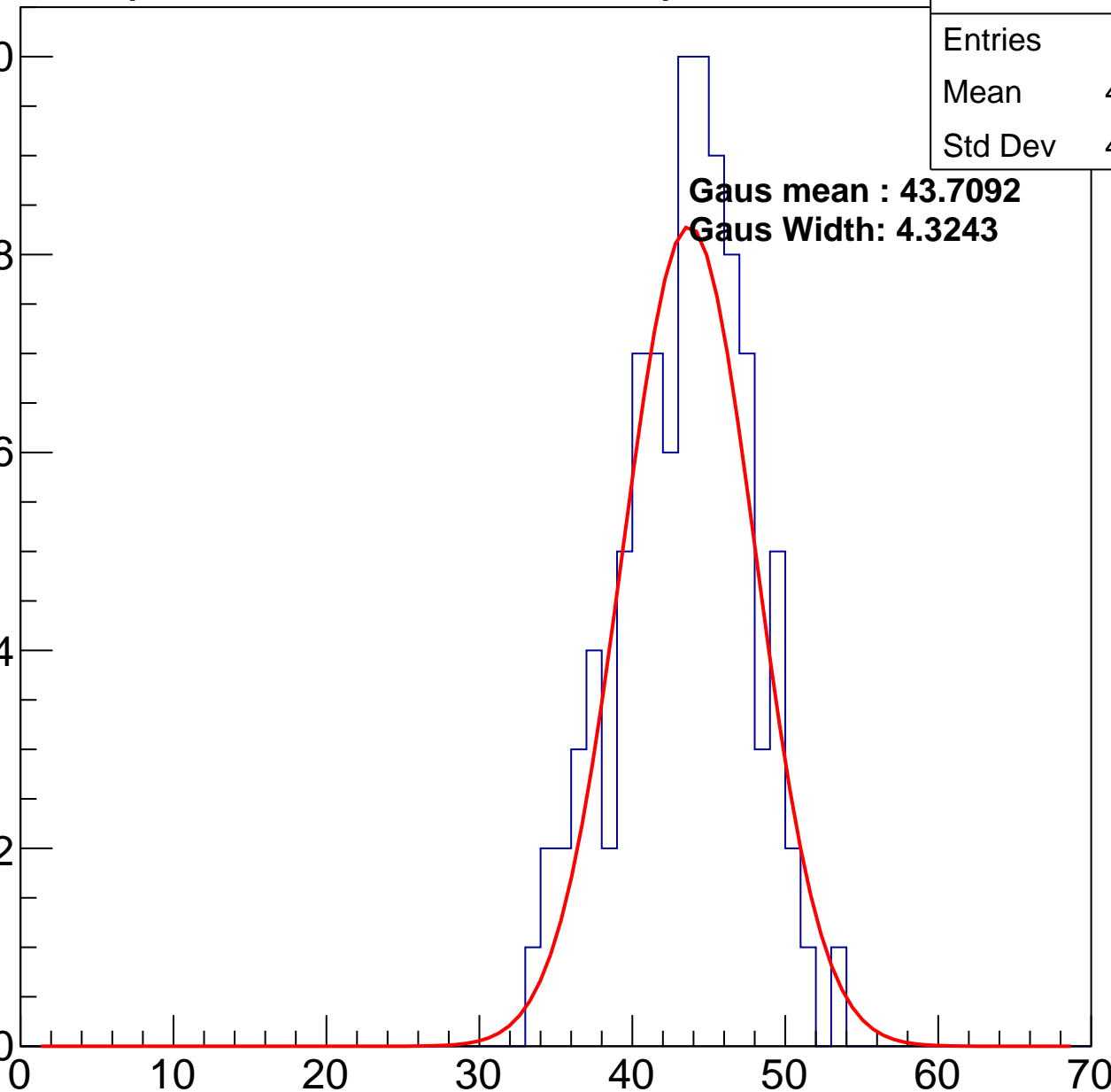
Entries	95
Mean	42.97
Std Dev	4.174

Gaus mean : 43.7092

Gaus Width: 4.3243

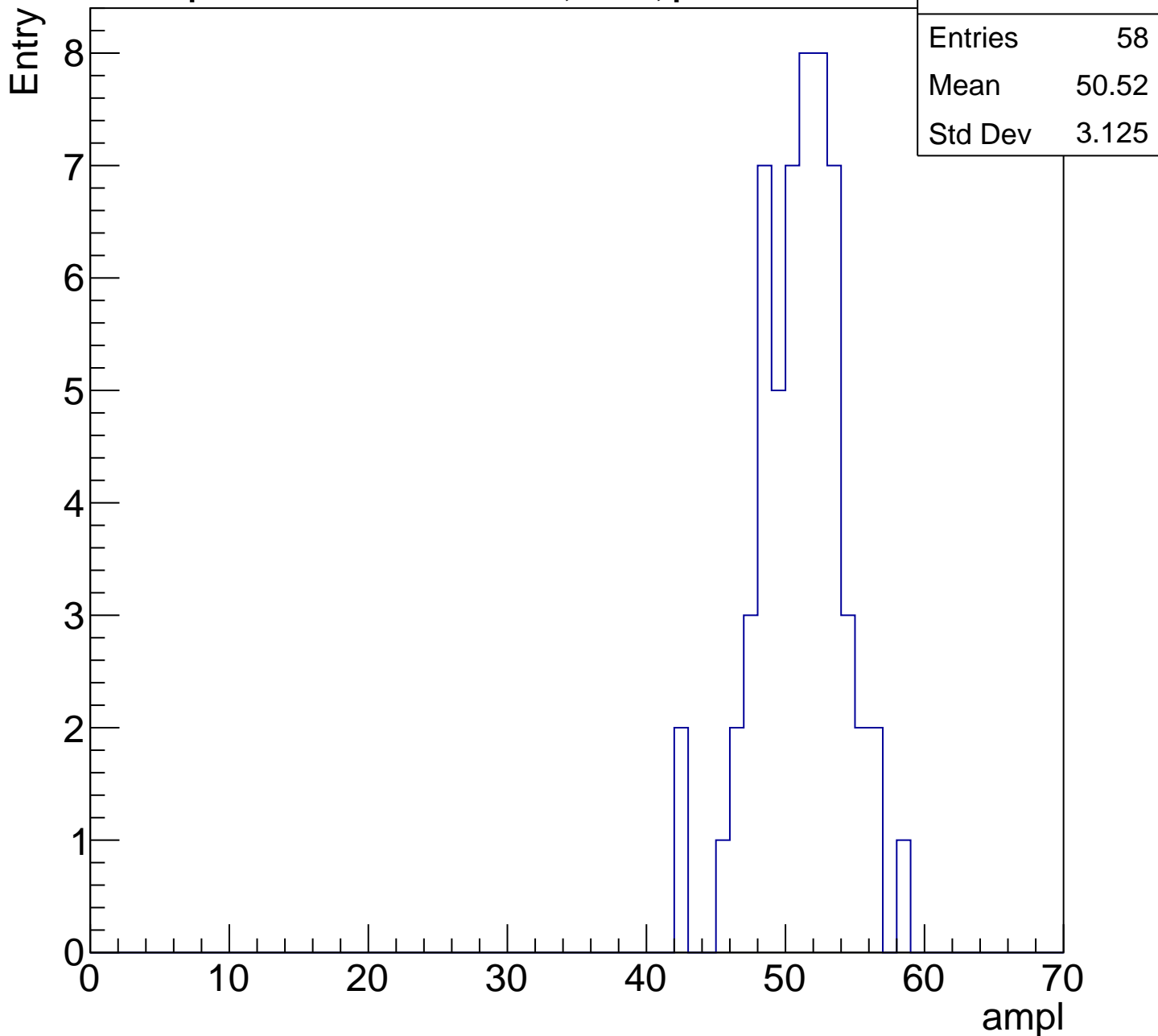
10
8
6
4
2
0

ampl



B0L002S, U7-ch46, adc3

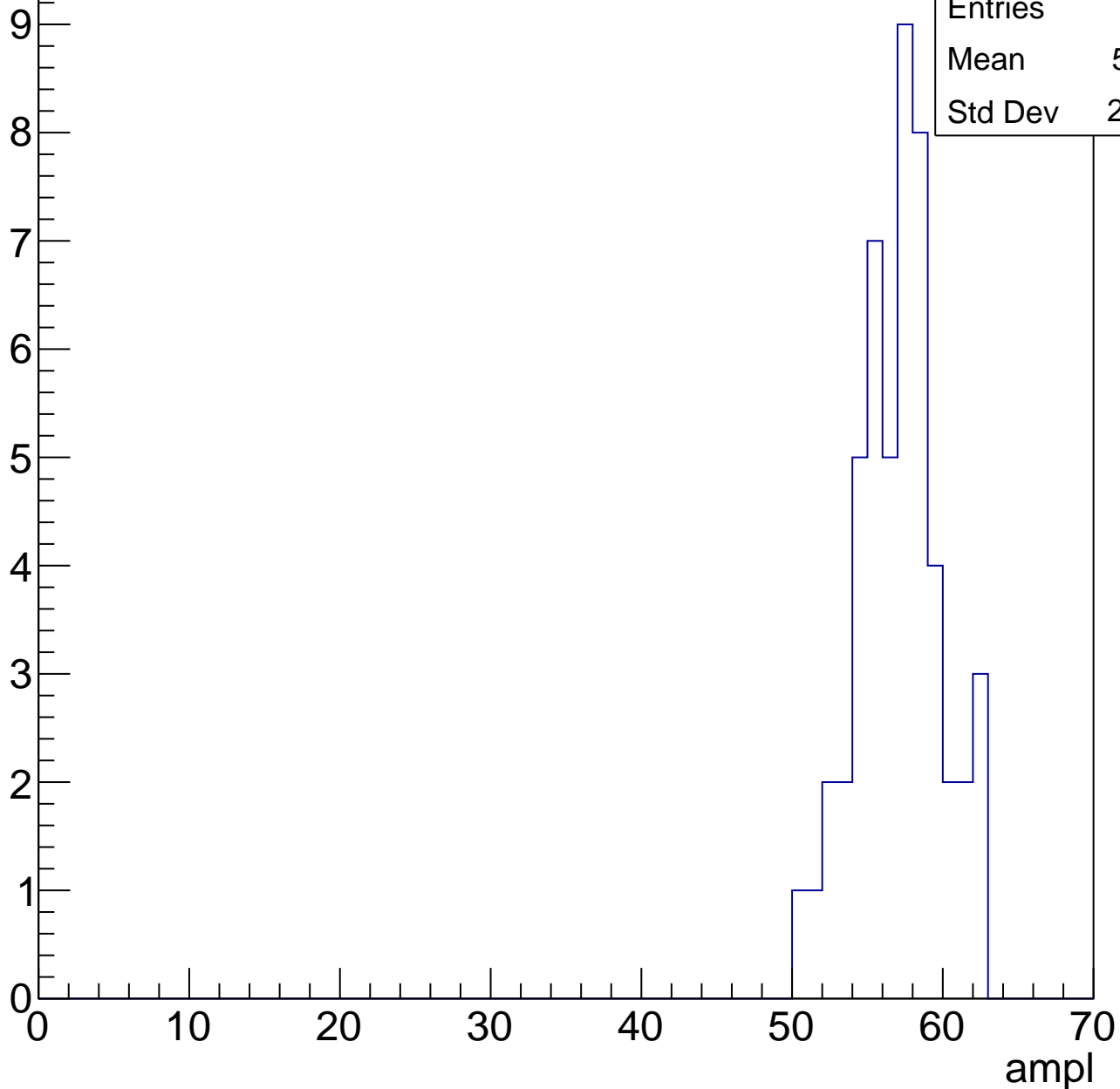
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

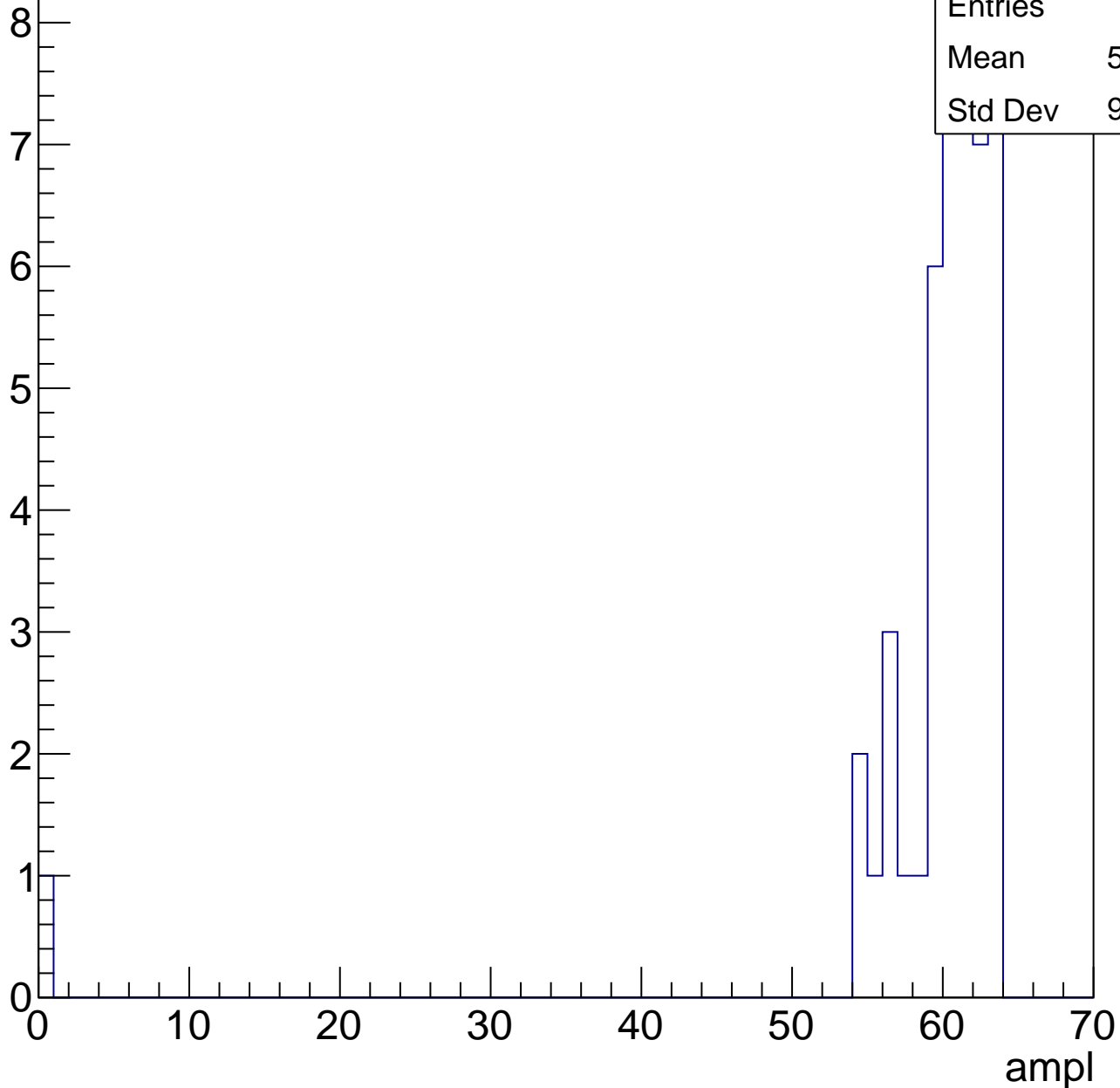
Entry



B0L002S, U7-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	46
Mean	58.83
Std Dev	9.099

B0L002S, U7-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U7-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch47, adc0

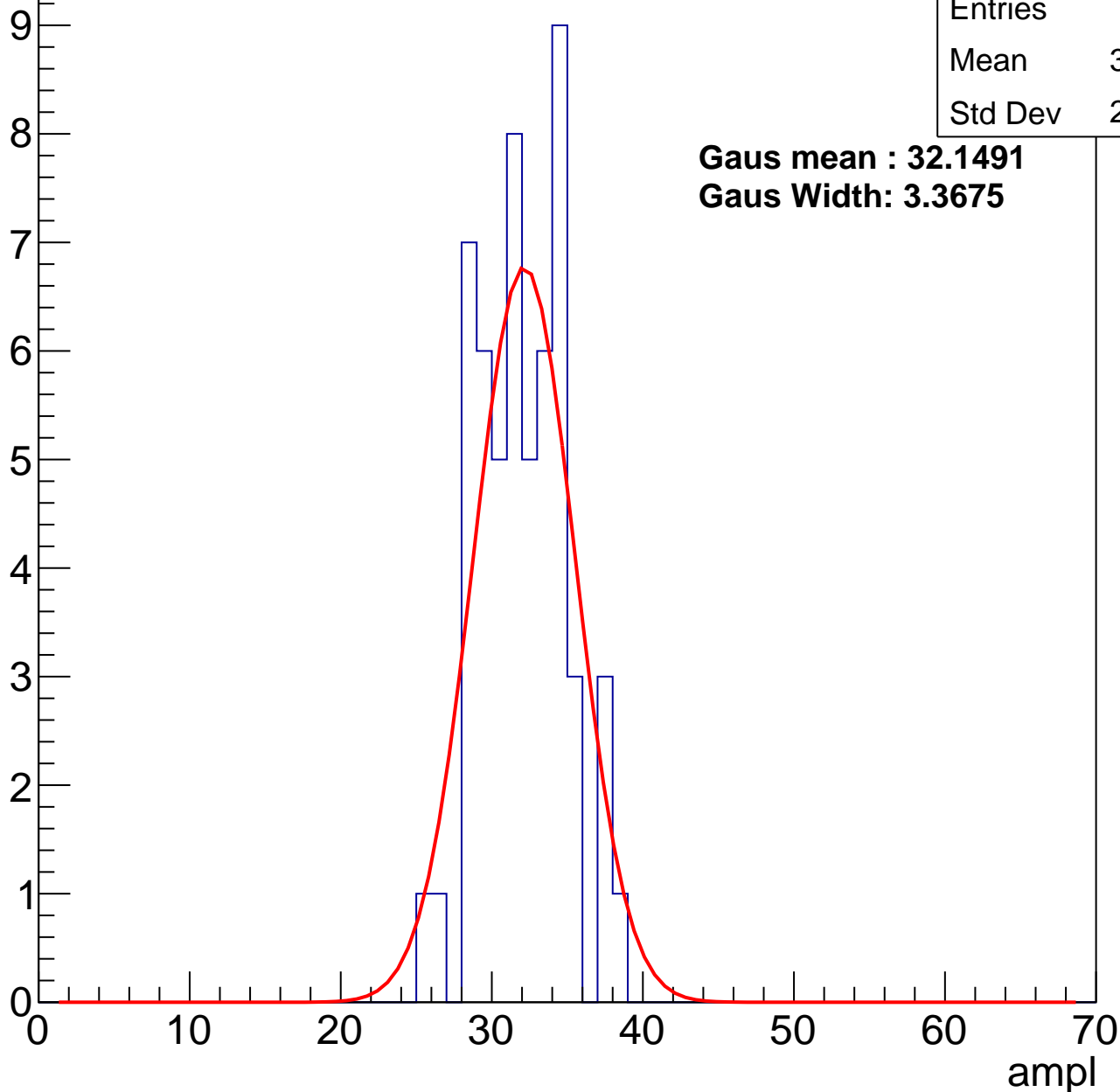
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	31.58
Std Dev	2.858

Gaus mean : 32.1491

Gaus Width: 3.3675



B0L002S, U7-ch47, adc1

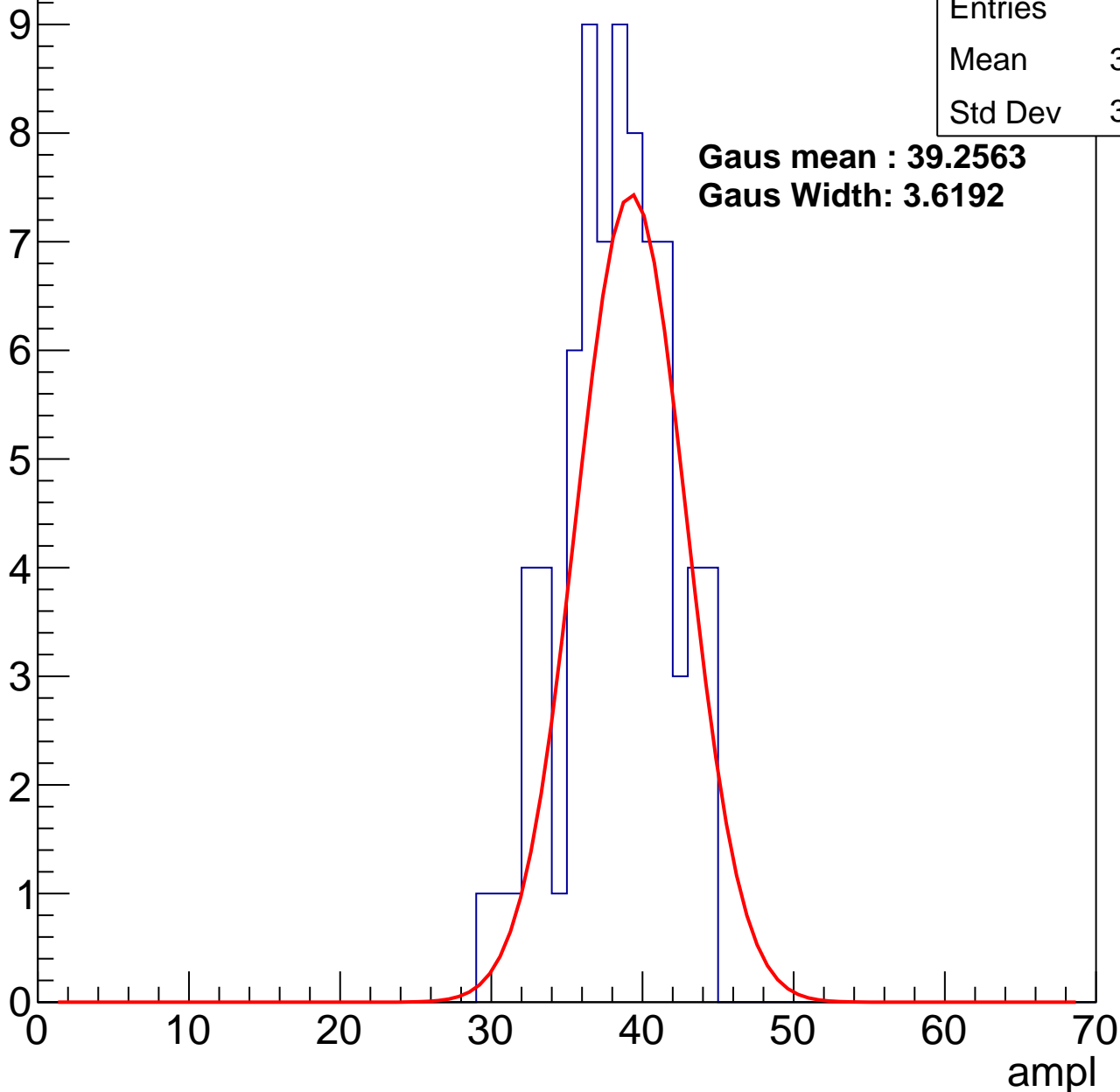
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.79
Std Dev	3.514

Gaus mean : 39.2563

Gaus Width: 3.6192



B0L002S, U7-ch47, adc2

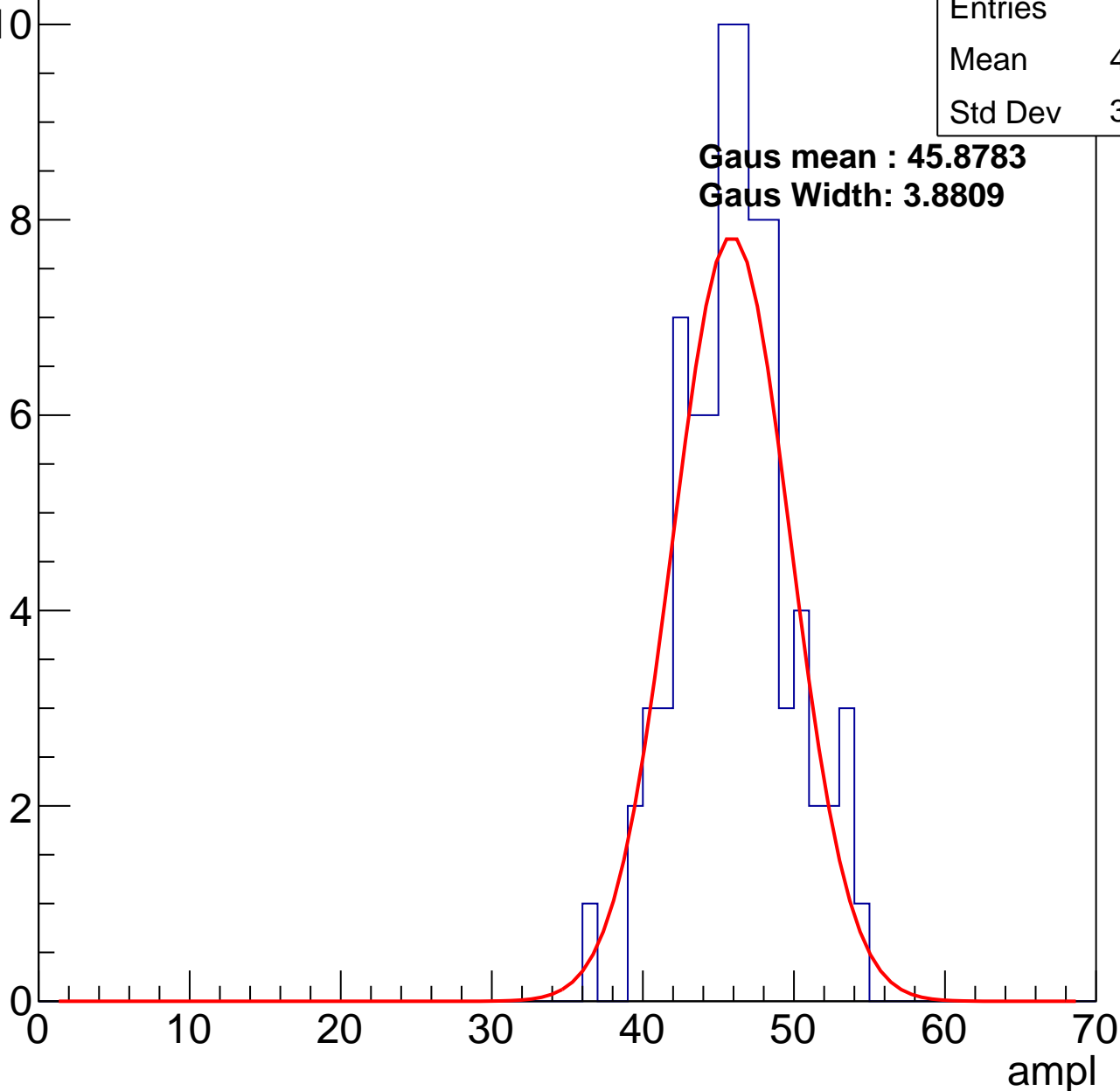
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	45.68
Std Dev	3.623

Gaus mean : 45.8783

Gaus Width: 3.8809

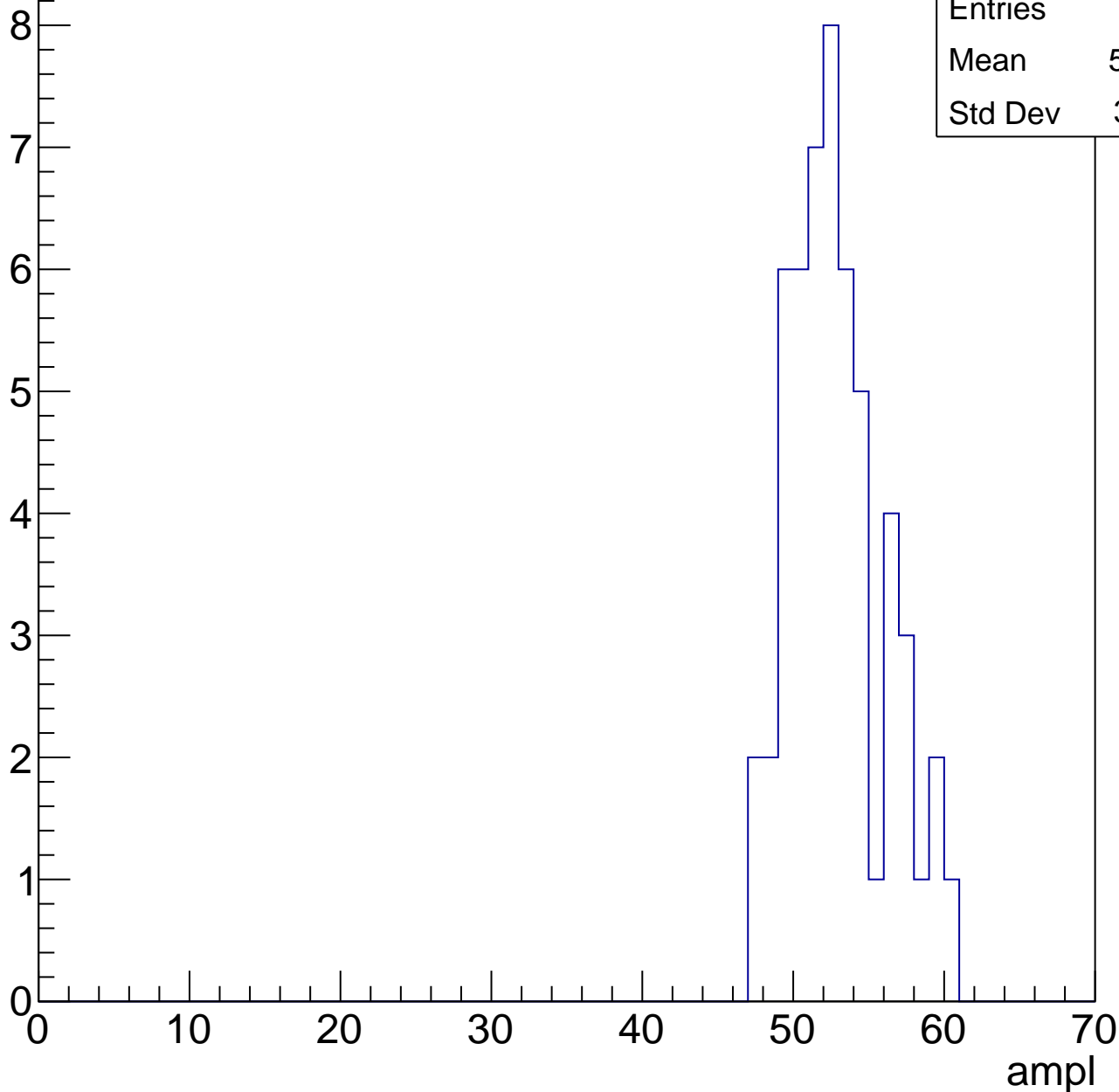


B0L002S, U7-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	52.43
Std Dev	3.131

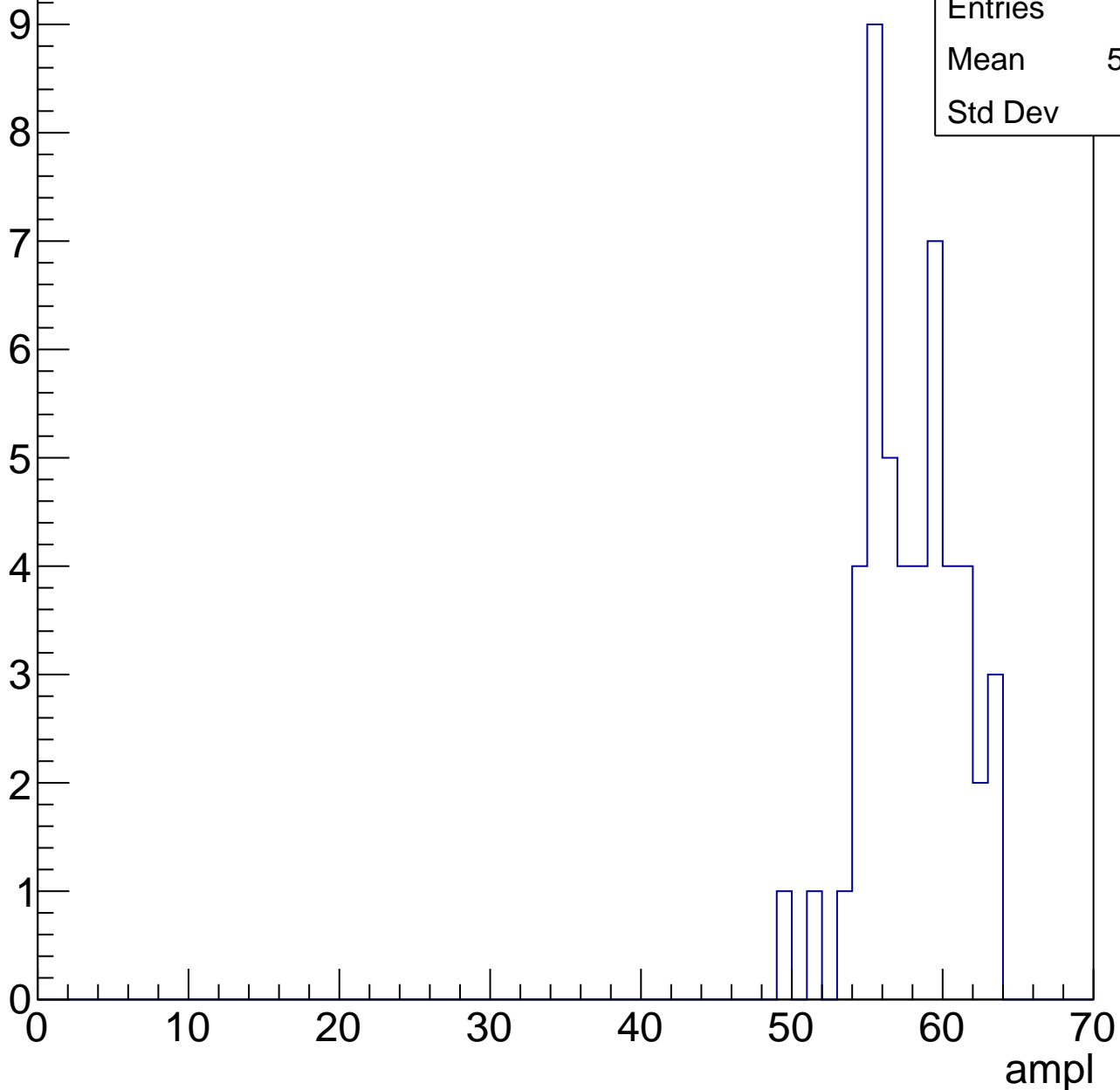


B0L002S, U7-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	57.43
Std Dev	3.11

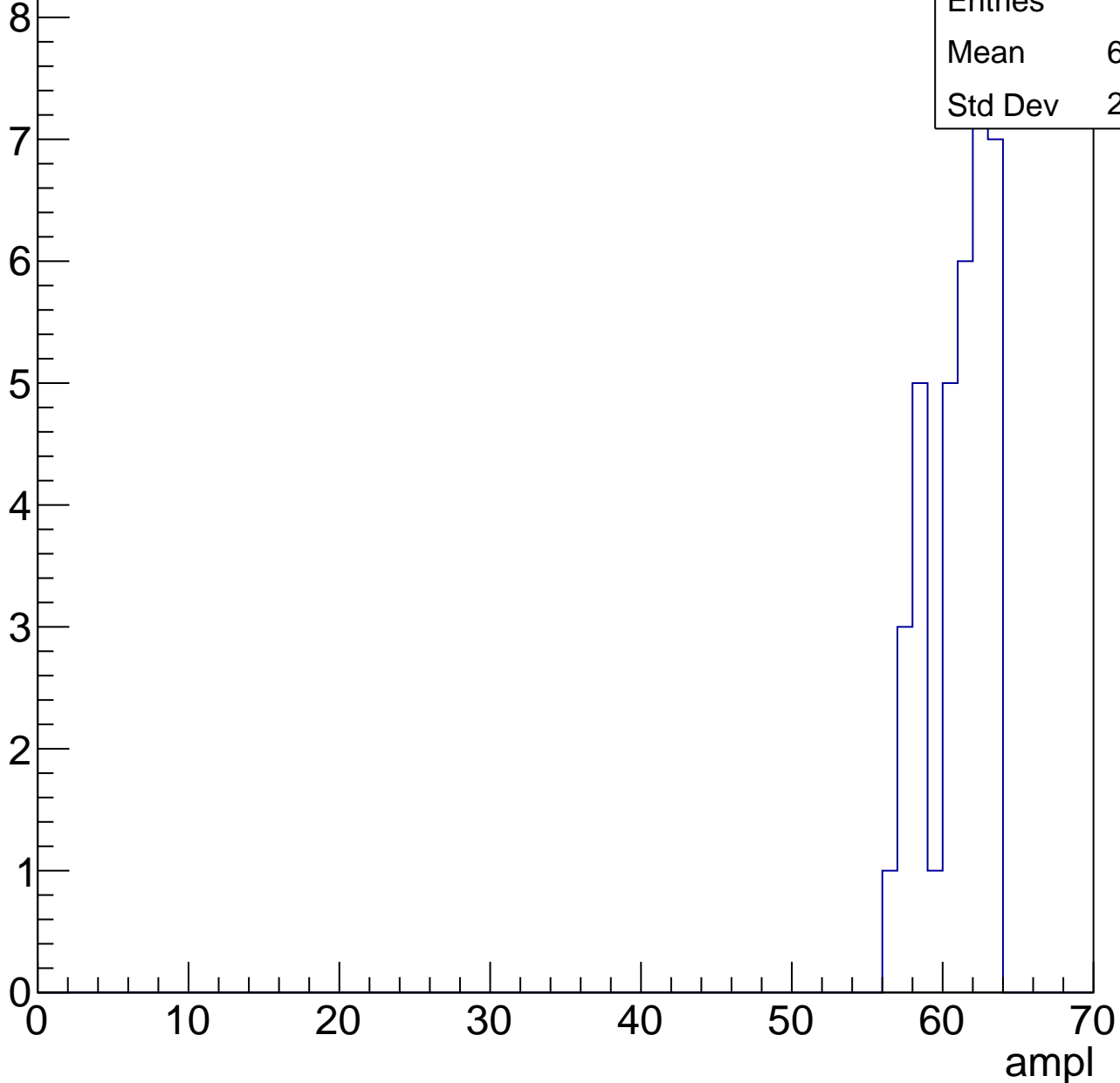


B0L002S, U7-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

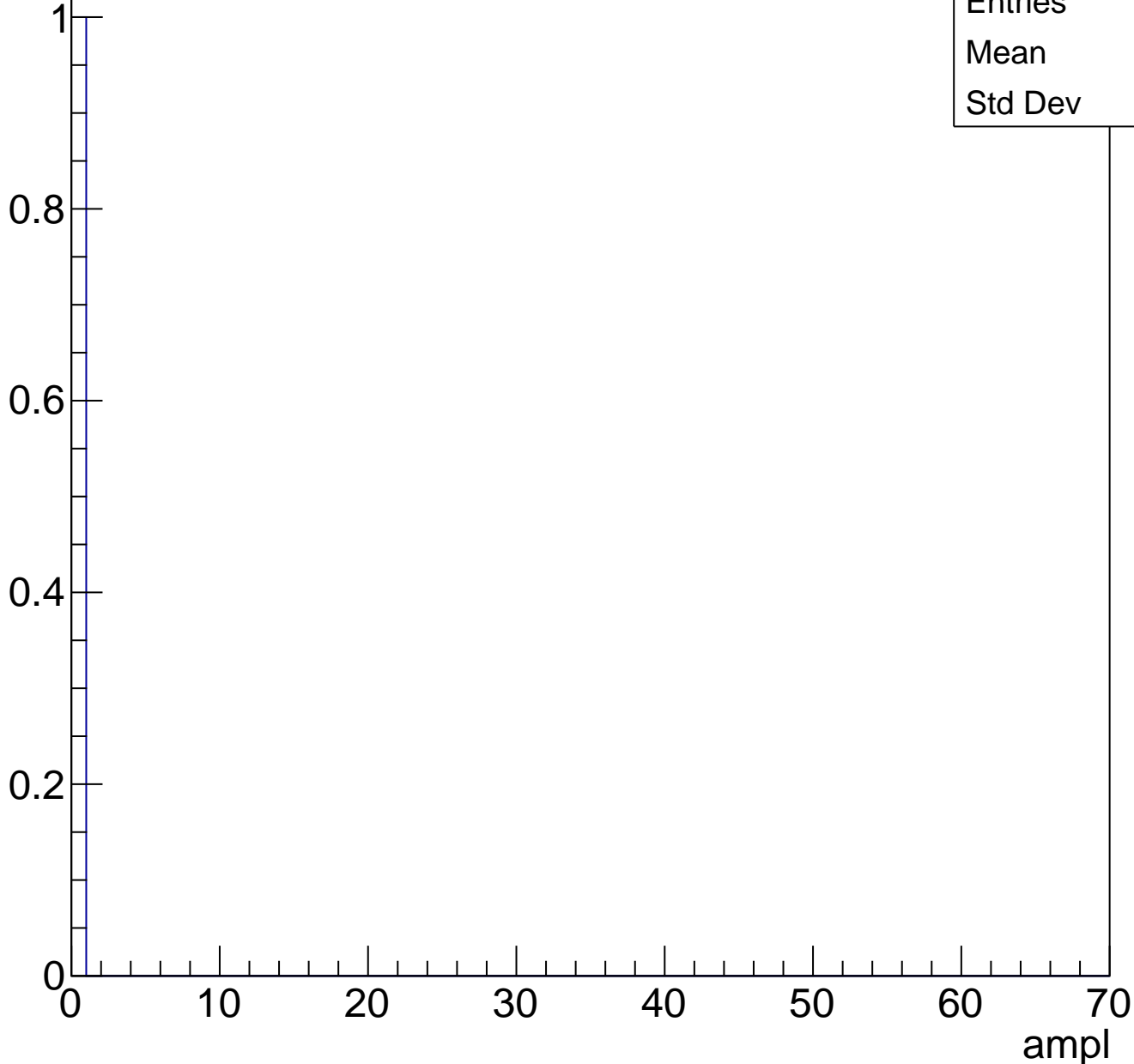
Entries	36
Mean	60.53
Std Dev	2.075



B0L002S, U7-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

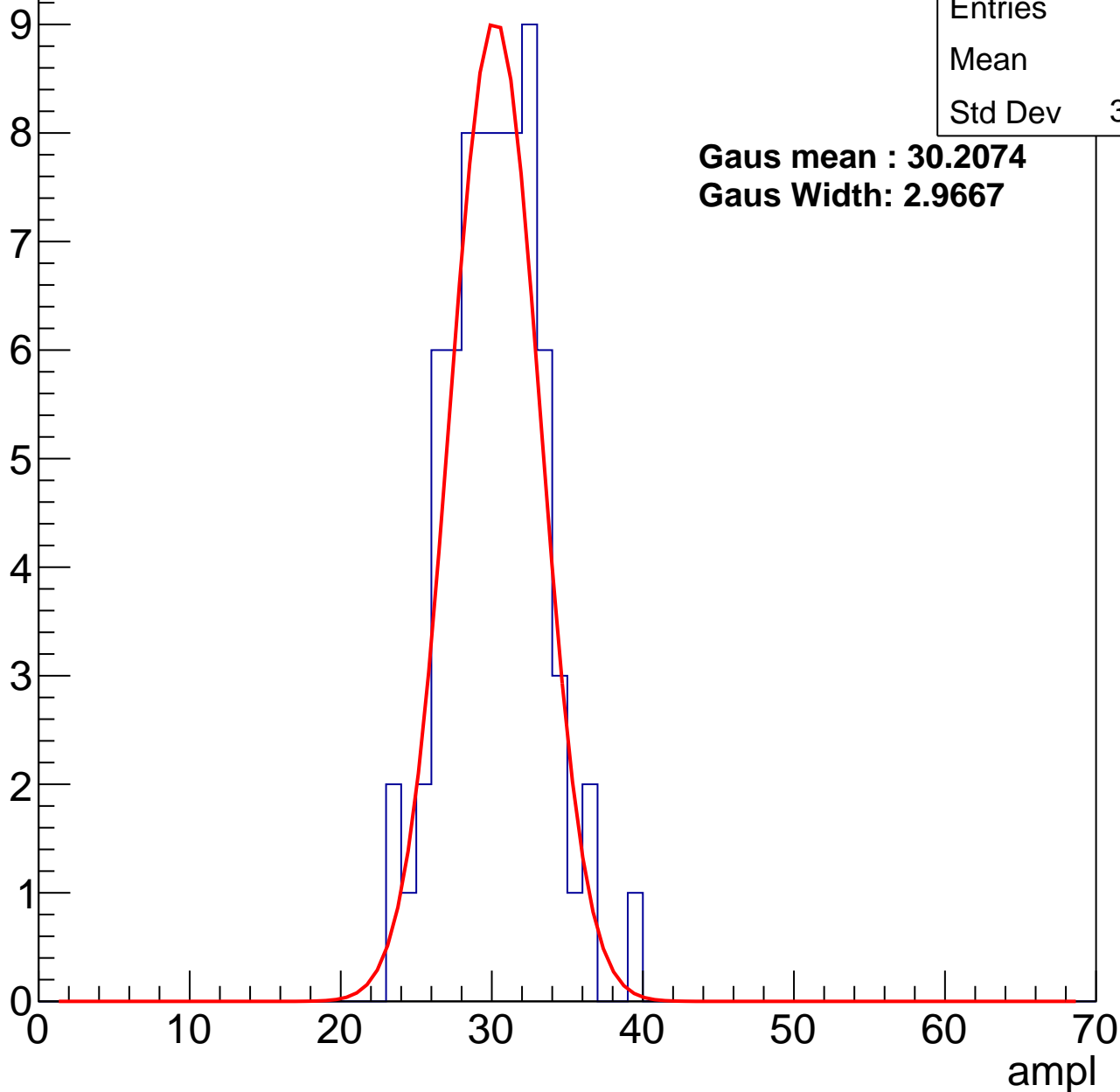


Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch48, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch48, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	36.77
Std Dev	3.325

Gaus mean : 37.2208

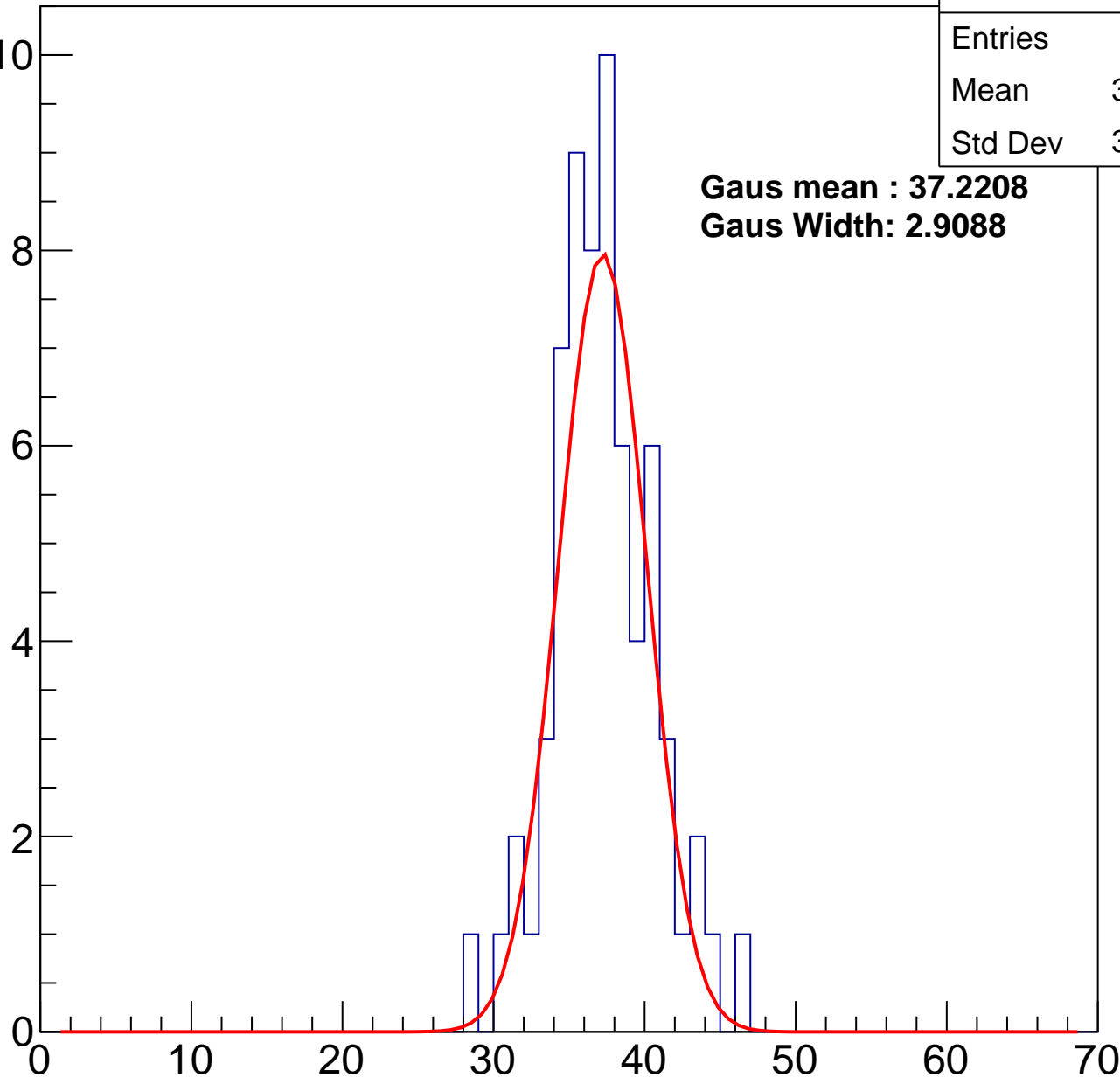
Gaus Width: 2.9088

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

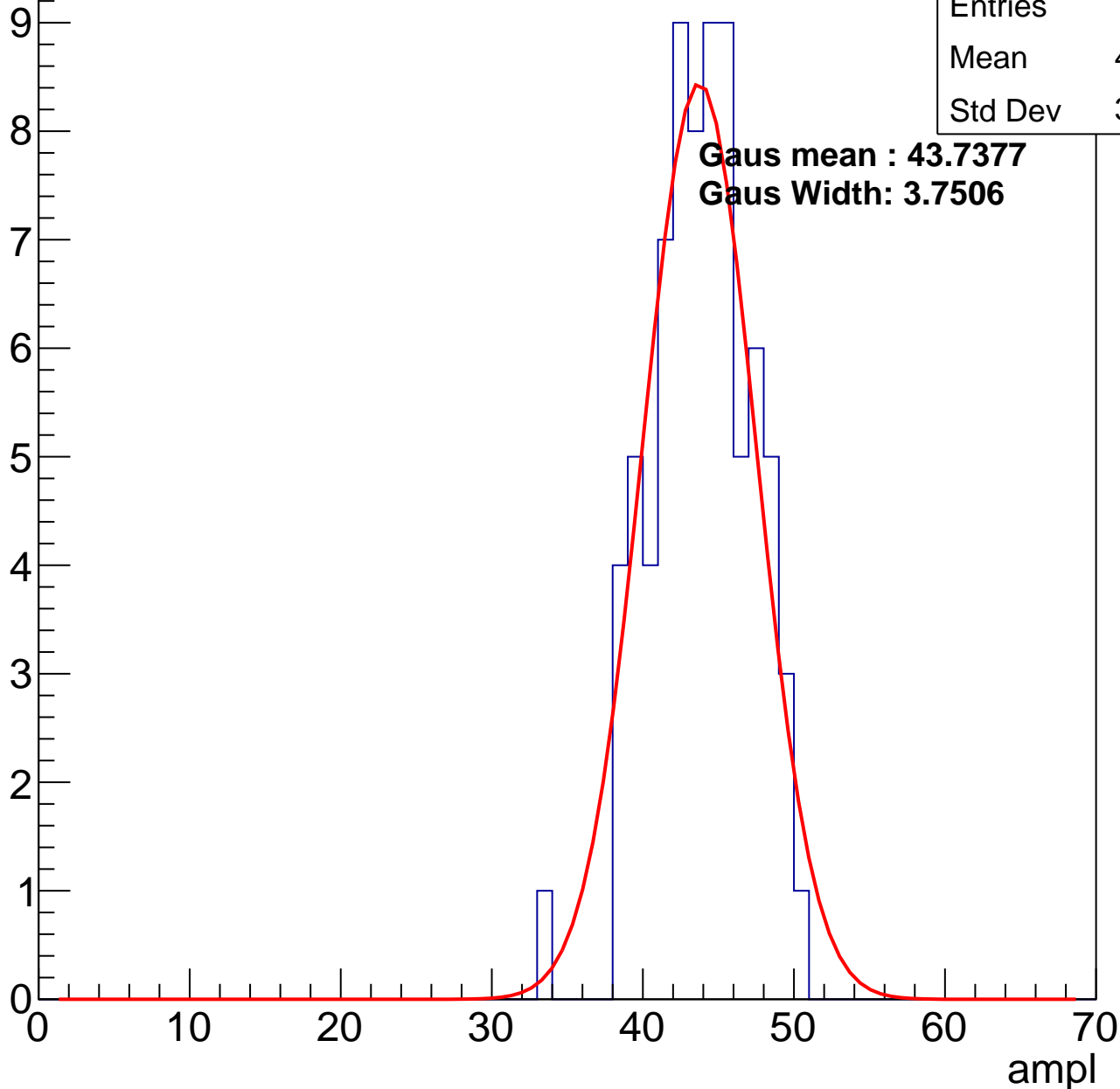
ampl



B0L002S, U7-ch48, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

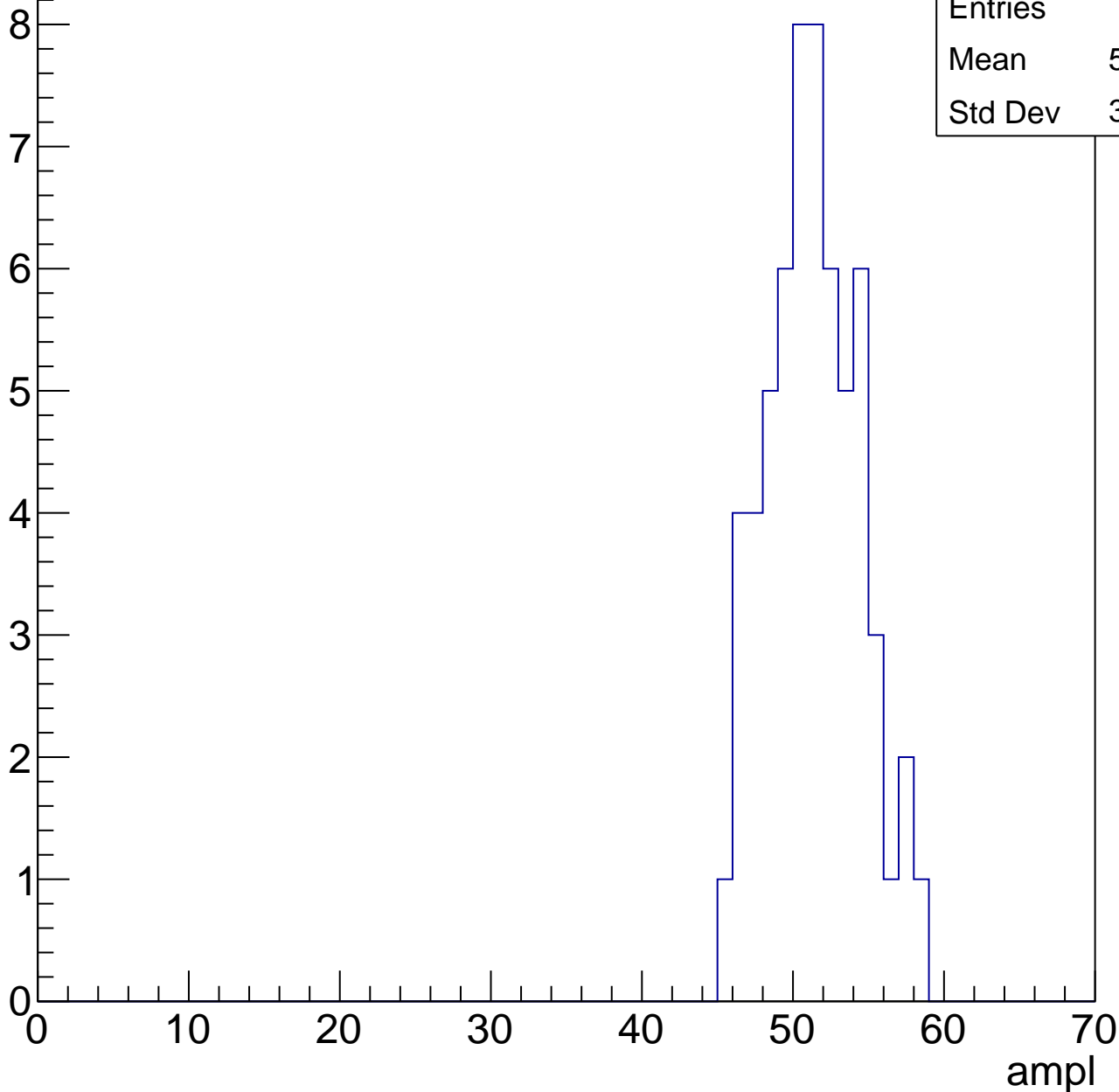


B0L002S, U7-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.88
Std Dev	3.028

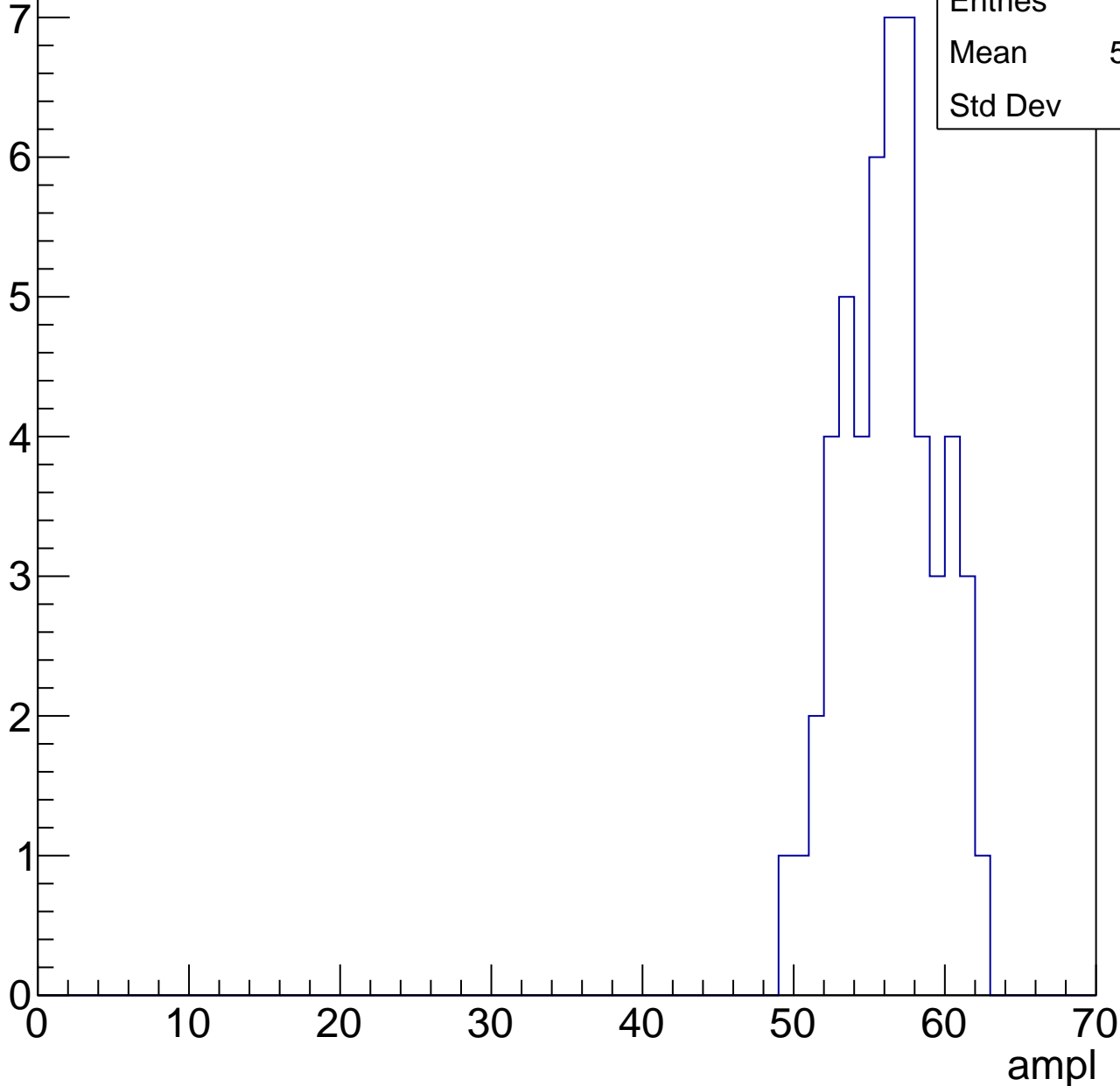


B0L002S, U7-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	55.87
Std Dev	3.07



B0L002S, U7-ch48, adc5

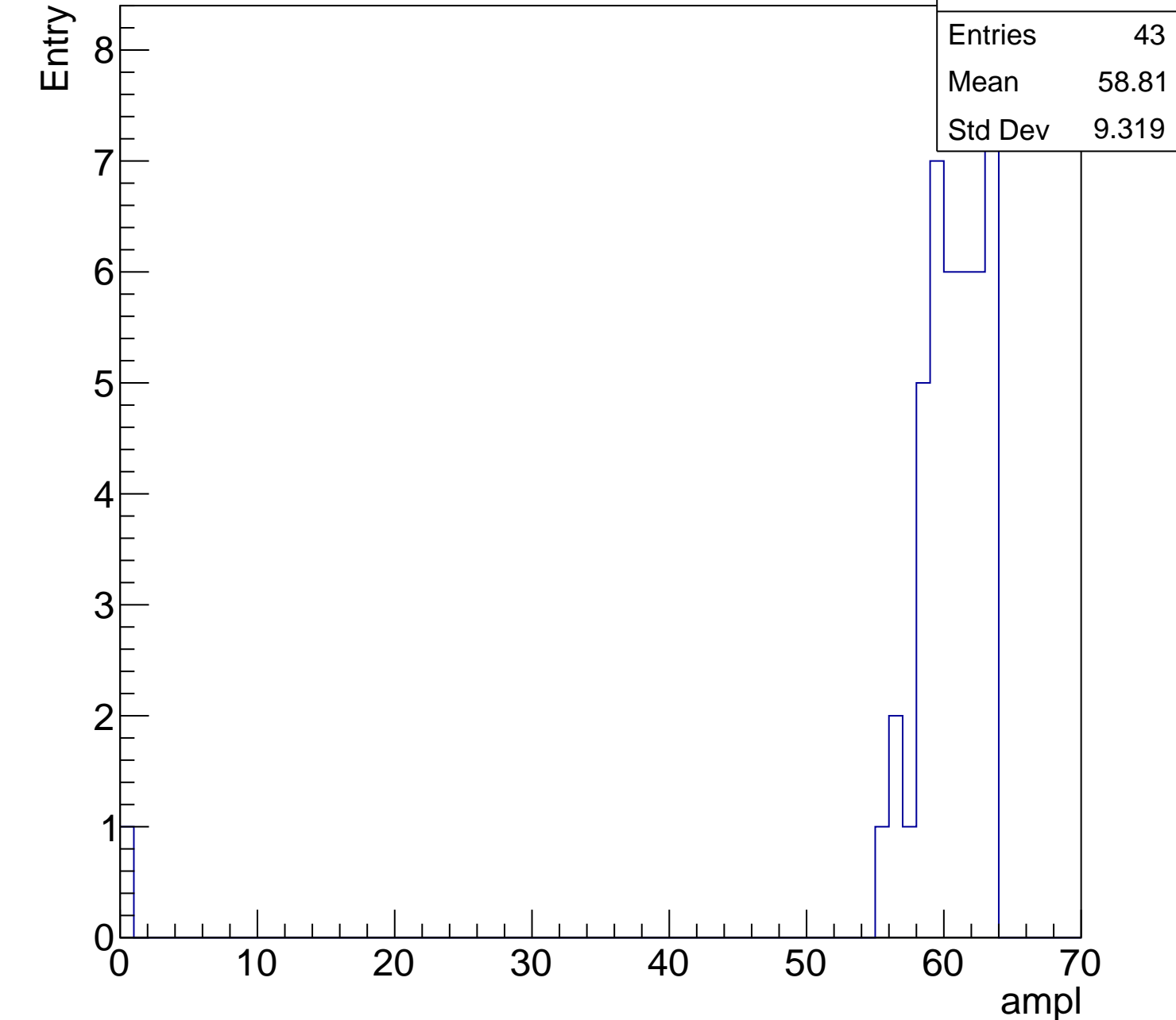
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.81
Std Dev	9.319

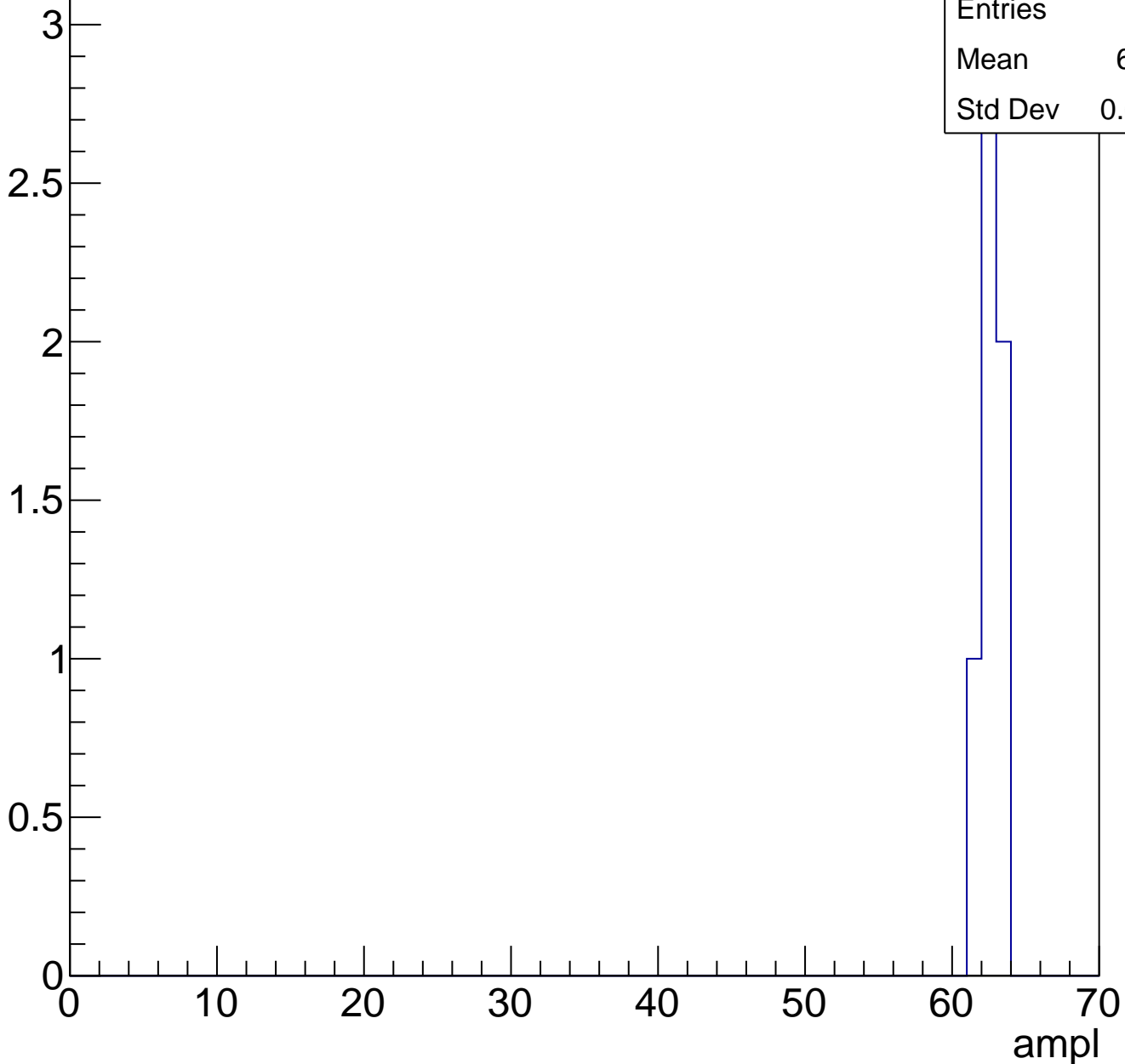
ampl



B0L002S, U7-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch49, adc0

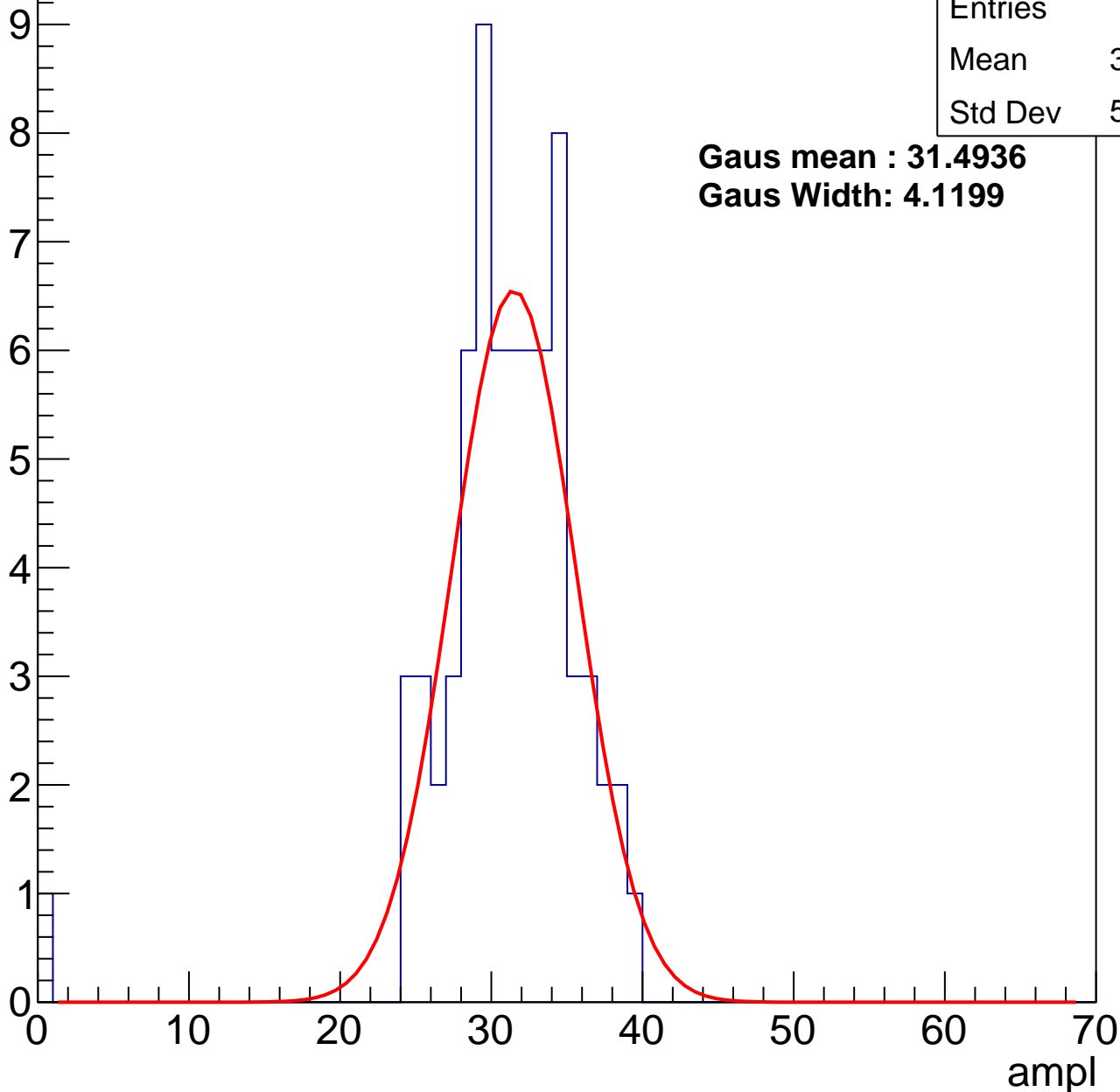
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.56
Std Dev	5.148

Gaus mean : 31.4936

Gaus Width: 4.1199



B0L002S, U7-ch49, adc1

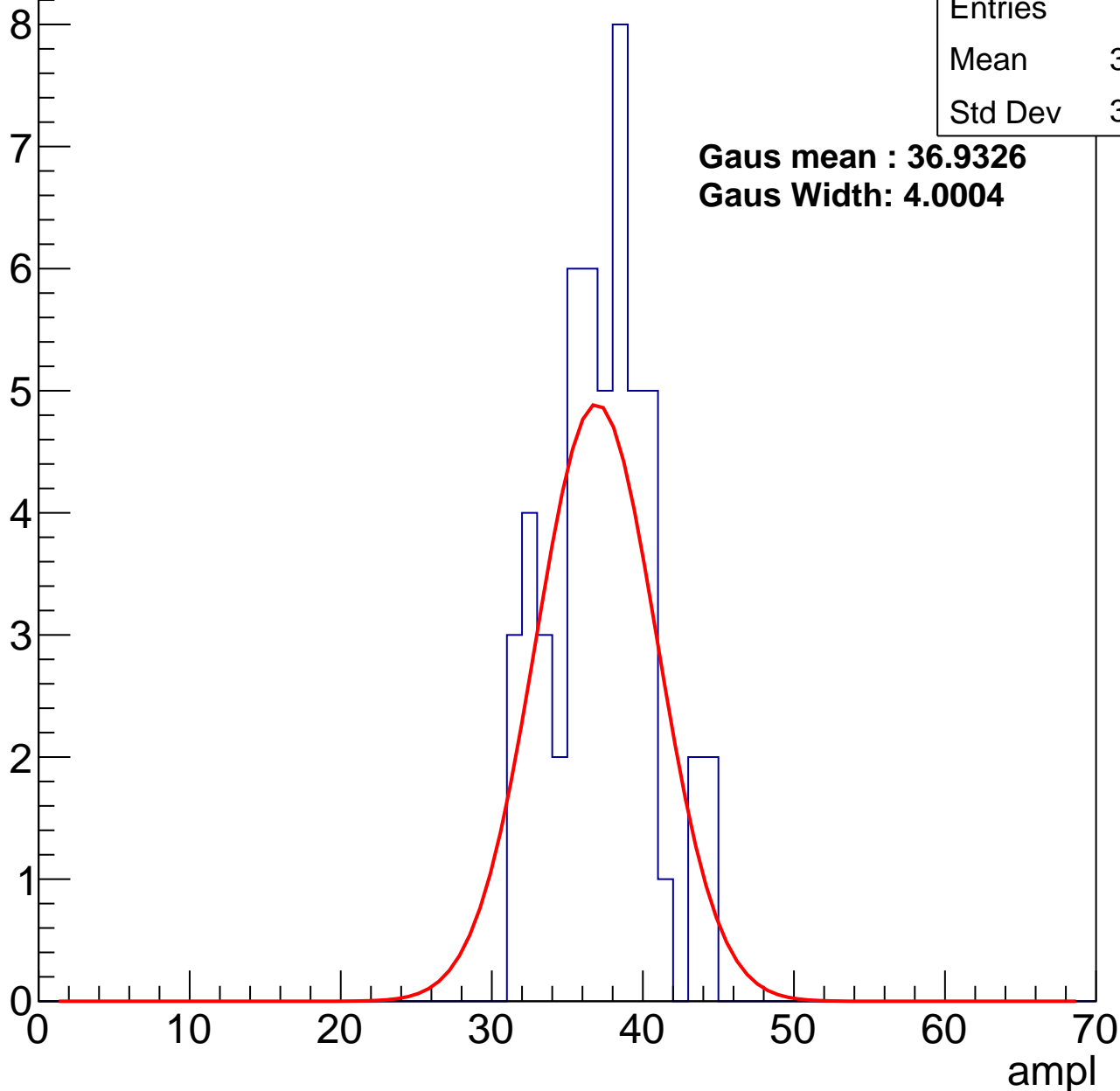
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	36.79
Std Dev	3.284

Gaus mean : 36.9326

Gaus Width: 4.0004



B0L002S, U7-ch49, adc2

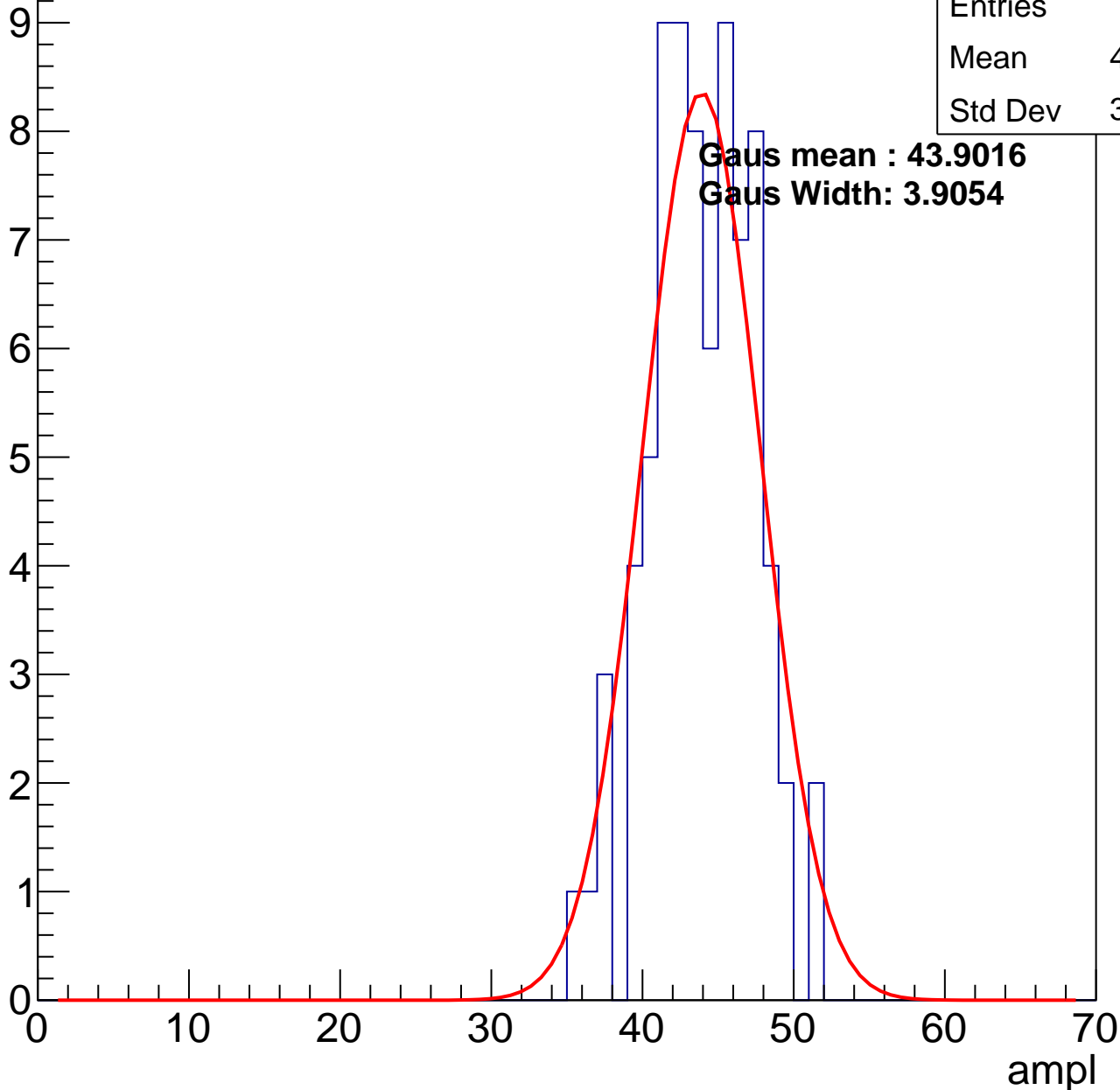
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	43.44
Std Dev	3.384

Gaus mean : 43.9016

Gaus Width: 3.9054

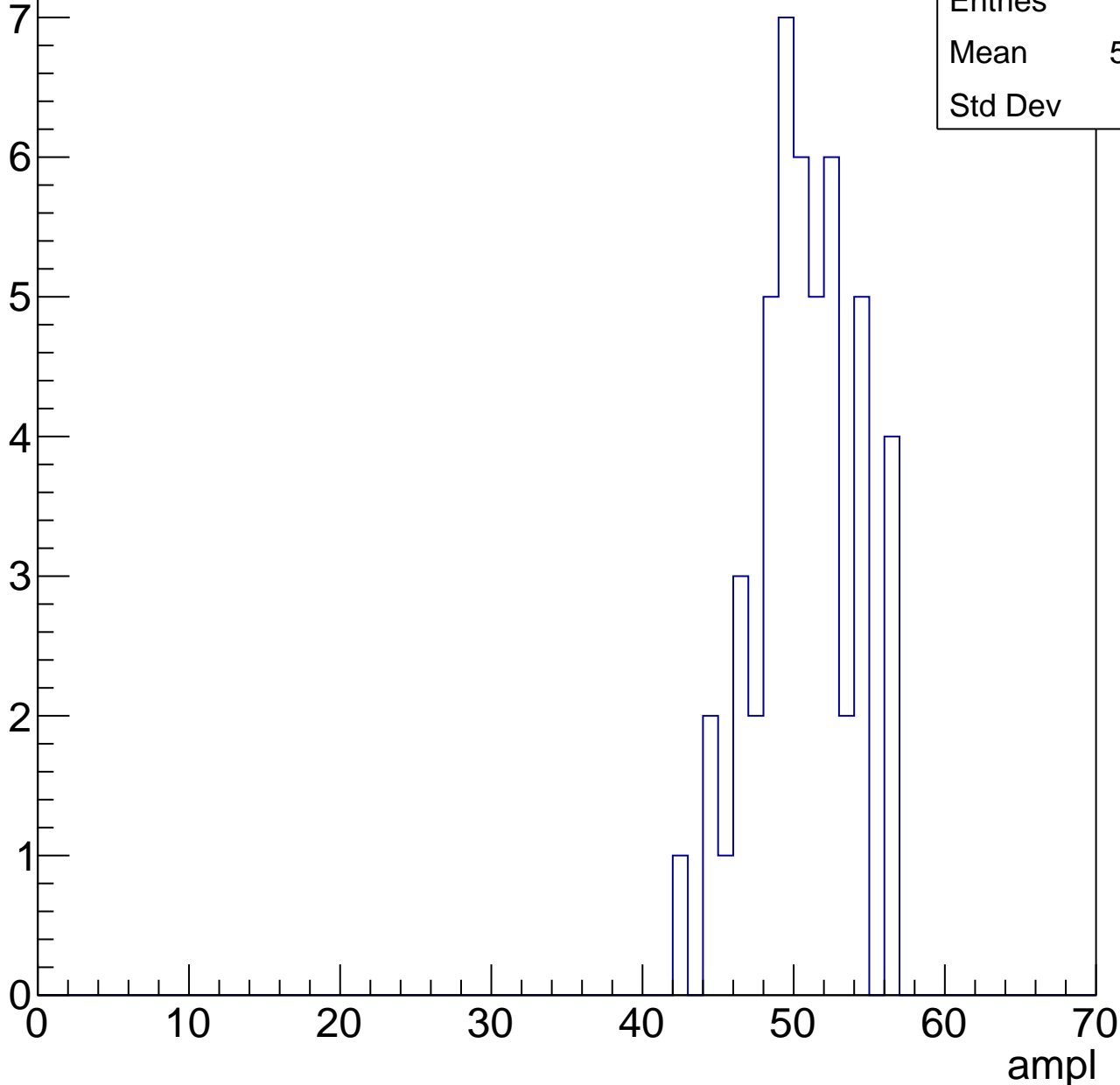


B0L002S, U7-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

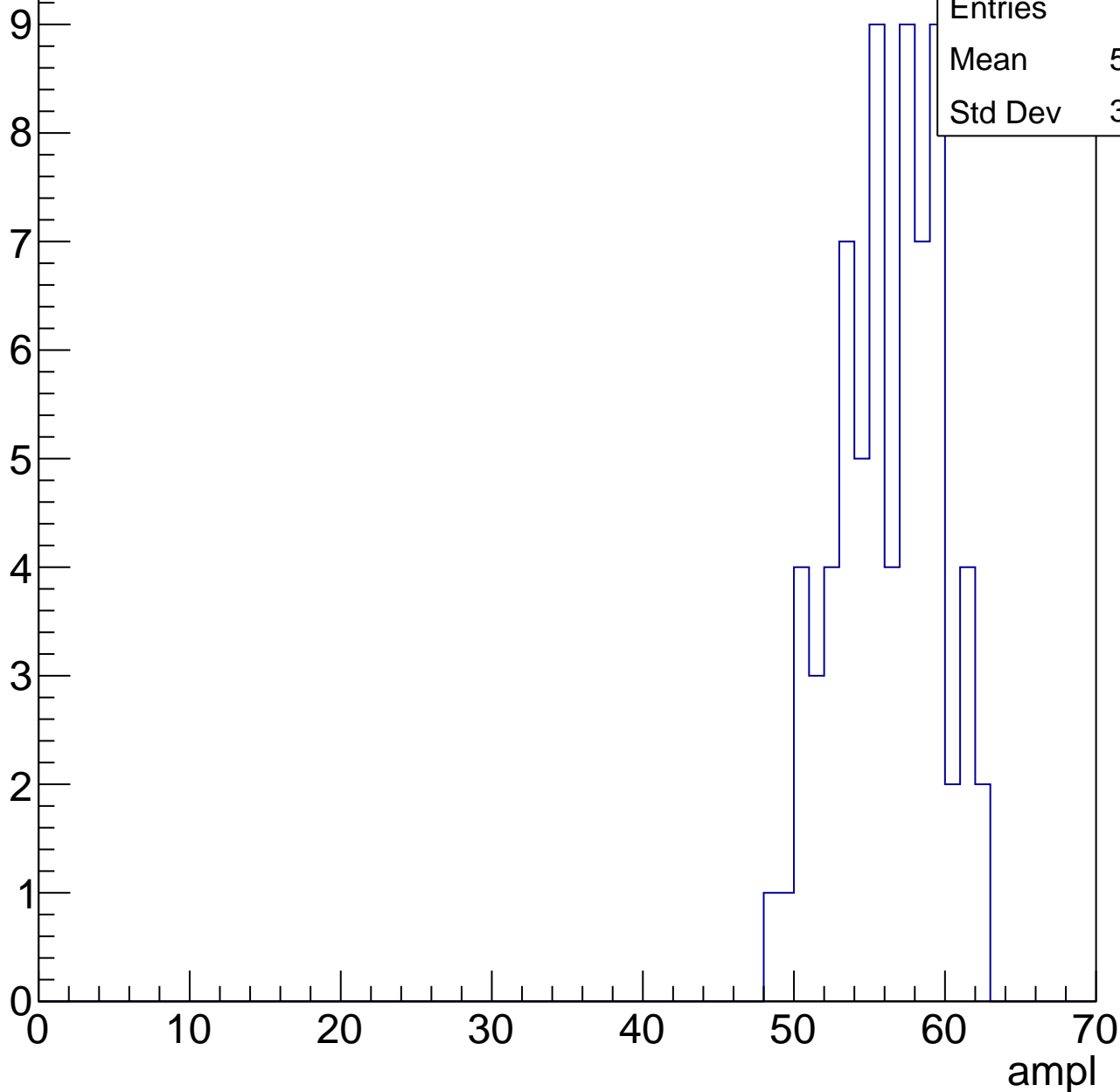
Entries	49
Mean	50.14
Std Dev	3.27



B0L002S, U7-ch49, adc4

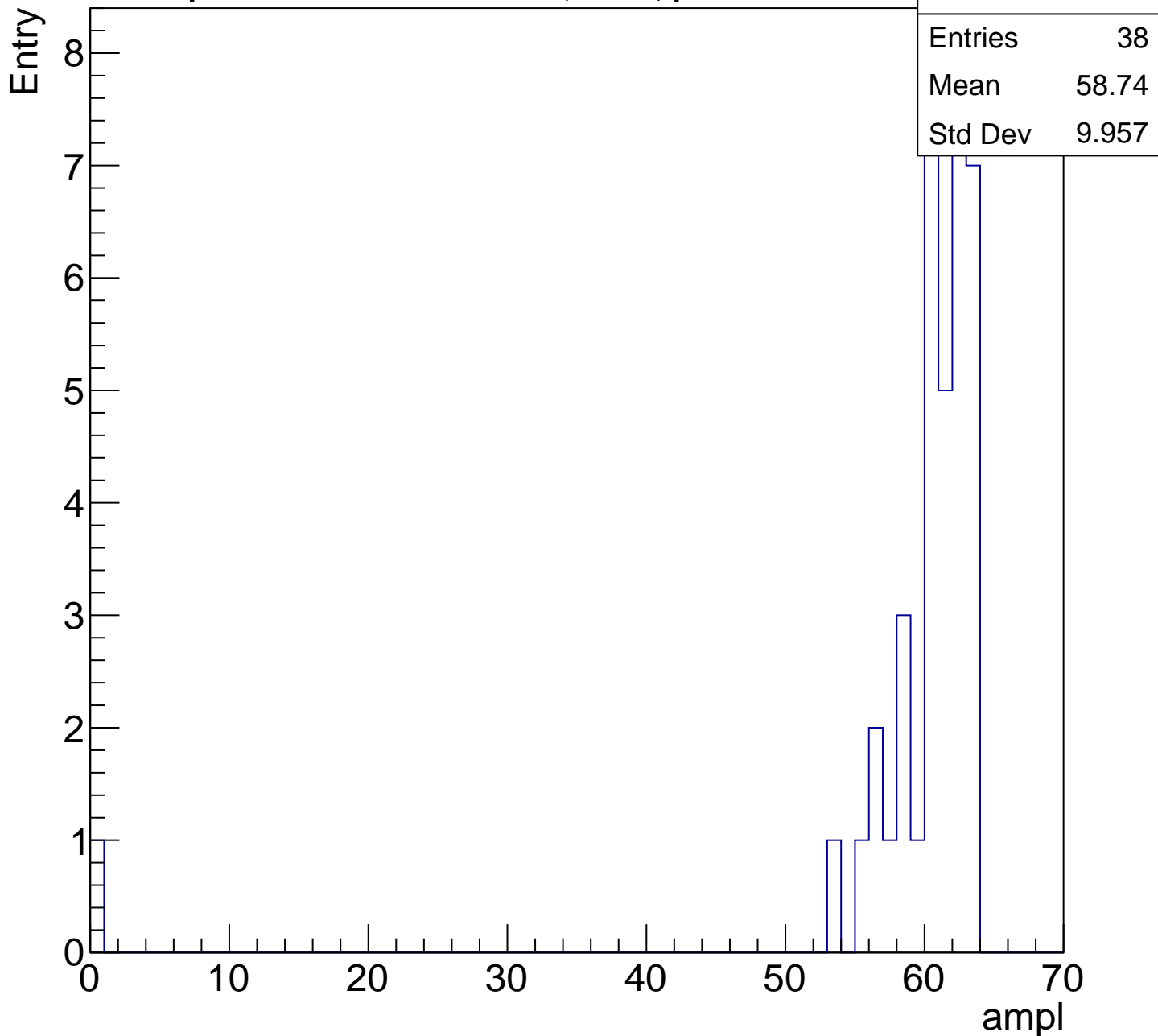
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch49, adc5

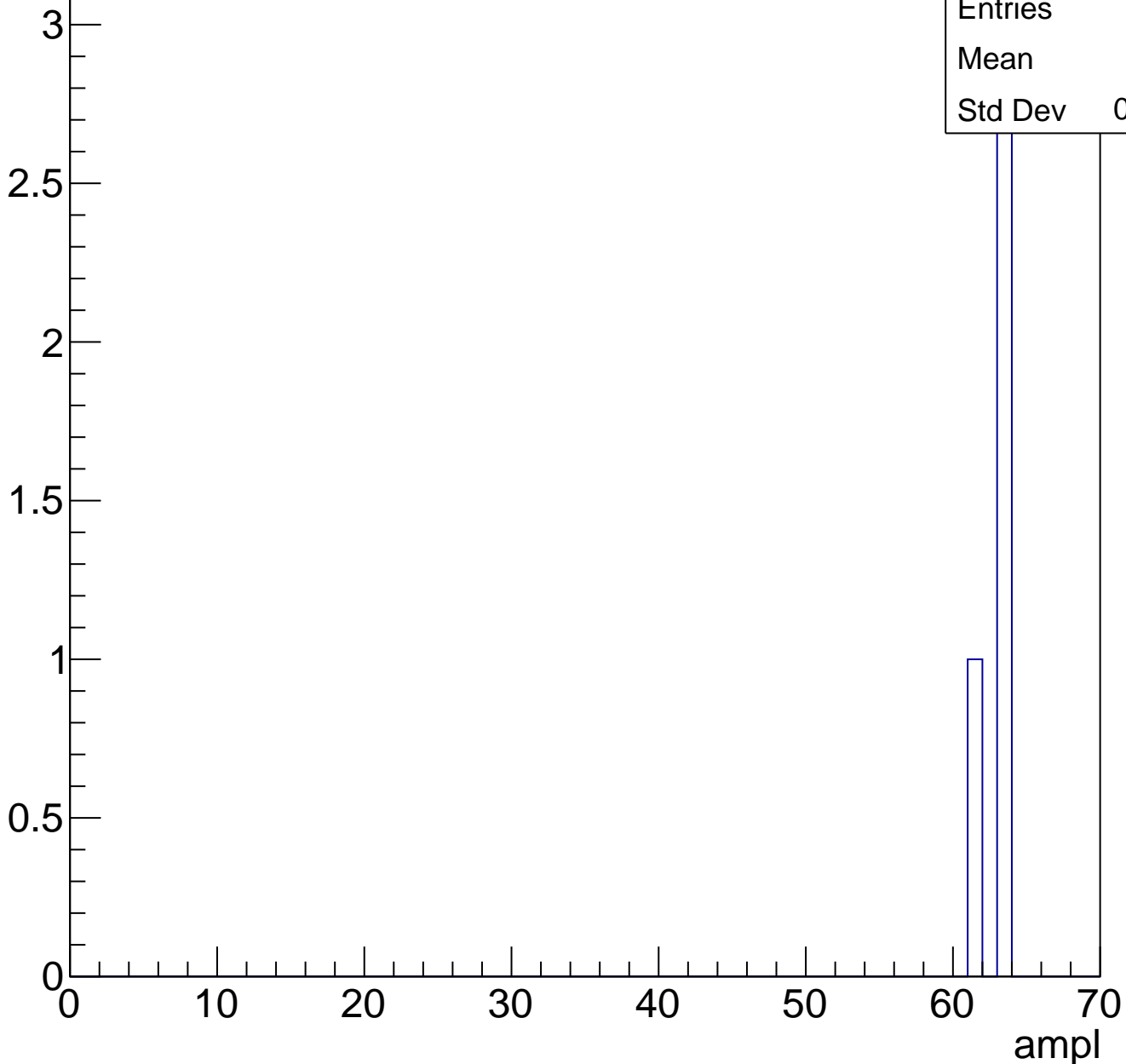
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch50, adc0

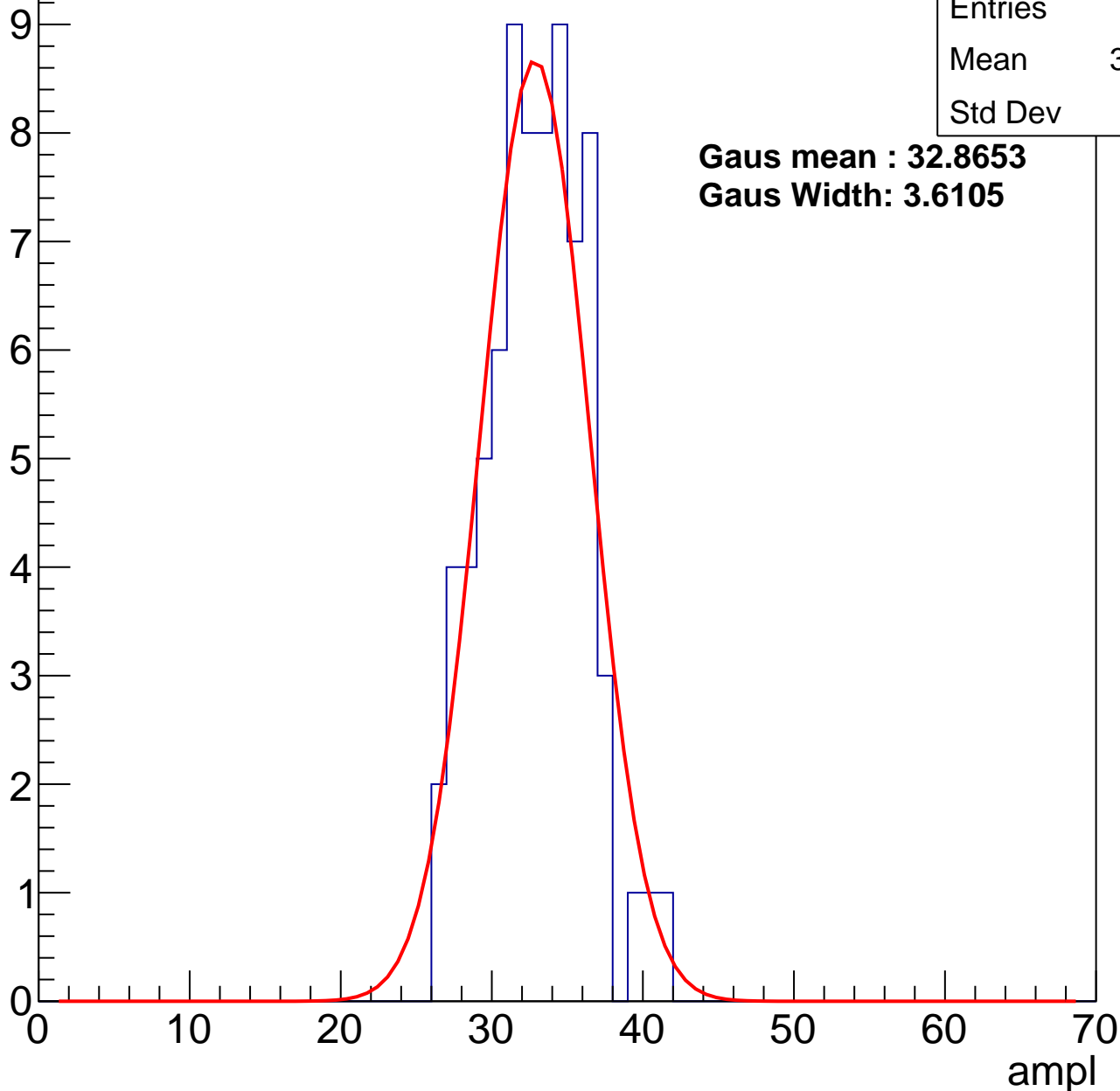
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	32.45
Std Dev	3.25

Gaus mean : 32.8653

Gaus Width: 3.6105



B0L002S, U7-ch50, adc1

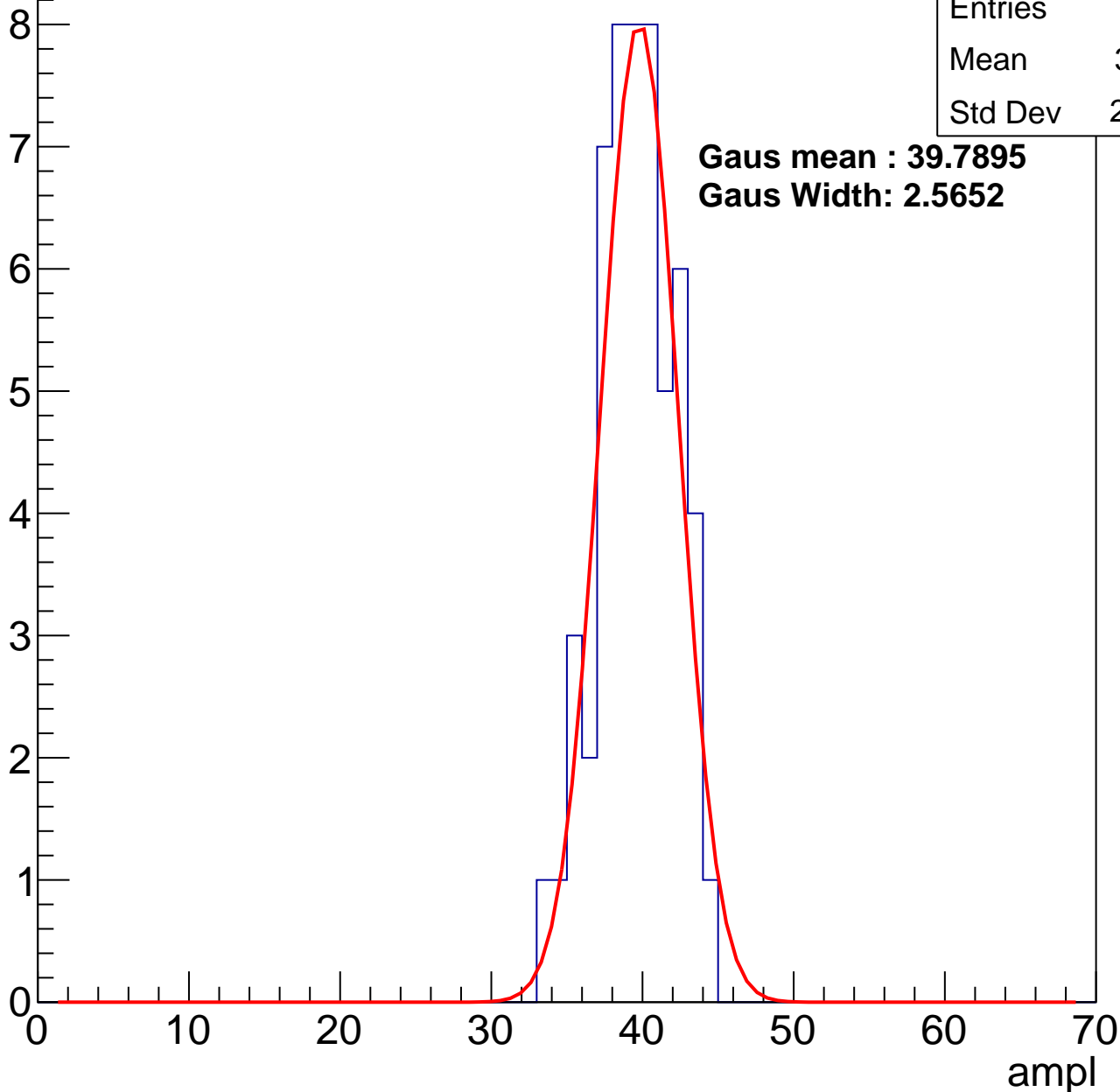
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	39.11
Std Dev	2.485

Gaus mean : 39.7895

Gaus Width: 2.5652



B0L002S, U7-ch50, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.38
Std Dev	3.153

Gaus mean : 46.1077

Gaus Width: 3.1784

10

8

6

4

2

0

ampl

0

10

20

30

40

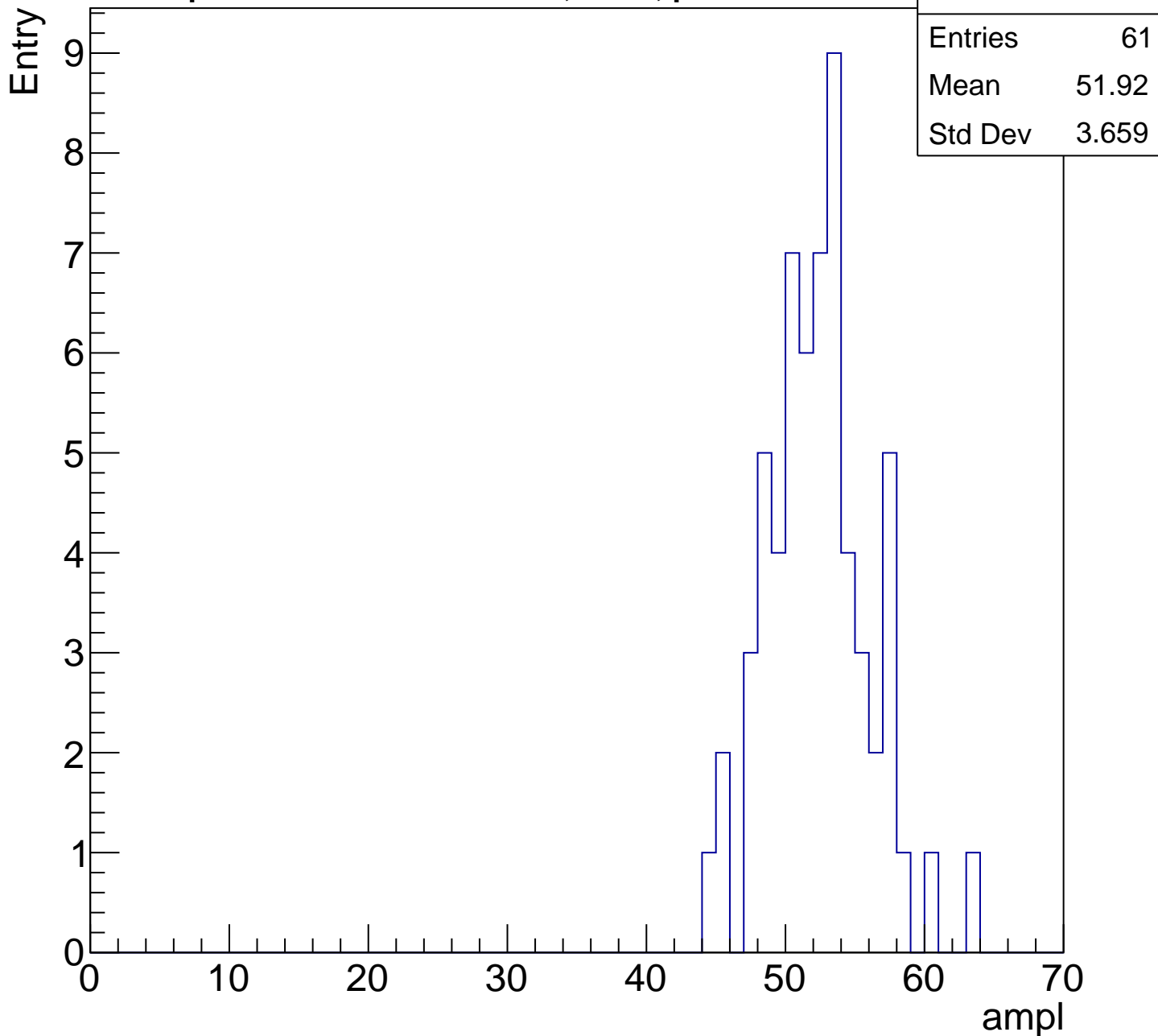
50

60

70

B0L002S, U7-ch50, adc3

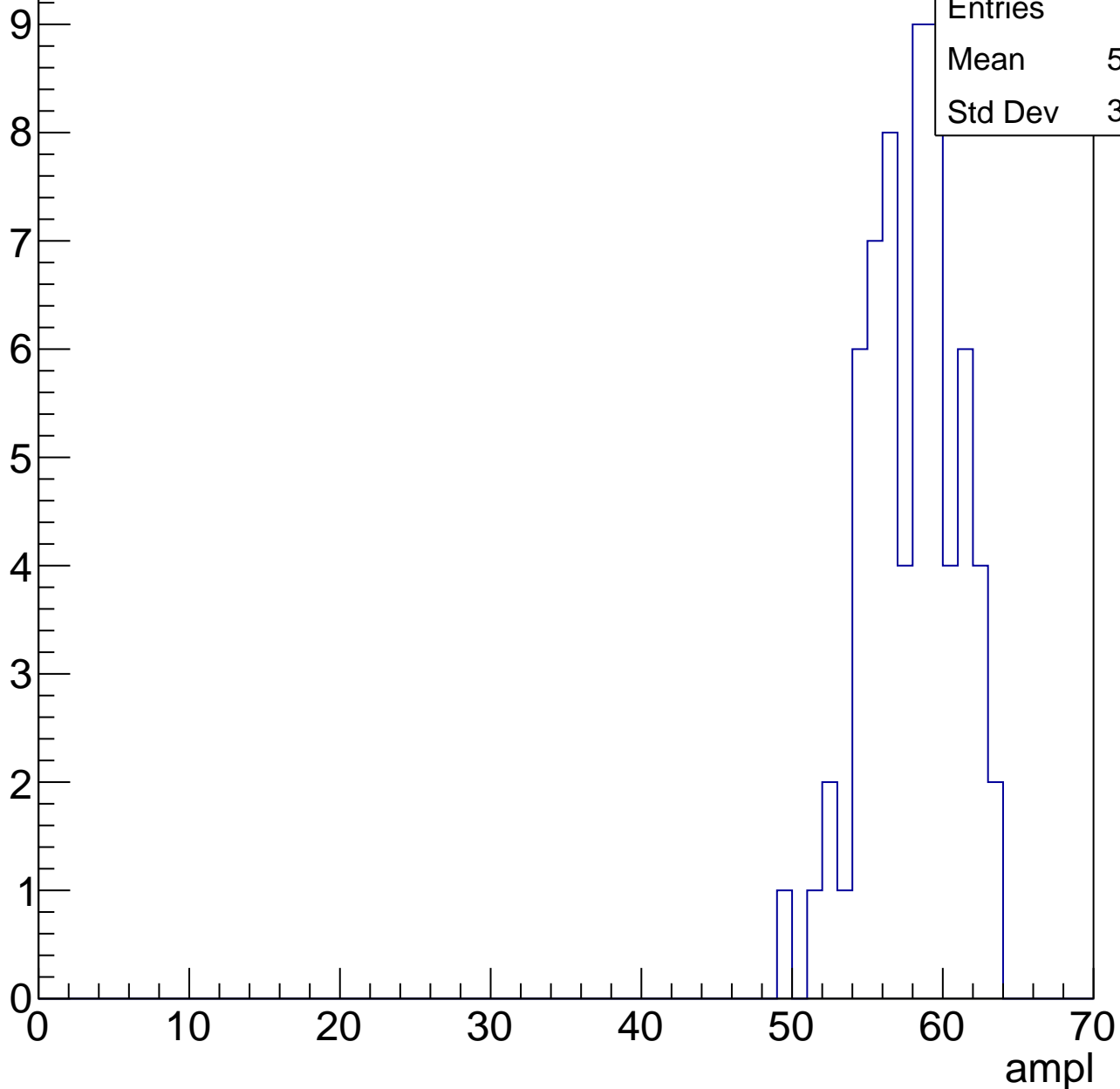
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

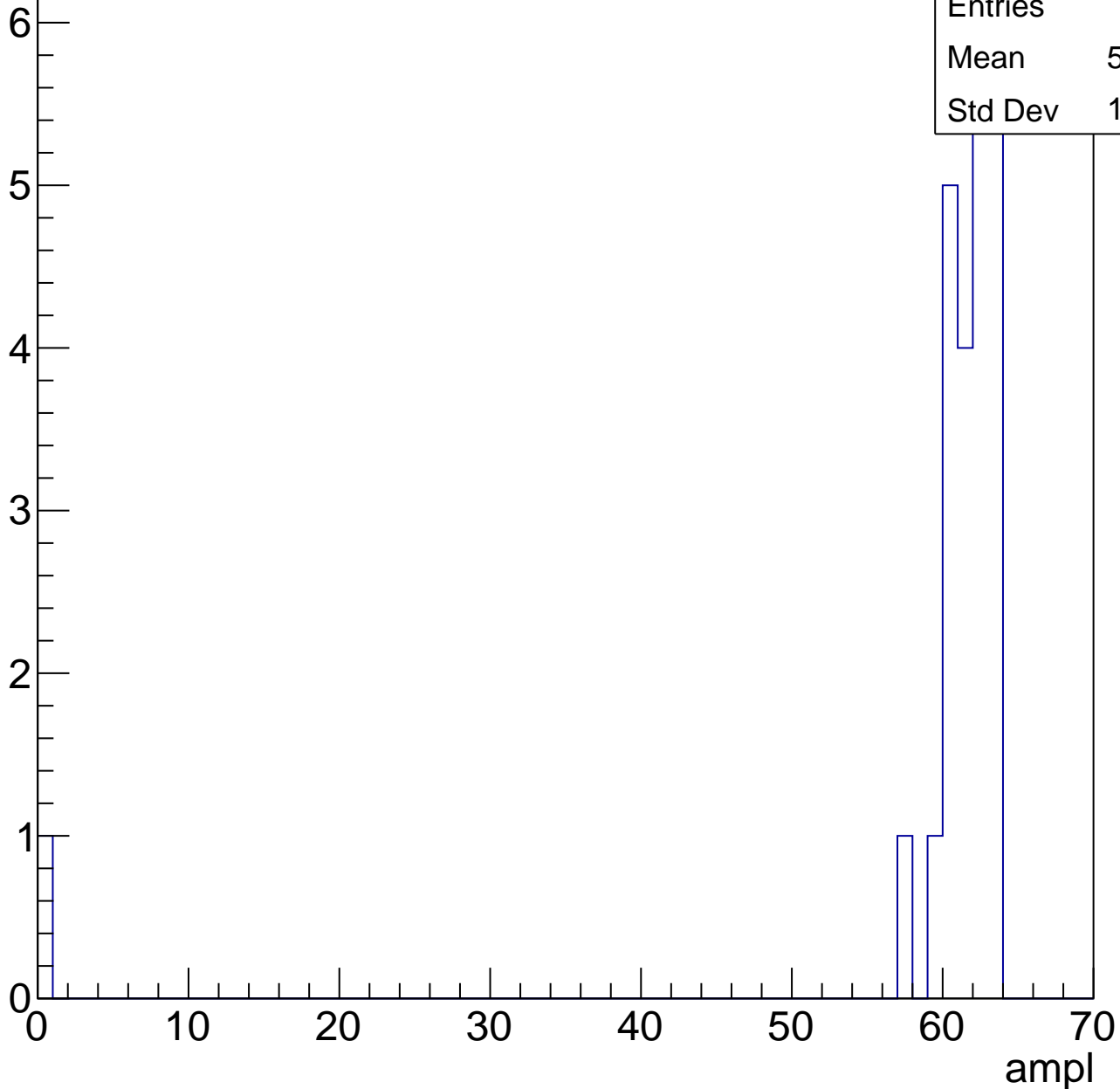


B0L002S, U7-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.75
Std Dev	12.34



B0L002S, U7-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

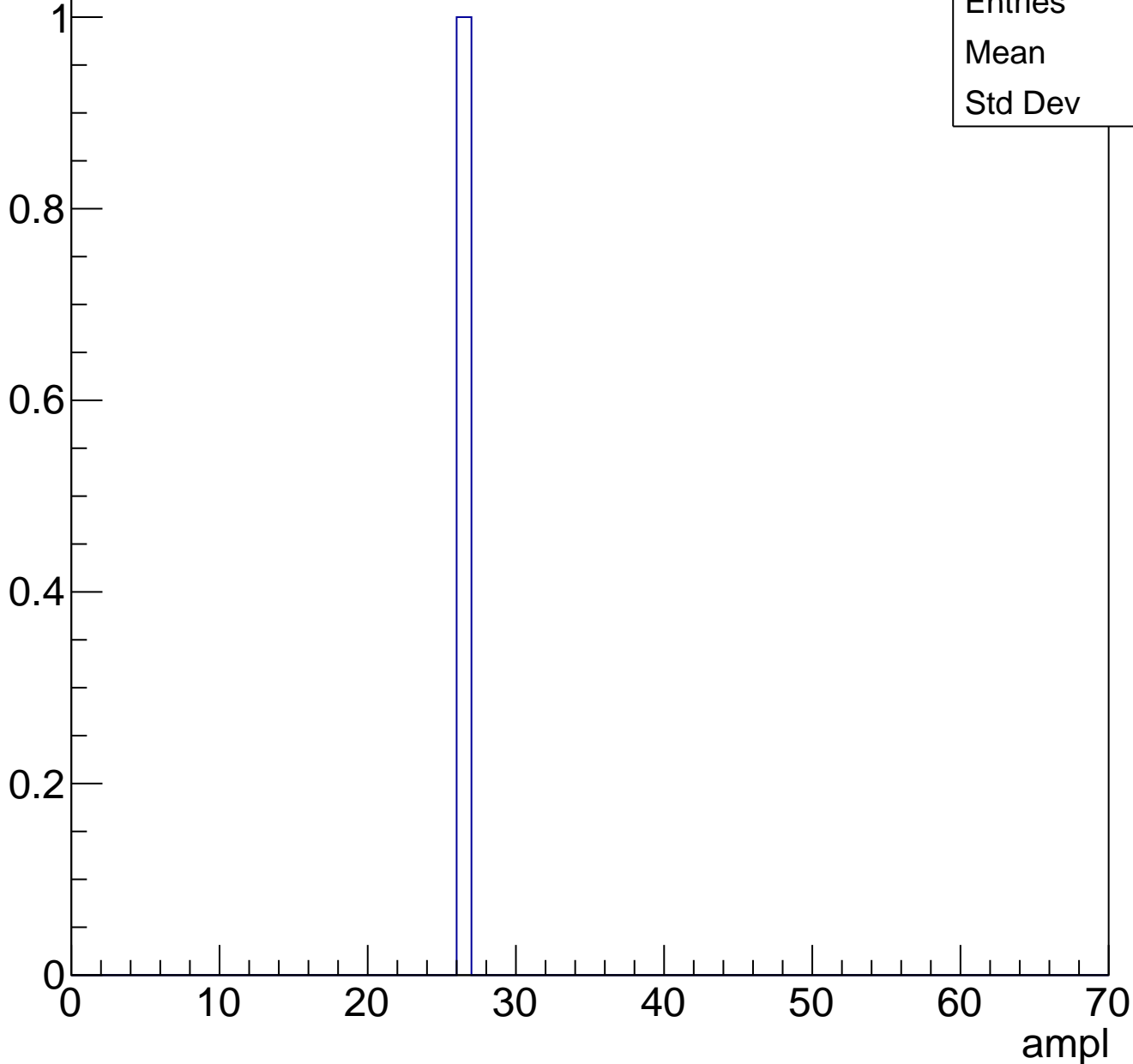
Entry



B0L002S, U7-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	26
Std Dev	0

B0L002S, U7-ch51, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	32.52
Std Dev	3.429

Gaus mean : 33.0076

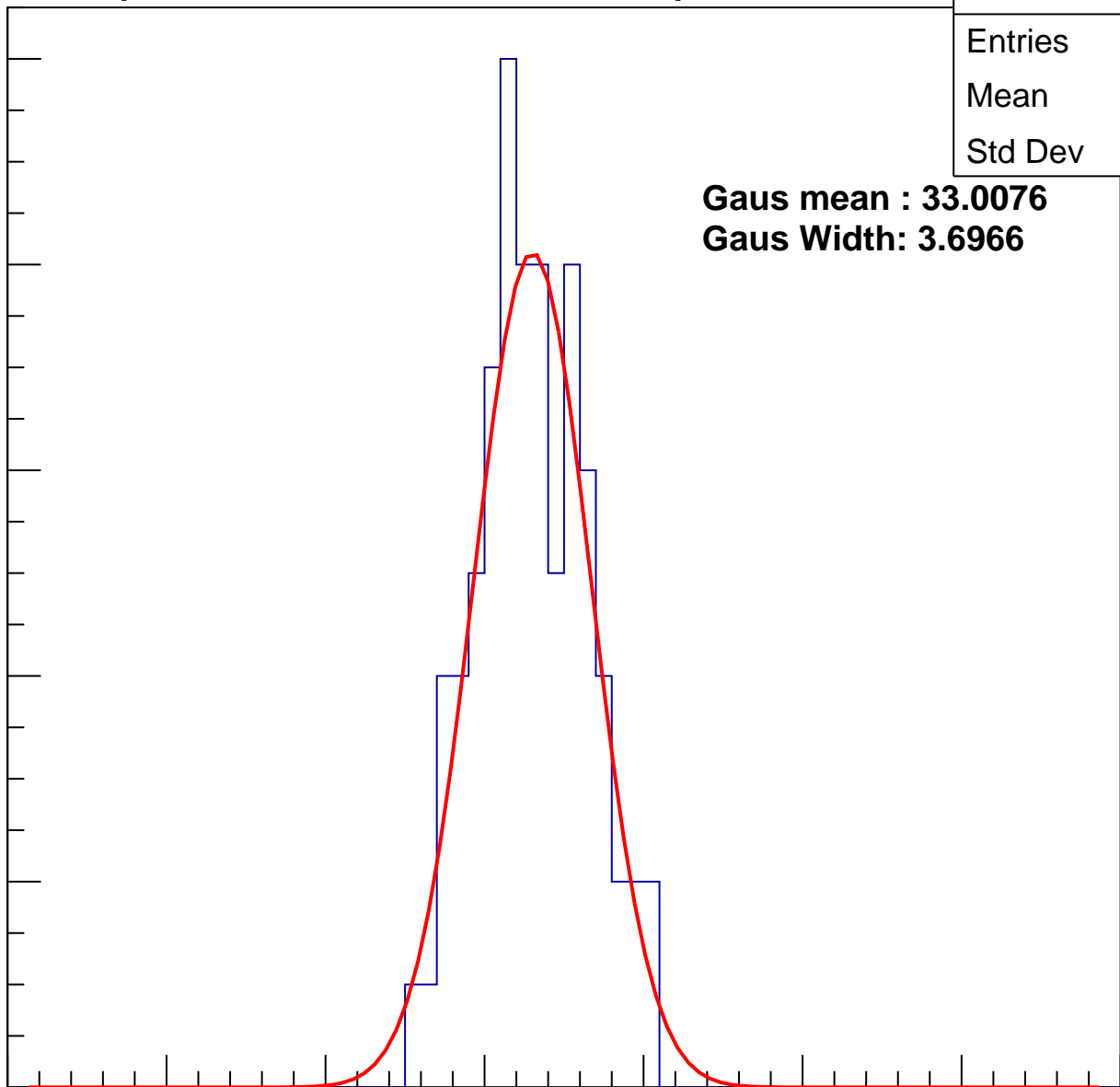
Gaus Width: 3.6966

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch51, adc1

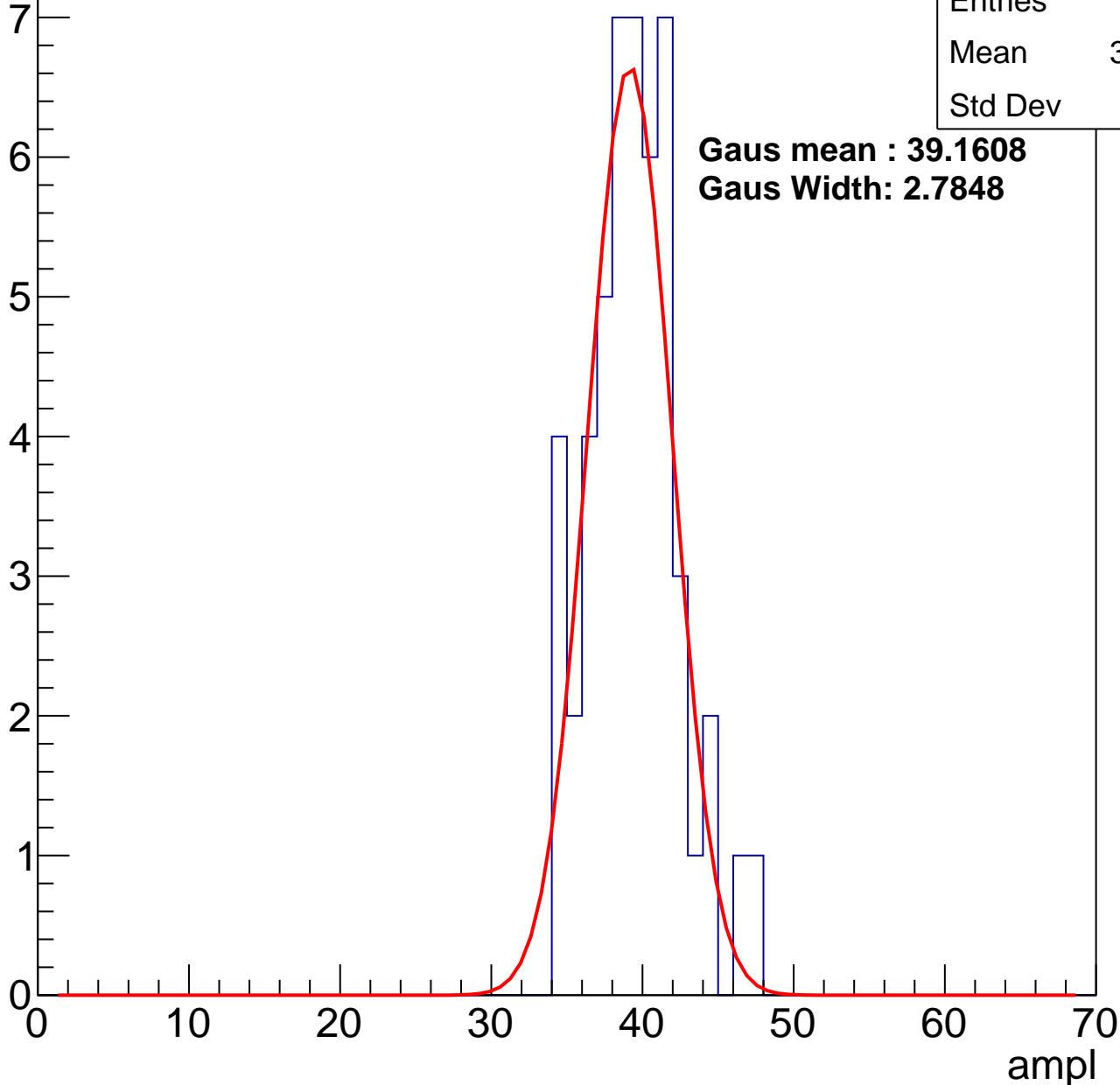
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	39.02
Std Dev	2.95

Gaus mean : 39.1608

Gaus Width: 2.7848



B0L002S, U7-ch51, adc2

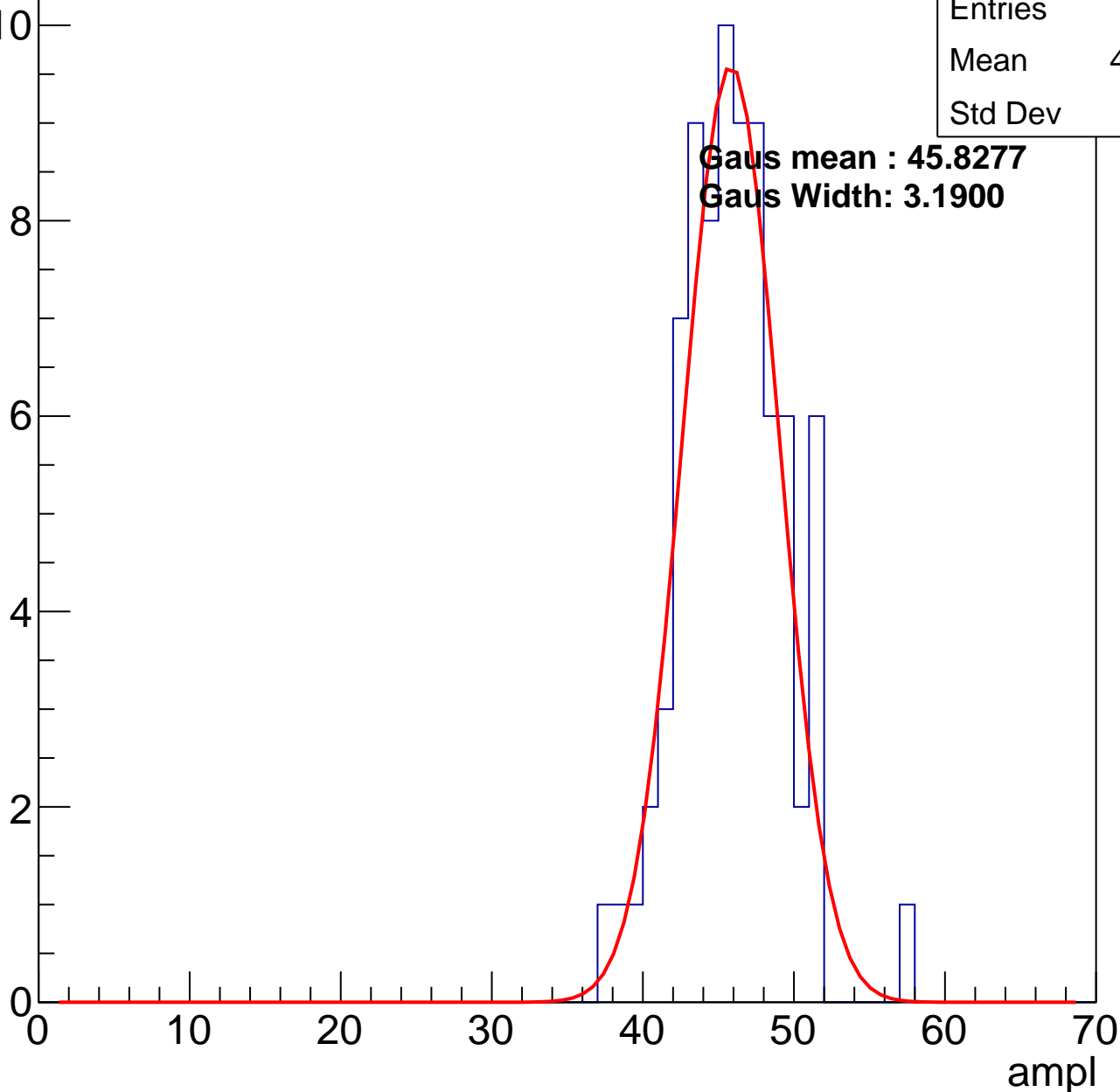
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	45.46
Std Dev	3.41

Gaus mean : 45.8277

Gaus Width: 3.1900

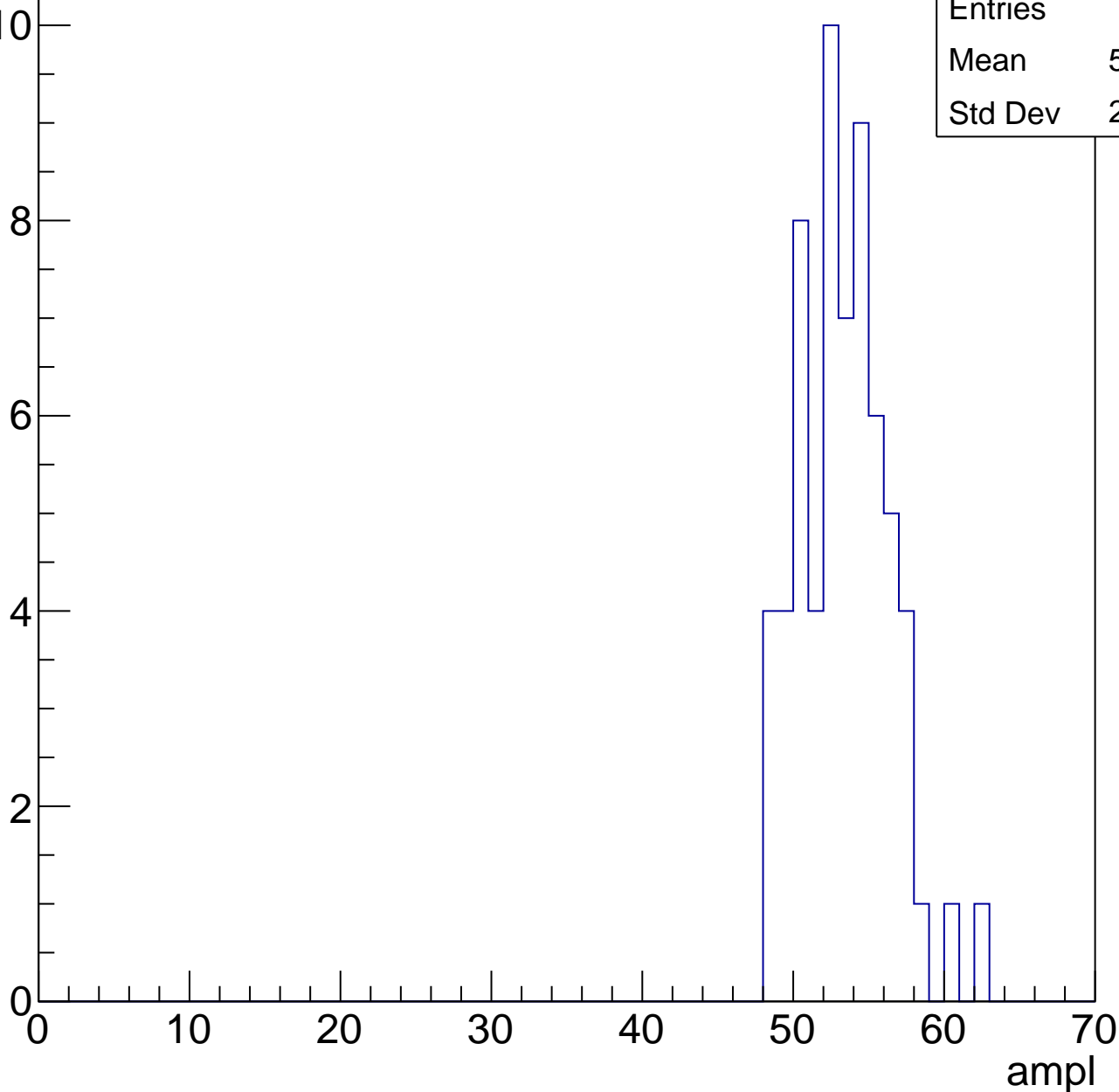


B0L002S, U7-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

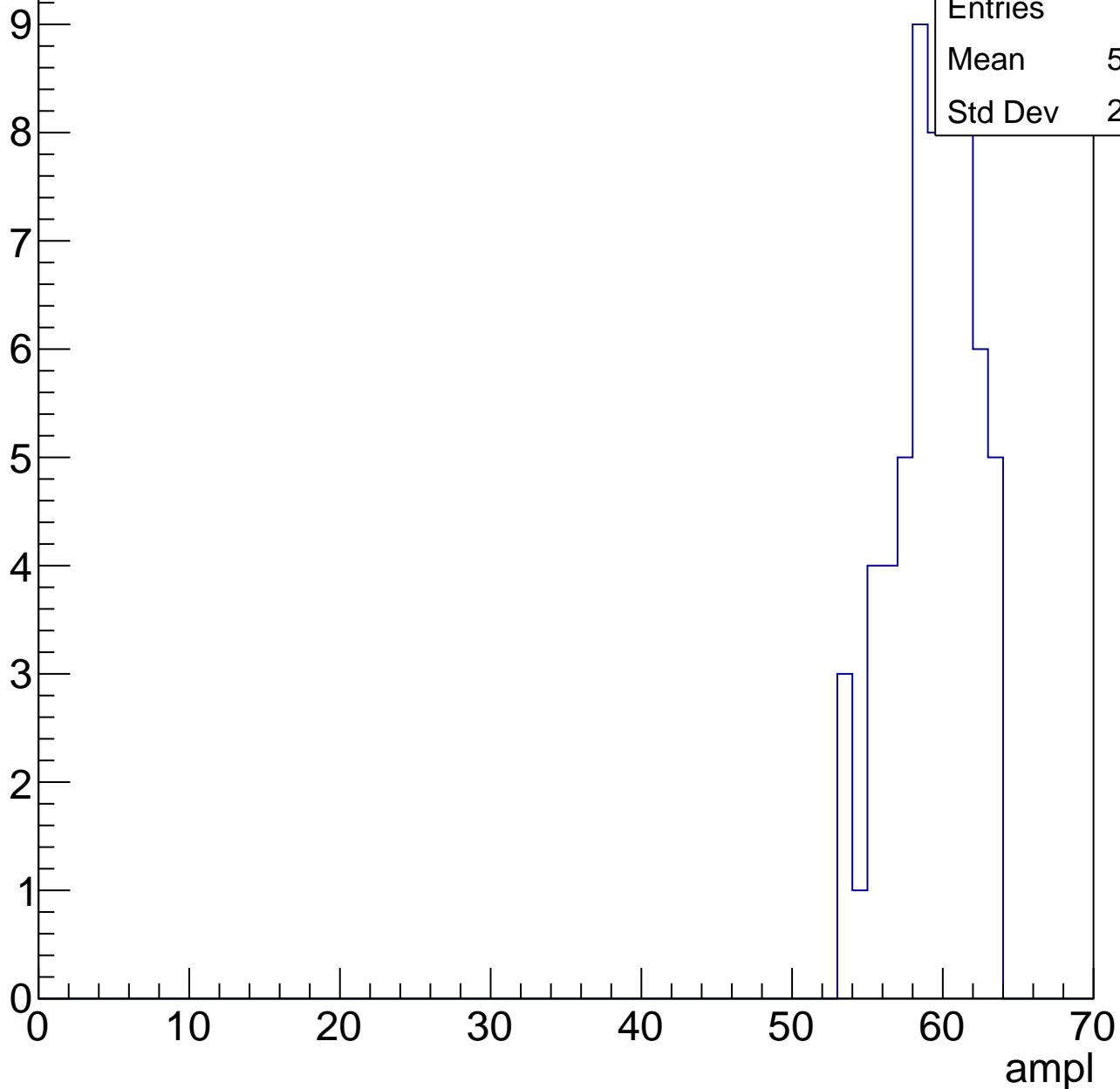
Entries	64
Mean	52.92
Std Dev	2.954



B0L002S, U7-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

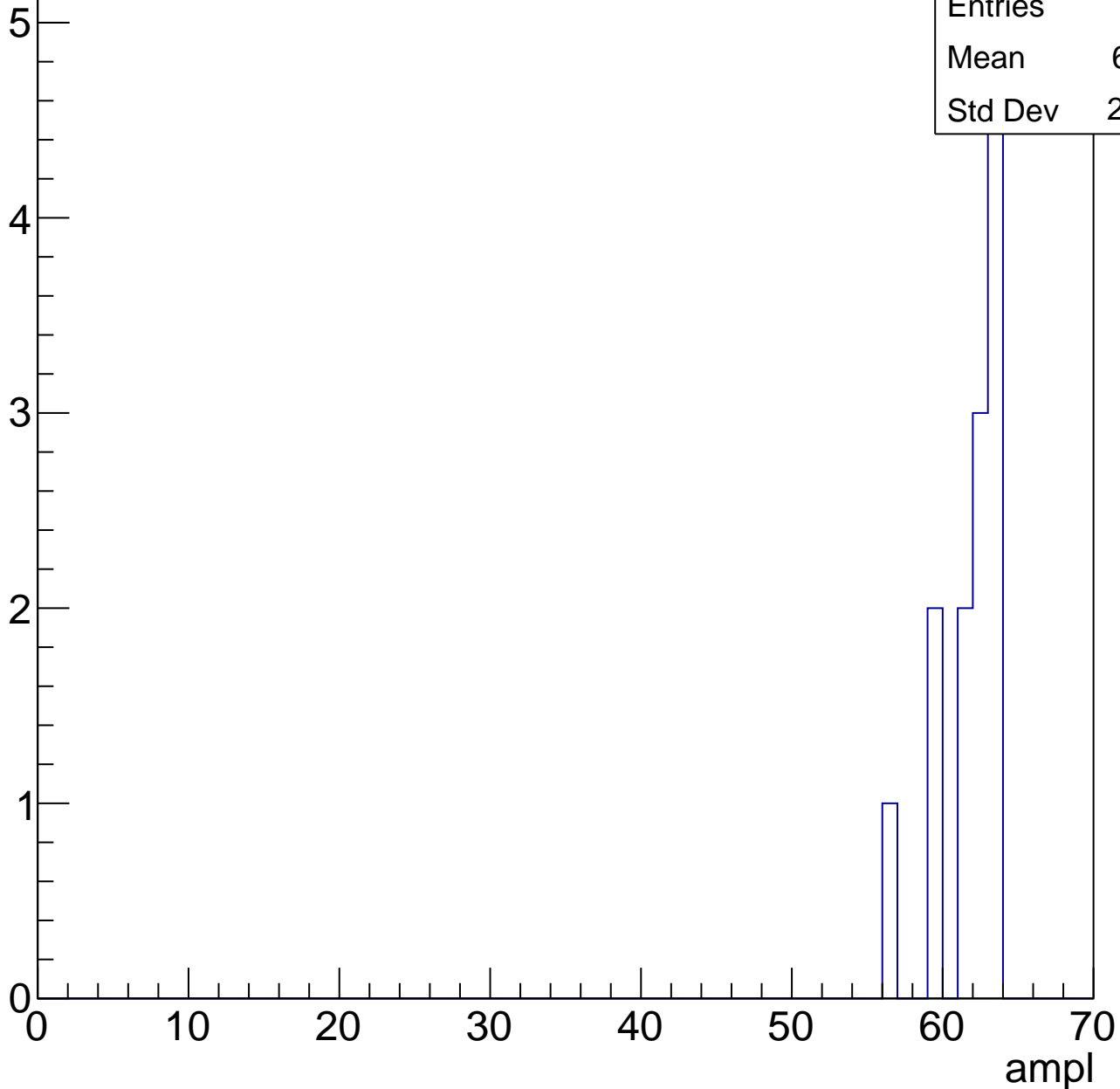


B0L002S, U7-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	61.31
Std Dev	2.053



B0L002S, U7-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch52, adc0

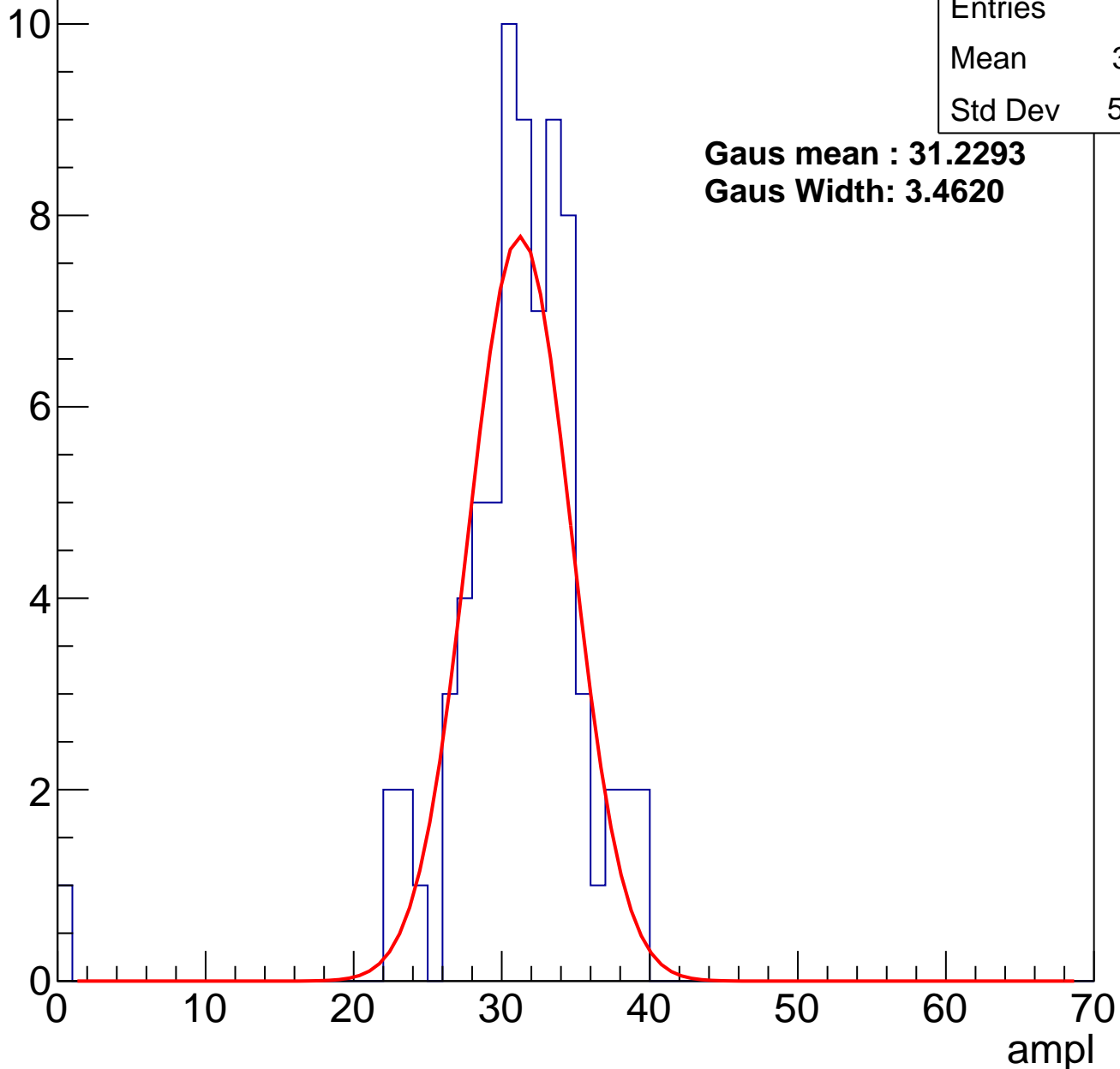
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	30.61
Std Dev	5.115

Gaus mean : 31.2293

Gaus Width: 3.4620

Entry



B0L002S, U7-ch52, adc1

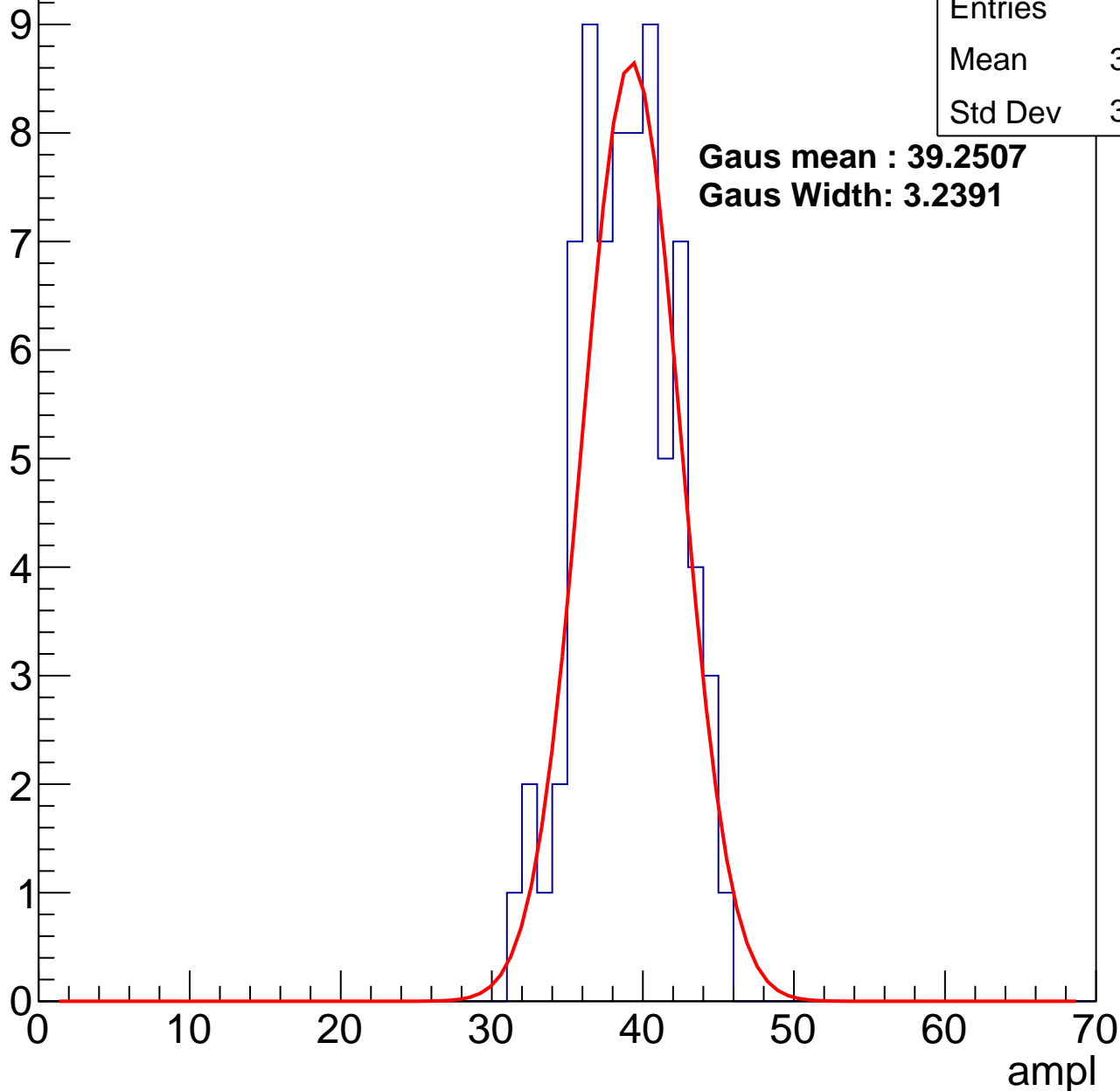
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.49
Std Dev	3.129

Gaus mean : 39.2507

Gaus Width: 3.2391



B0L002S, U7-ch52, adc2

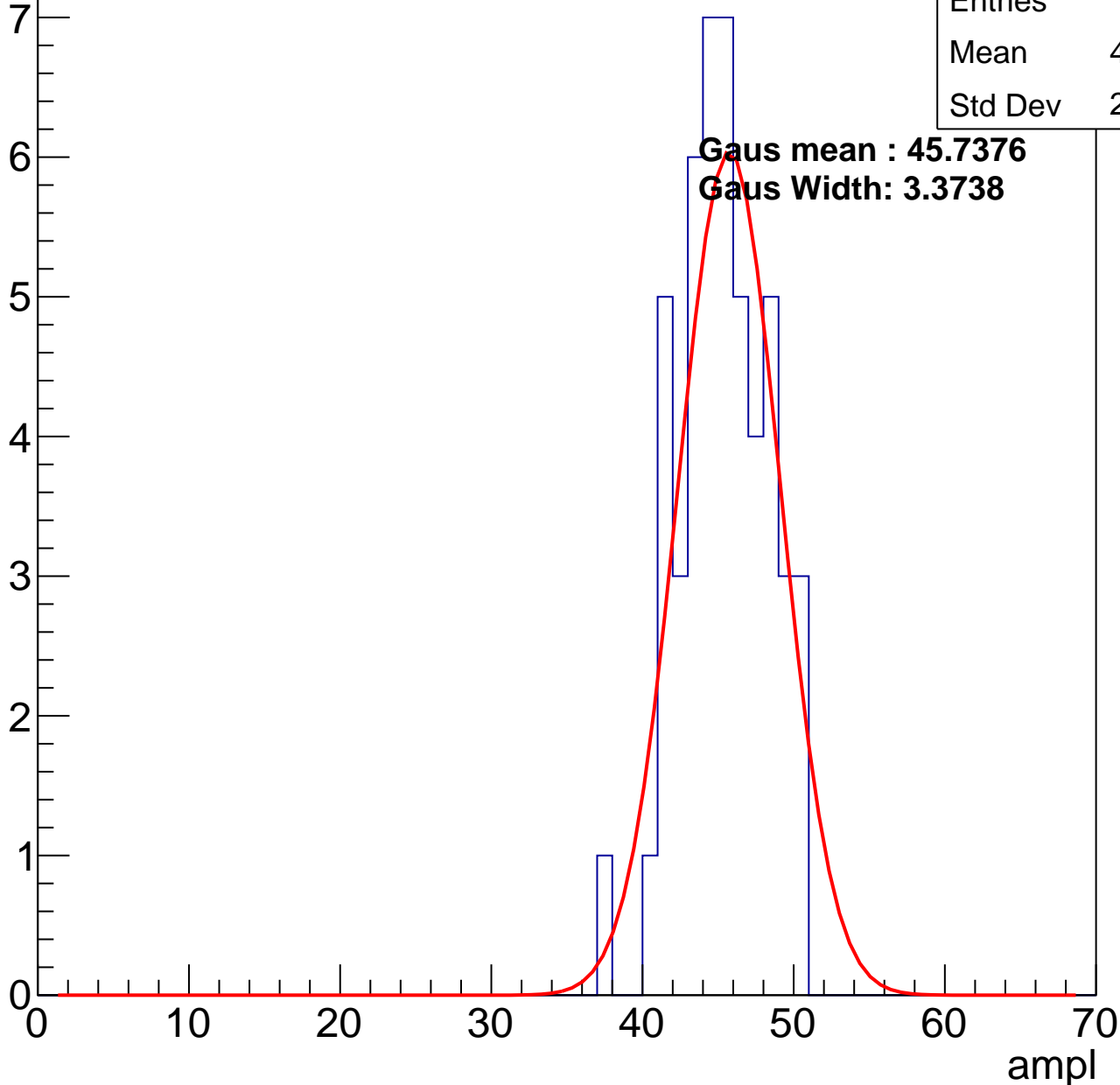
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	44.88
Std Dev	2.882

Gaus mean : 45.7376

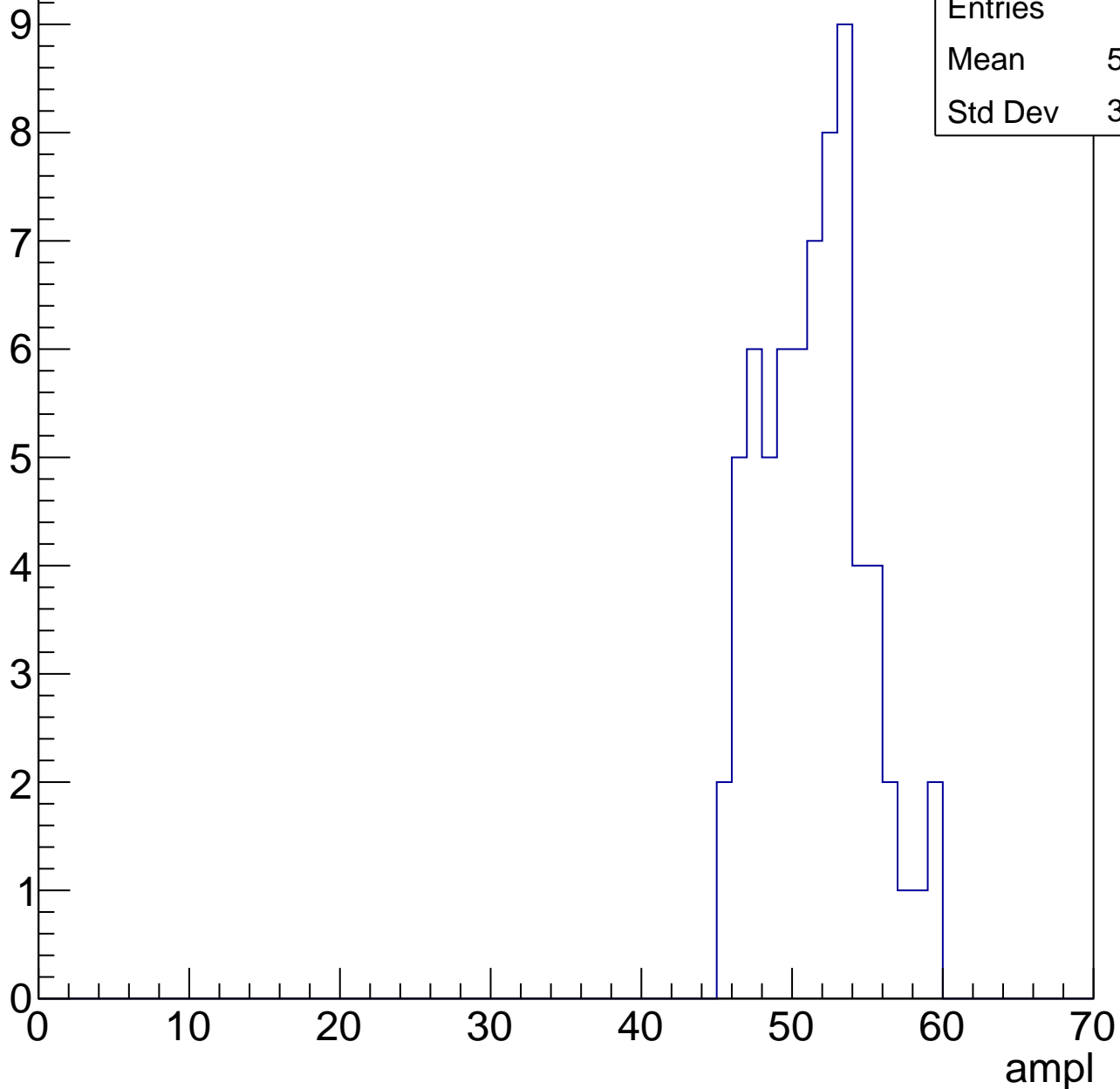
Gaus Width: 3.3738



B0L002S, U7-ch52, adc3

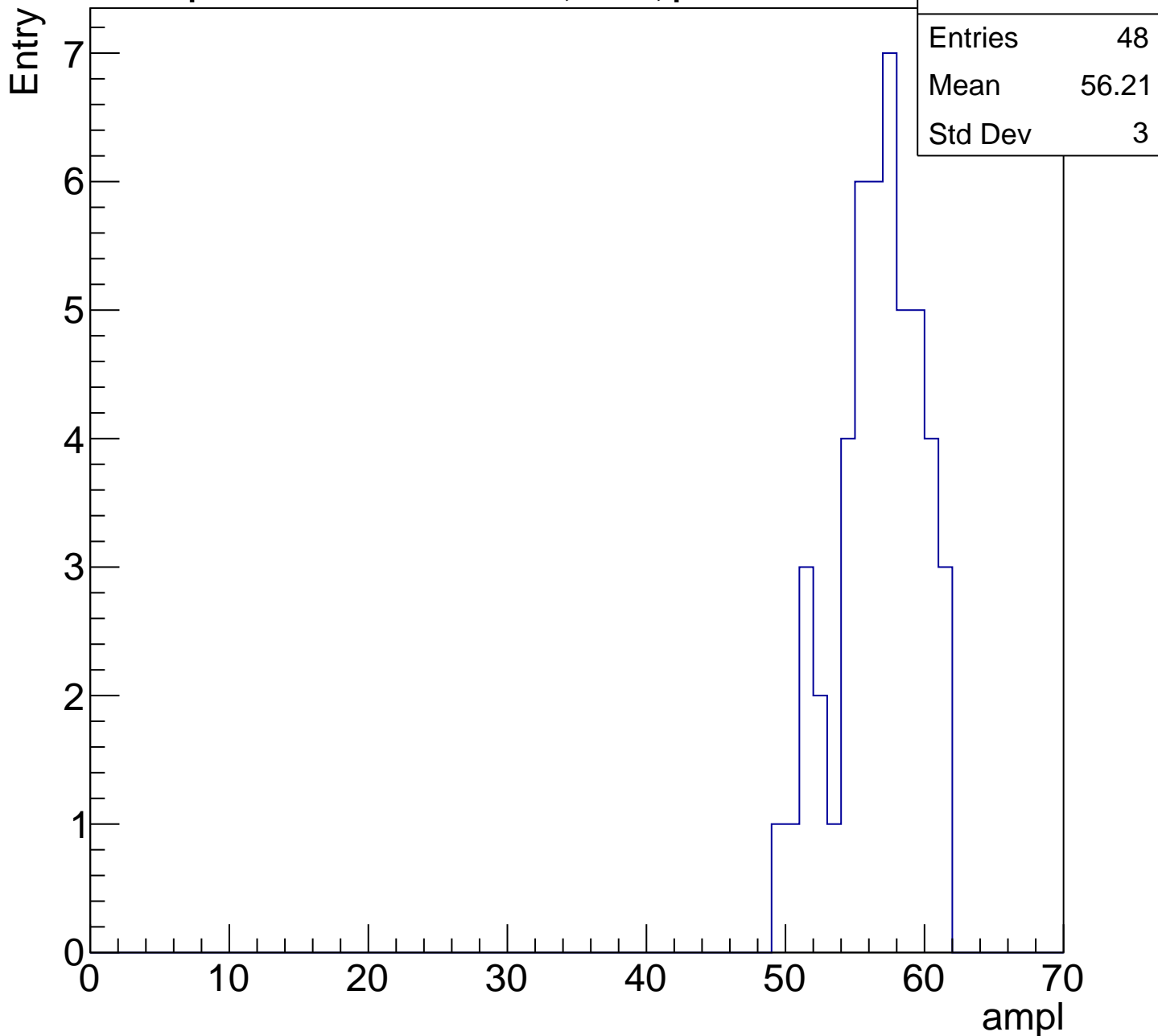
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

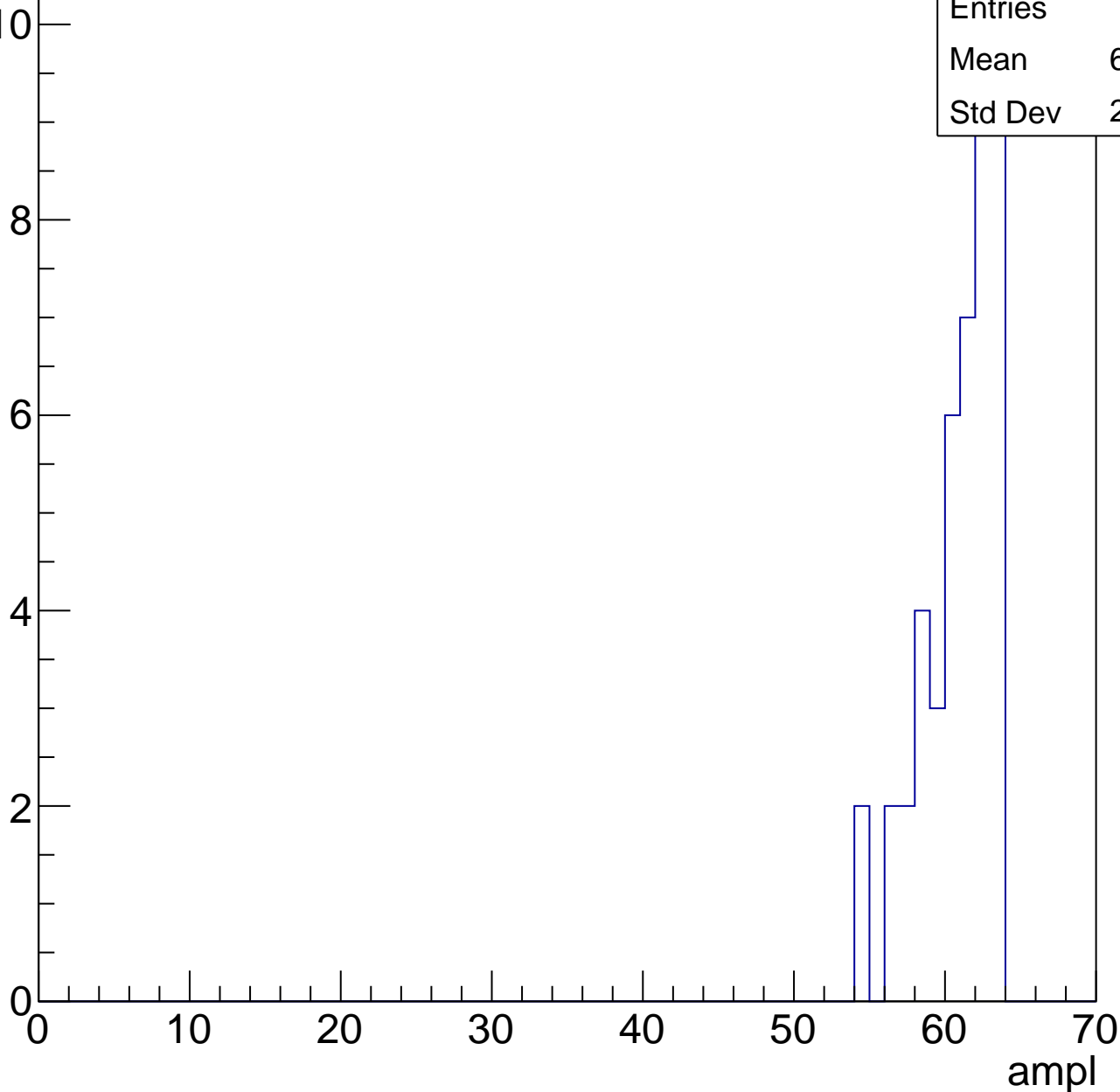


B0L002S, U7-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

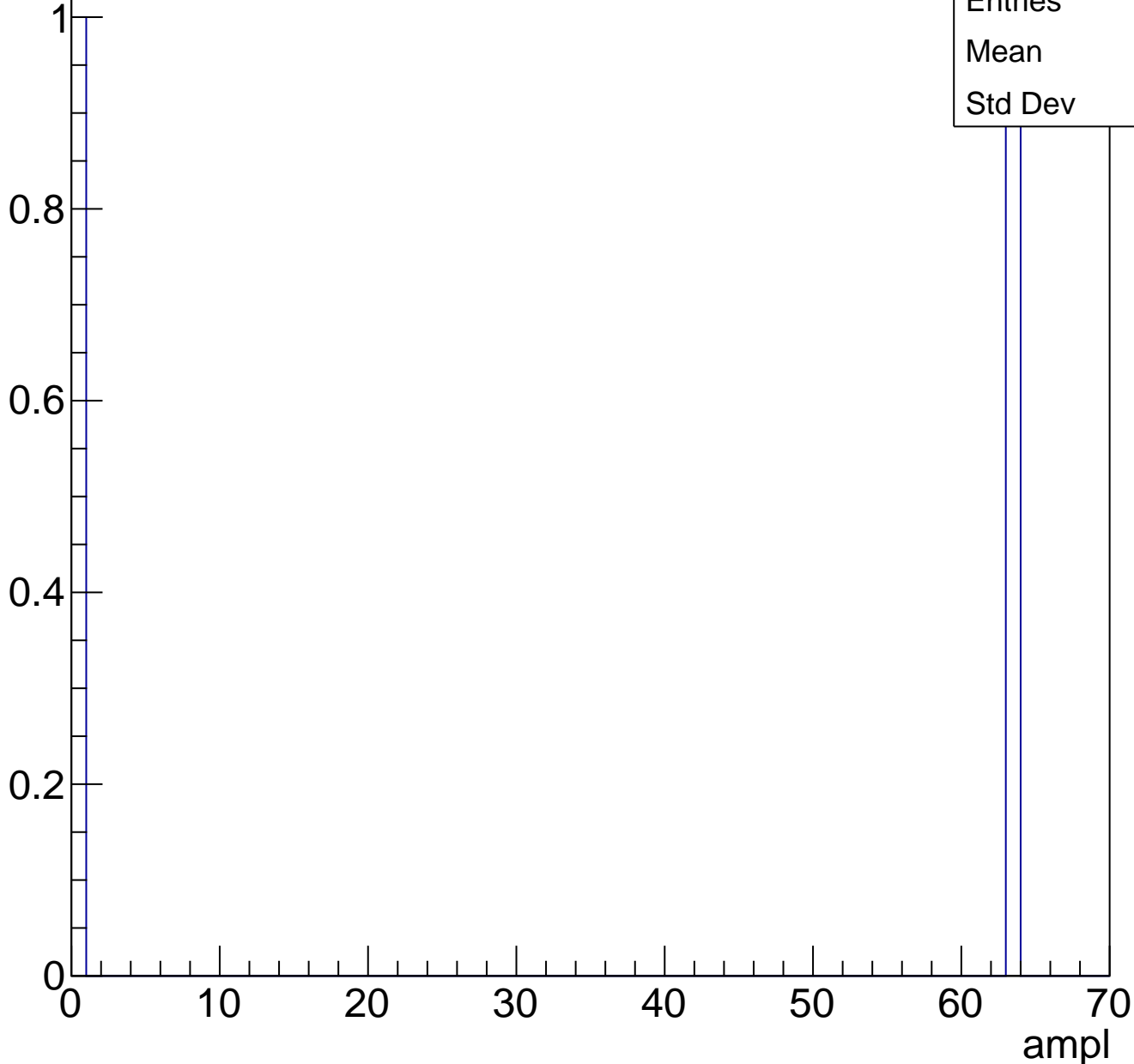
Entries	45
Mean	60.38
Std Dev	2.416



B0L002S, U7-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch53, adc0

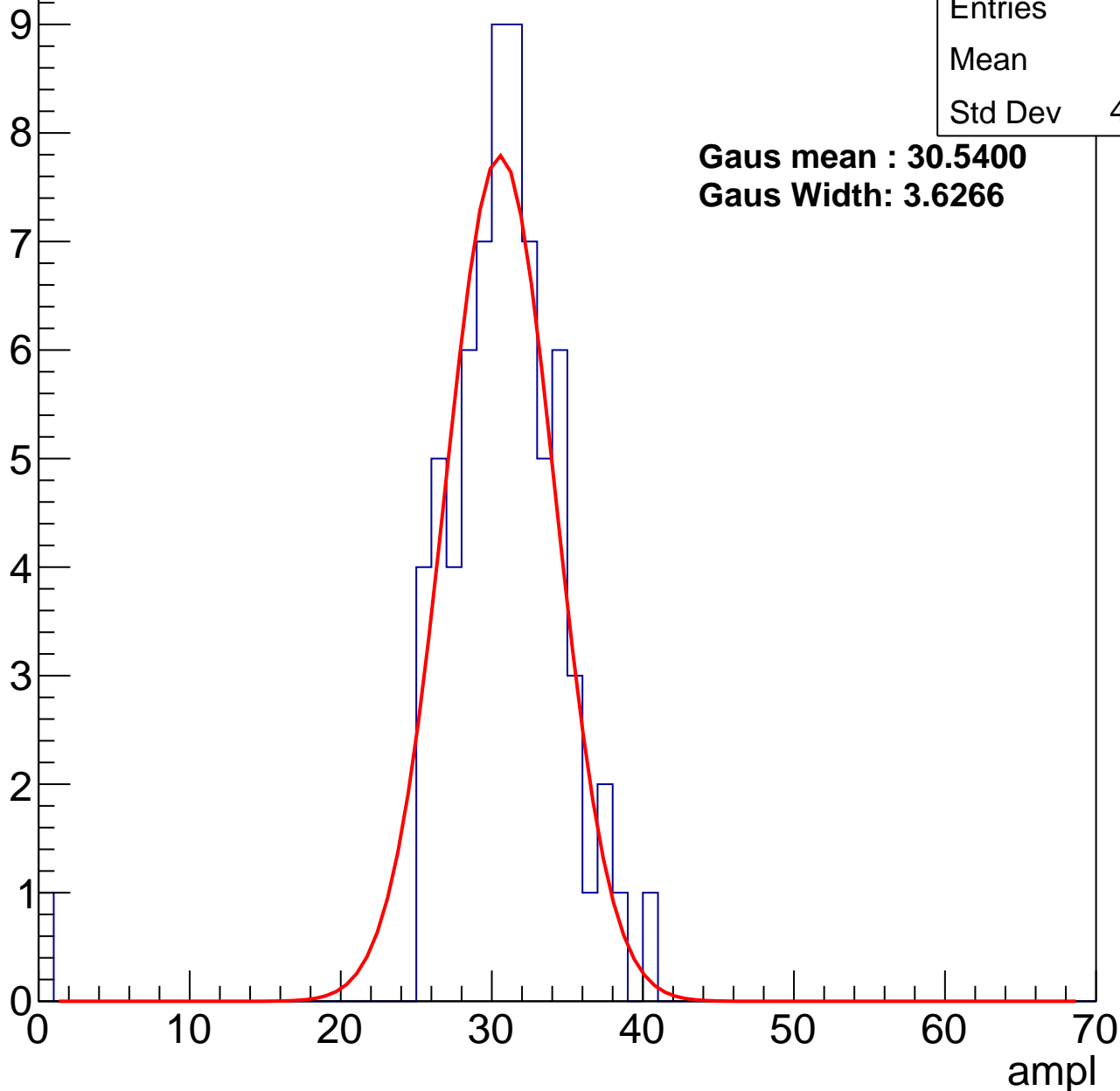
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.2
Std Dev	4.886

Gaus mean : 30.5400

Gaus Width: 3.6266



B0L002S, U7-ch53, adc1

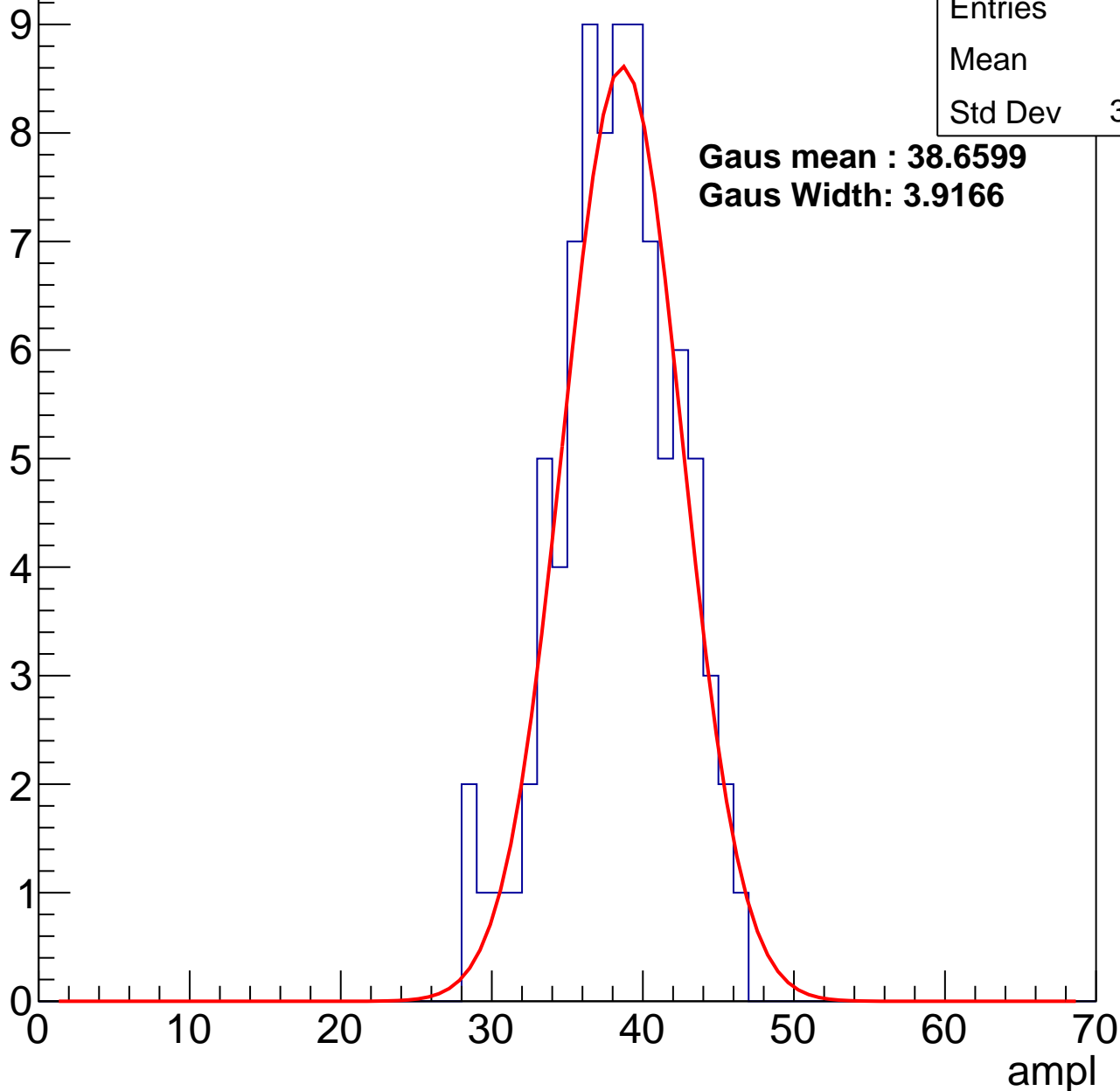
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	37.8
Std Dev	3.912

Gaus mean : 38.6599

Gaus Width: 3.9166



B0L002S, U7-ch53, adc2

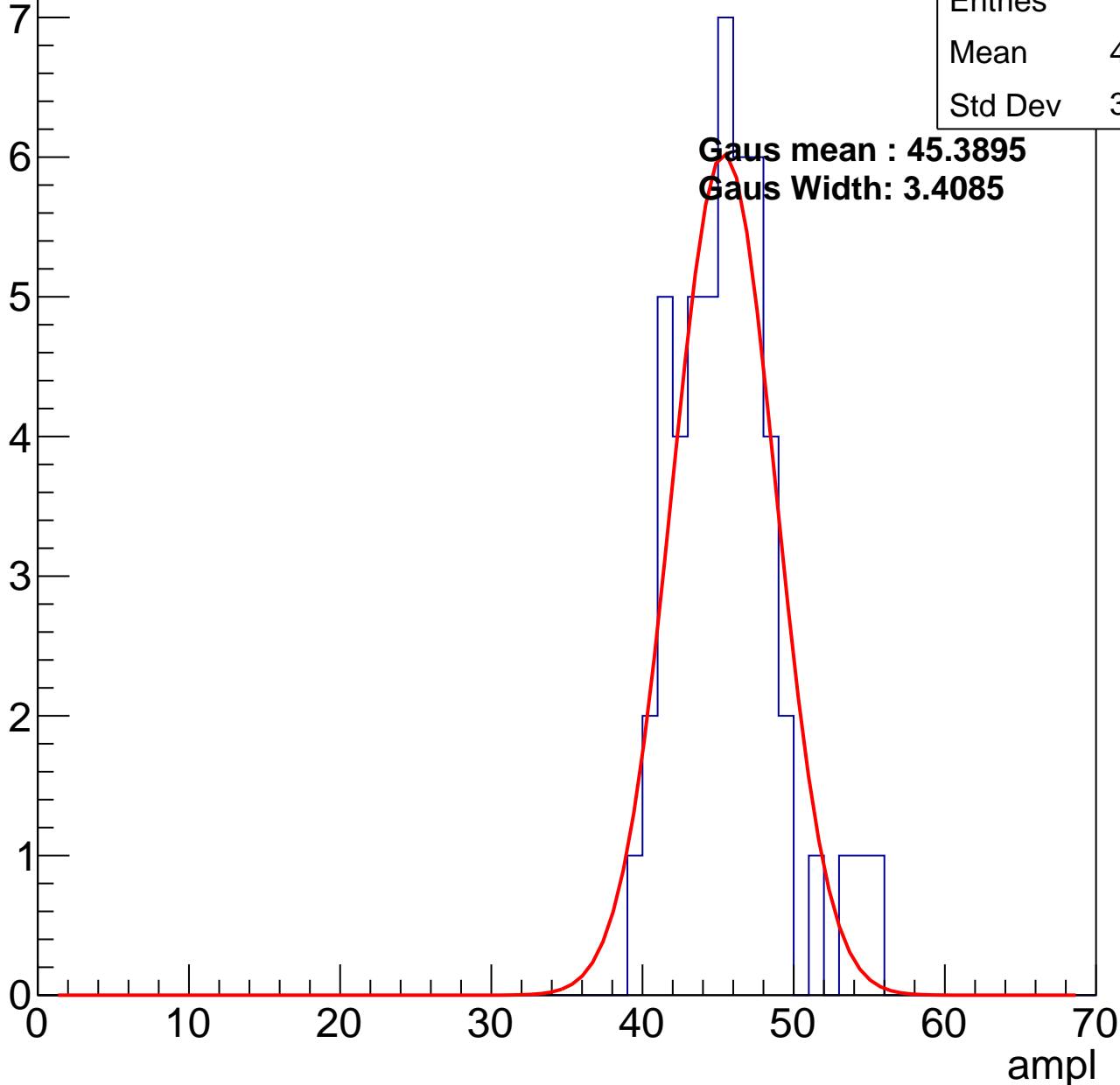
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.16
Std Dev	3.444

Gaus mean : 45.3895

Gaus Width: 3.4085

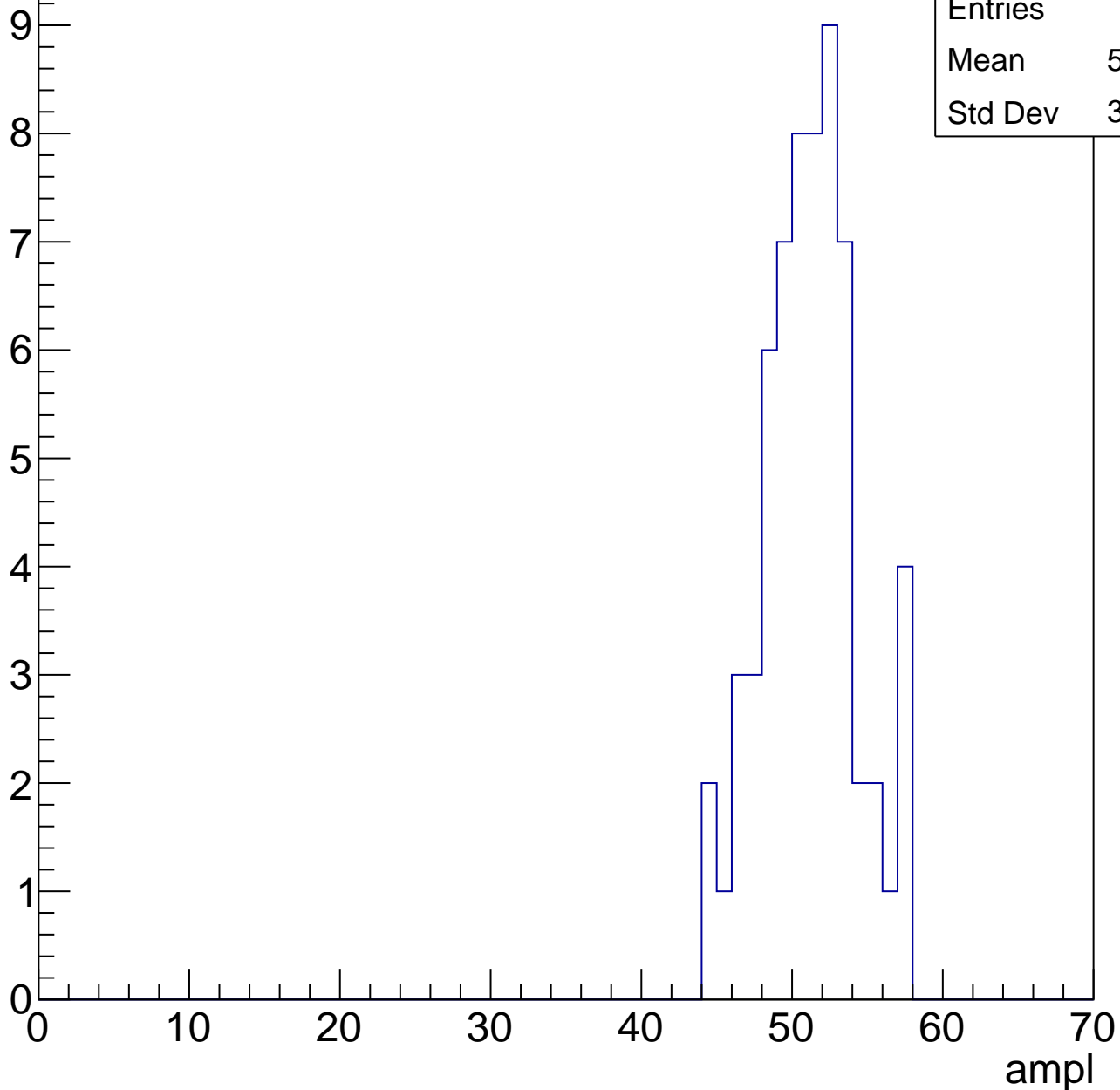


B0L002S, U7-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.67
Std Dev	3.076



B0L002S, U7-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	56.99
Std Dev	3.342

Entry

10

8

6

4

2

0

0

10

20

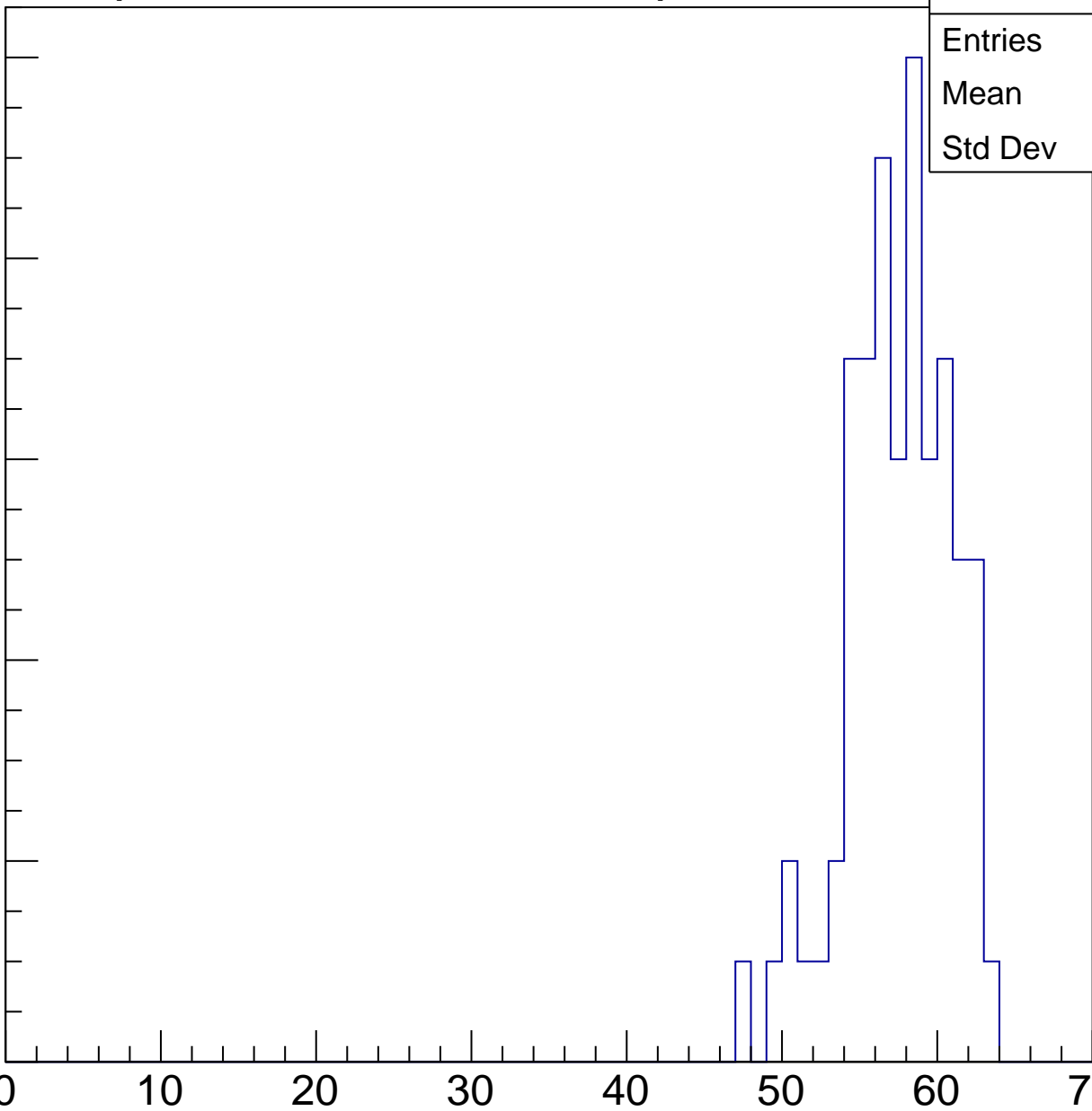
30

40

50

60

ampl

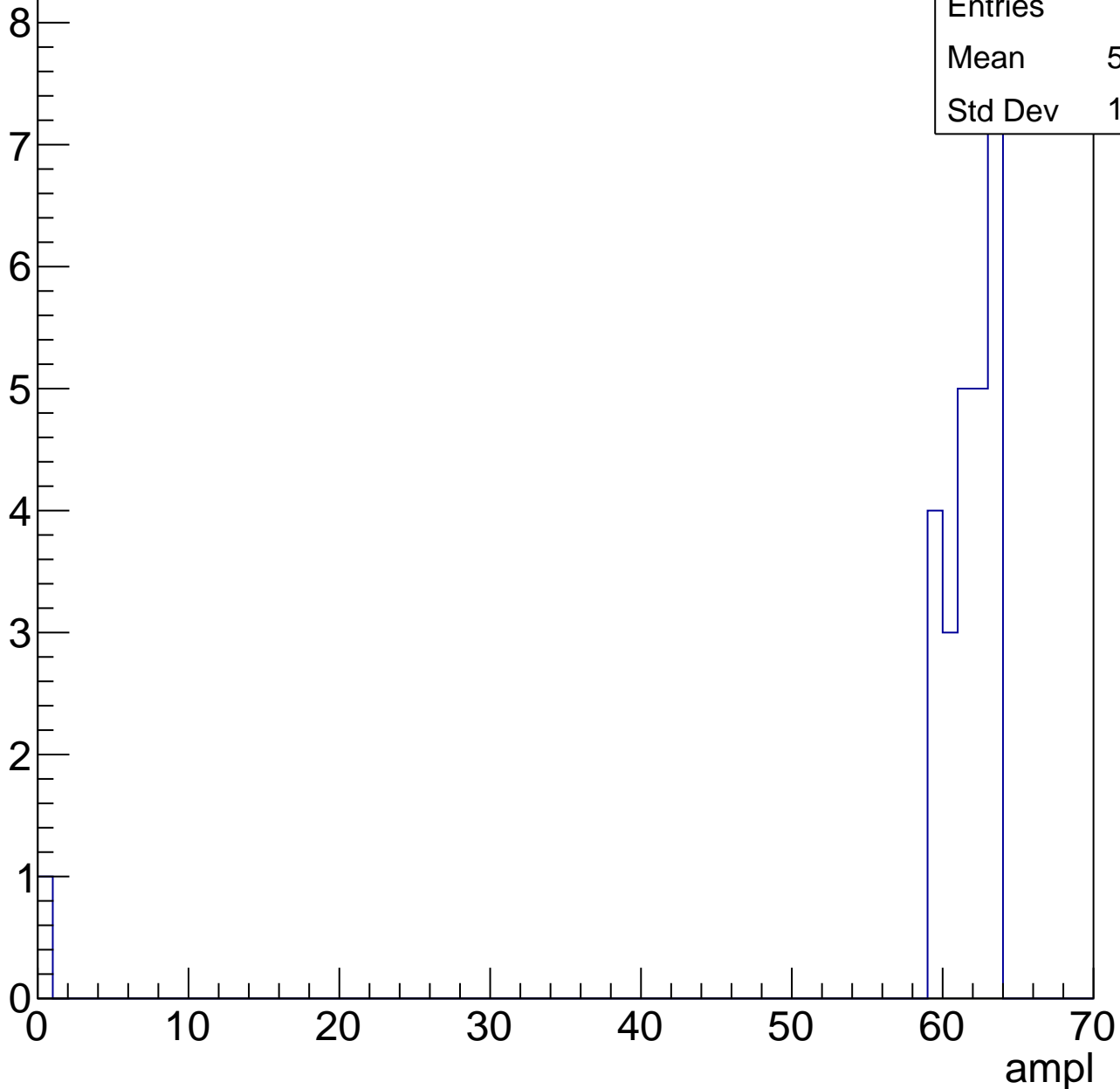


B0L002S, U7-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	59.04
Std Dev	11.89



B0L002S, U7-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch54, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	29.37
Std Dev	3.106

Gaus mean : 29.8621

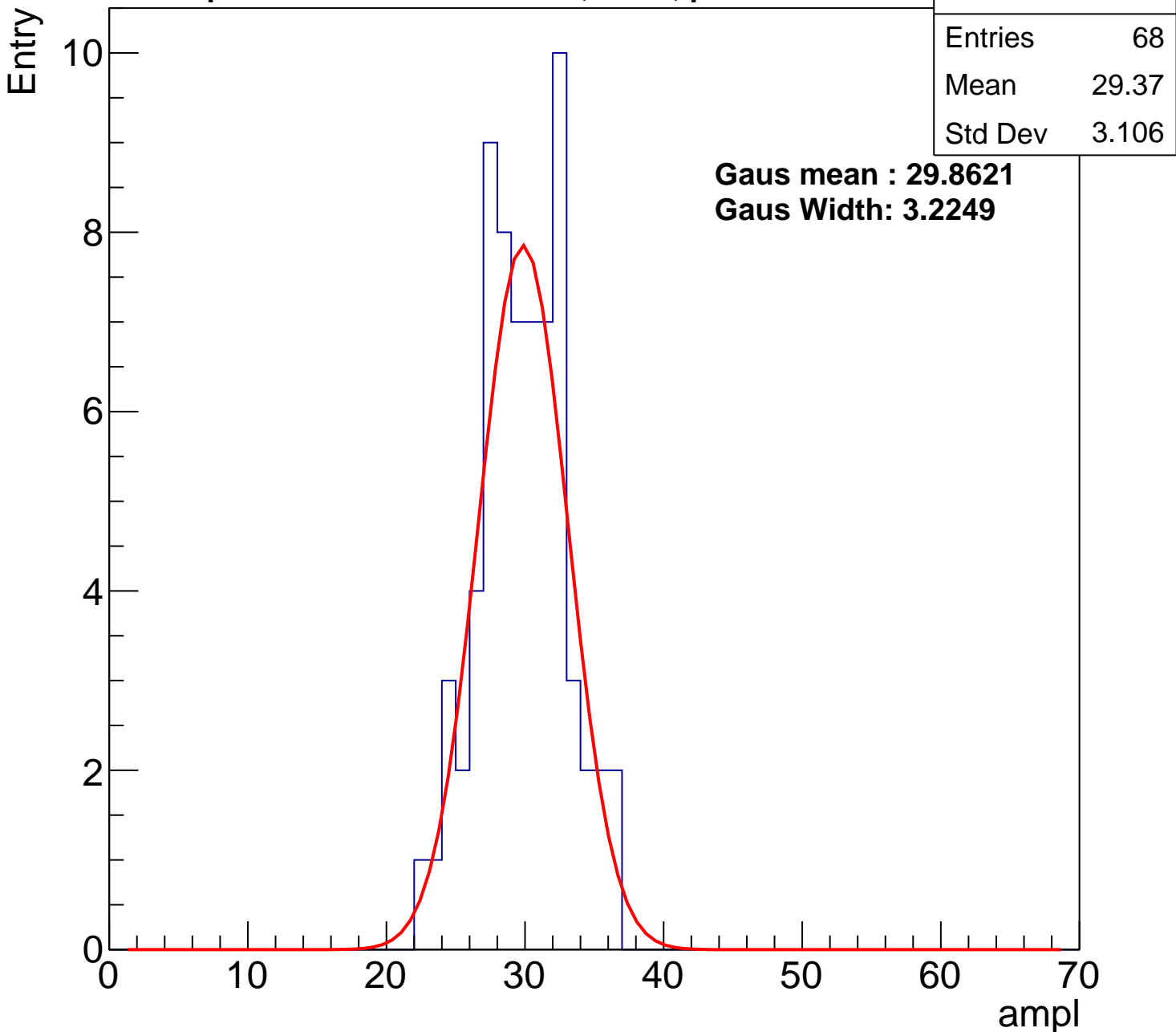
Gaus Width: 3.2249

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch54, adc1

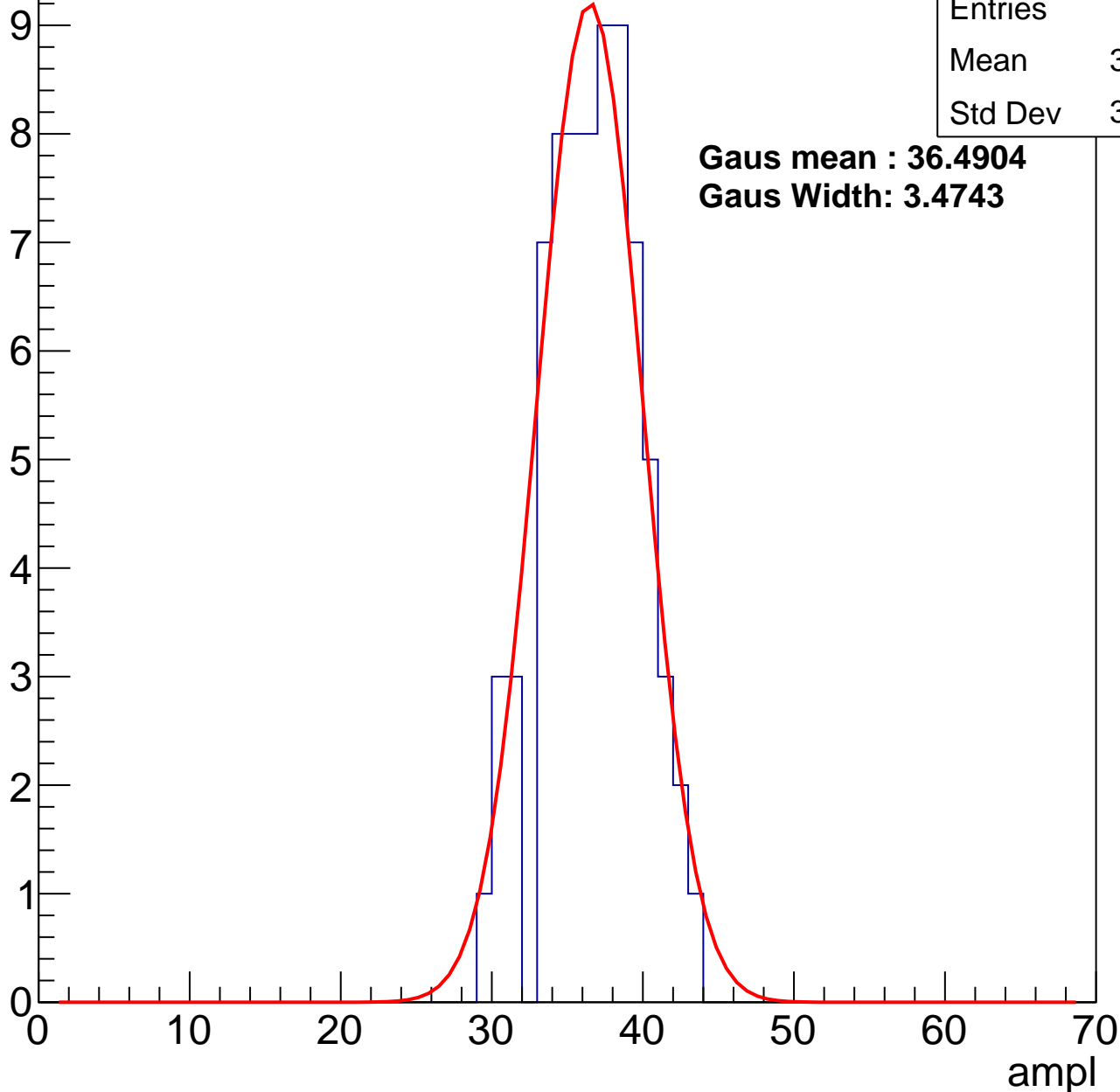
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.23
Std Dev	3.109

Gaus mean : 36.4904

Gaus Width: 3.4743



B0L002S, U7-ch54, adc2

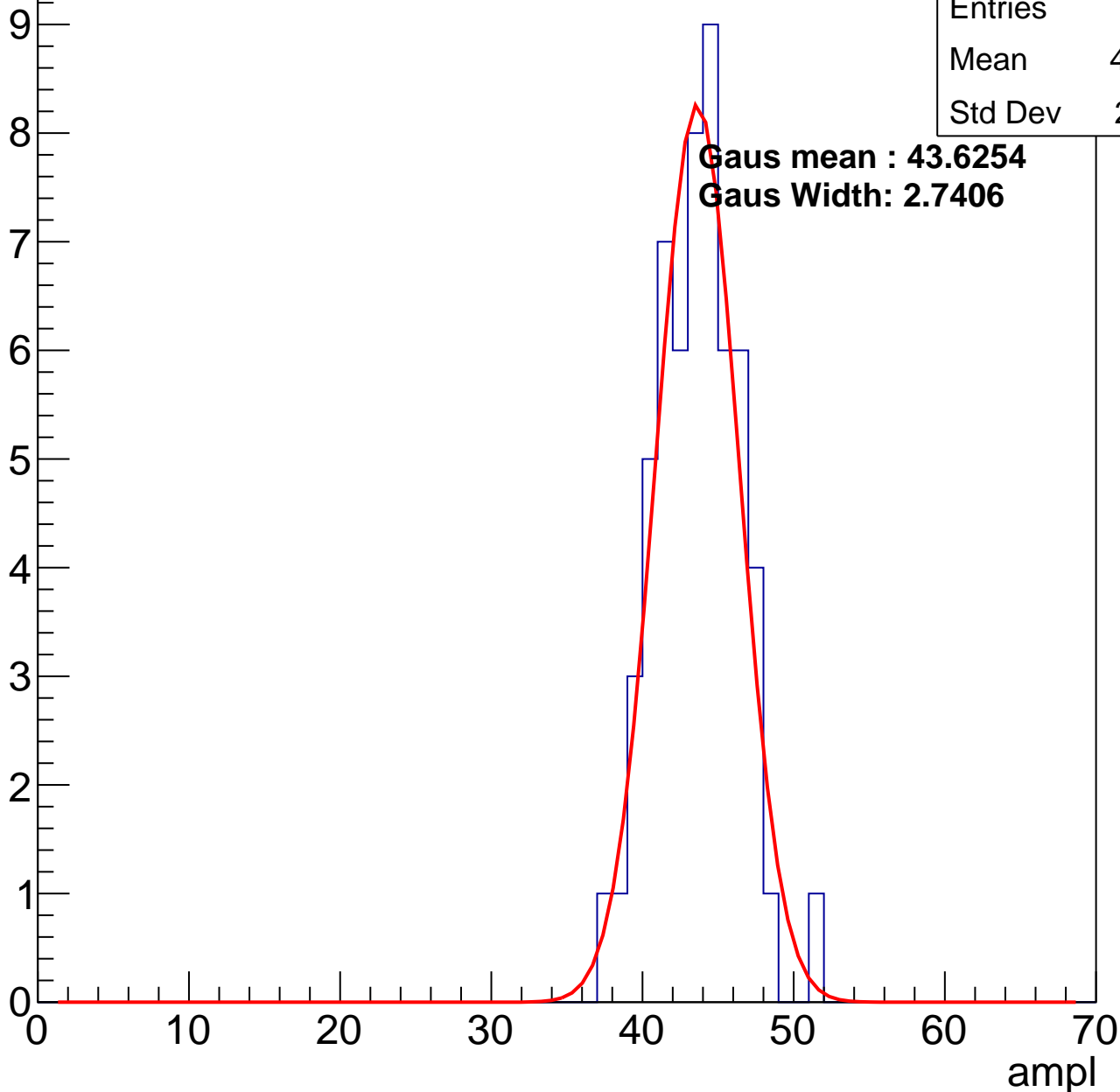
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.17
Std Dev	2.711

Gaus mean : 43.6254

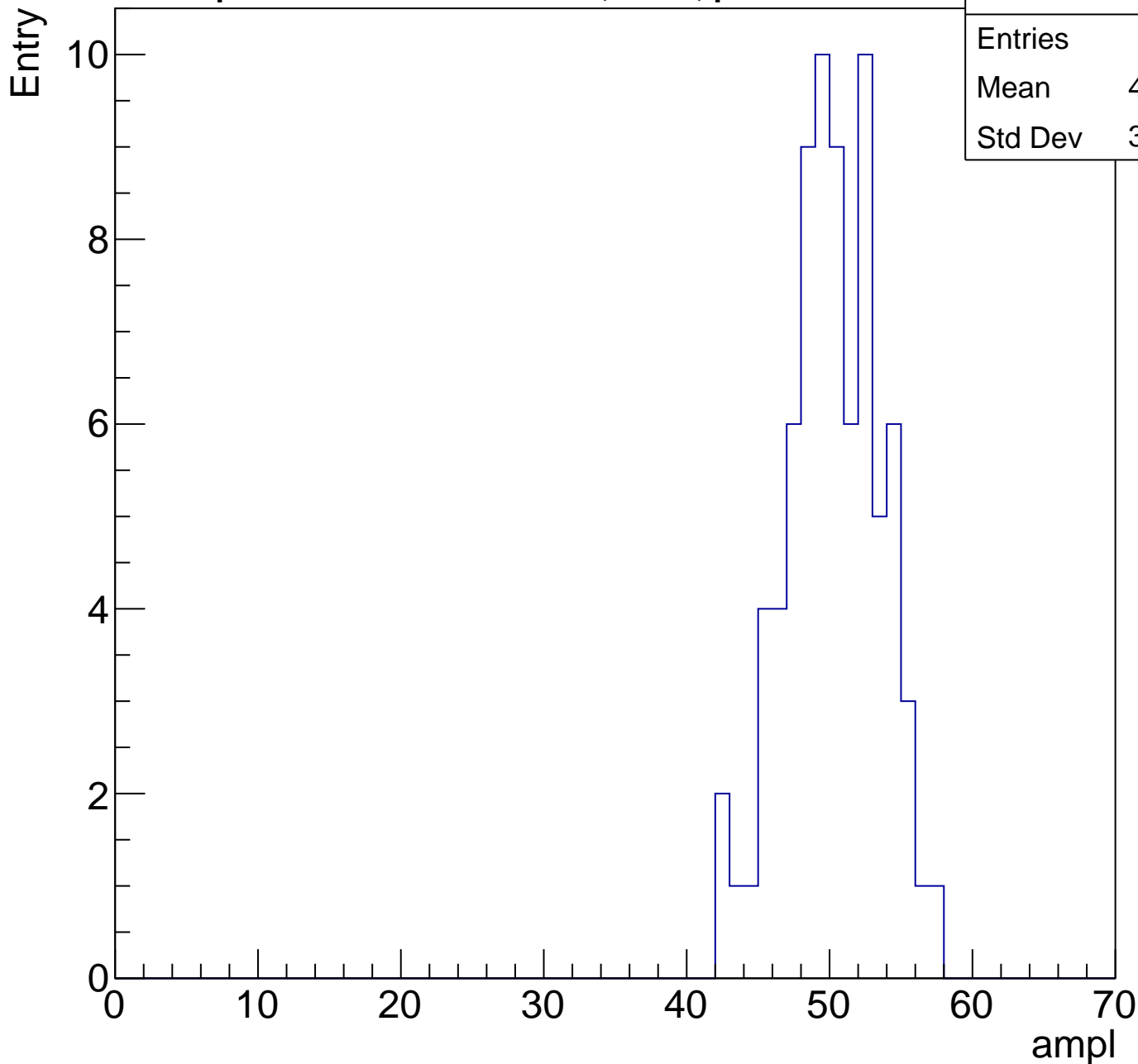
Gaus Width: 2.7406



B0L002S, U7-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	49.77
Std Dev	3.242

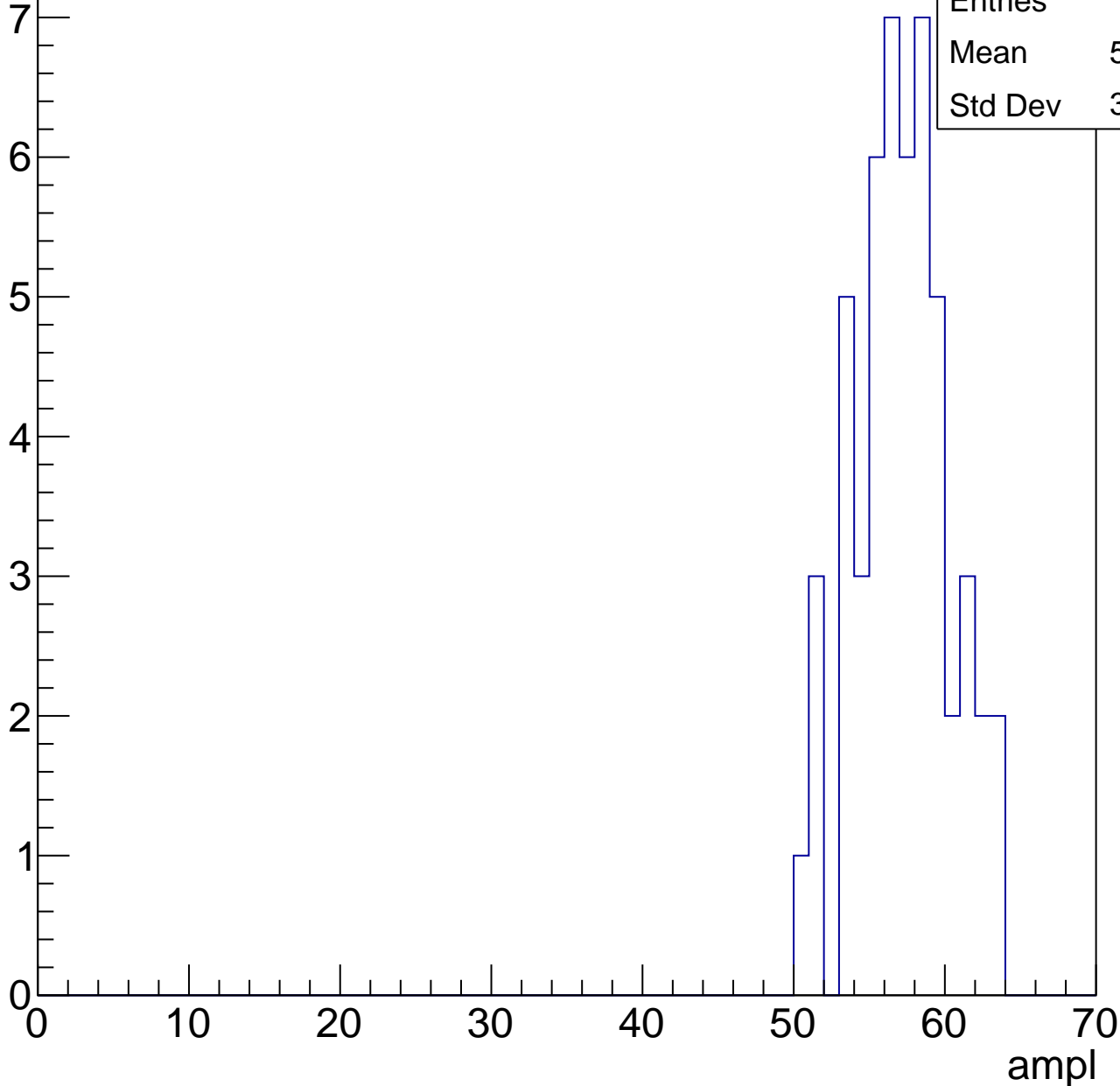


B0L002S, U7-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.69
Std Dev	3.117



B0L002S, U7-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	59.95
Std Dev	2.287

10

20

30

40

50

60

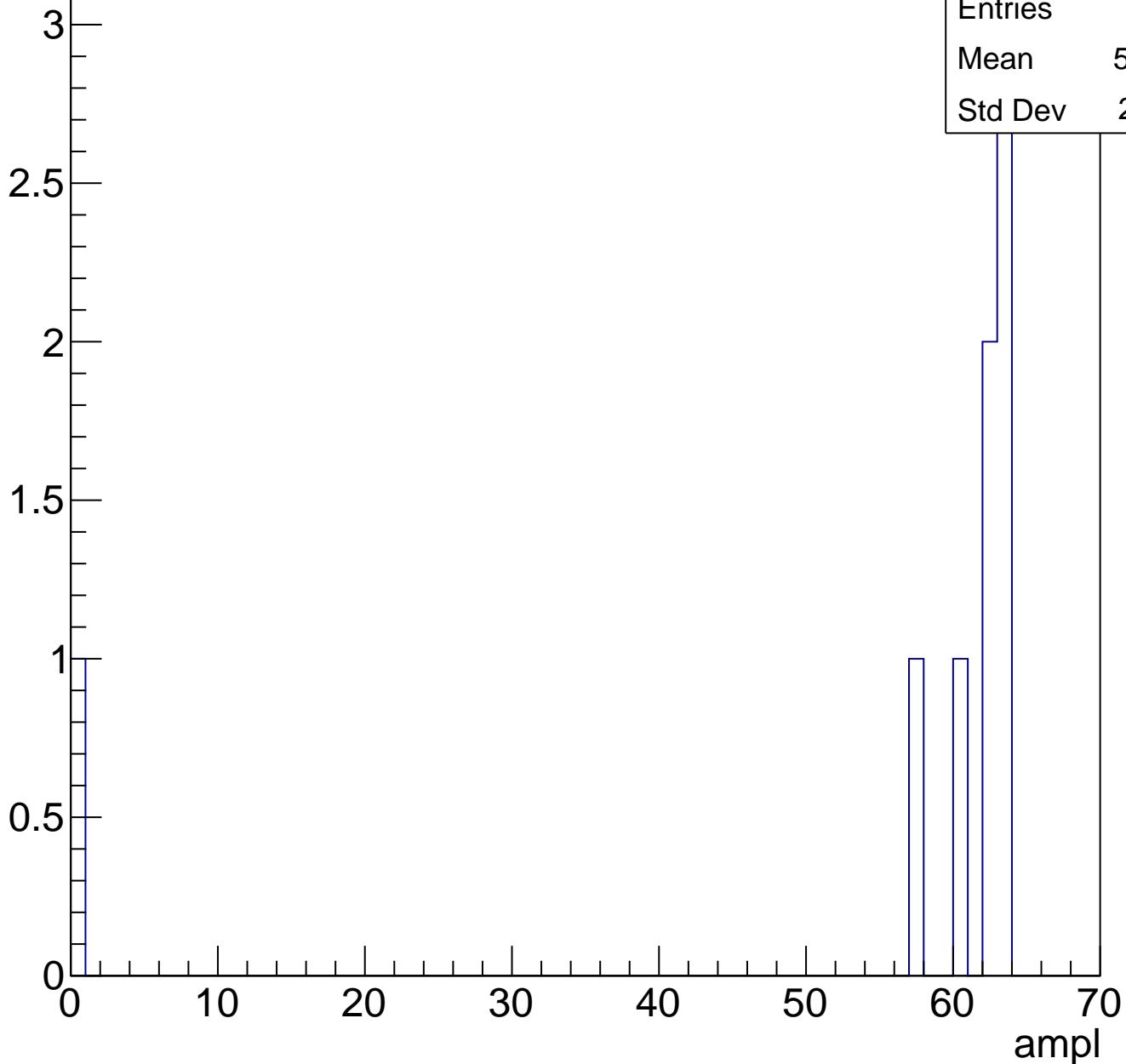
70

ampl

B0L002S, U7-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch55, adc0

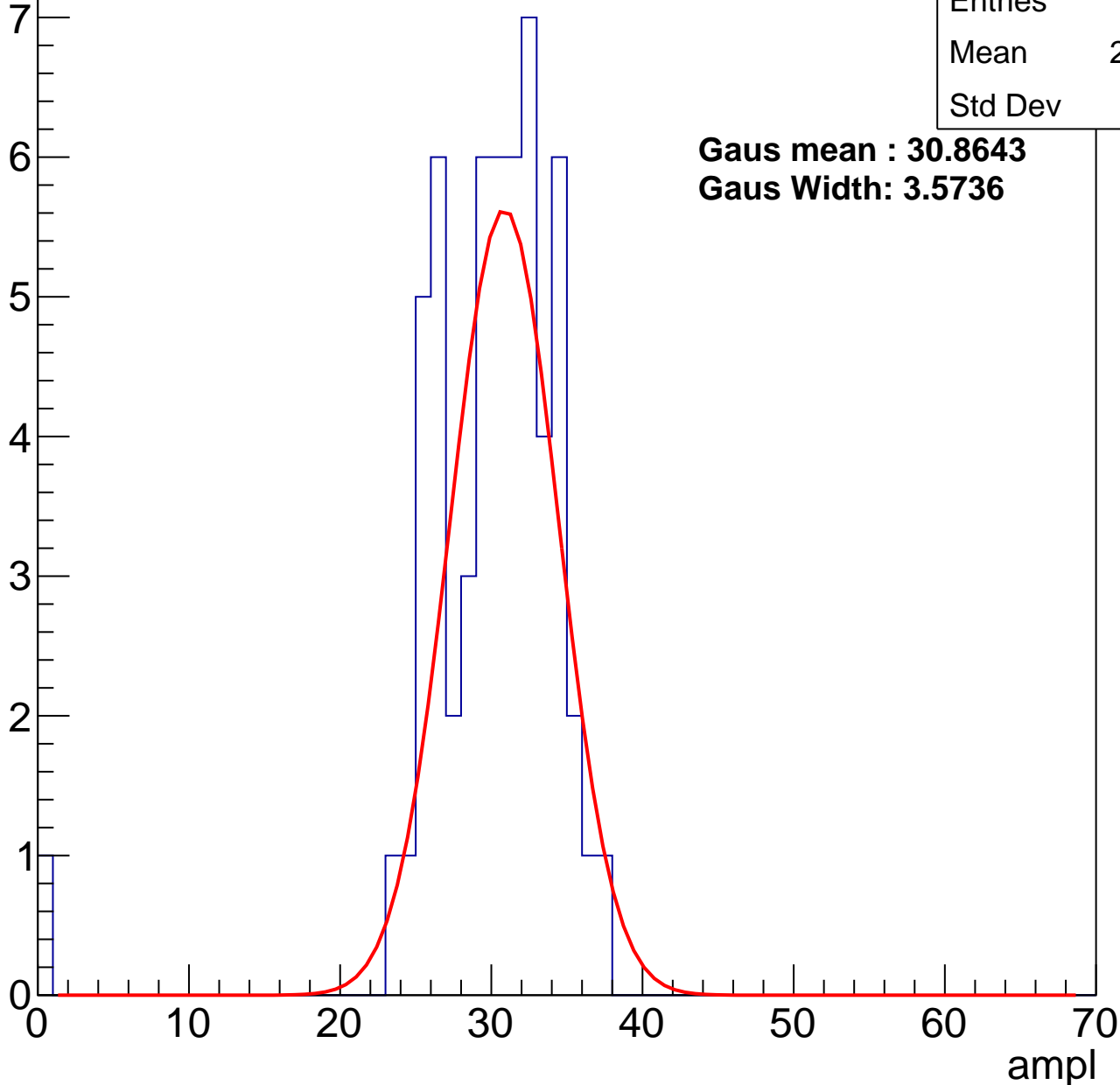
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.47
Std Dev	5.13

Gaus mean : 30.8643

Gaus Width: 3.5736



B0L002S, U7-ch55, adc1

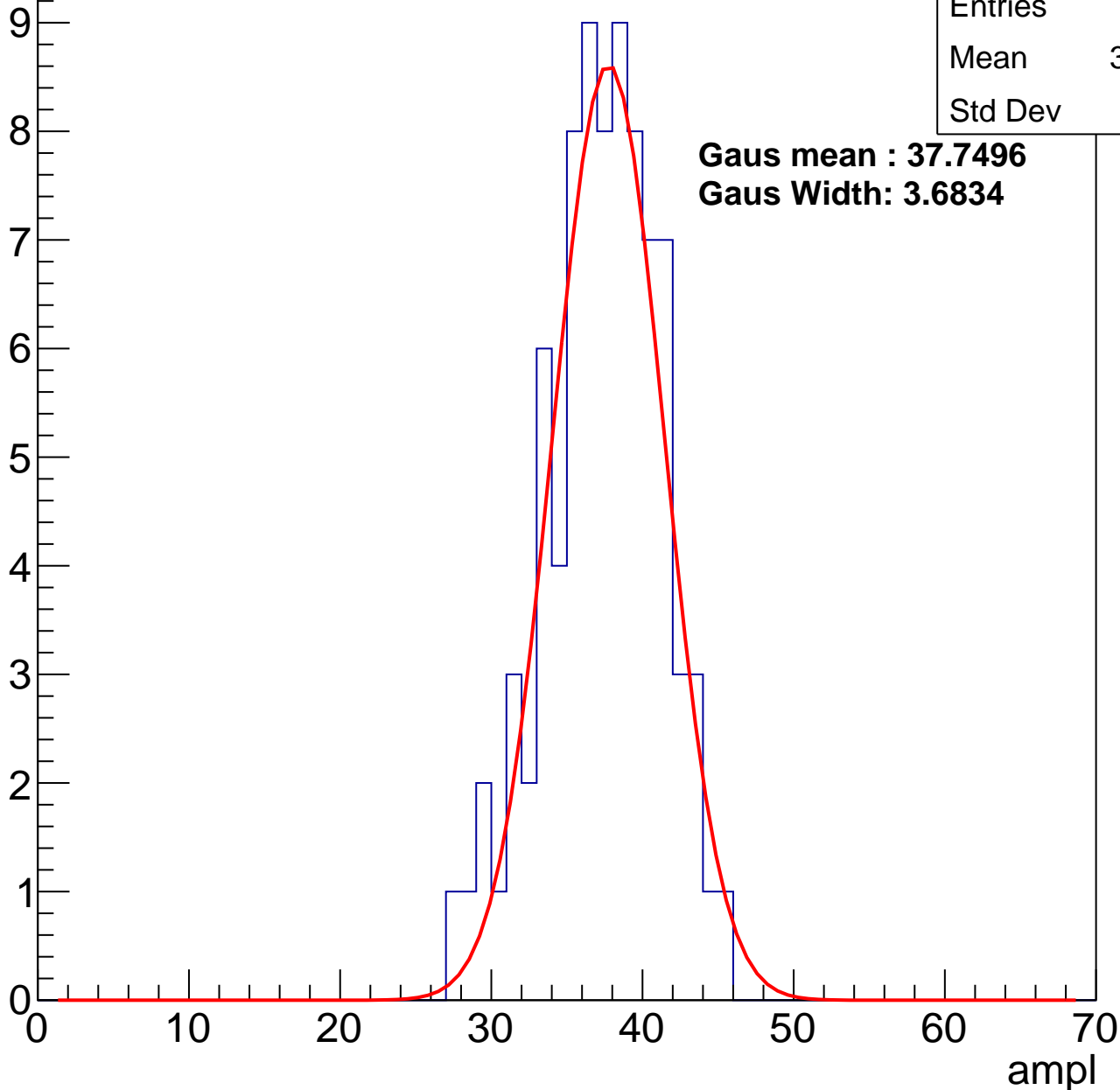
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	36.89
Std Dev	3.78

Gaus mean : 37.7496

Gaus Width: 3.6834



B0L002S, U7-ch55, adc2

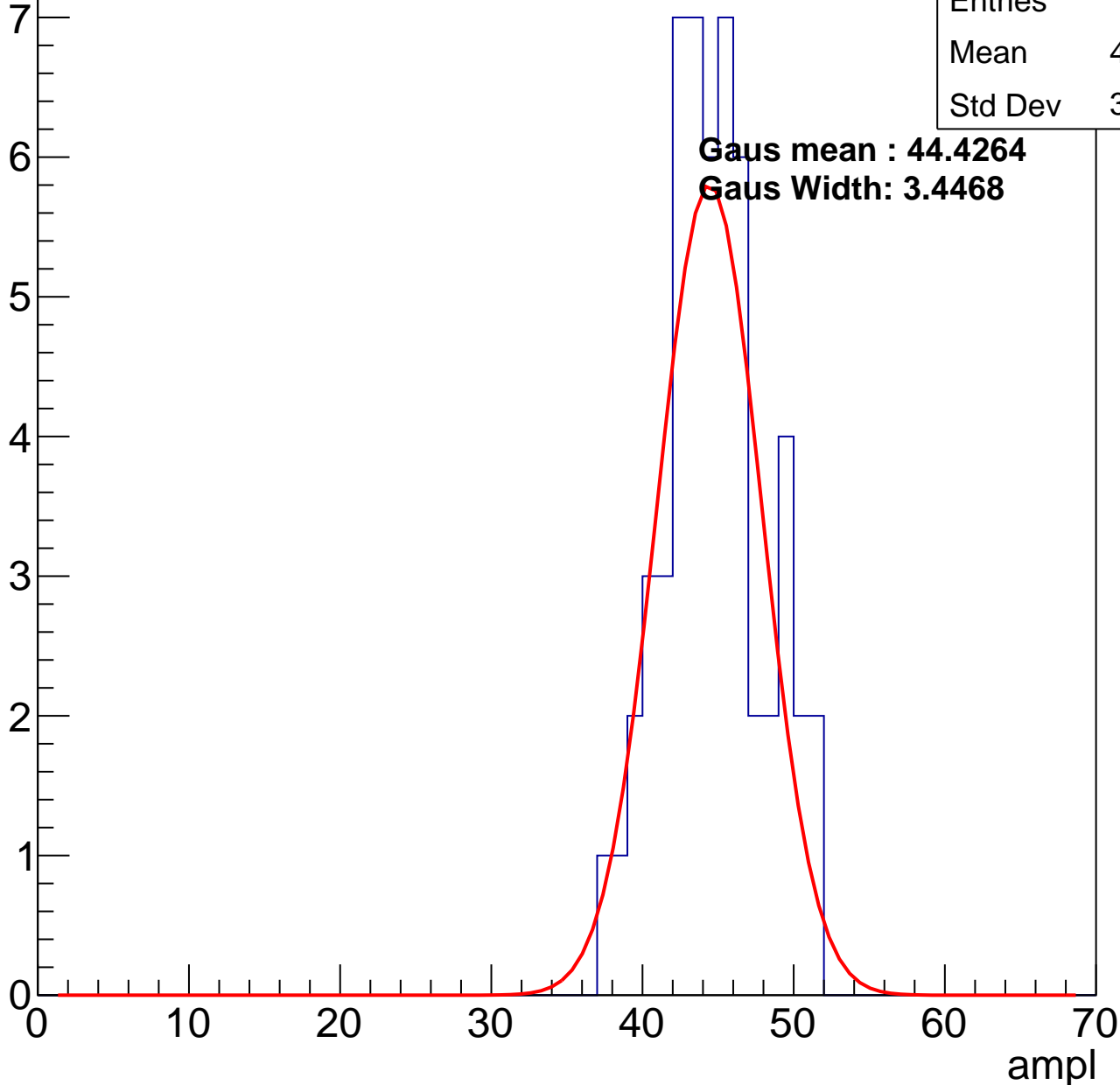
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.25
Std Dev	3.282

Gaus mean : 44.4264

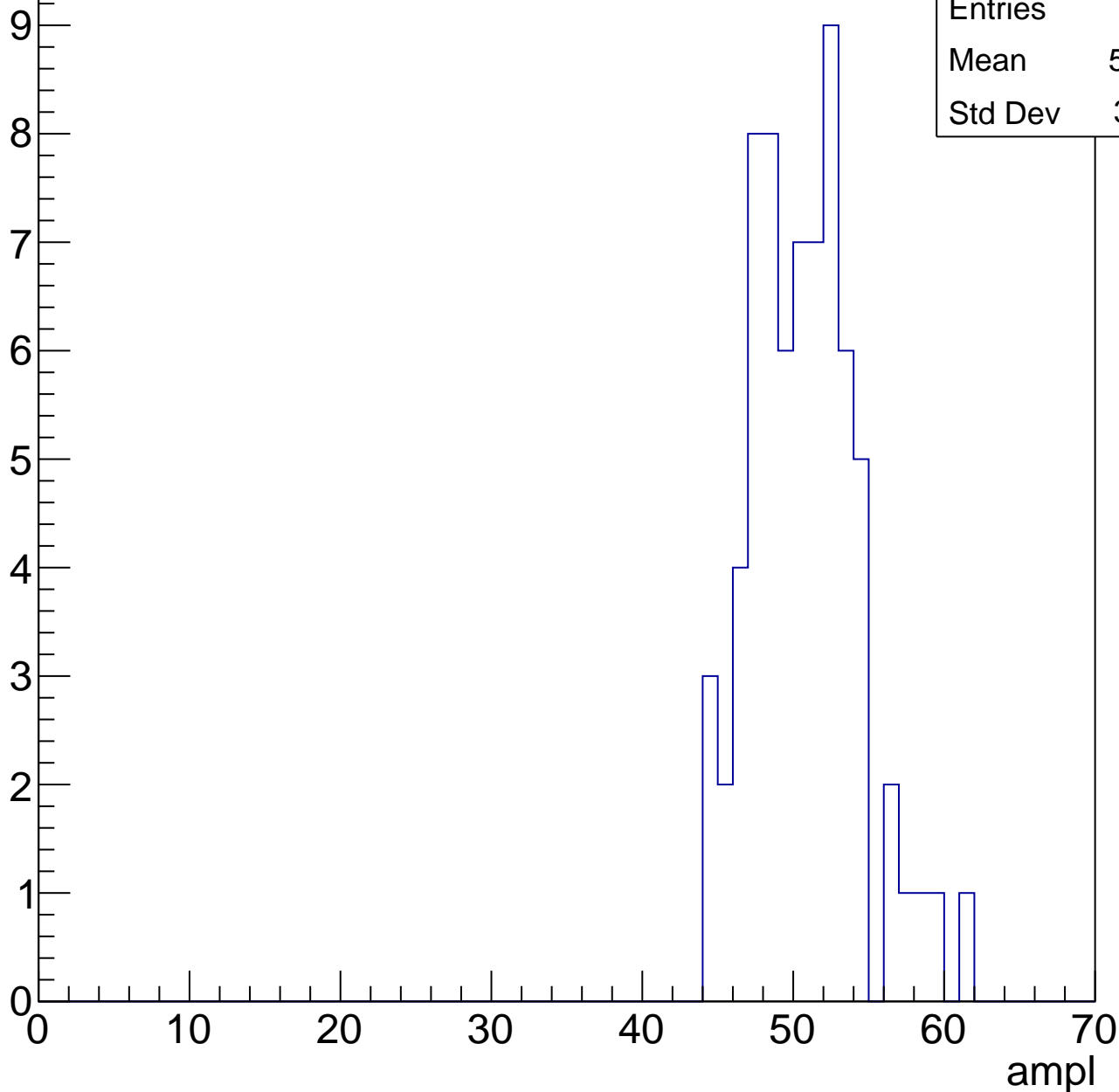
Gaus Width: 3.4468



B0L002S, U7-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

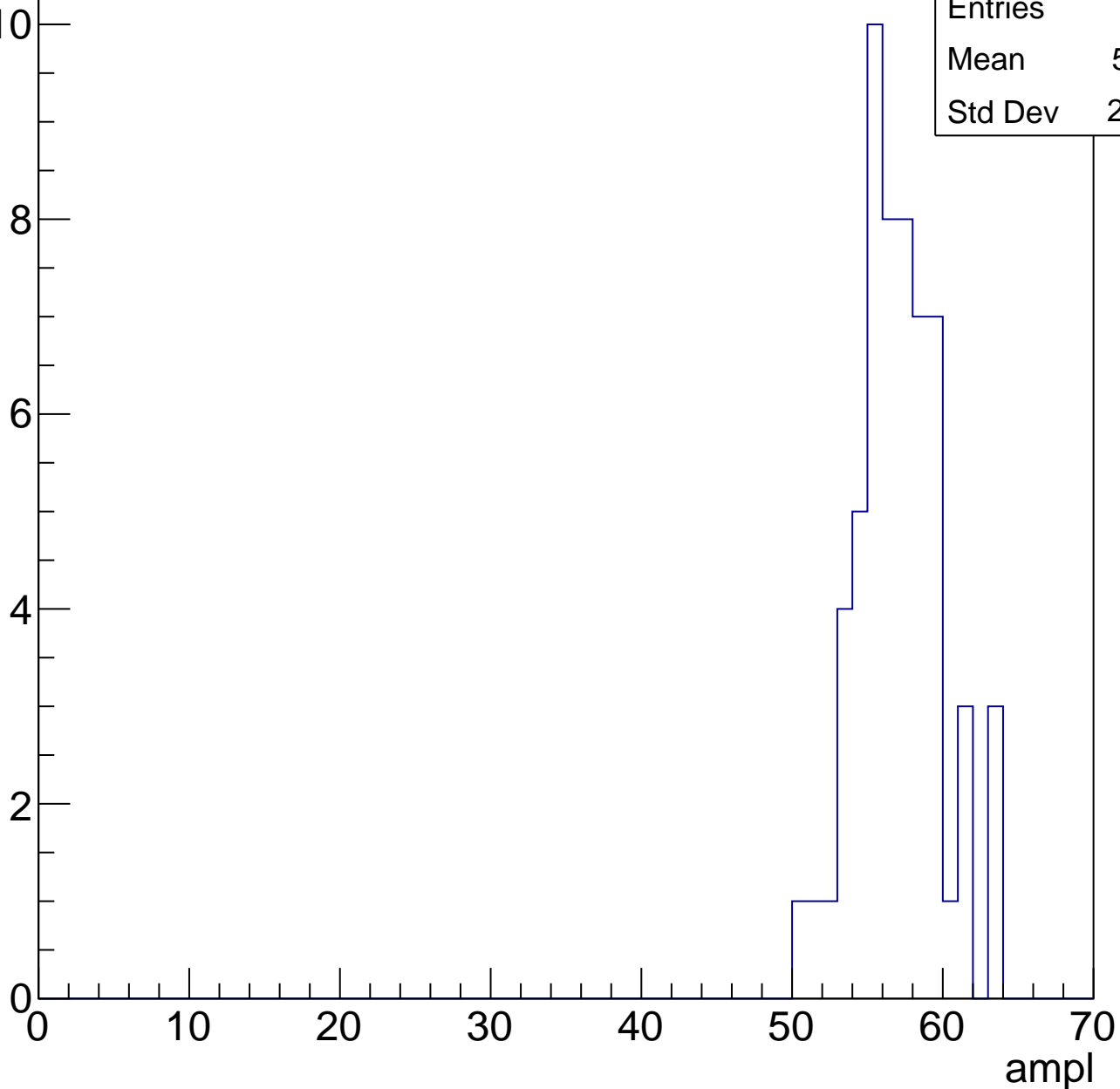


B0L002S, U7-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.61
Std Dev	2.786



B0L002S, U7-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	36
Mean	59.22
Std Dev	10.12

B0L002S, U7-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

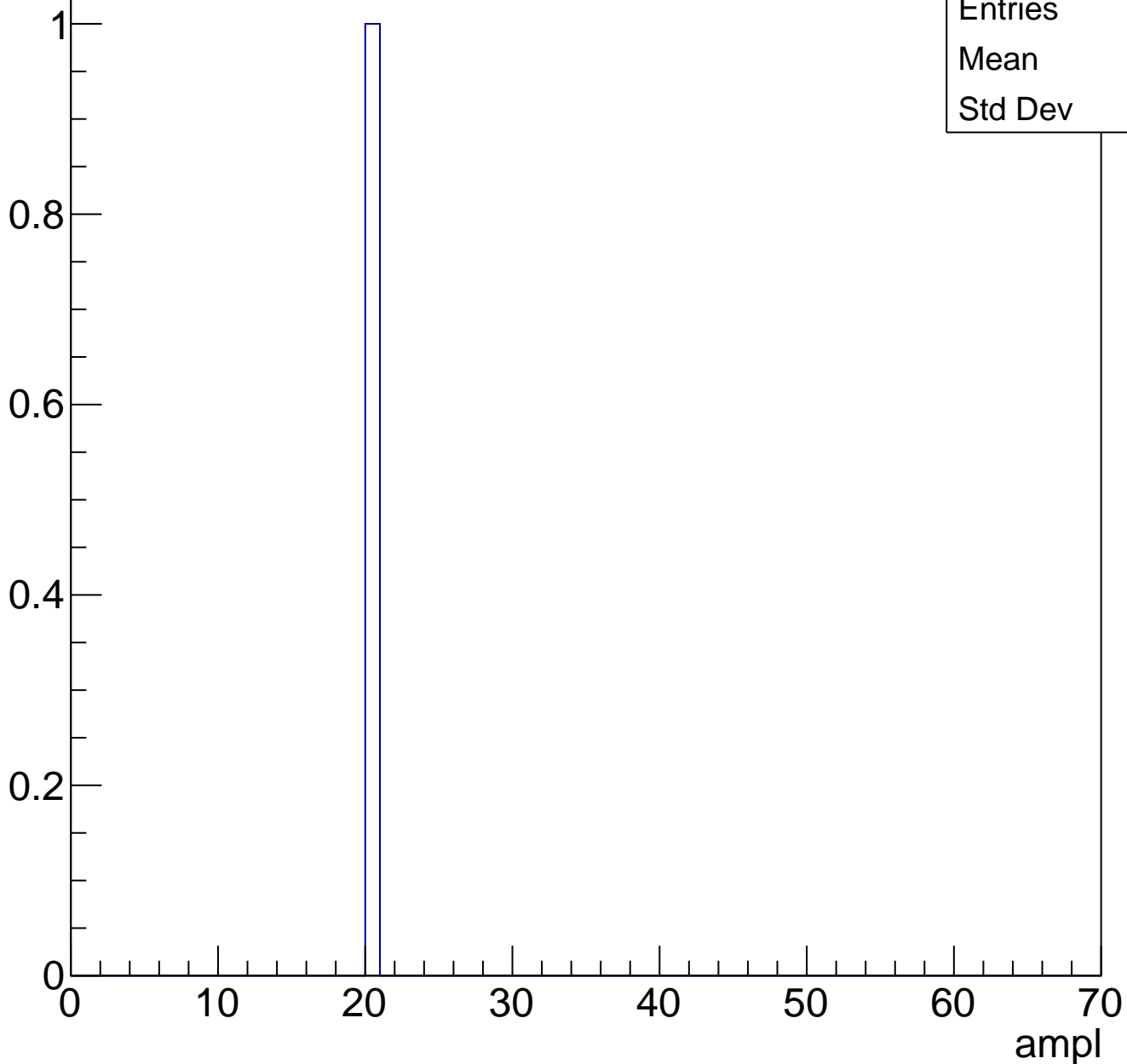
Entry



B0L002S, U7-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U7-ch56, adc0

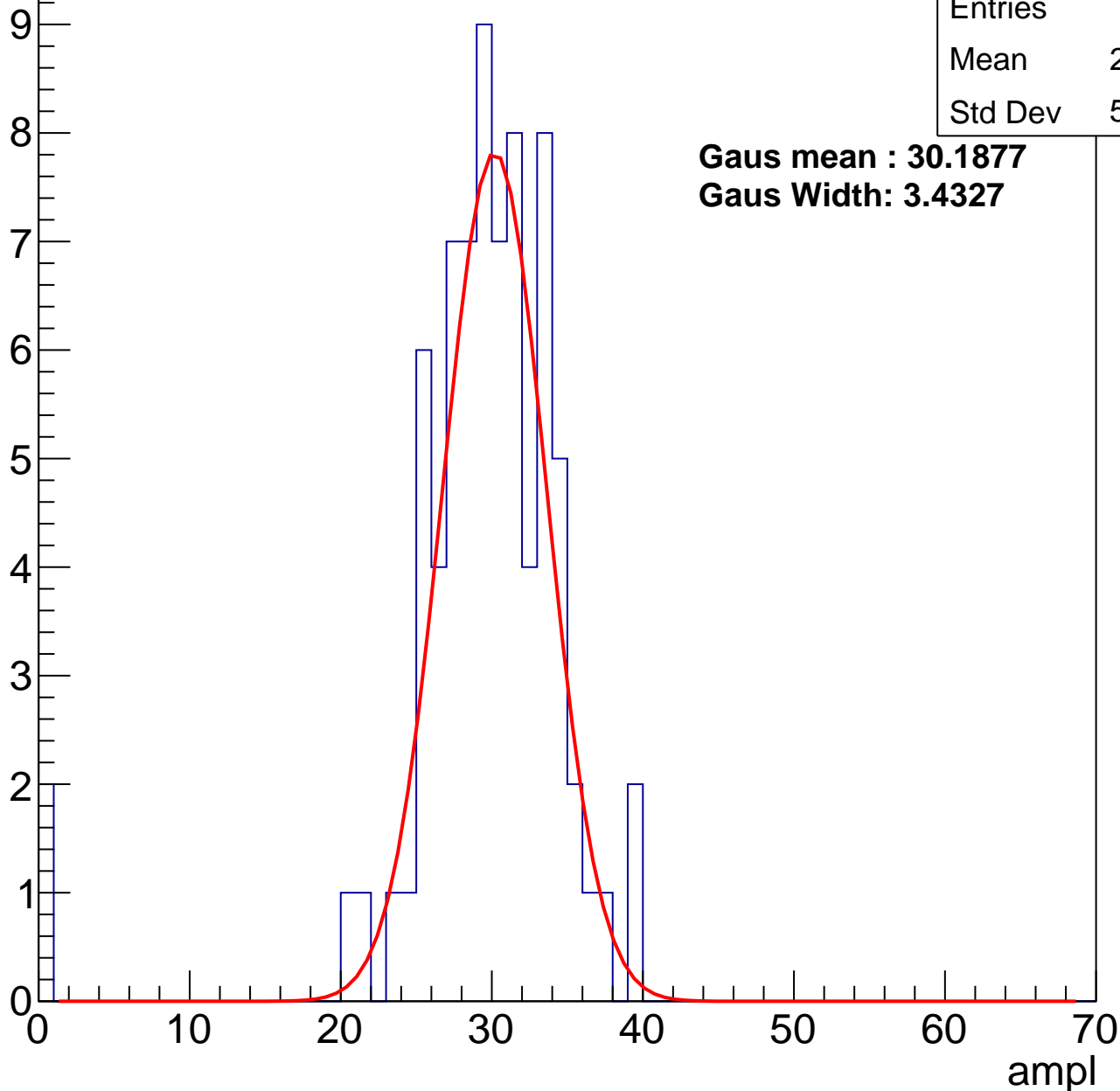
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	28.95
Std Dev	5.995

Gaus mean : 30.1877

Gaus Width: 3.4327



B0L002S, U7-ch56, adc1

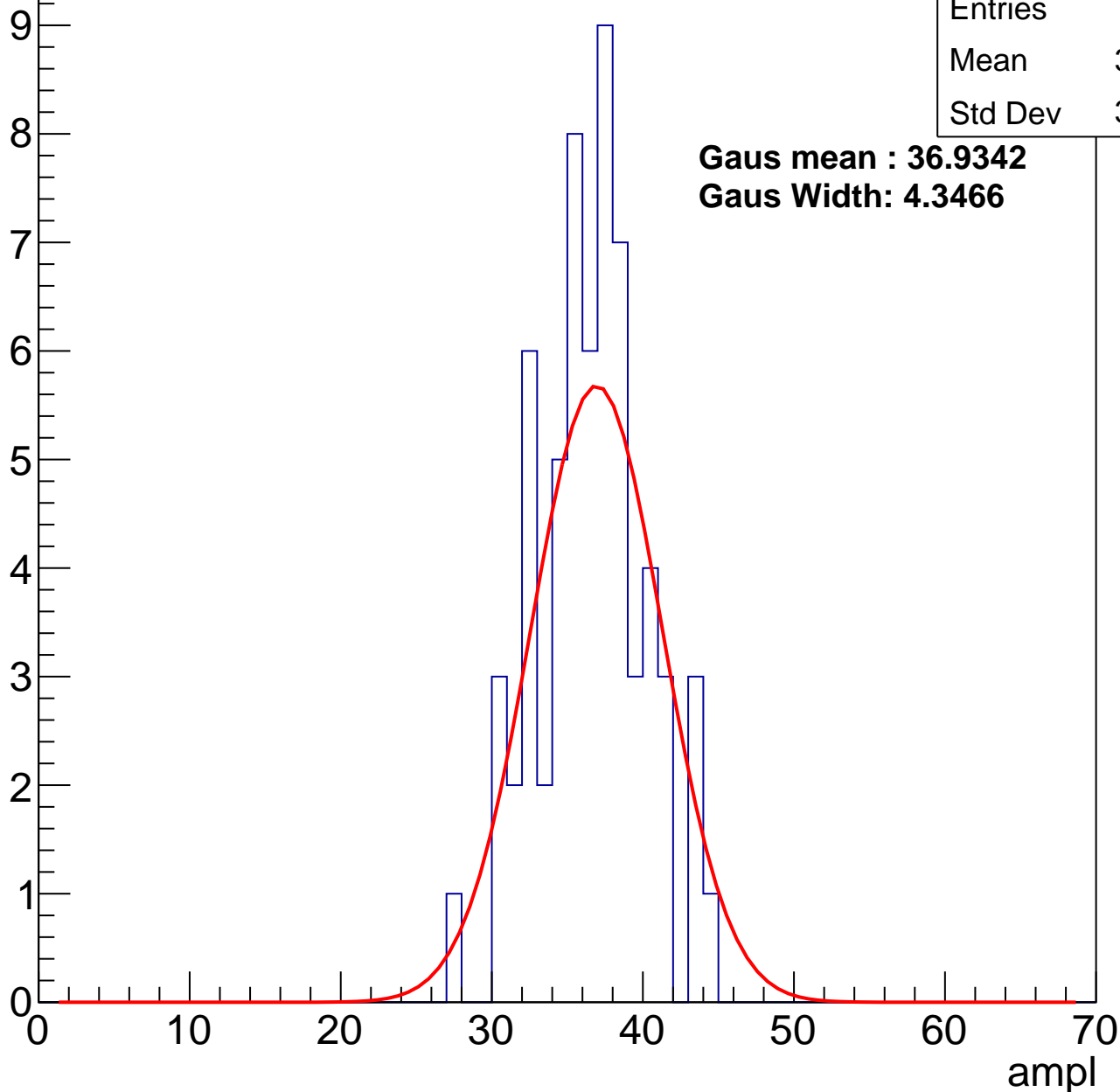
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.11
Std Dev	3.551

Gaus mean : 36.9342

Gaus Width: 4.3466



B0L002S, U7-ch56, adc2

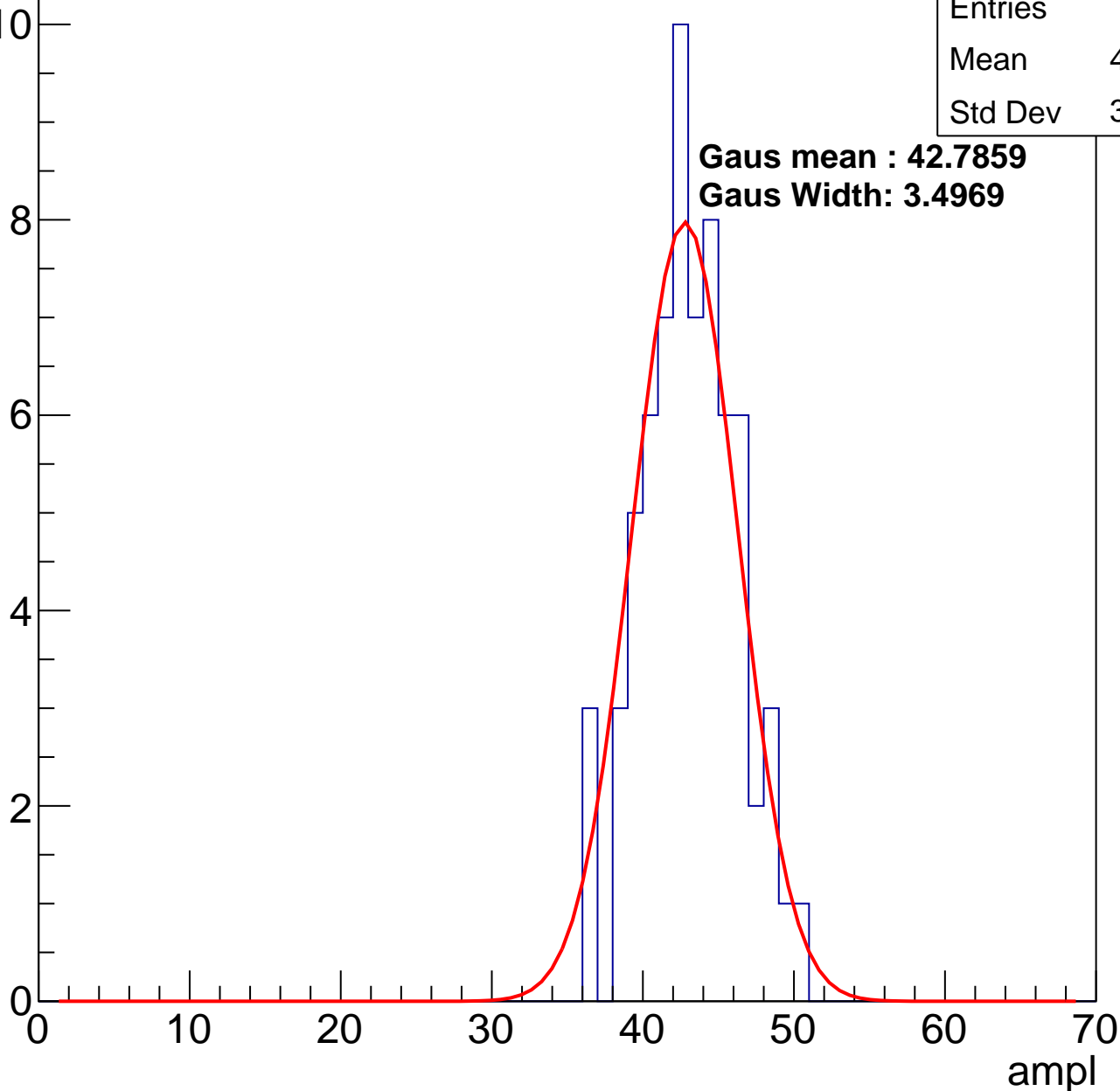
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.65
Std Dev	3.124

Gaus mean : 42.7859

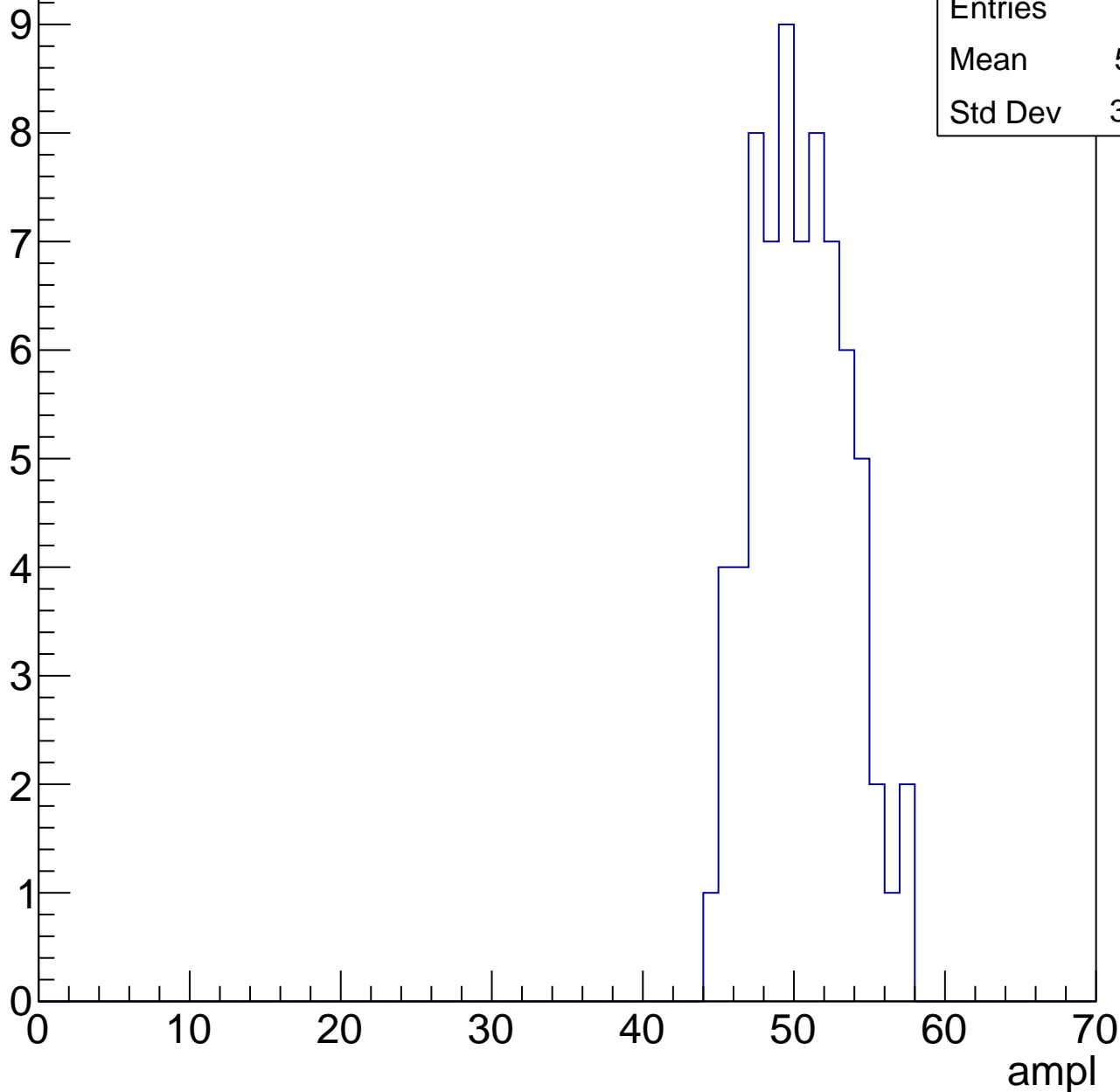
Gaus Width: 3.4969



B0L002S, U7-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

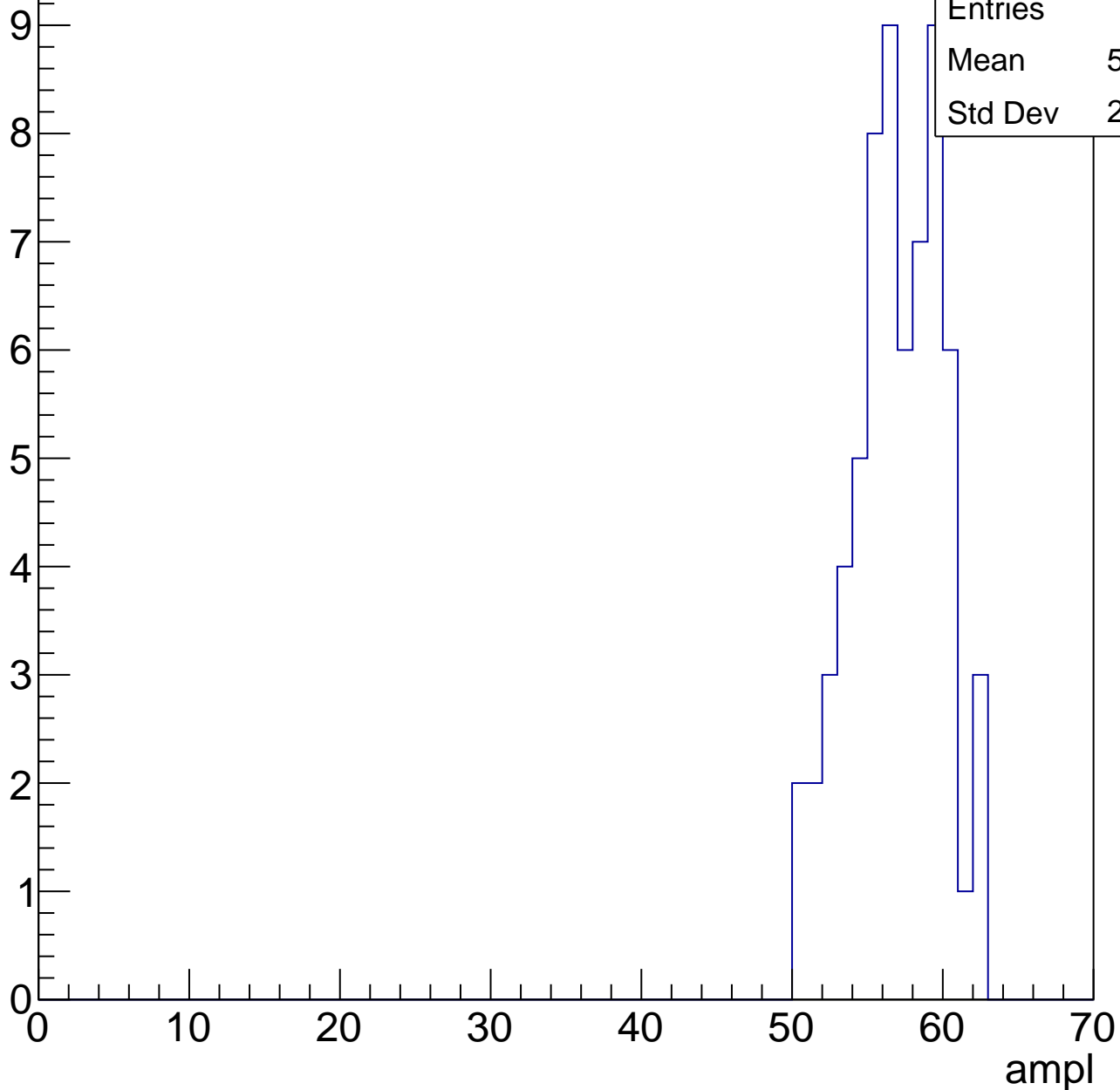


Entries	71
Mean	50.01
Std Dev	3.056

B0L002S, U7-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

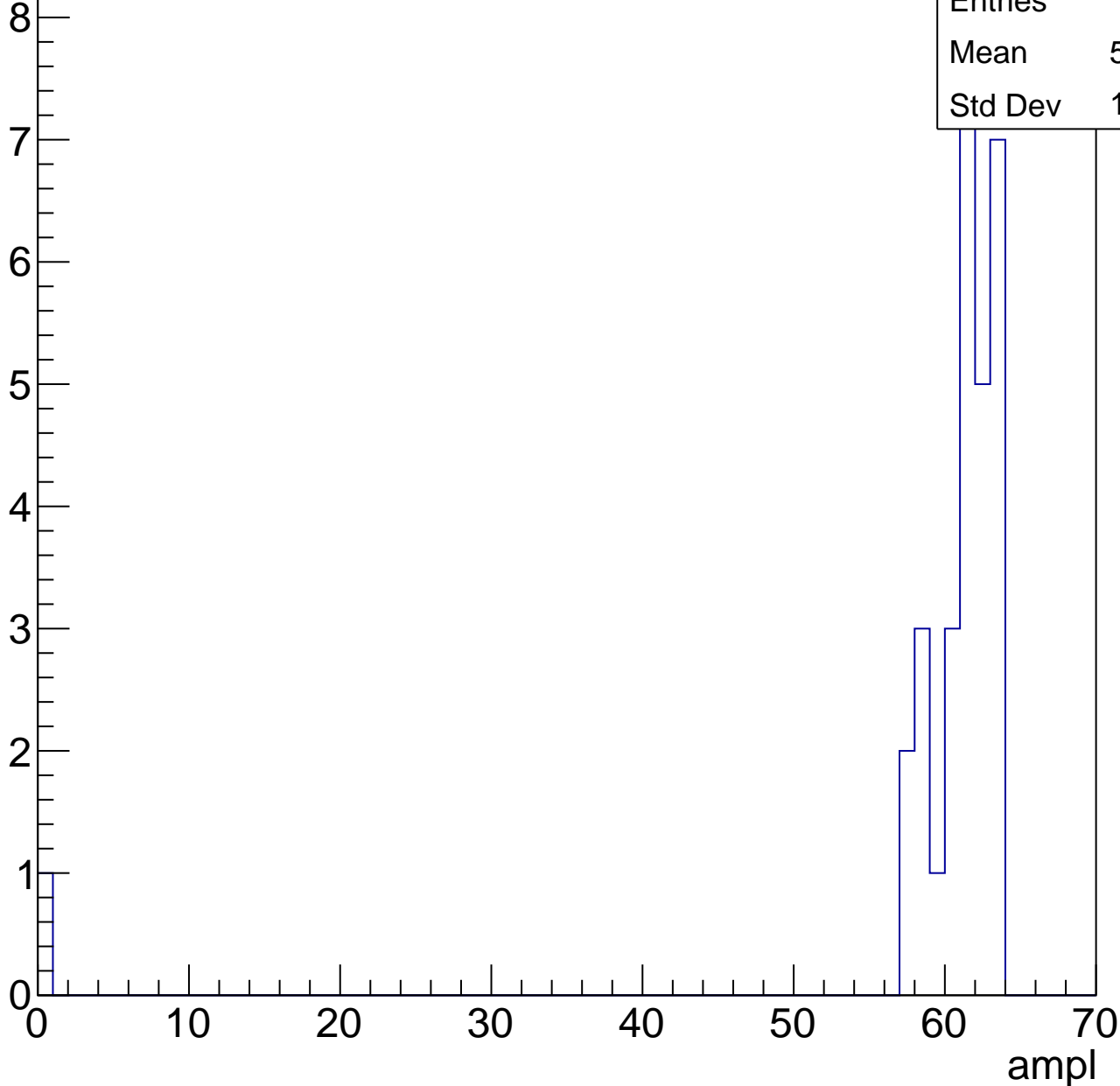


B0L002S, U7-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

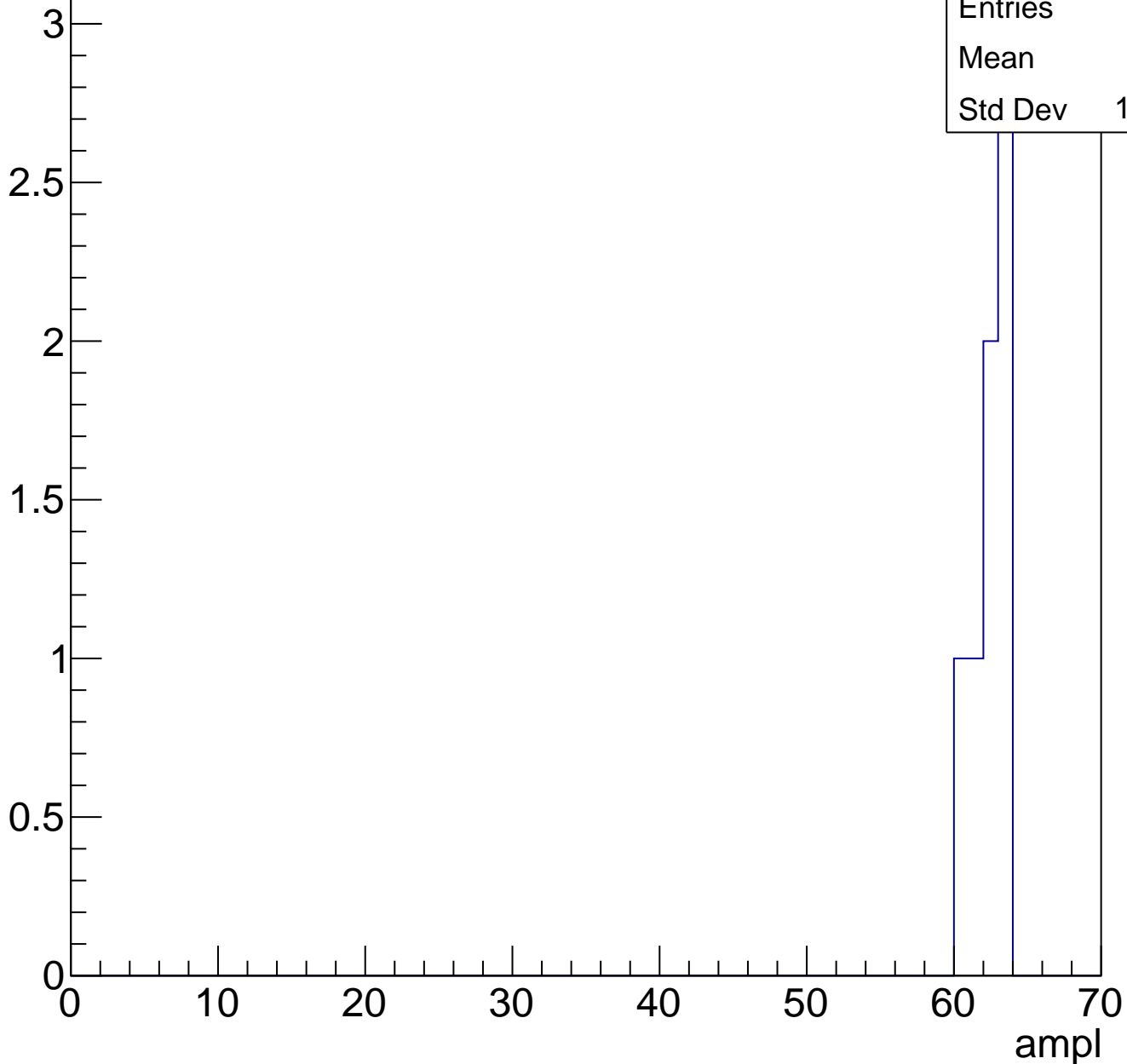
Entries	30
Mean	58.87
Std Dev	11.08



B0L002S, U7-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch57, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	31.12
Std Dev	2.821

Gaus mean : 31.9524

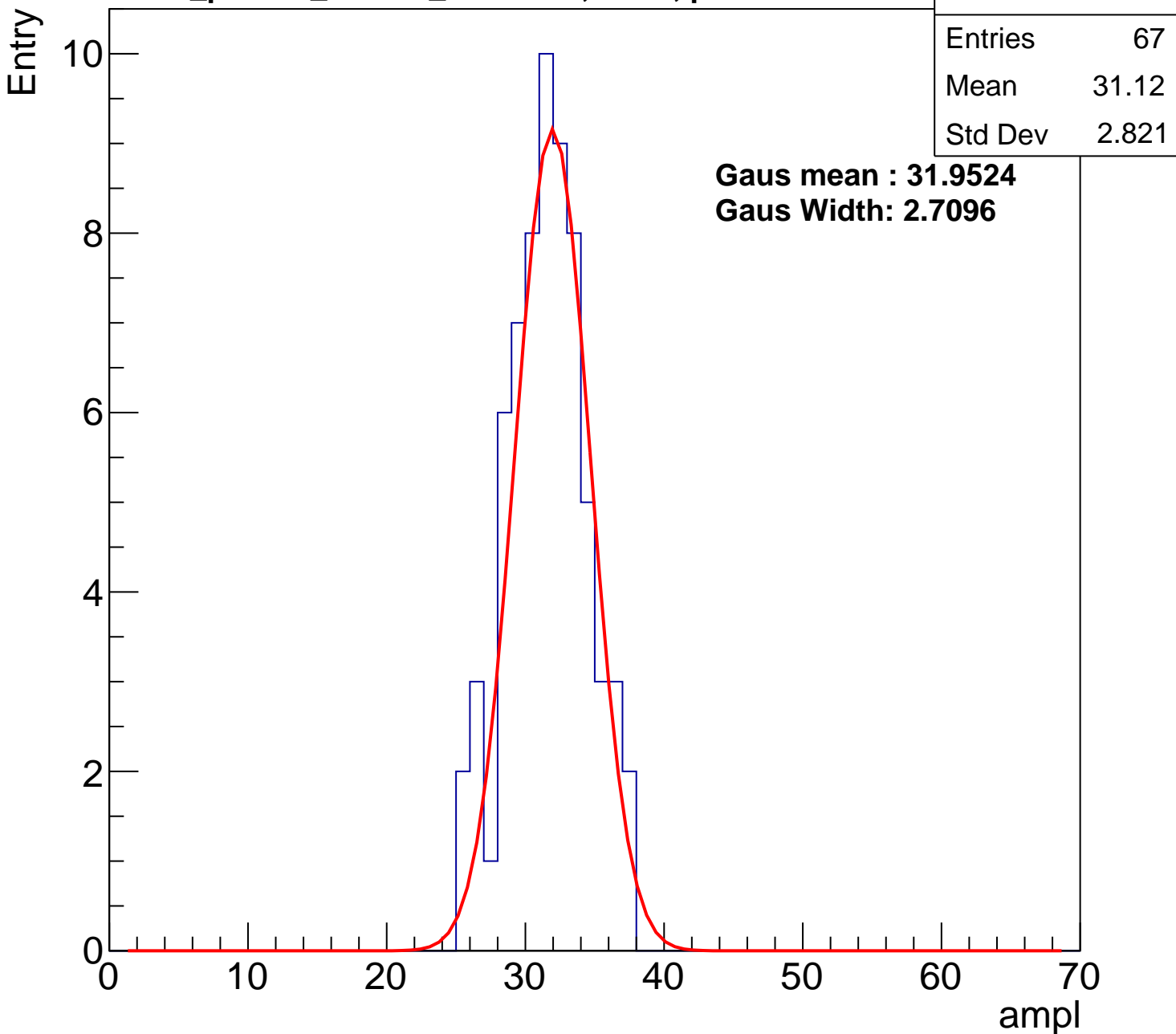
Gaus Width: 2.7096

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch57, adc1

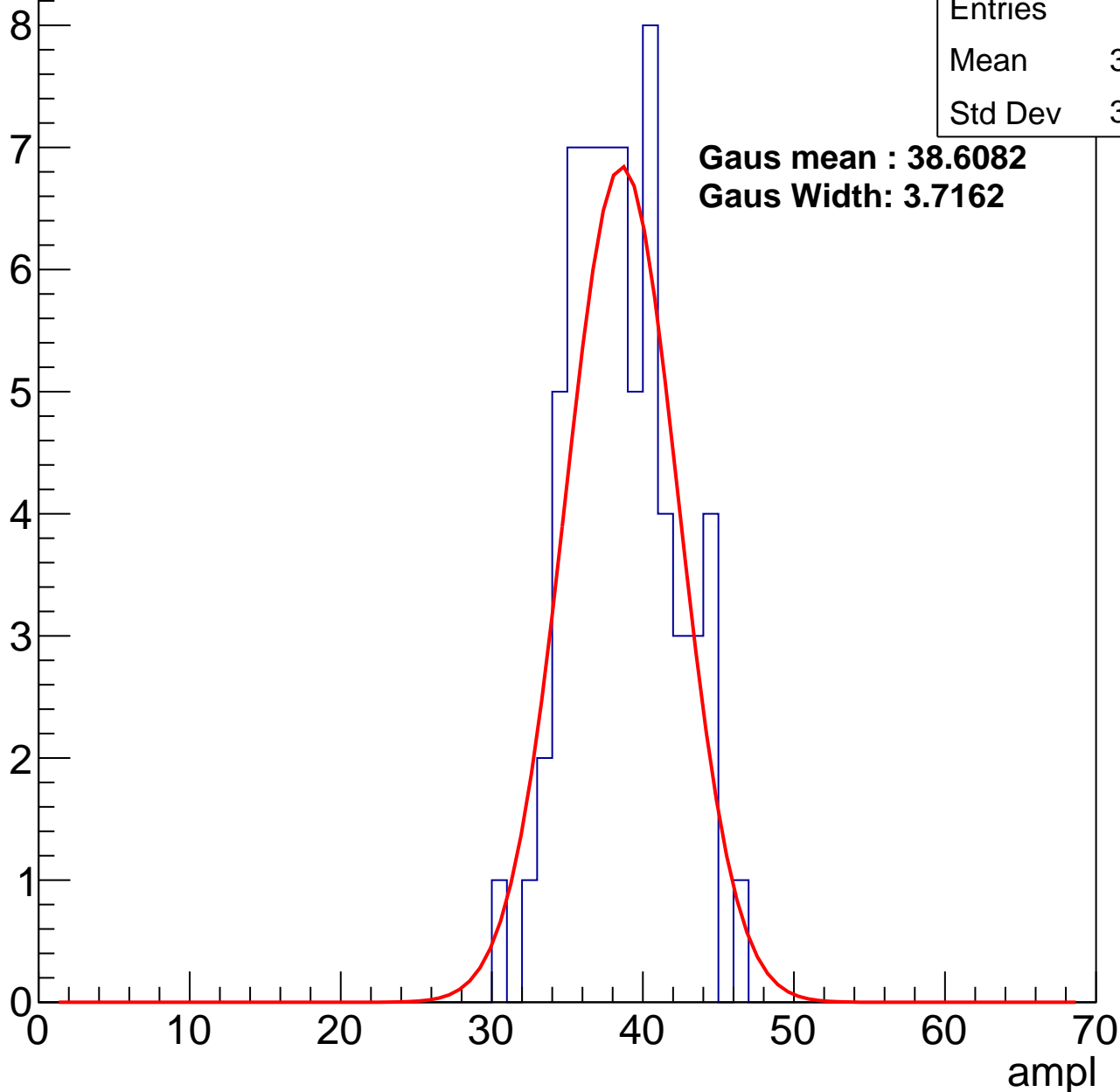
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.09
Std Dev	3.355

Gaus mean : 38.6082

Gaus Width: 3.7162



B0L002S, U7-ch57, adc2

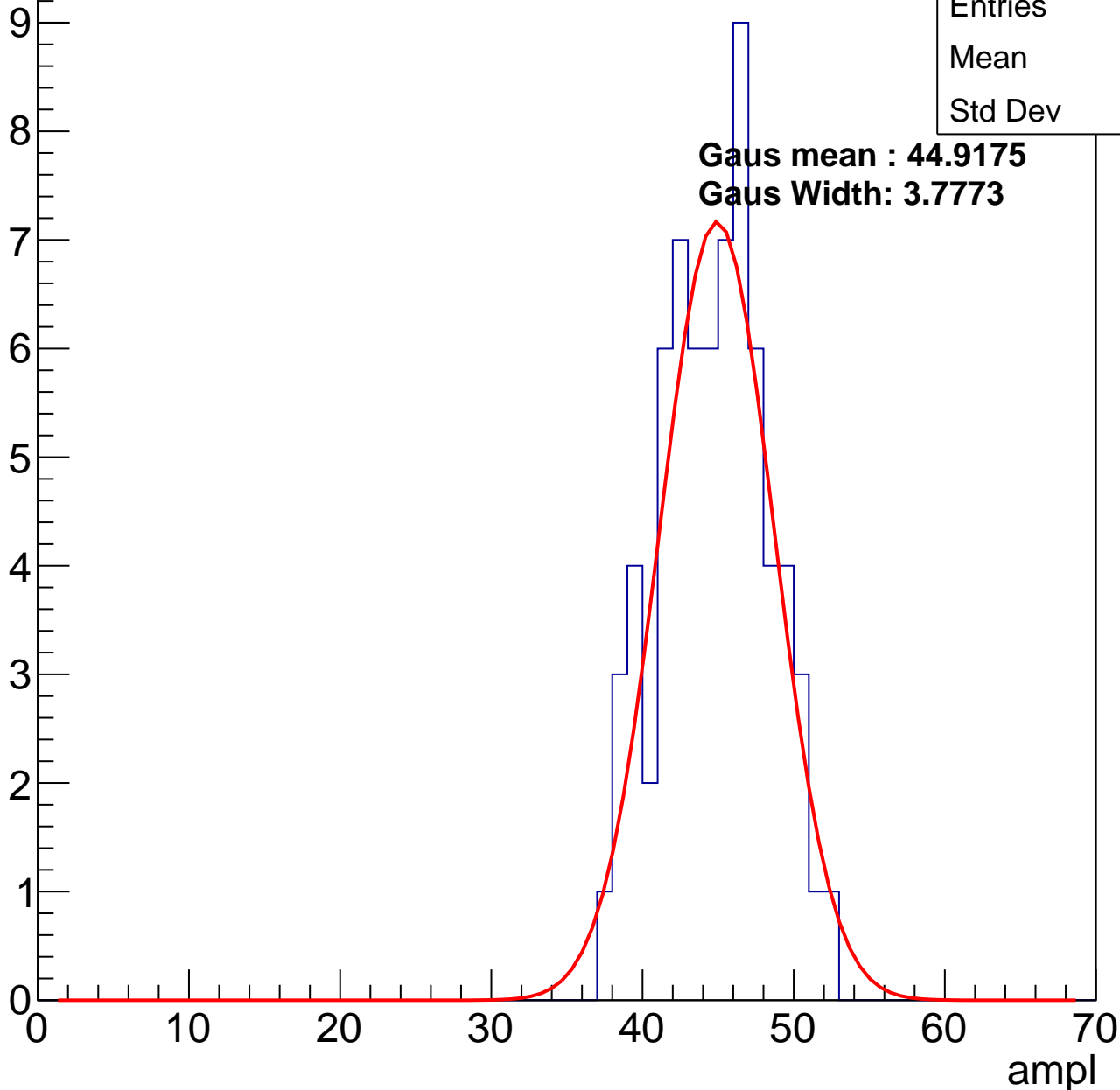
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.3
Std Dev	3.49

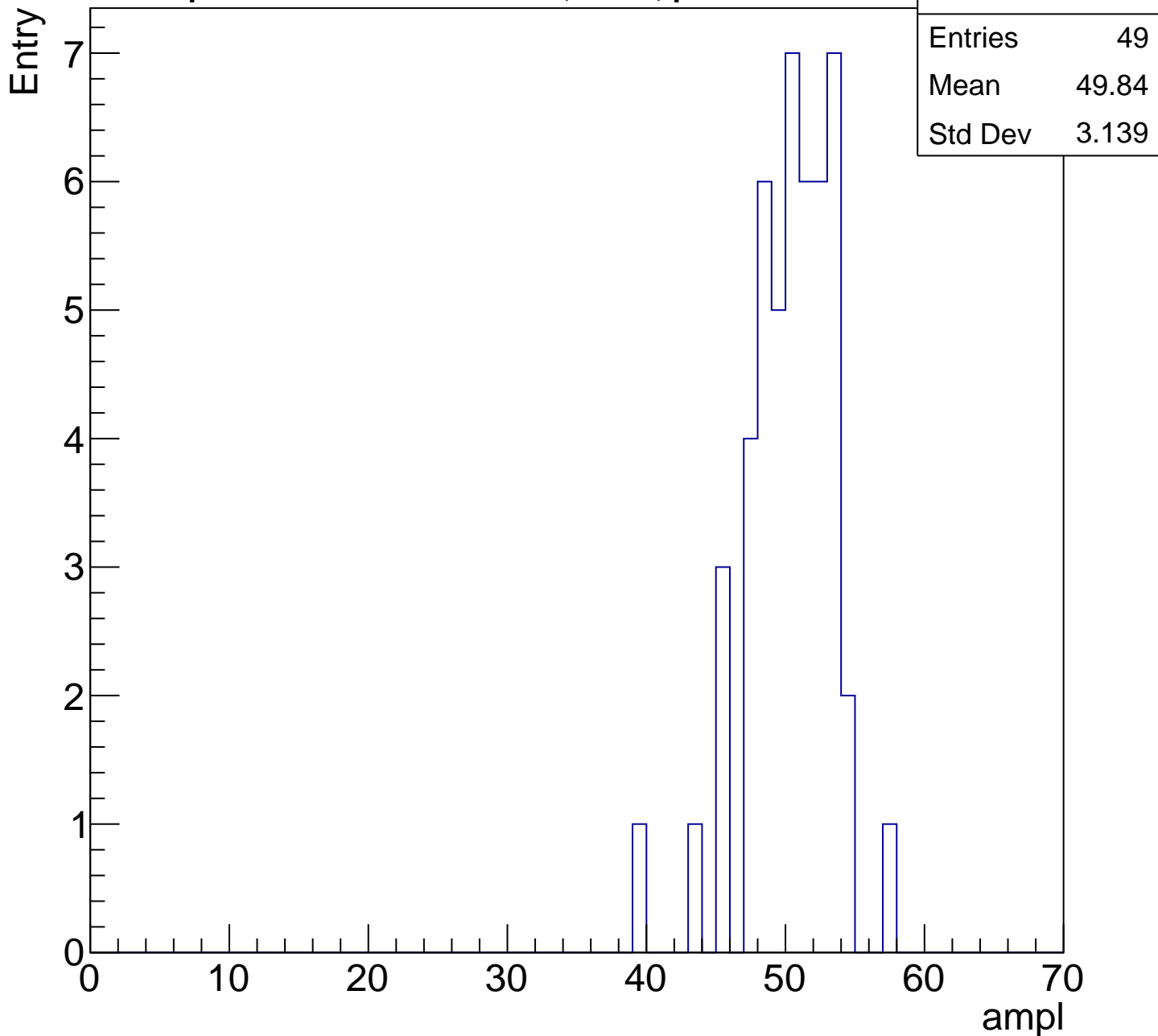
Gaus mean : 44.9175

Gaus Width: 3.7773



B0L002S, U7-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	56.29
Std Dev	2.782

Entry

10

8

6

4

2

0

0

10

20

30

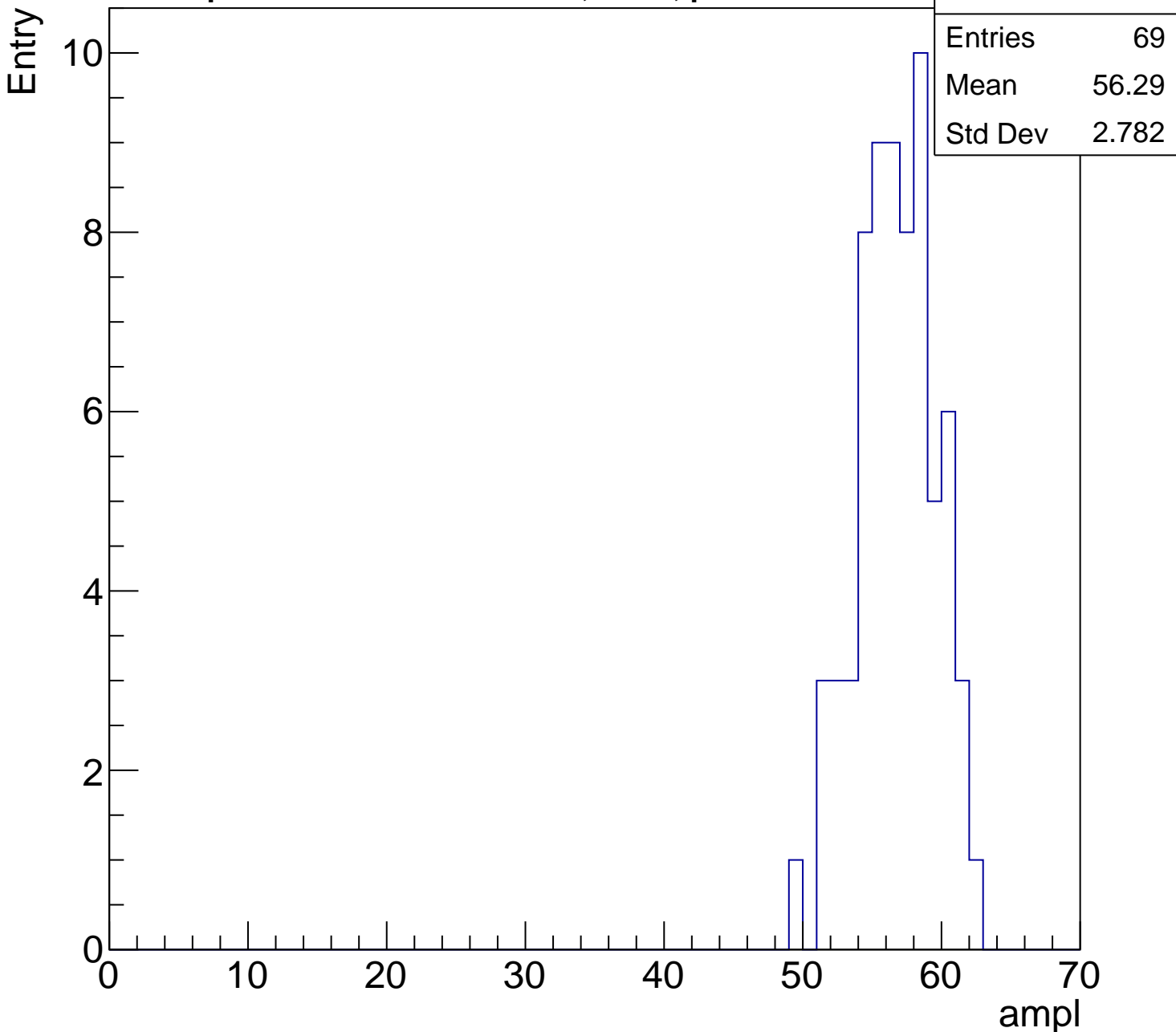
40

50

60

ampl

70

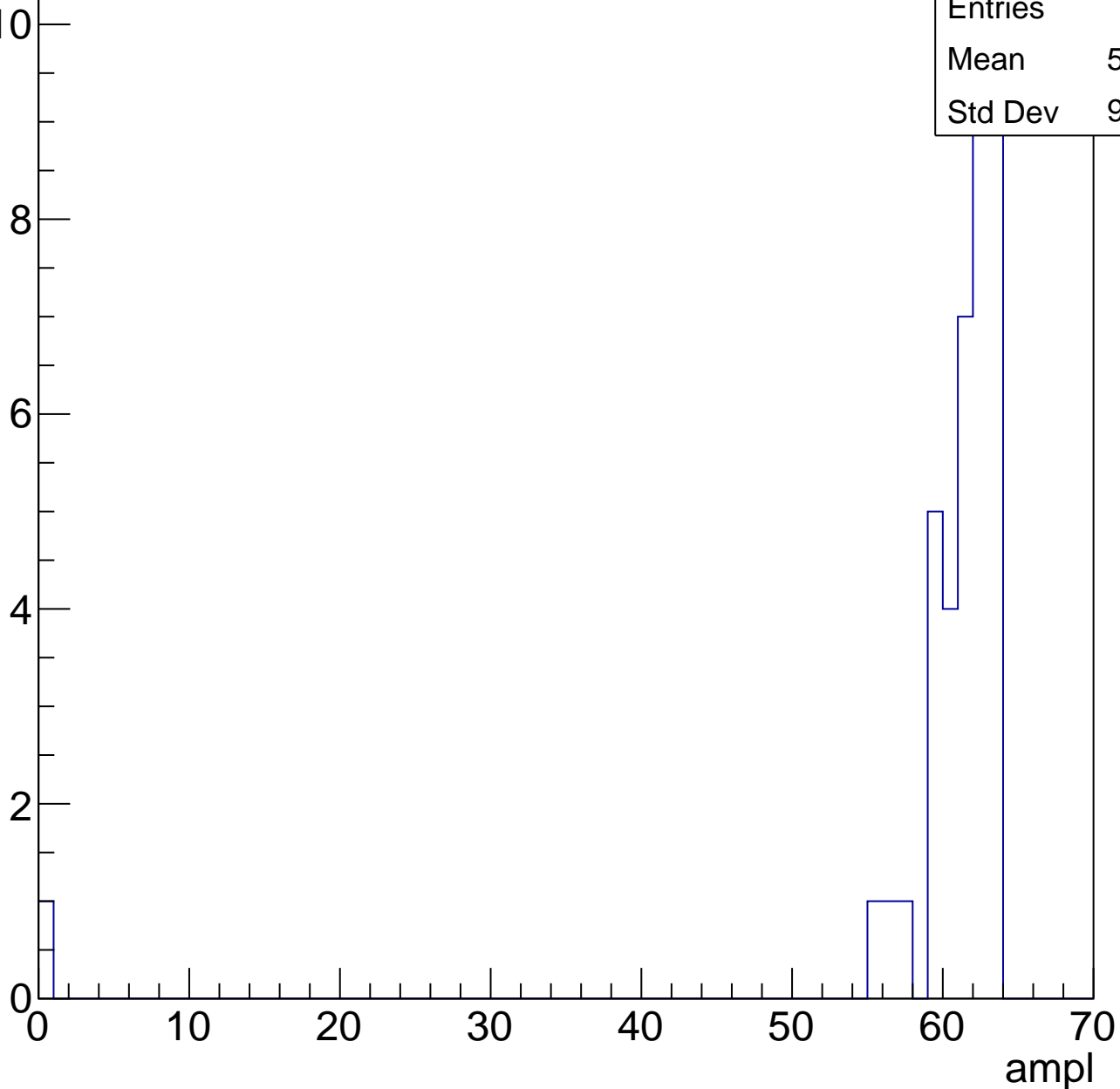


B0L002S, U7-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.44
Std Dev	9.839



B0L002S, U7-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch58, adc0

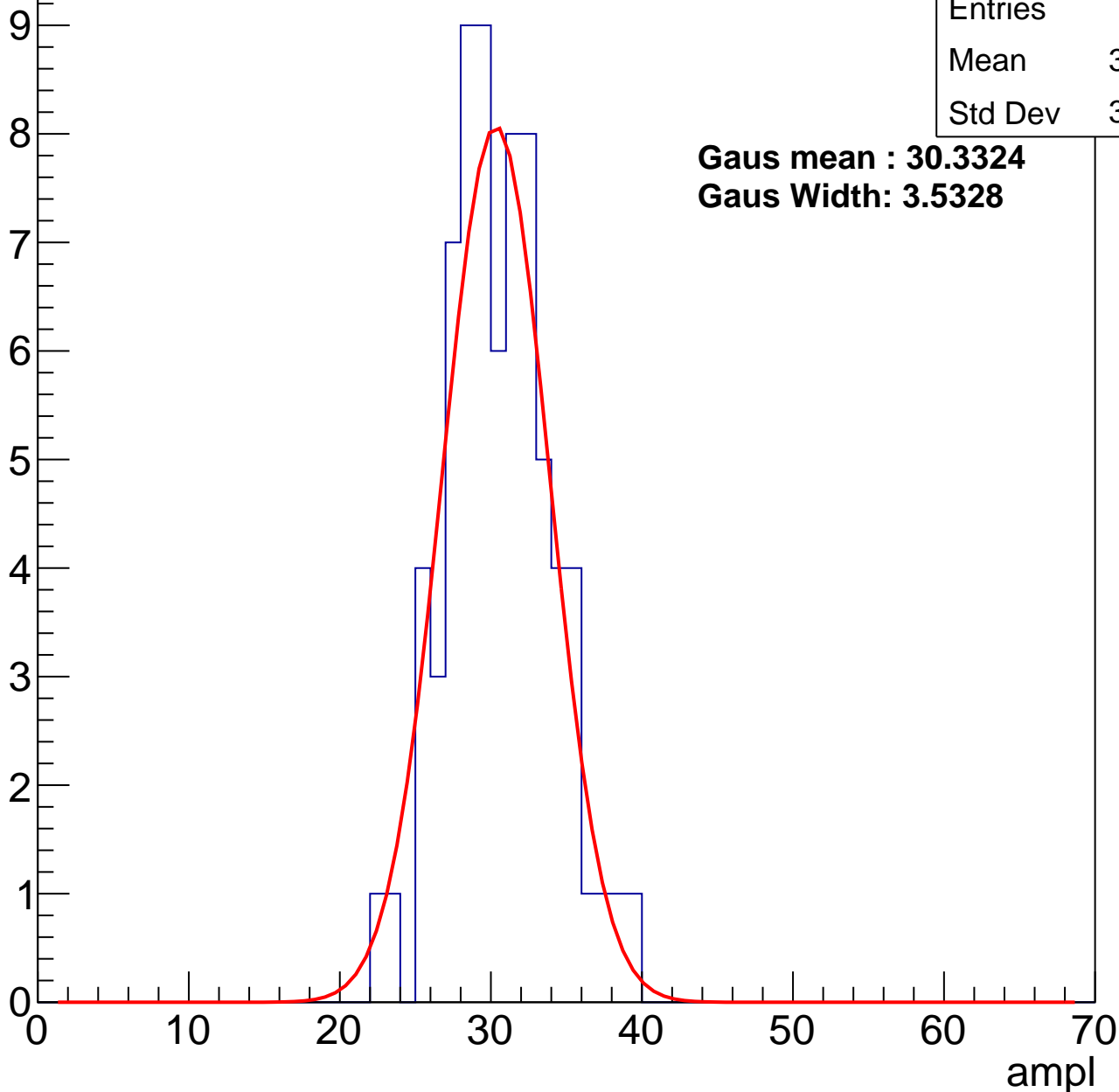
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.14
Std Dev	3.406

Gaus mean : 30.3324

Gaus Width: 3.5328



B0L002S, U7-ch58, adc1

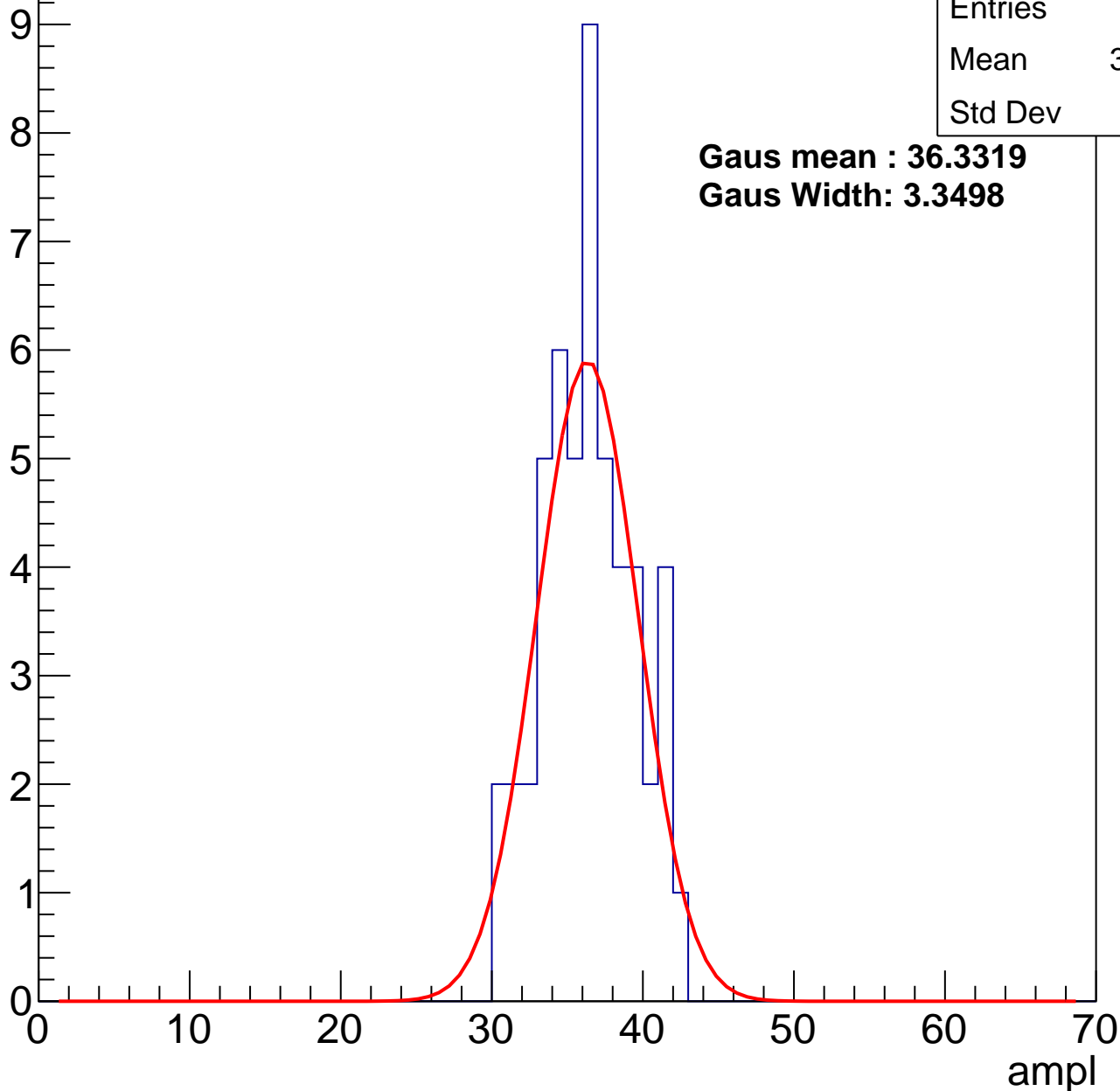
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	35.94
Std Dev	2.98

Gaus mean : 36.3319

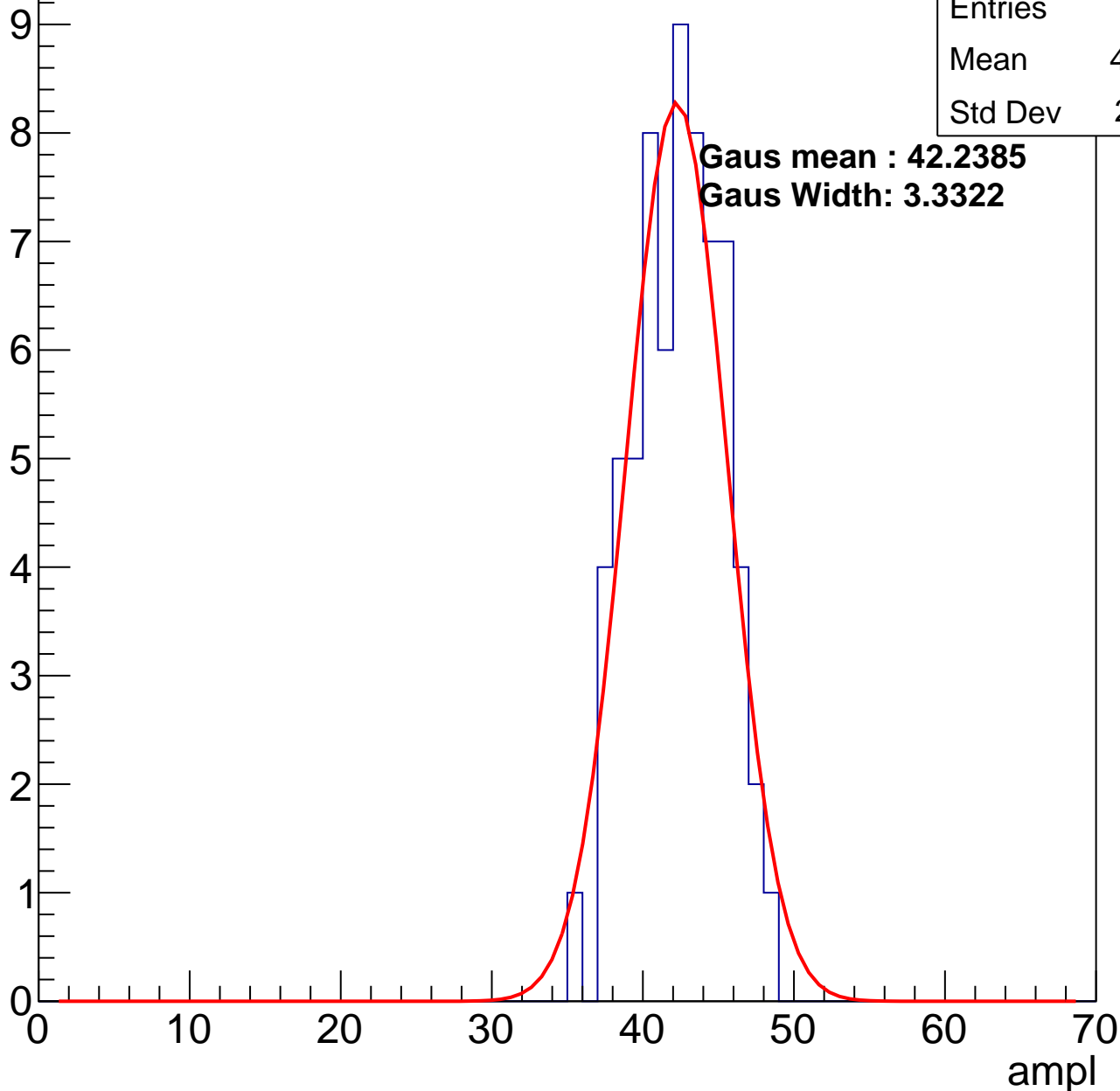
Gaus Width: 3.3498



B0L002S, U7-ch58, adc2

calib_packv5_042523_0143.root, FC#8, port C1

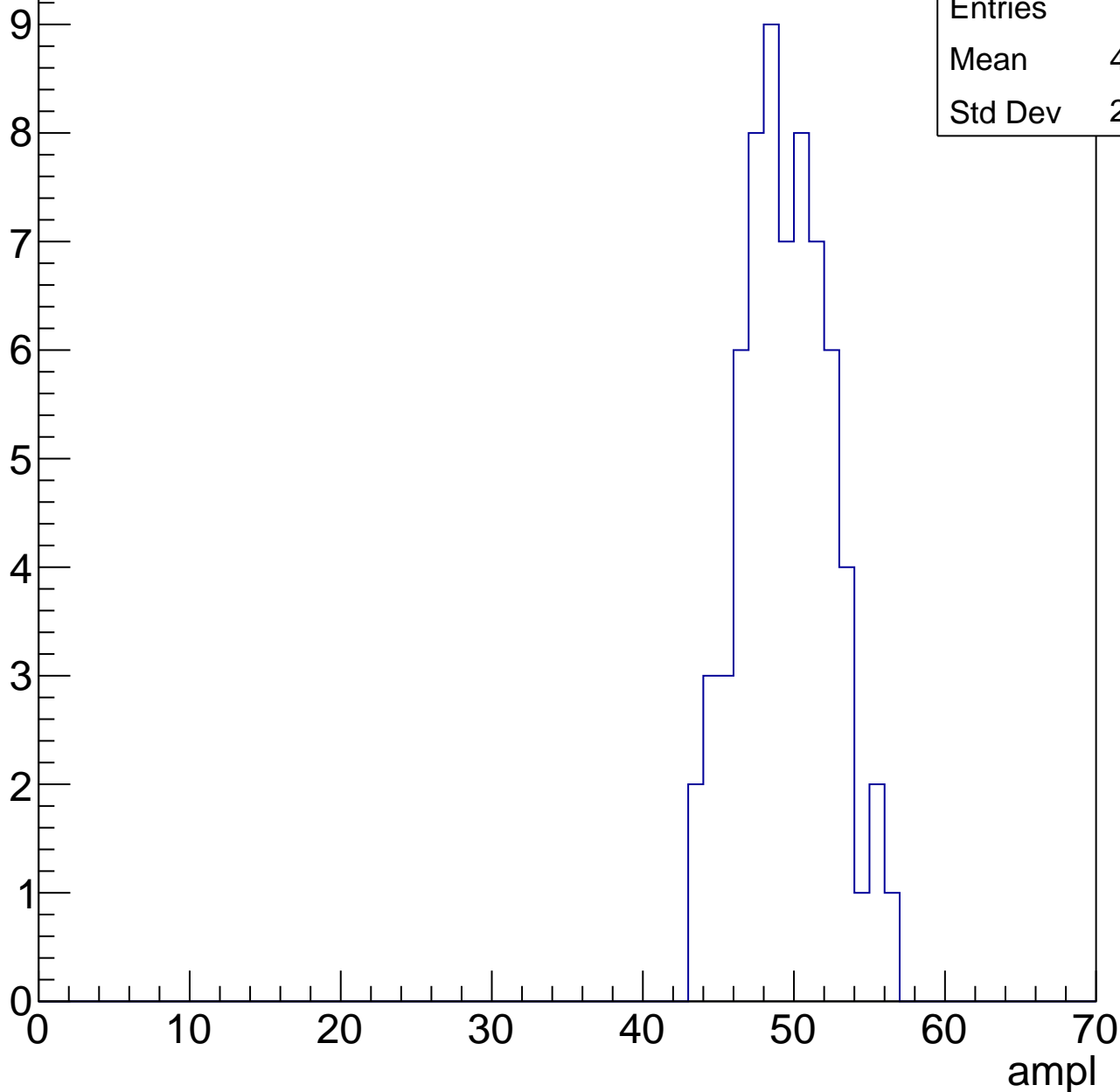
Entry



B0L002S, U7-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

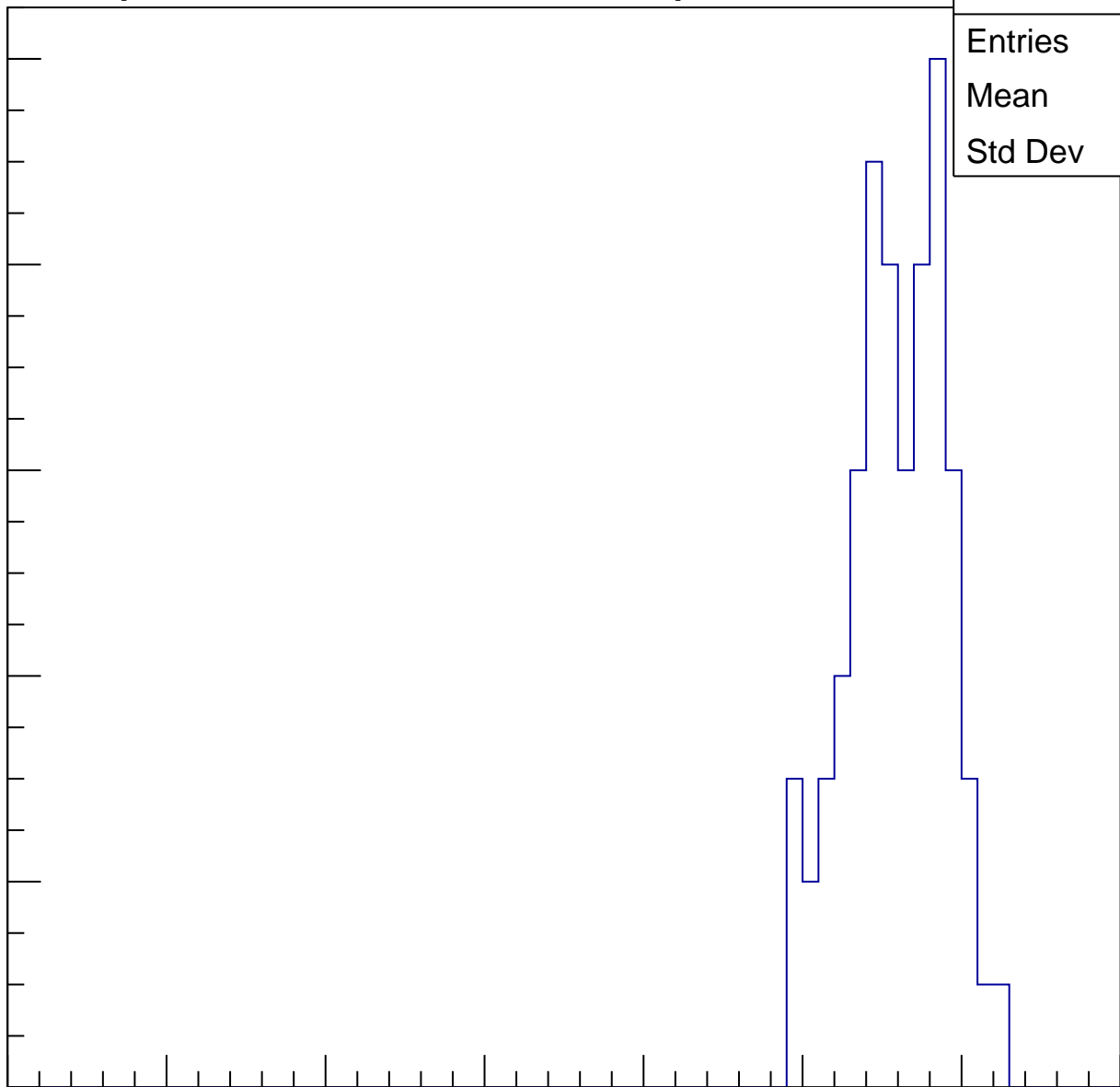
Entries	70
Mean	55.44
Std Dev	3.036

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

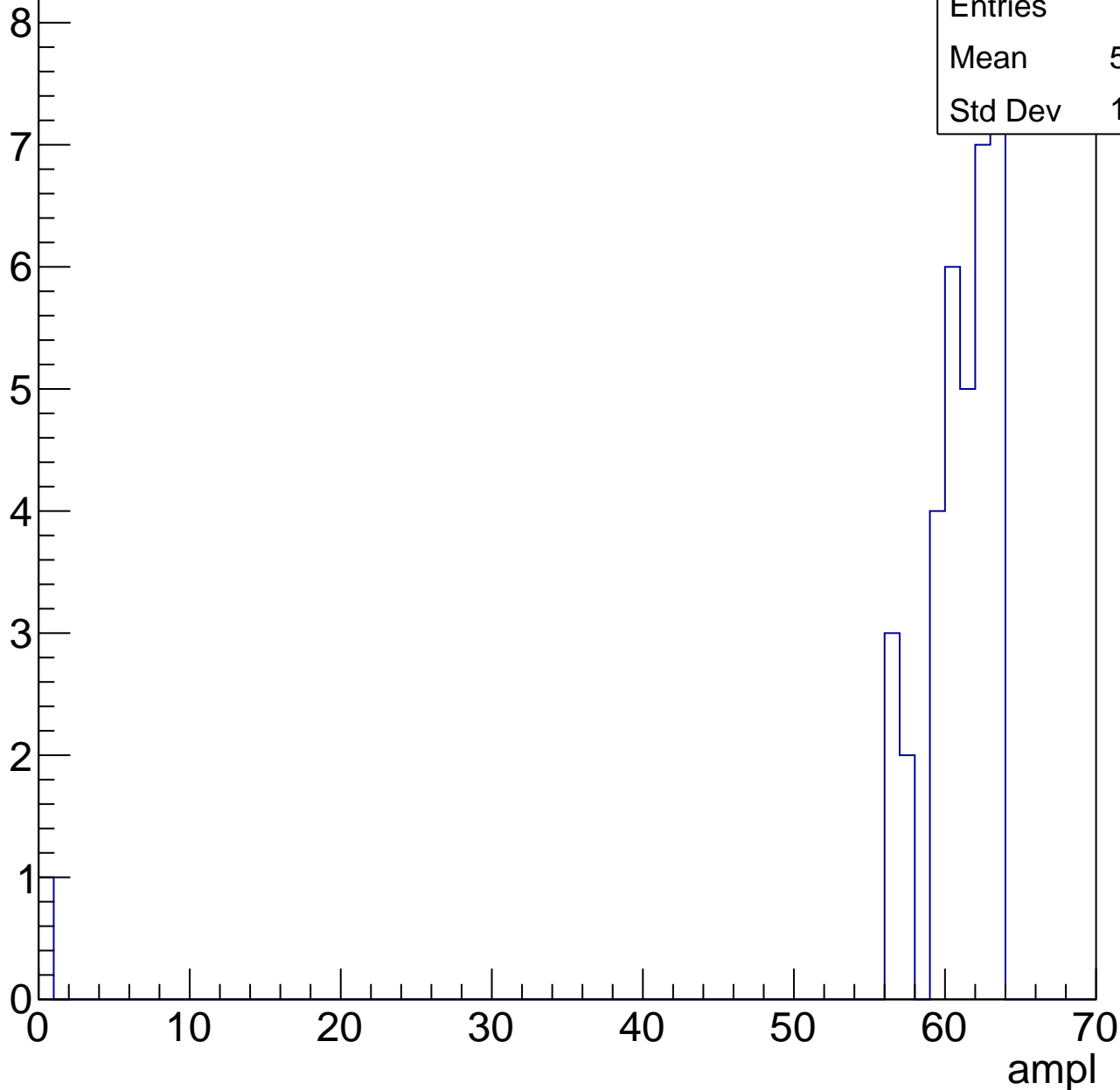


B0L002S, U7-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

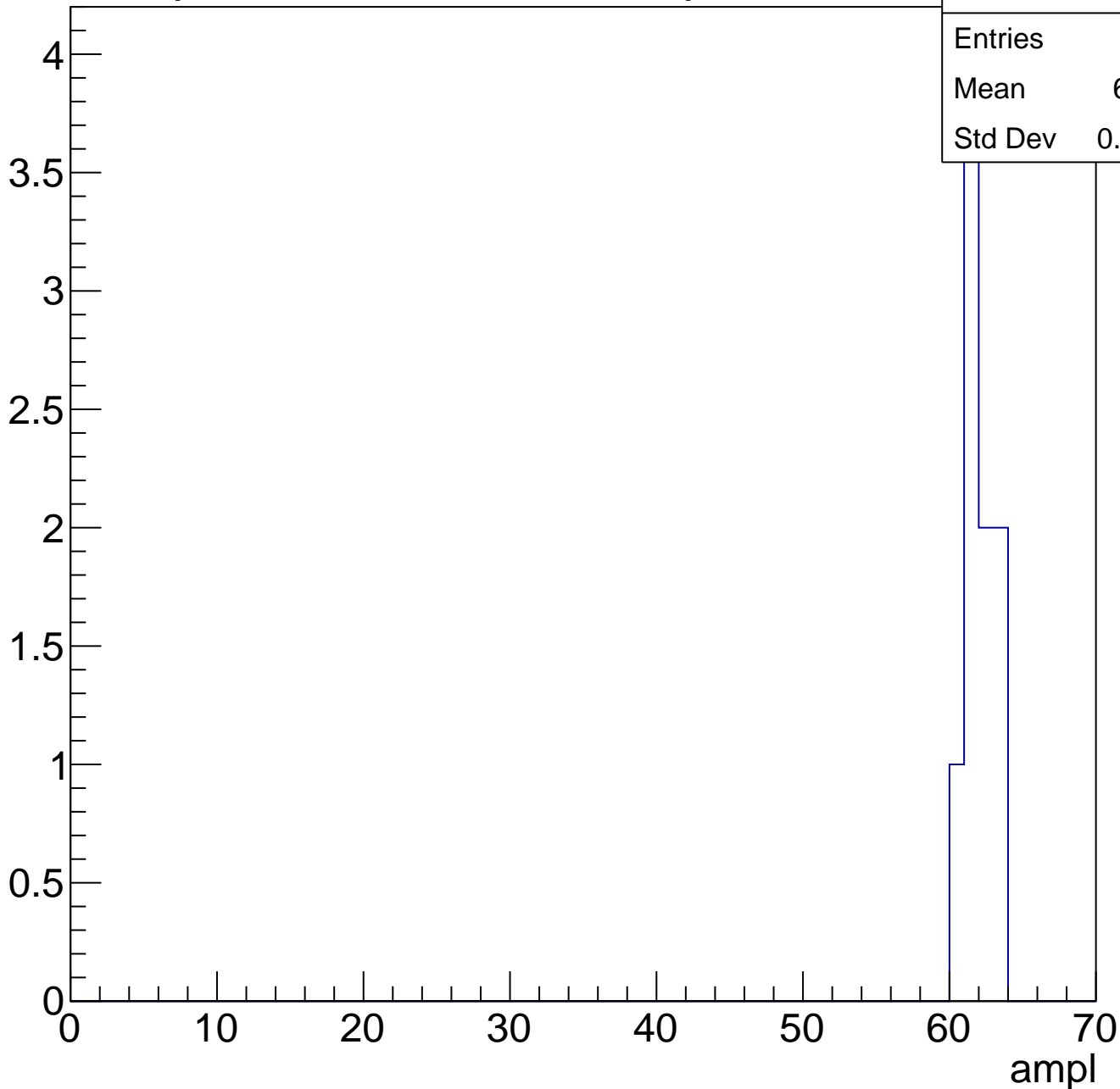
Entries	36
Mean	58.92
Std Dev	10.18



B0L002S, U7-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch59, adc0

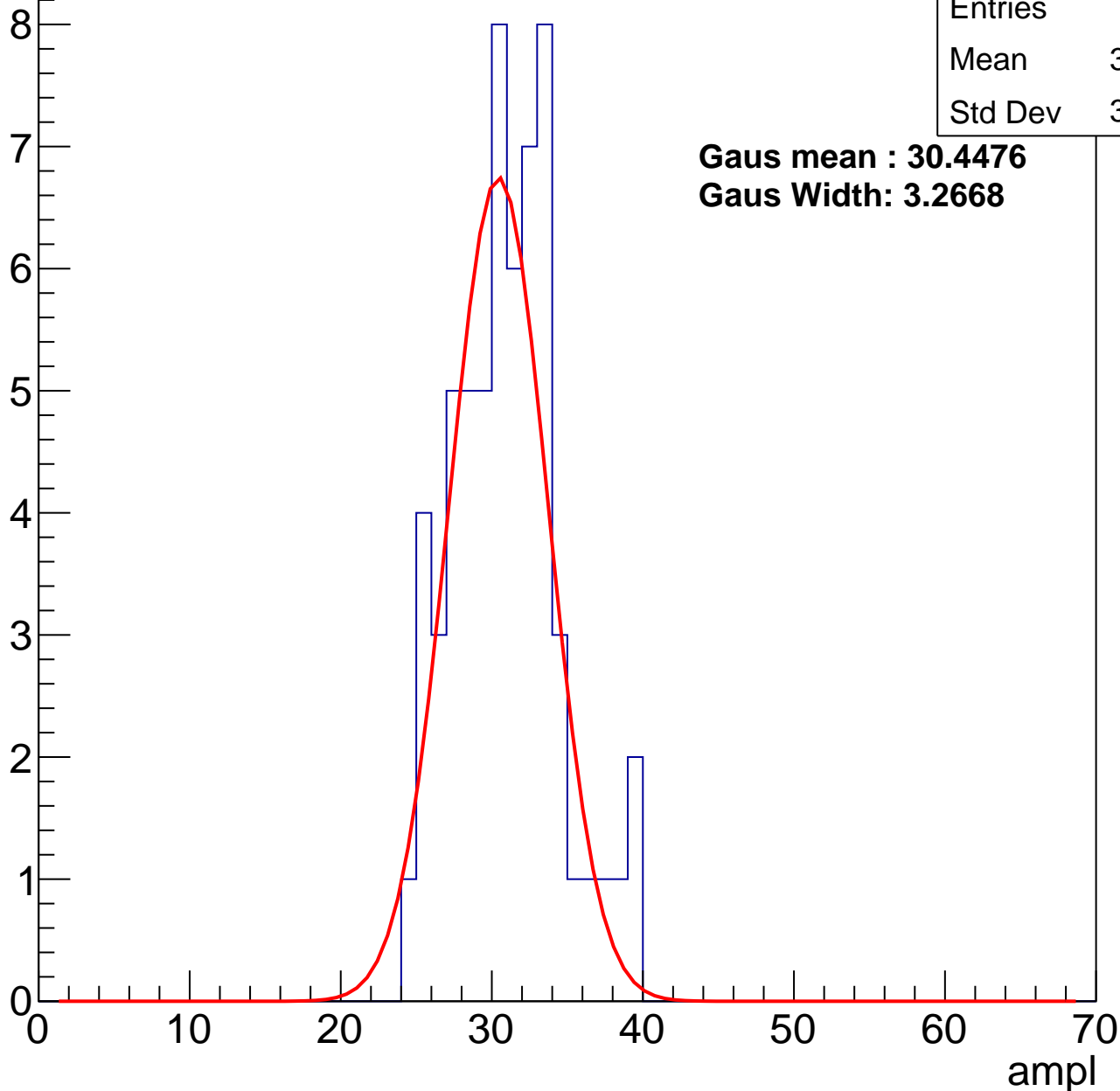
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.52
Std Dev	3.453

Gaus mean : 30.4476

Gaus Width: 3.2668



B0L002S, U7-ch59, adc1

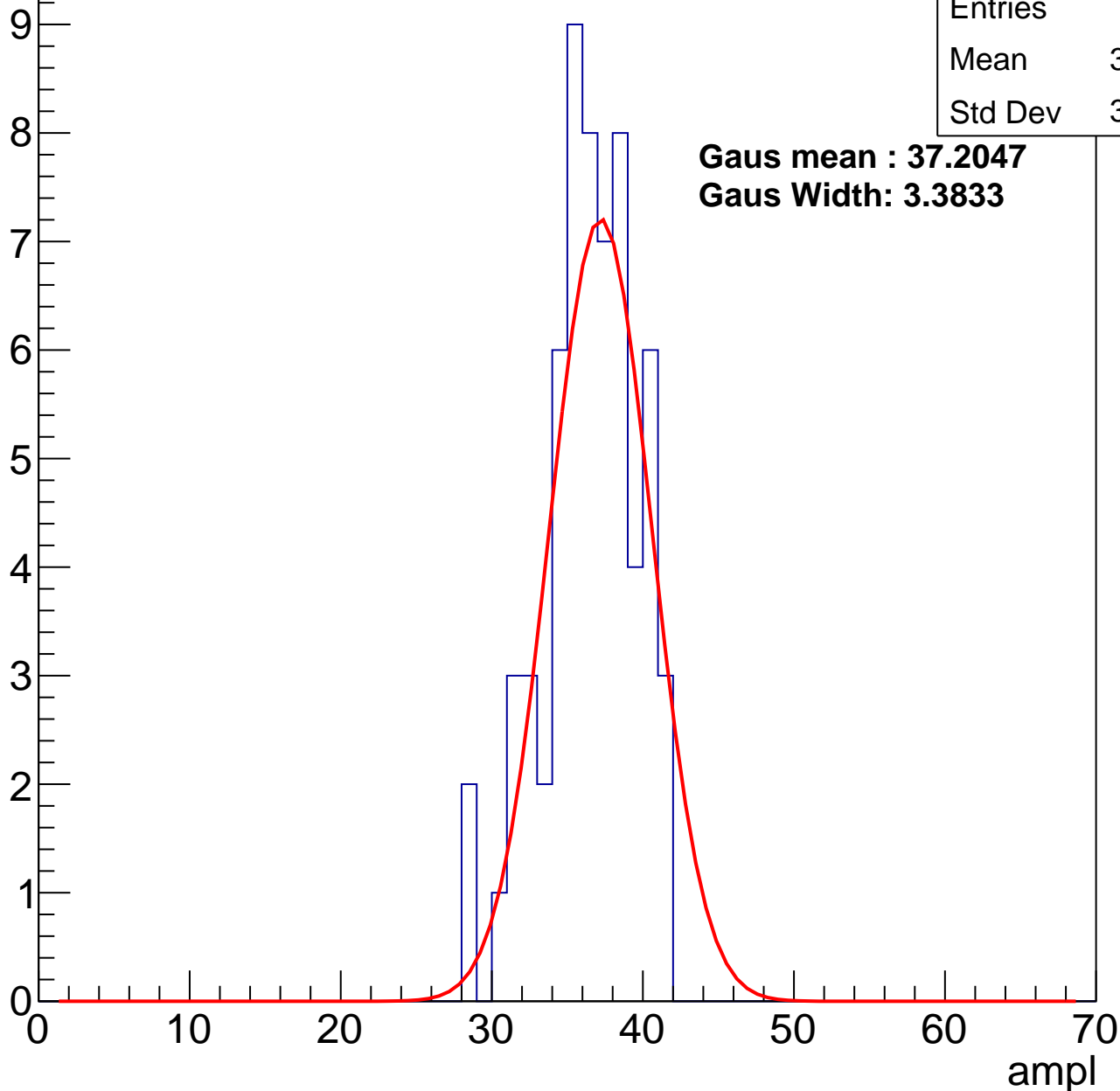
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	35.97
Std Dev	3.069

Gaus mean : 37.2047

Gaus Width: 3.3833



B0L002S, U7-ch59, adc2

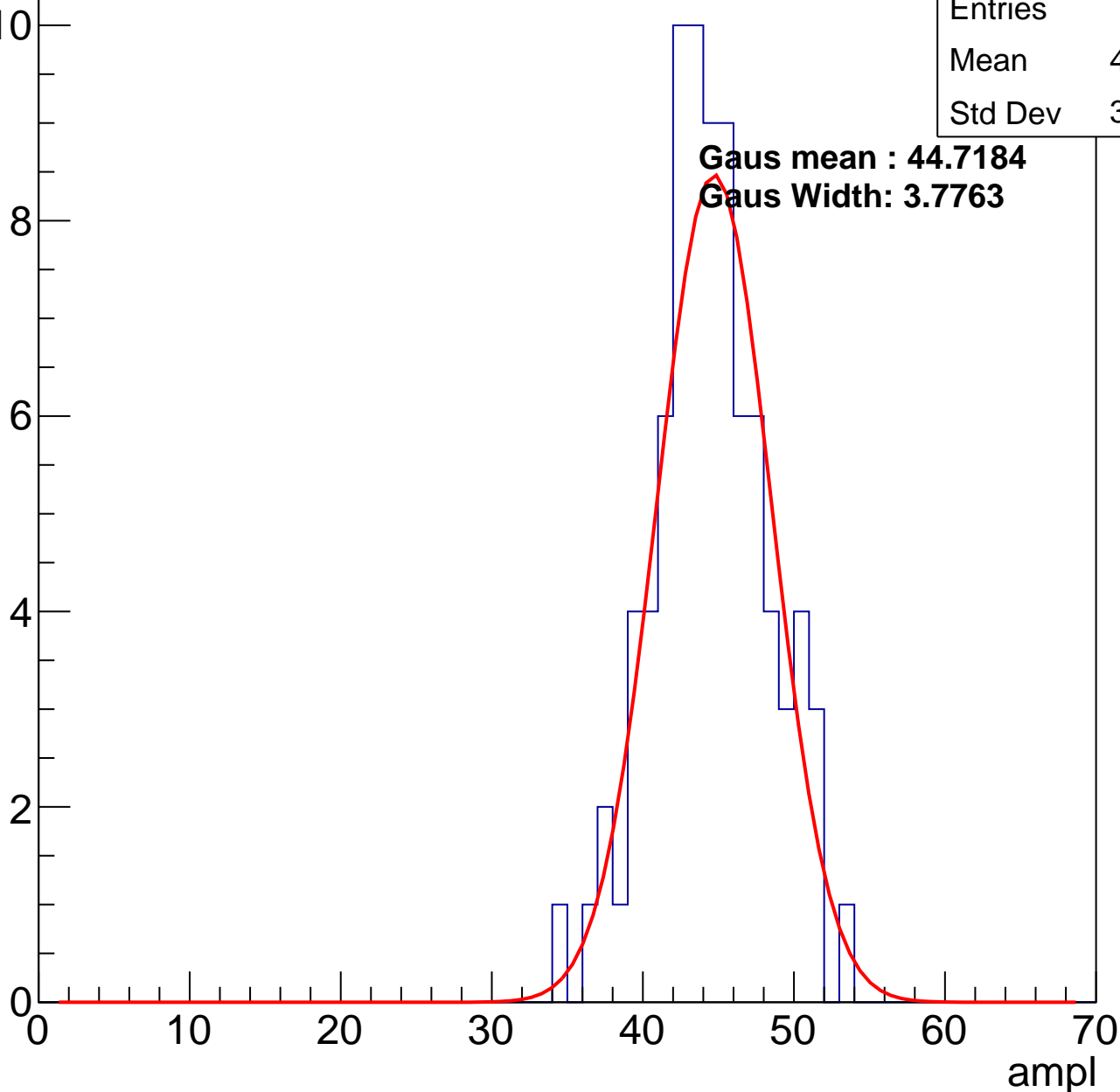
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	44.02
Std Dev	3.722

Gaus mean : 44.7184

Gaus Width: 3.7763

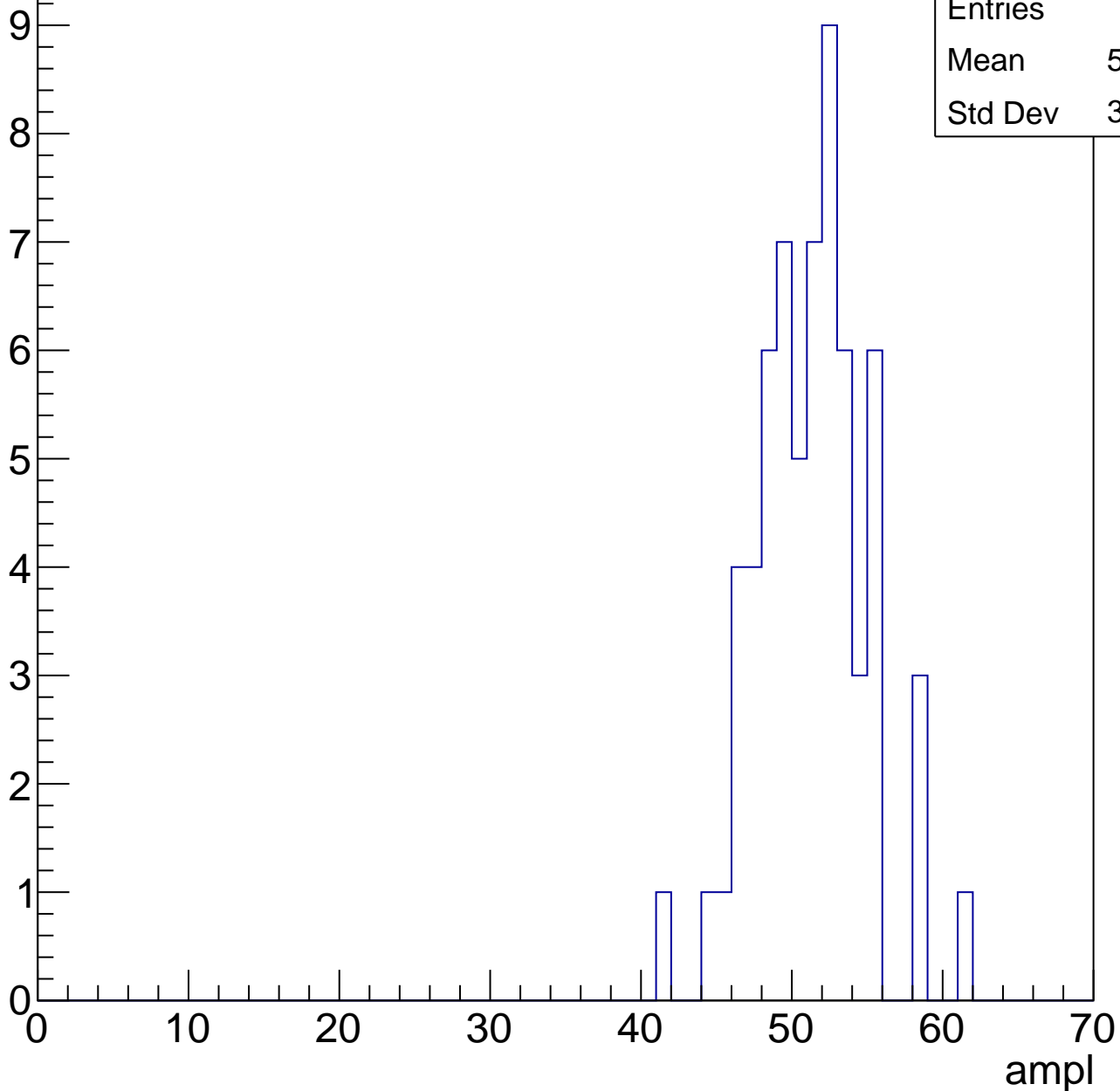


B0L002S, U7-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

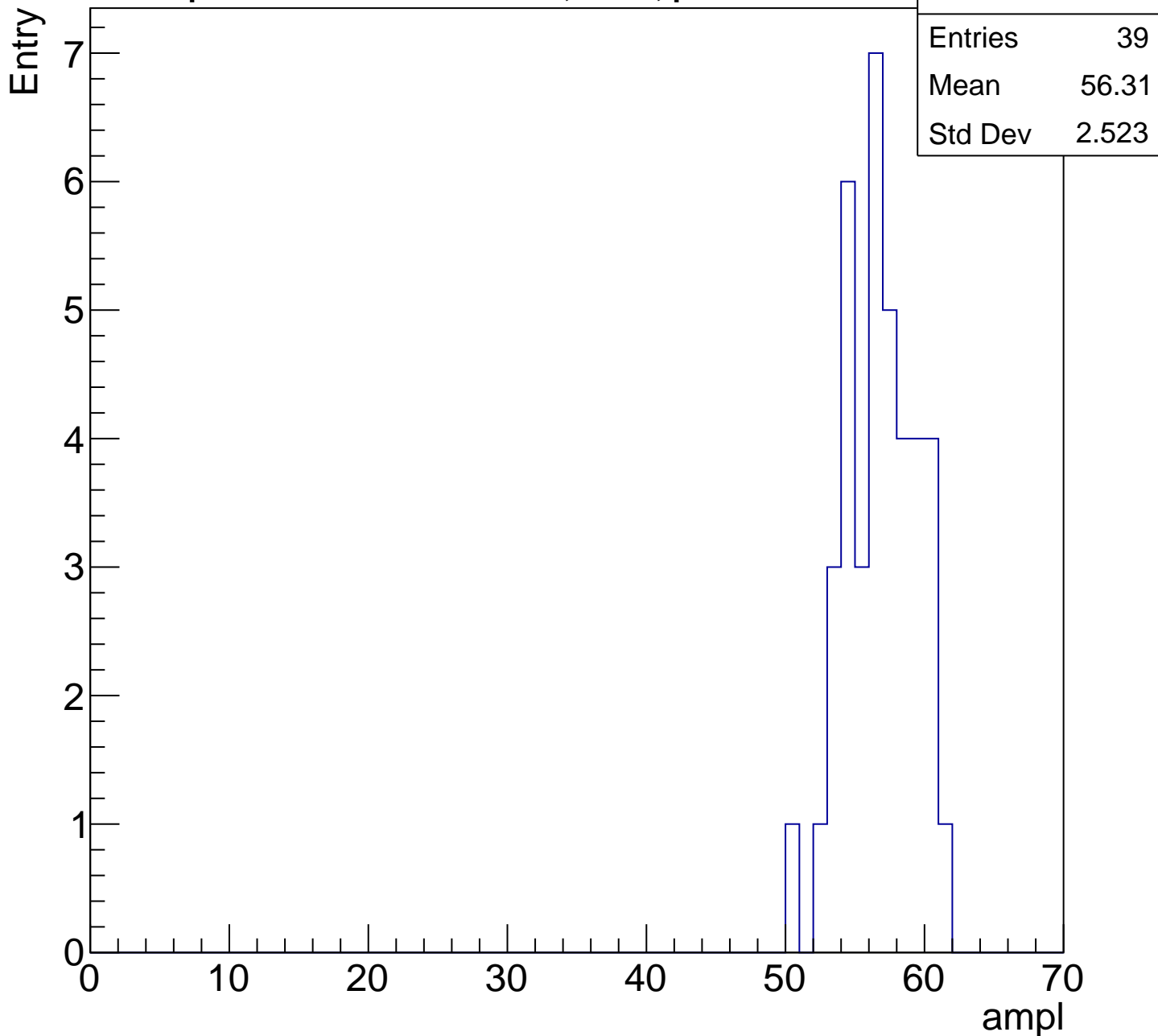
Entry

Entries	64
Mean	50.83
Std Dev	3.617



B0L002S, U7-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	51
Mean	60.08
Std Dev	2.424

Entry

10

8

6

4

2

0

0

10

20

30

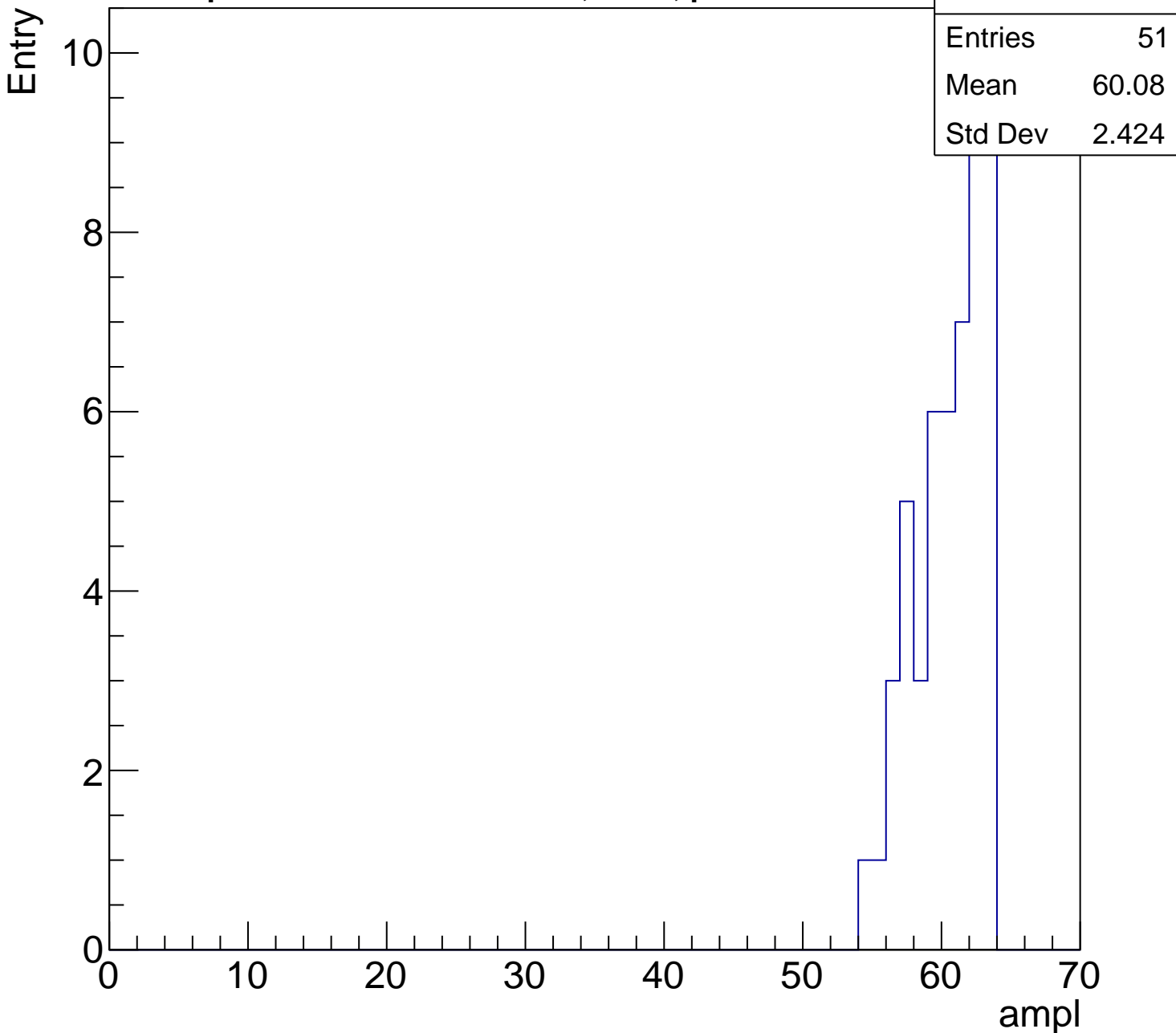
40

50

60

ampl

70



B0L002S, U7-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U7-ch60, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	29.47
Std Dev	2.941

Gaus mean : 29.9824

Gaus Width: 2.9149

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L002S, U7-ch60, adc1

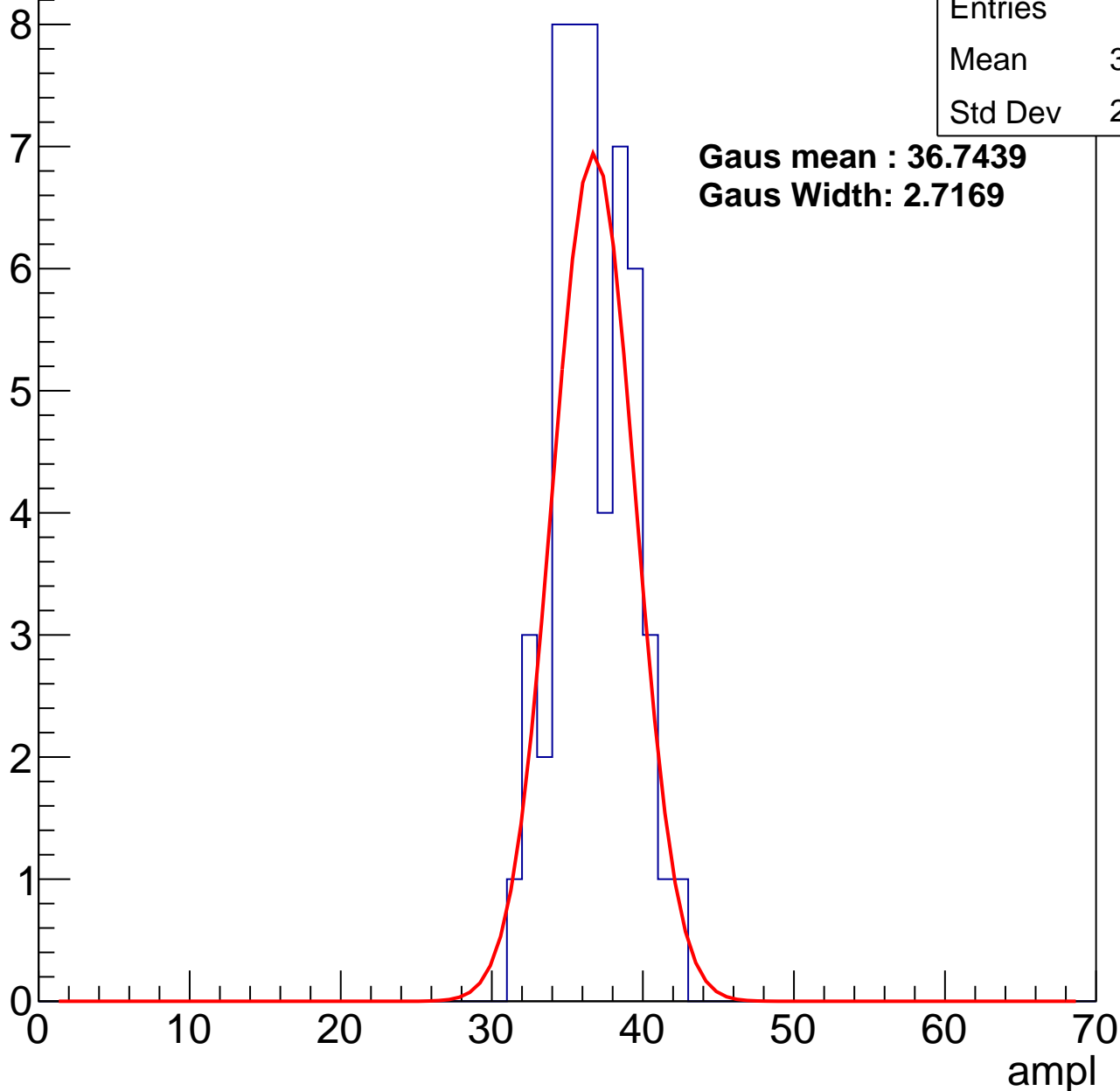
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	36.23
Std Dev	2.493

Gaus mean : 36.7439

Gaus Width: 2.7169



B0L002S, U7-ch60, adc2

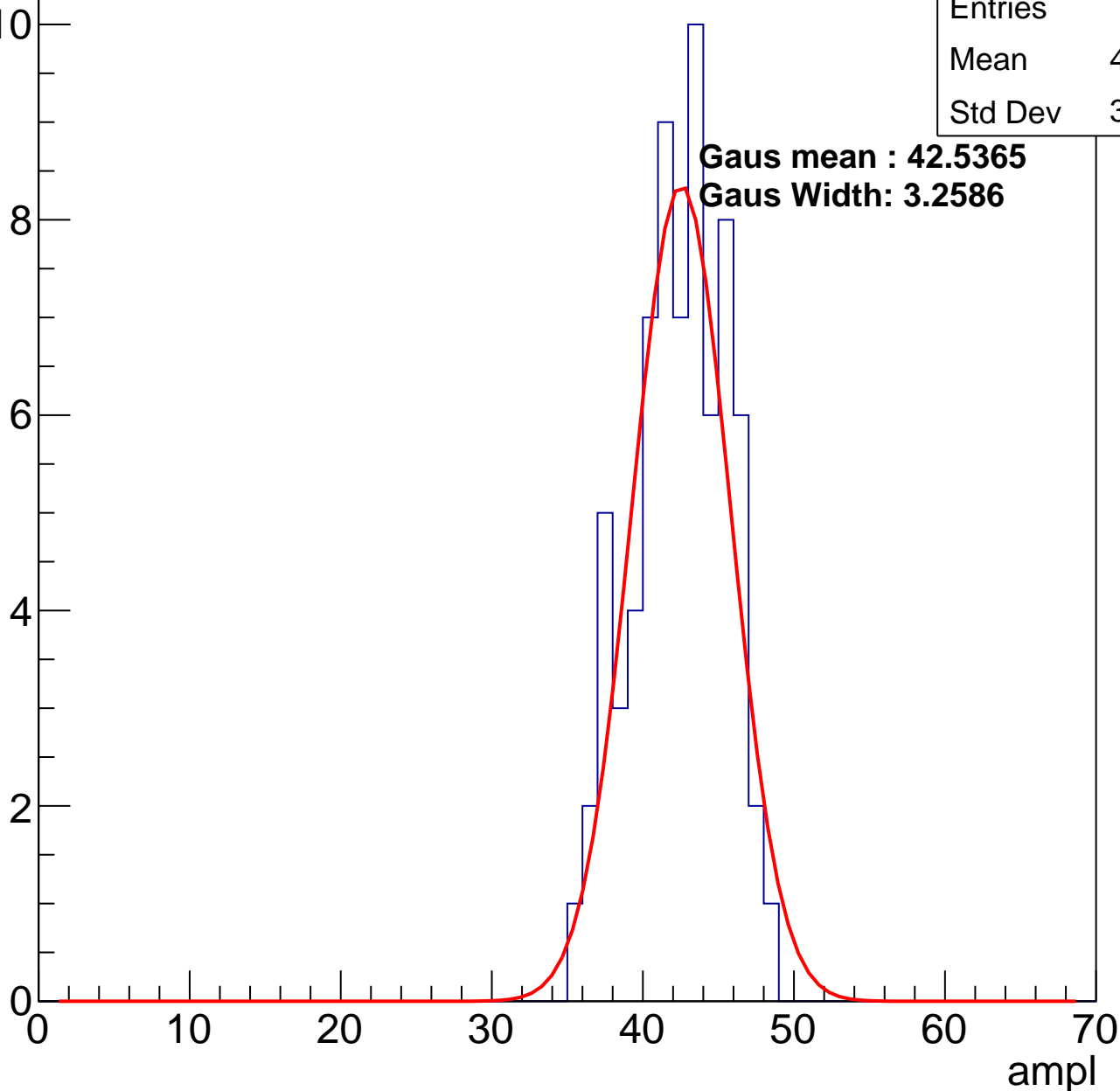
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	41.93
Std Dev	3.037

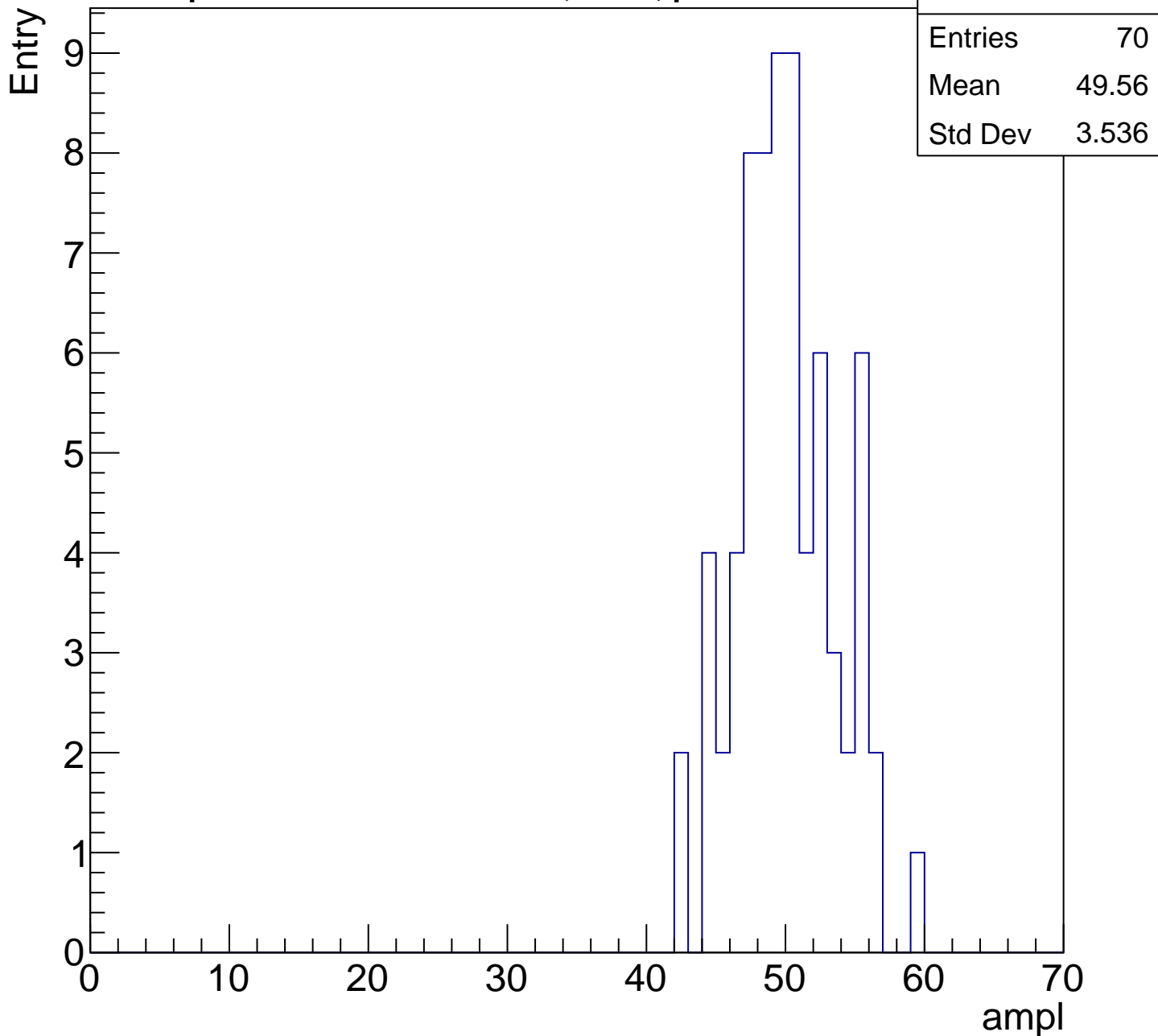
Gaus mean : 42.5365

Gaus Width: 3.2586



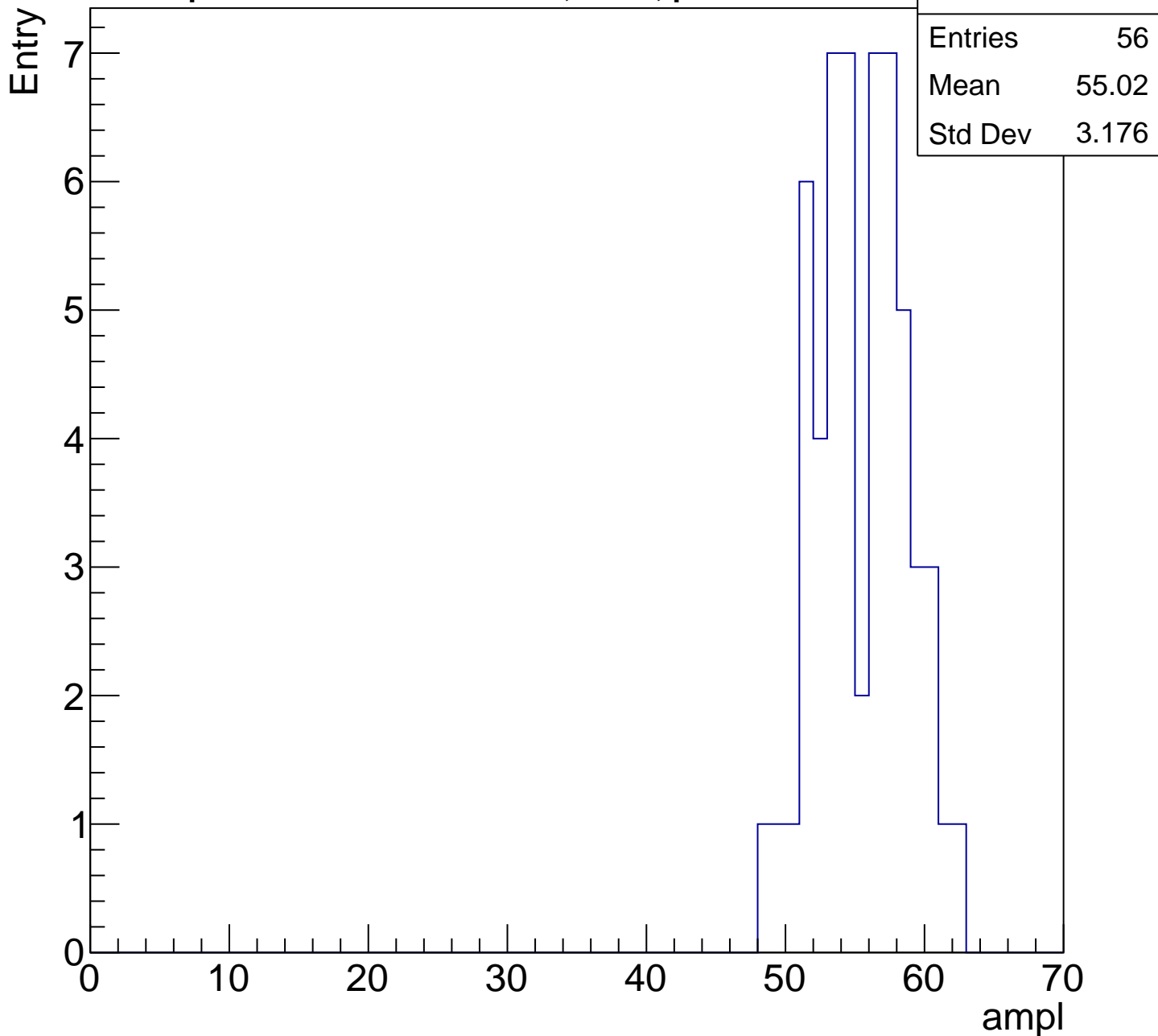
B0L002S, U7-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

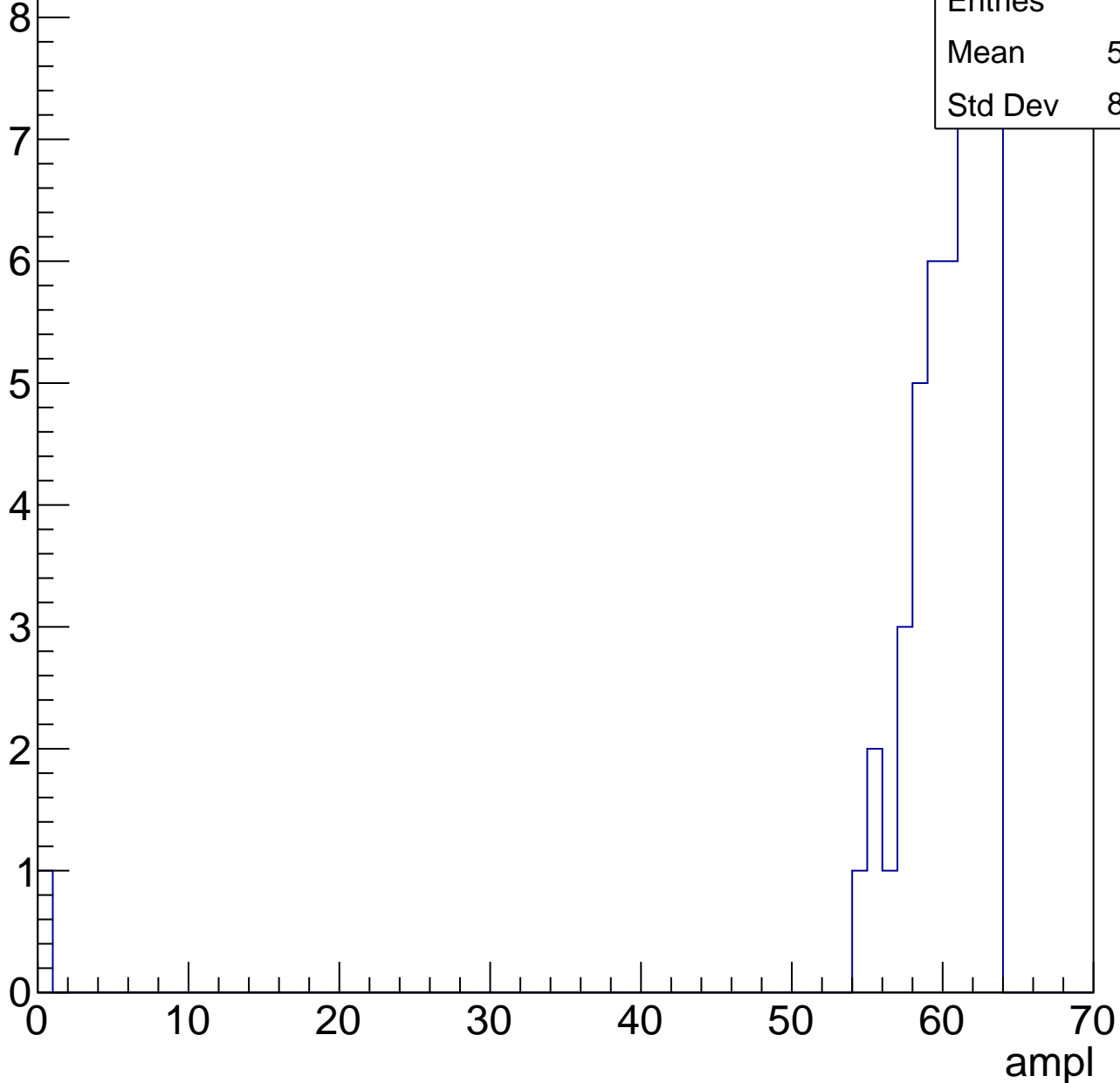


B0L002S, U7-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.84
Std Dev	8.807



B0L002S, U7-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

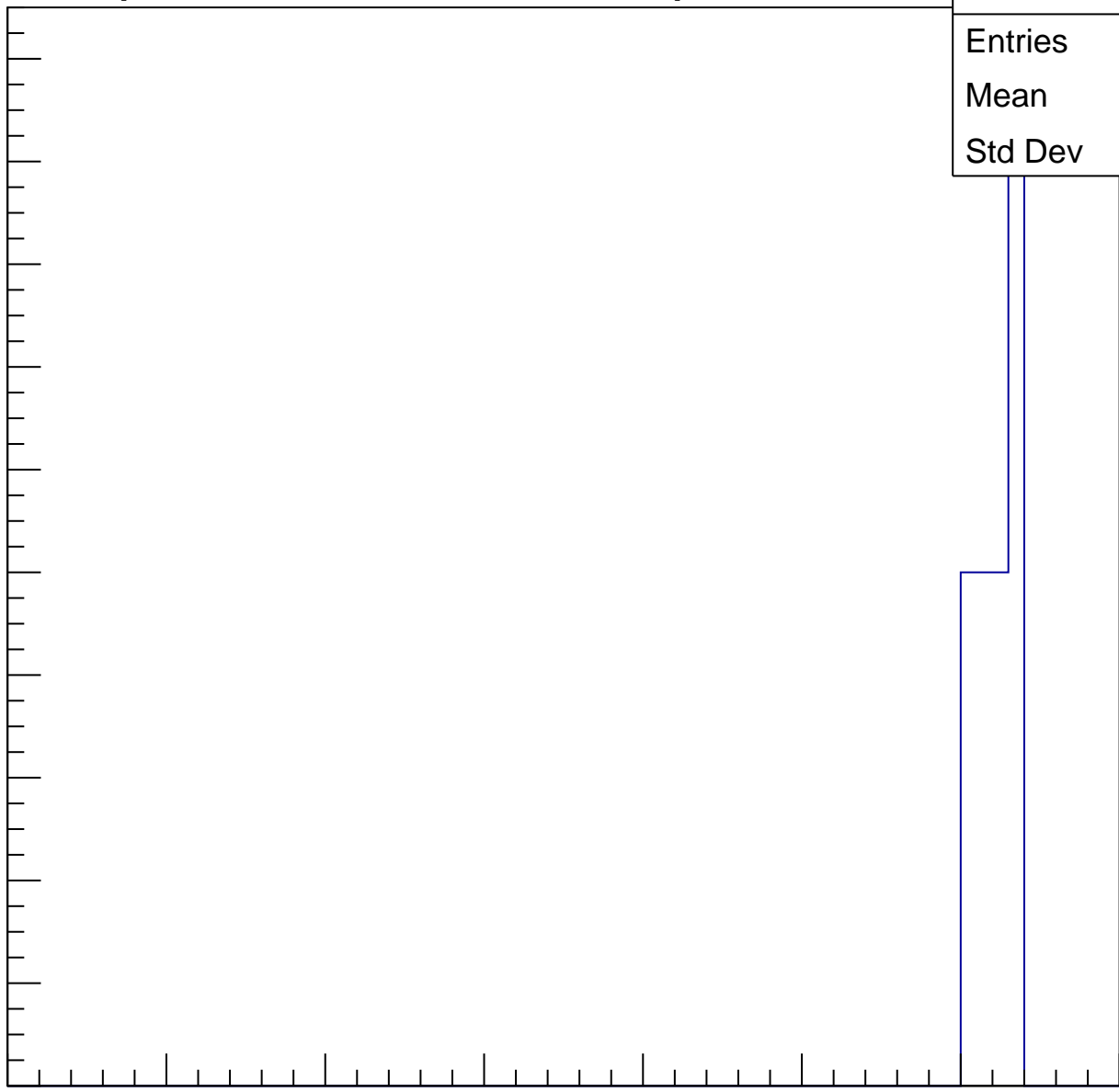
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch61, adc0

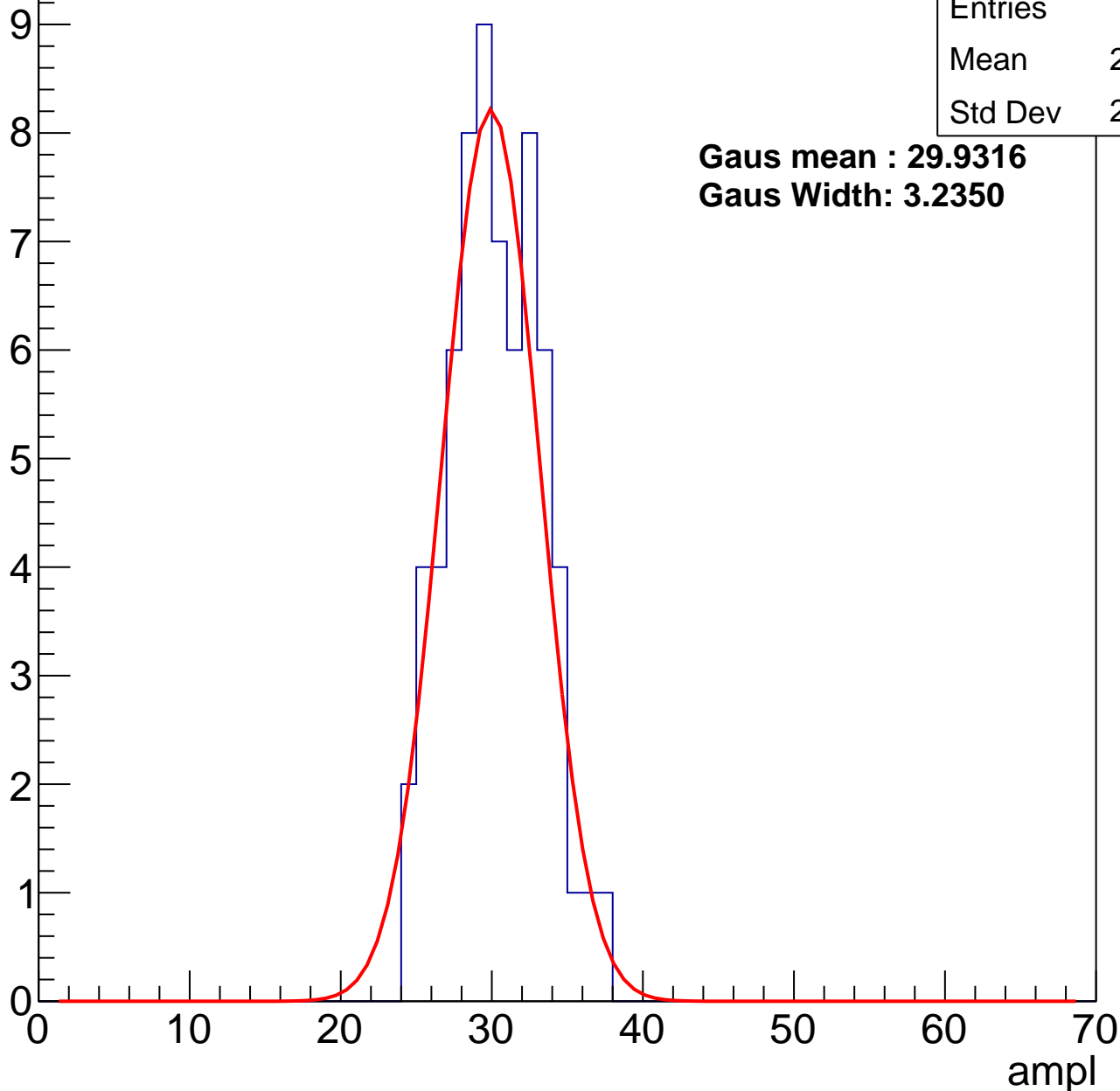
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.75
Std Dev	2.974

Gaus mean : 29.9316

Gaus Width: 3.2350



B0L002S, U7-ch61, adc1

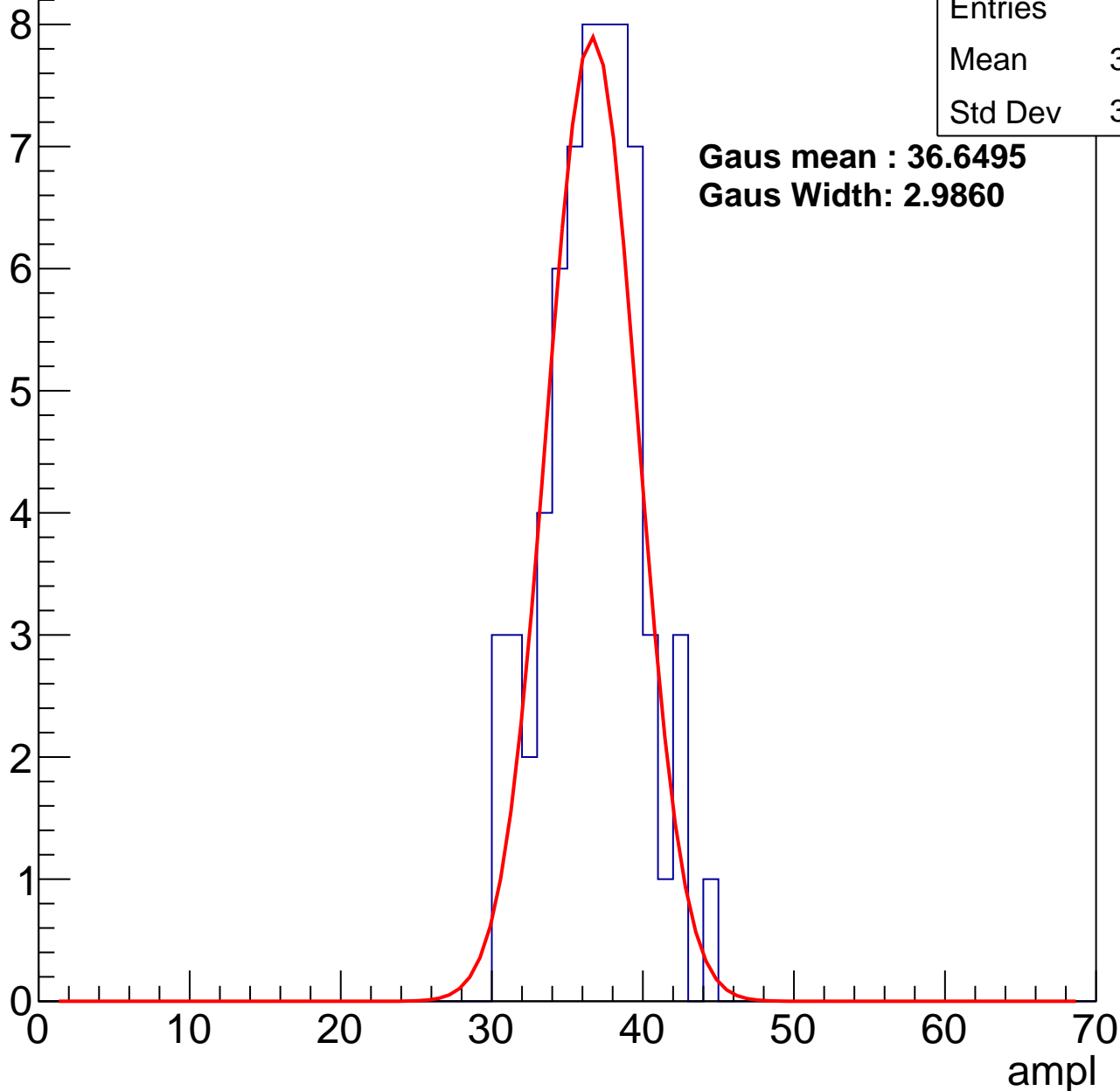
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.25
Std Dev	3.127

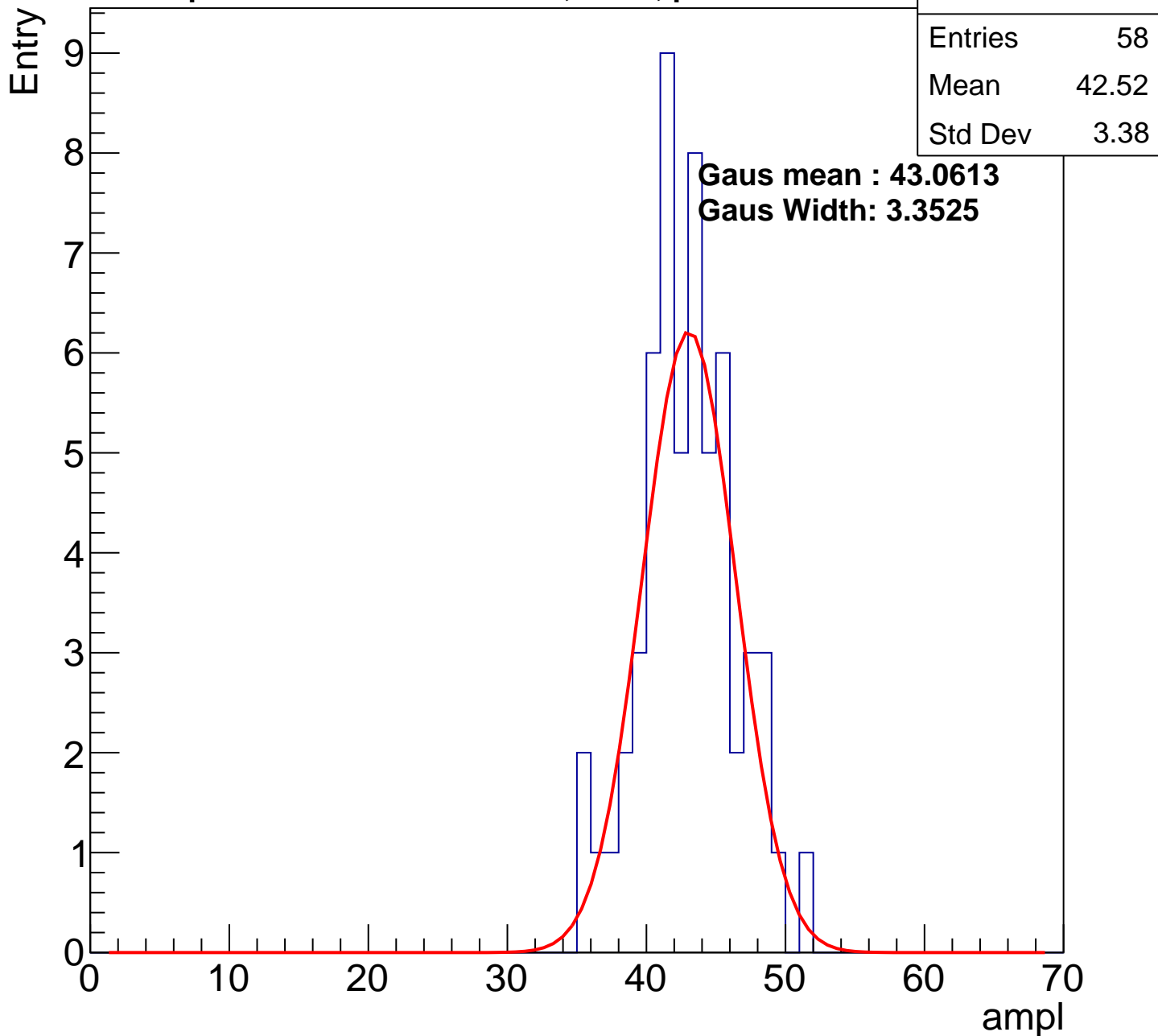
Gaus mean : 36.6495

Gaus Width: 2.9860



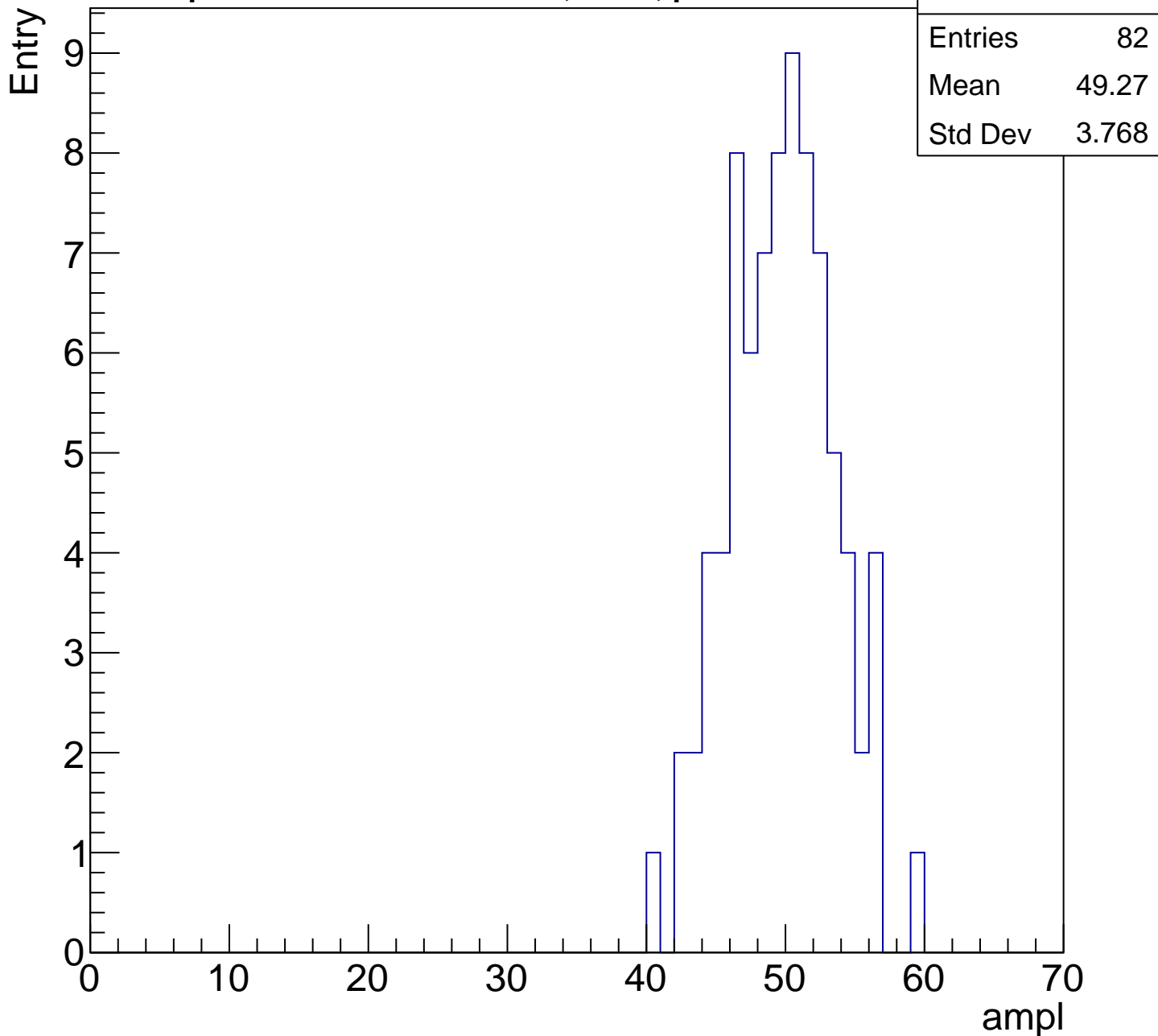
B0L002S, U7-ch61, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

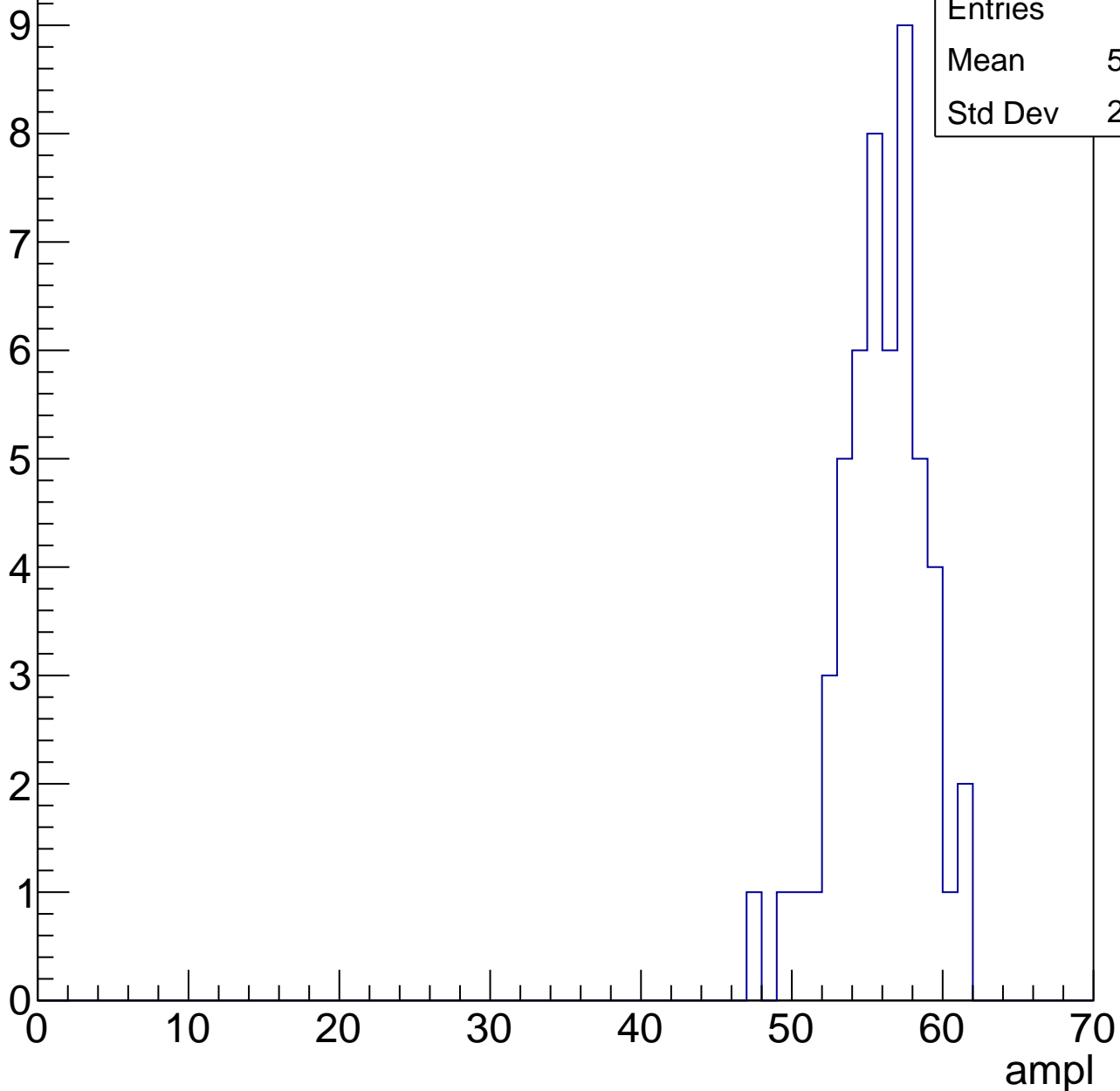


B0L002S, U7-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

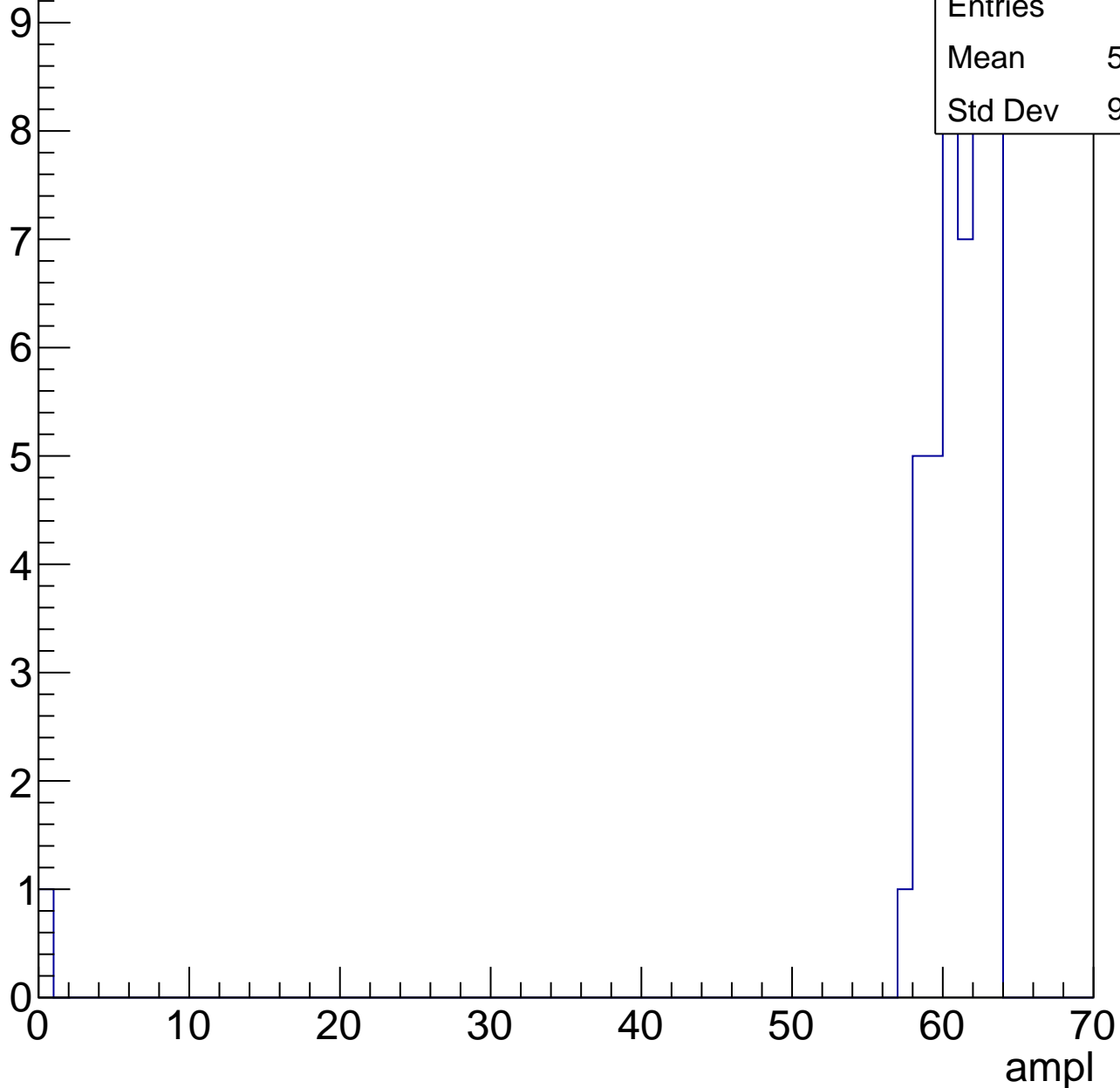
Entries	53
Mean	55.45
Std Dev	2.852



B0L002S, U7-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch62, adc0

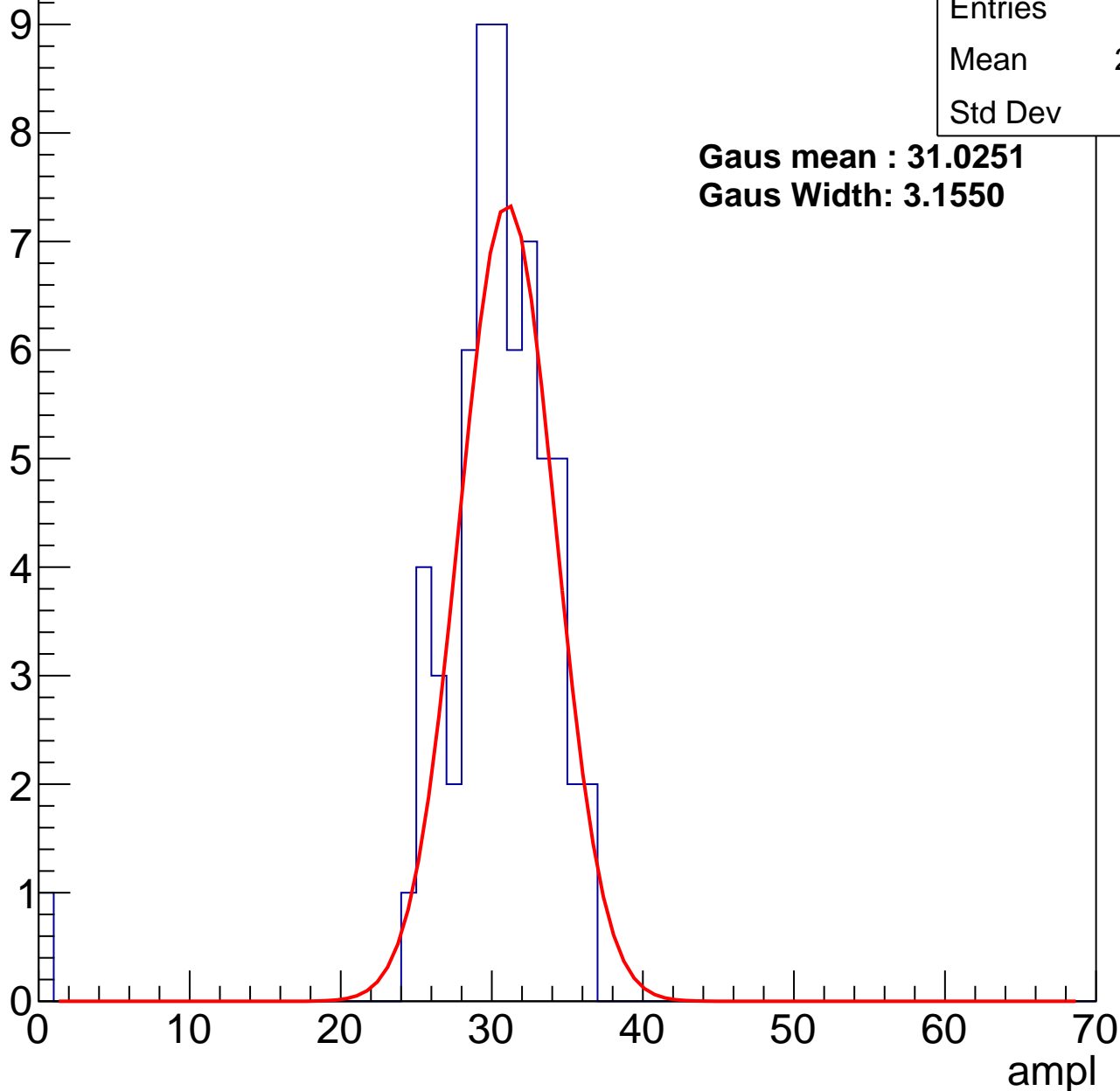
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.71
Std Dev	4.77

Gaus mean : 31.0251

Gaus Width: 3.1550



B0L002S, U7-ch62, adc1

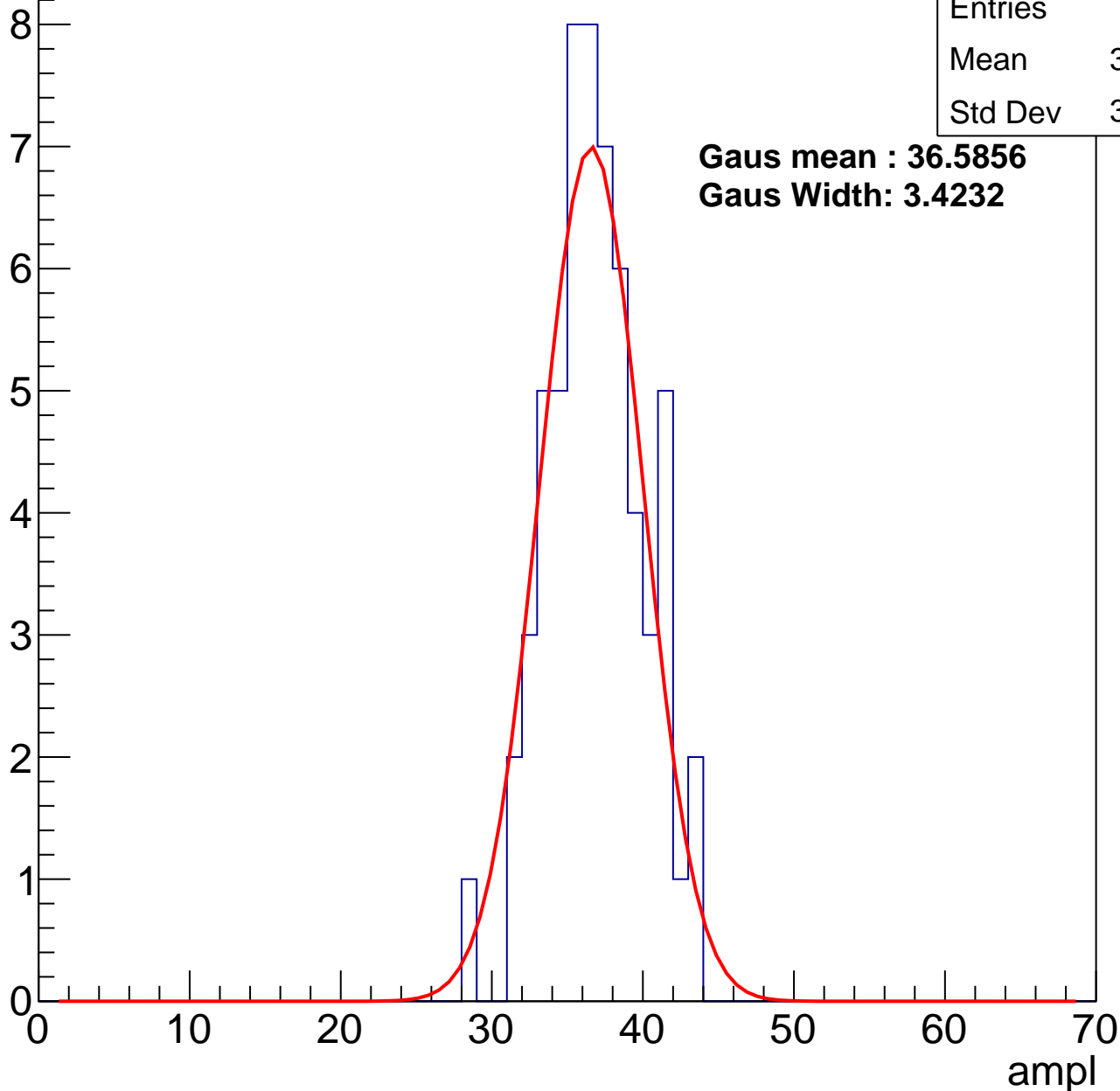
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.42
Std Dev	3.159

Gaus mean : 36.5856

Gaus Width: 3.4232



B0L002S, U7-ch62, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	42.95
Std Dev	3.232

Gaus mean : 42.7425

Gaus Width: 3.5808

10

8

6

4

2

0

0

10

20

30

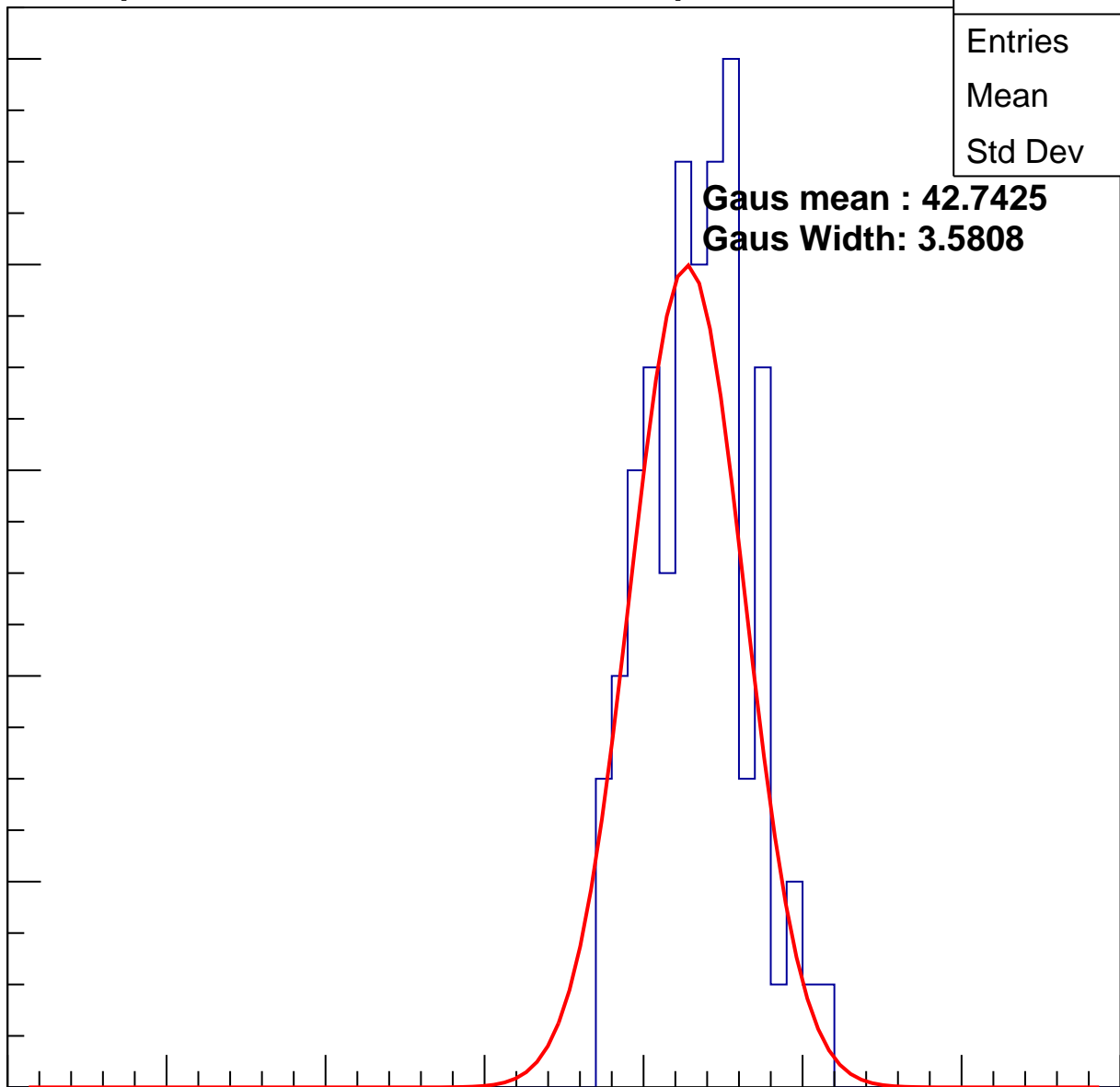
40

50

60

70

ampl

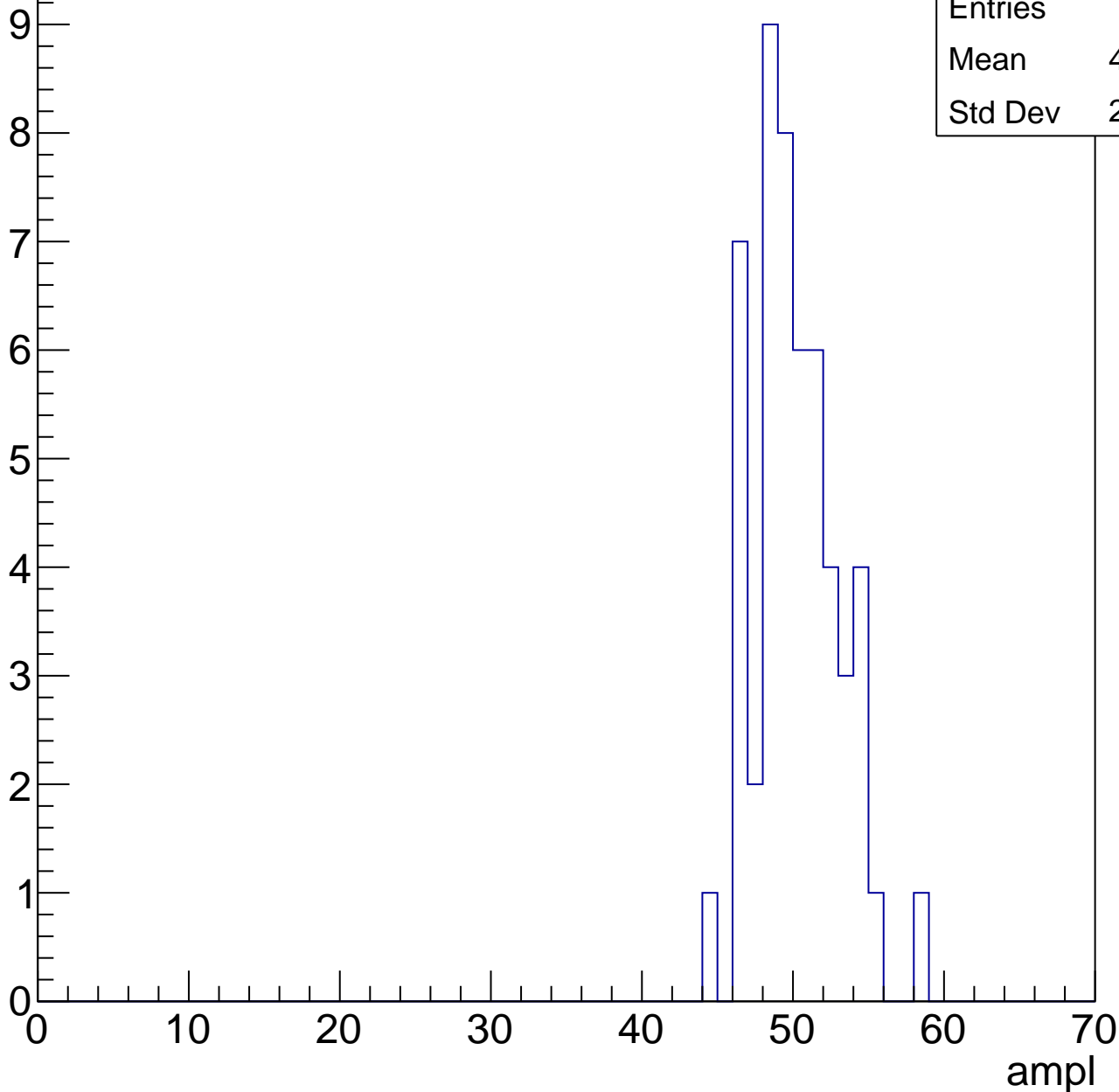


B0L002S, U7-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	49.73
Std Dev	2.802

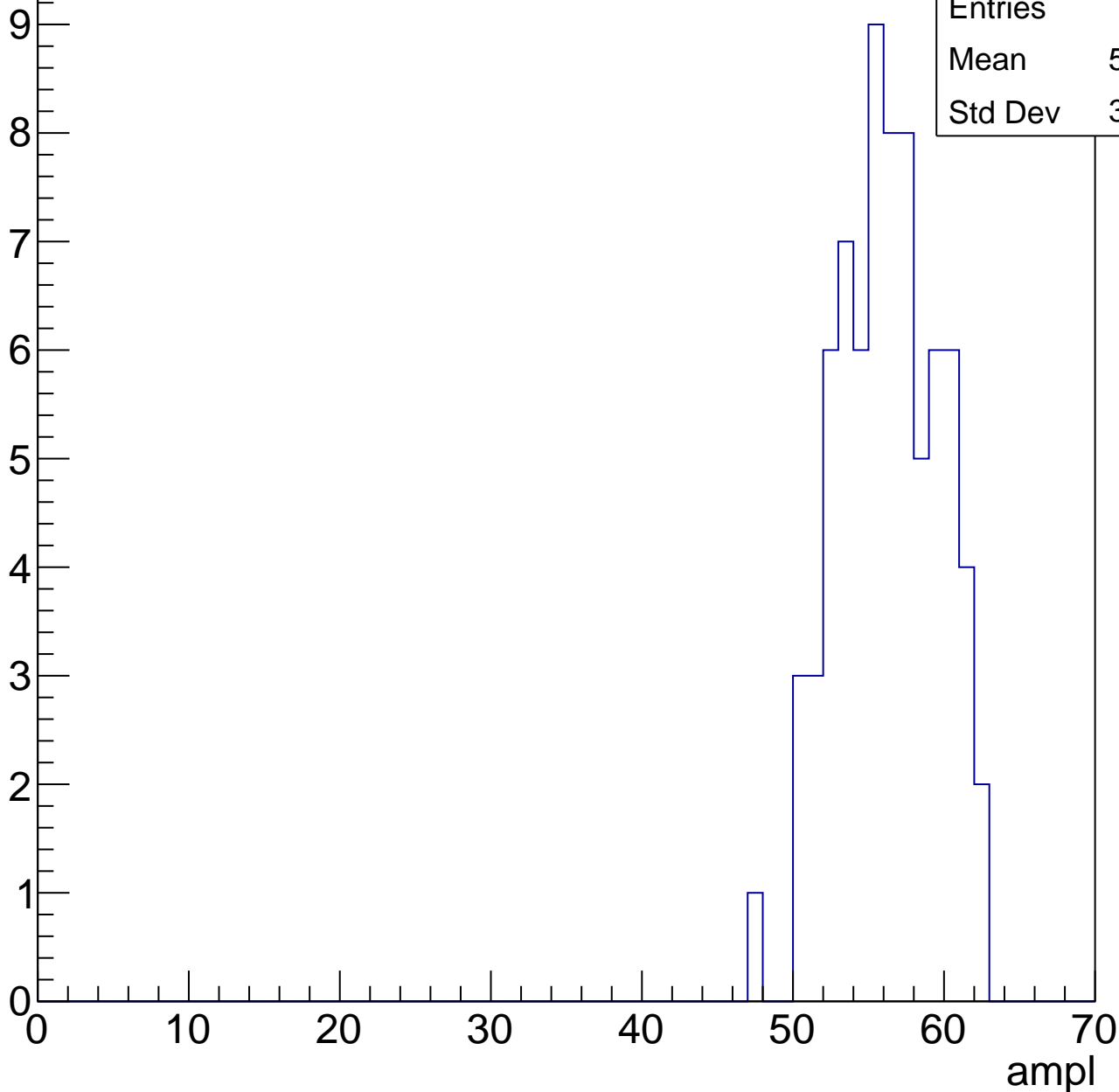


B0L002S, U7-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	55.78
Std Dev	3.293

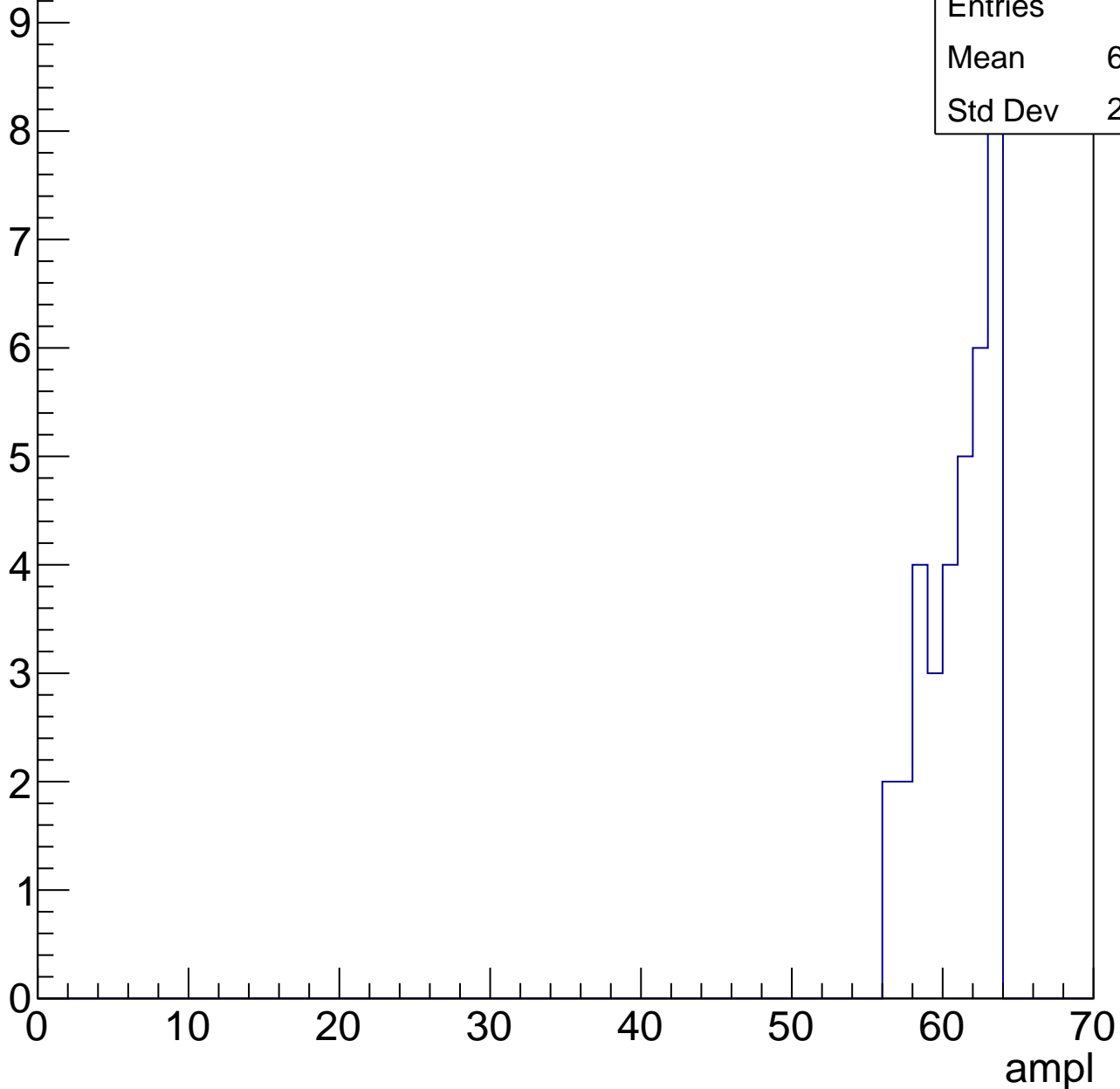


B0L002S, U7-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.54
Std Dev	2.195



B0L002S, U7-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

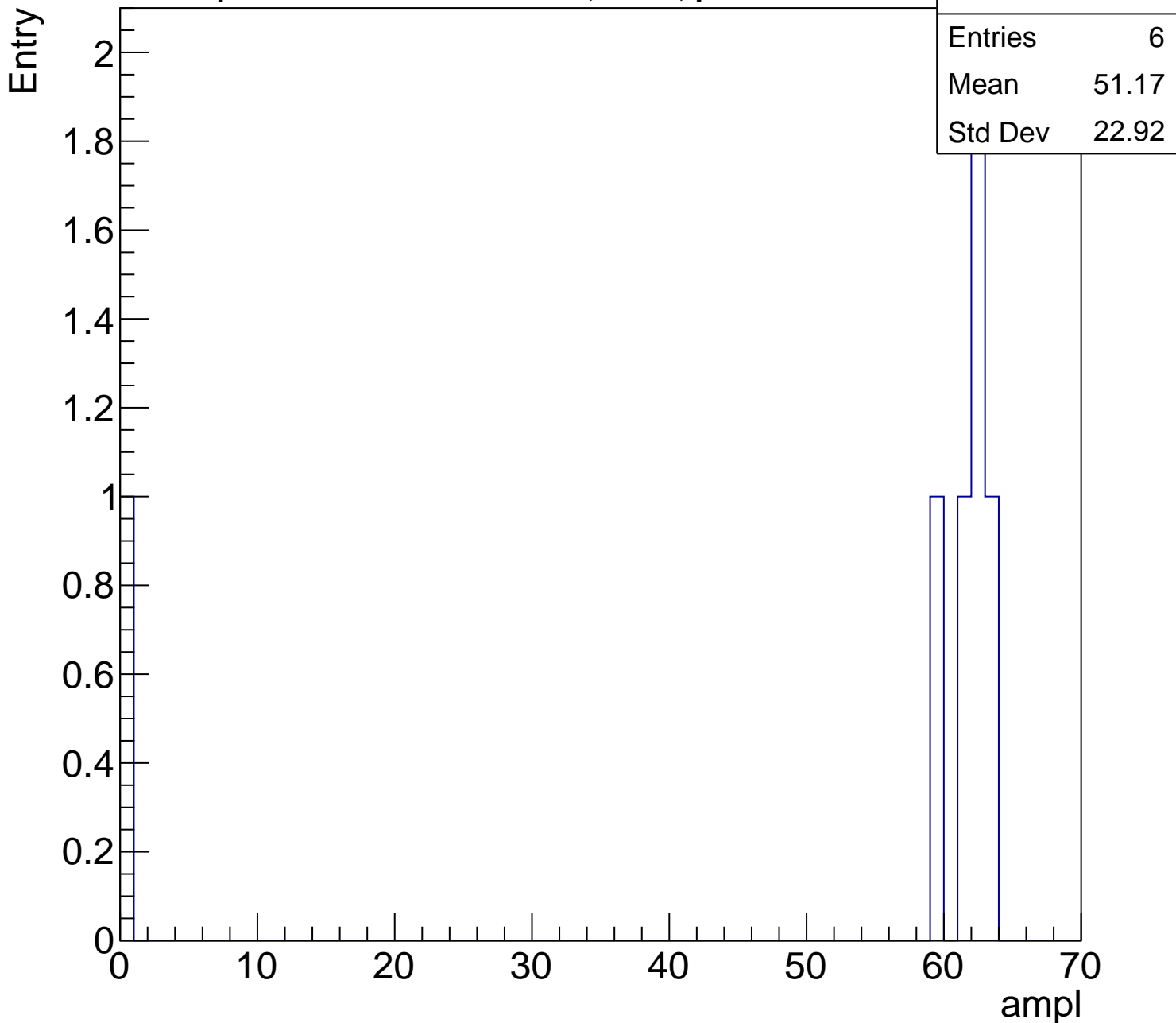
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.92

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch63, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	28.7
Std Dev	3.196

Gaus mean : 28.6200

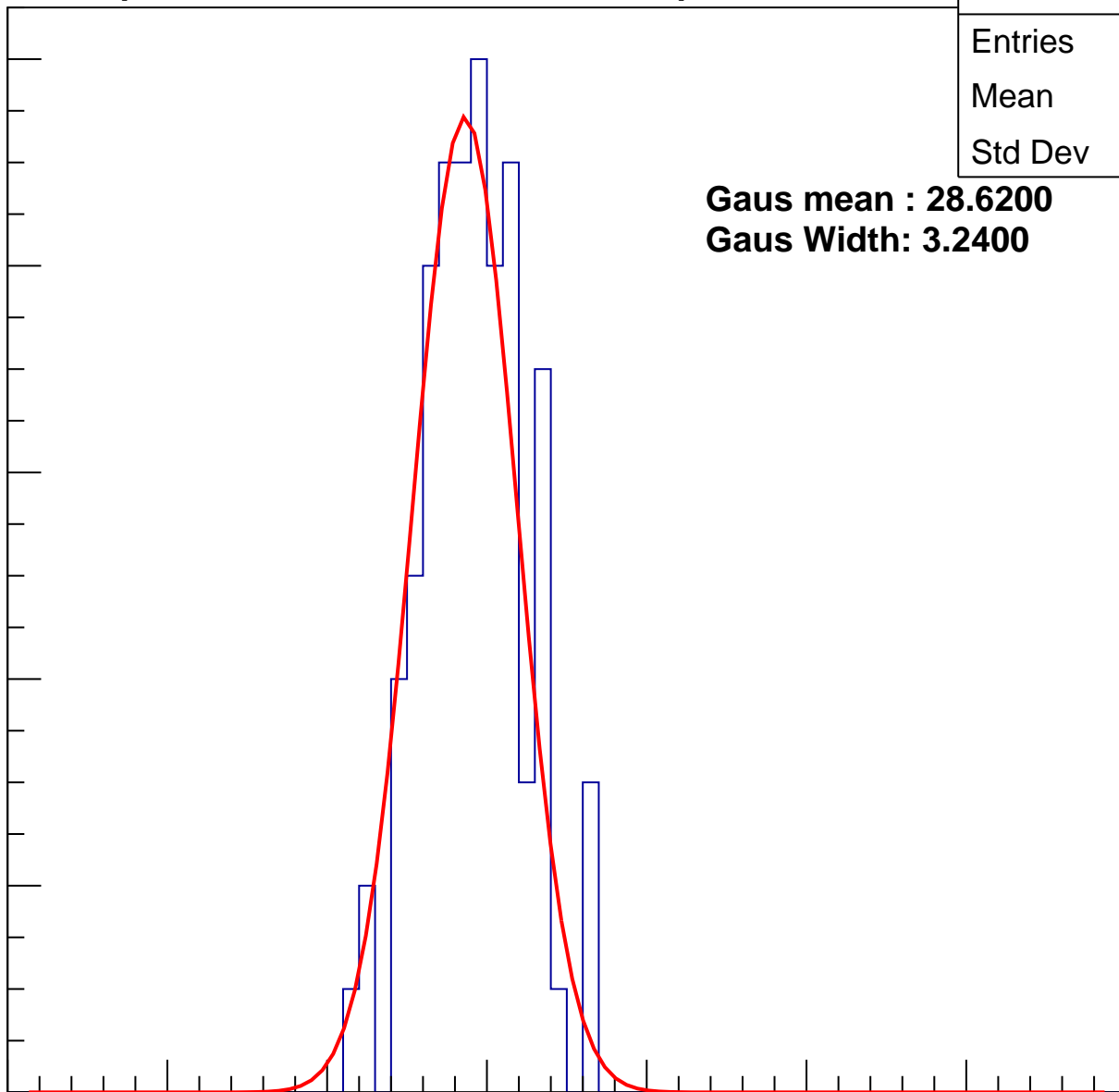
Gaus Width: 3.2400

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch63, adc1

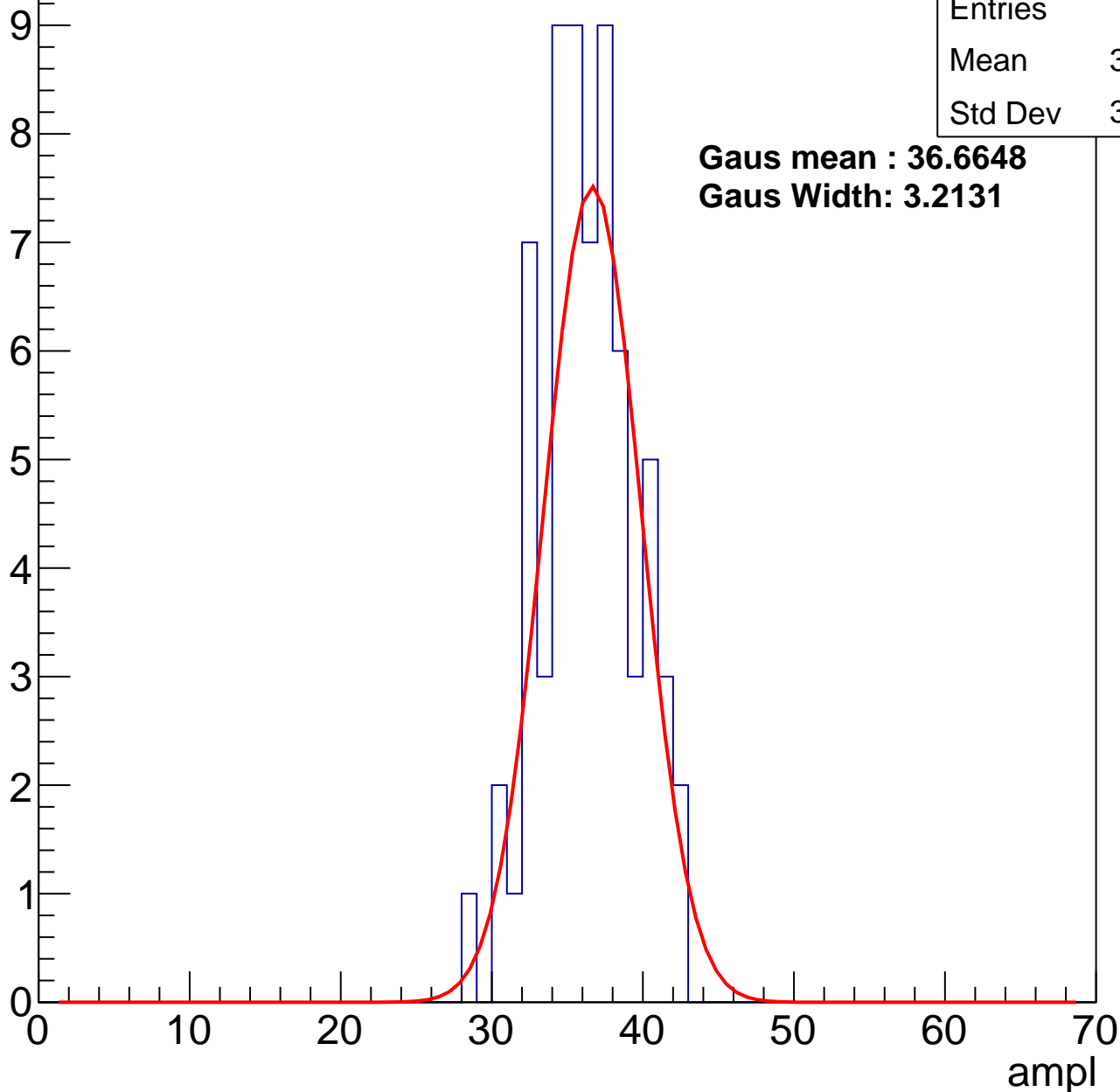
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.82
Std Dev	3.066

Gaus mean : 36.6648

Gaus Width: 3.2131



B0L002S, U7-ch63, adc2

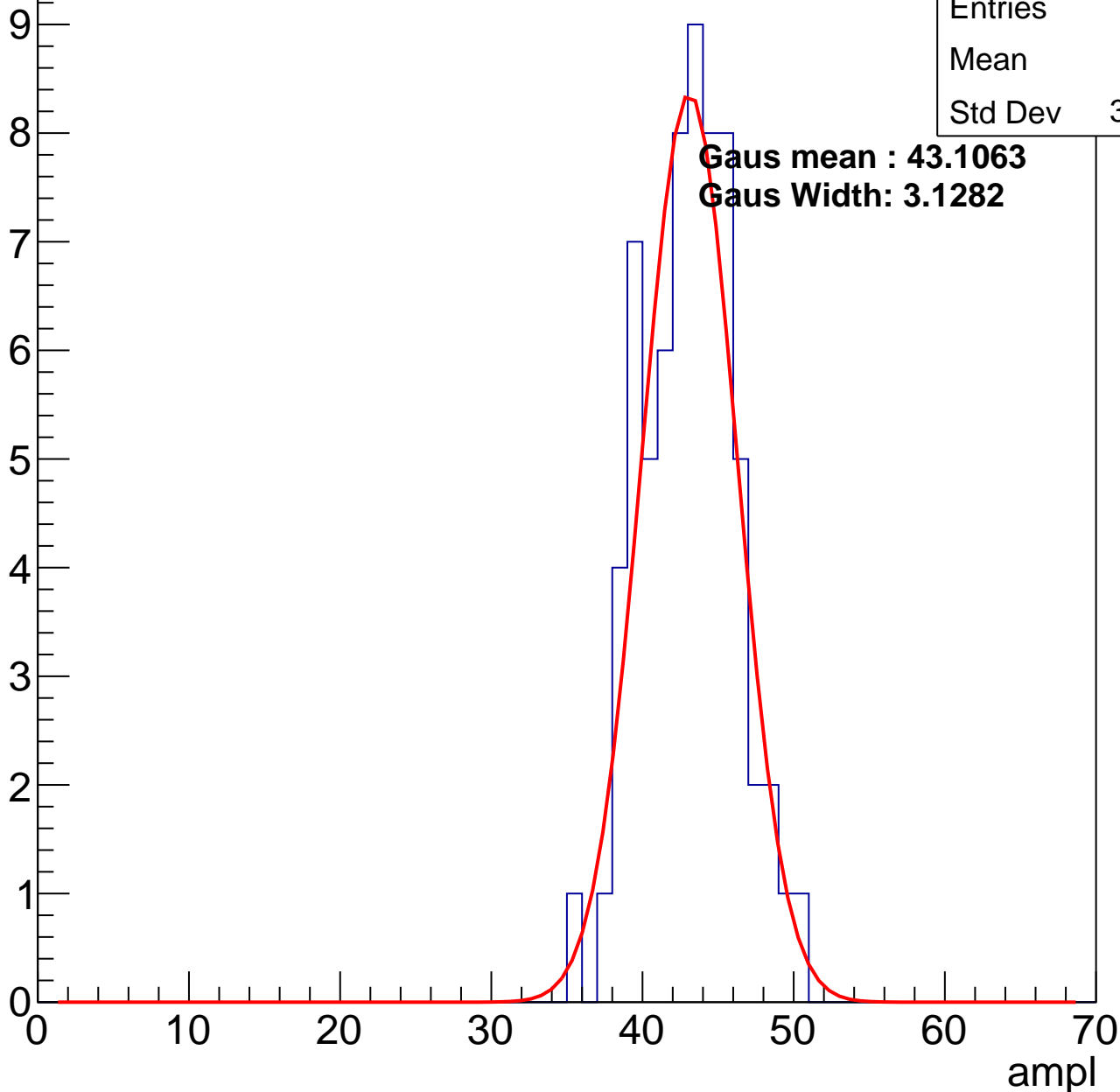
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.6
Std Dev	3.044

Gaus mean : 43.1063

Gaus Width: 3.1282

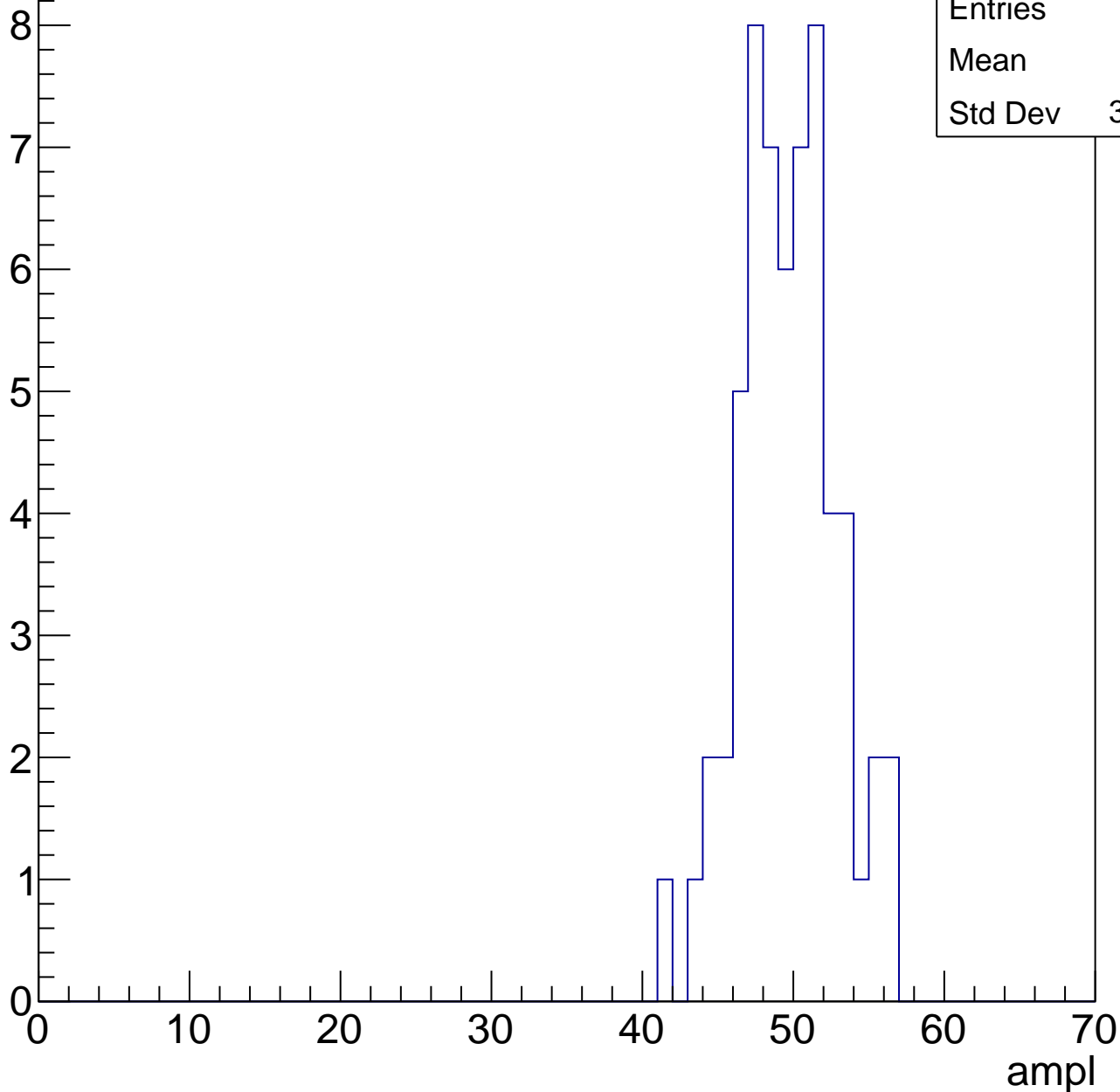


B0L002S, U7-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

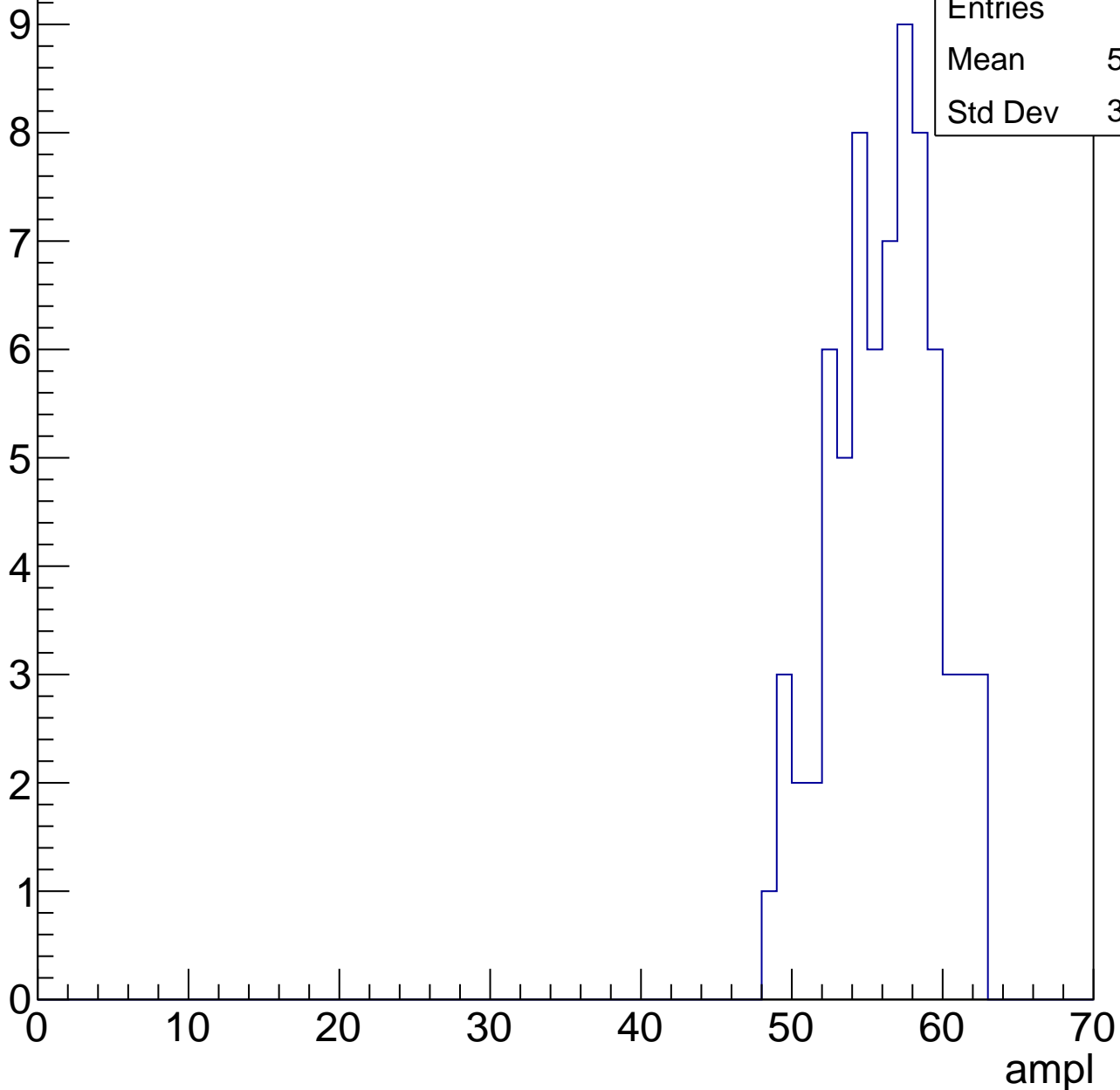
Entries	60
Mean	49.2
Std Dev	3.156



B0L002S, U7-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	72
Mean	55.67
Std Dev	3.395

B0L002S, U7-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	36
Mean	58.36
Std Dev	10.18

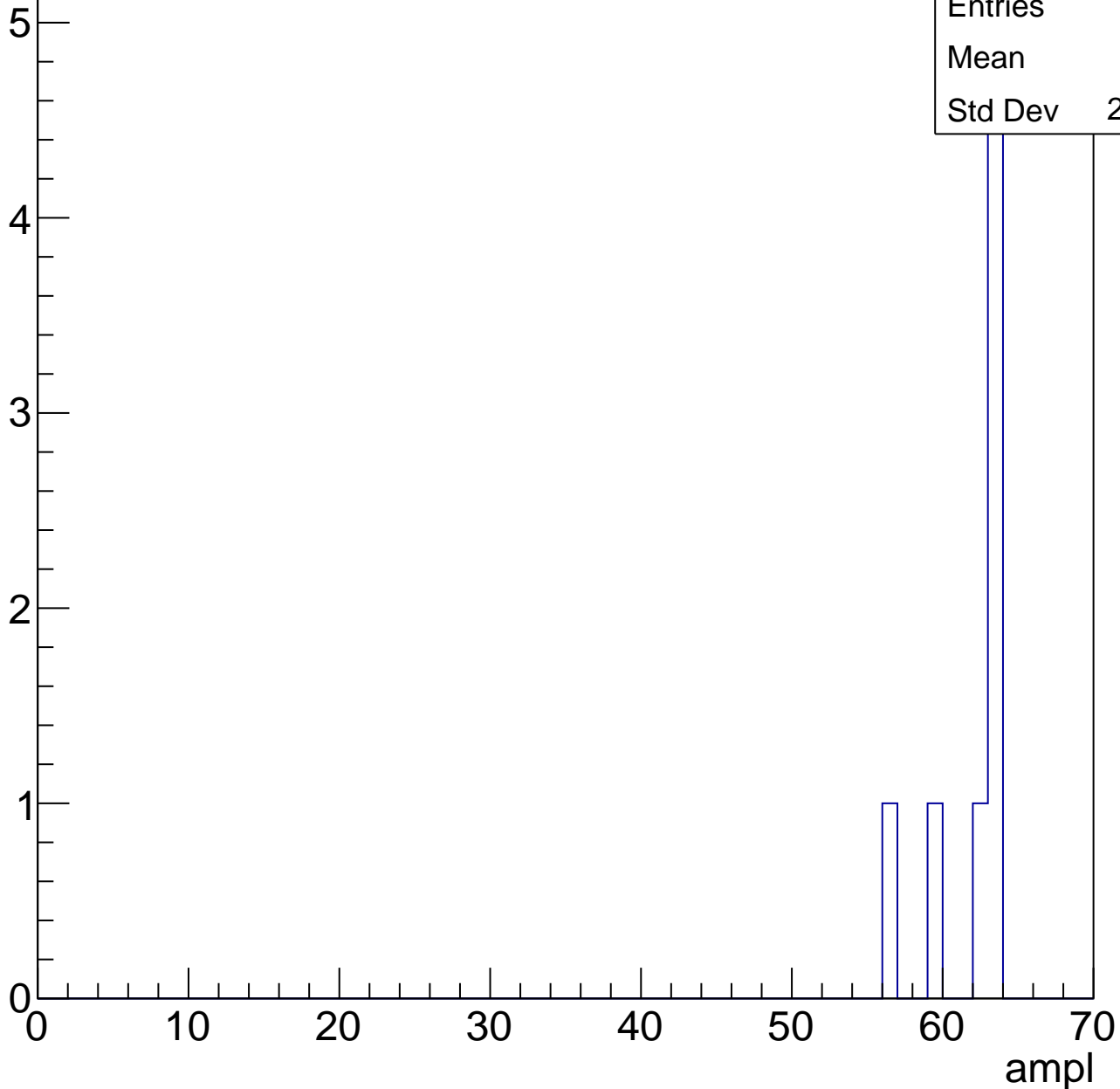
ampl

B0L002S, U7-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	61.5
Std Dev	2.449



B0L002S, U7-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch64, adc0

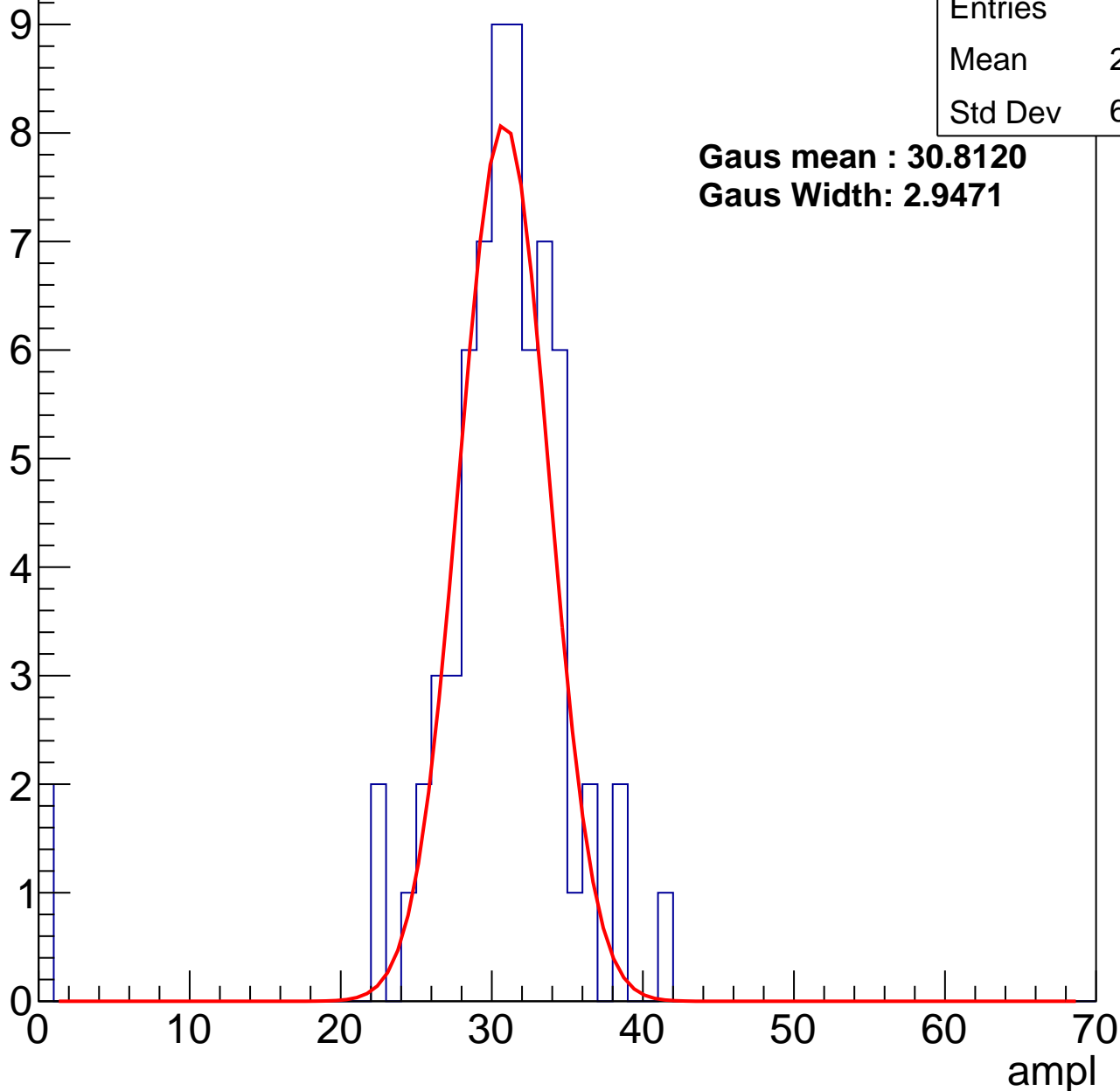
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.68
Std Dev	6.198

Gaus mean : 30.8120

Gaus Width: 2.9471



B0L002S, U7-ch64, adc1

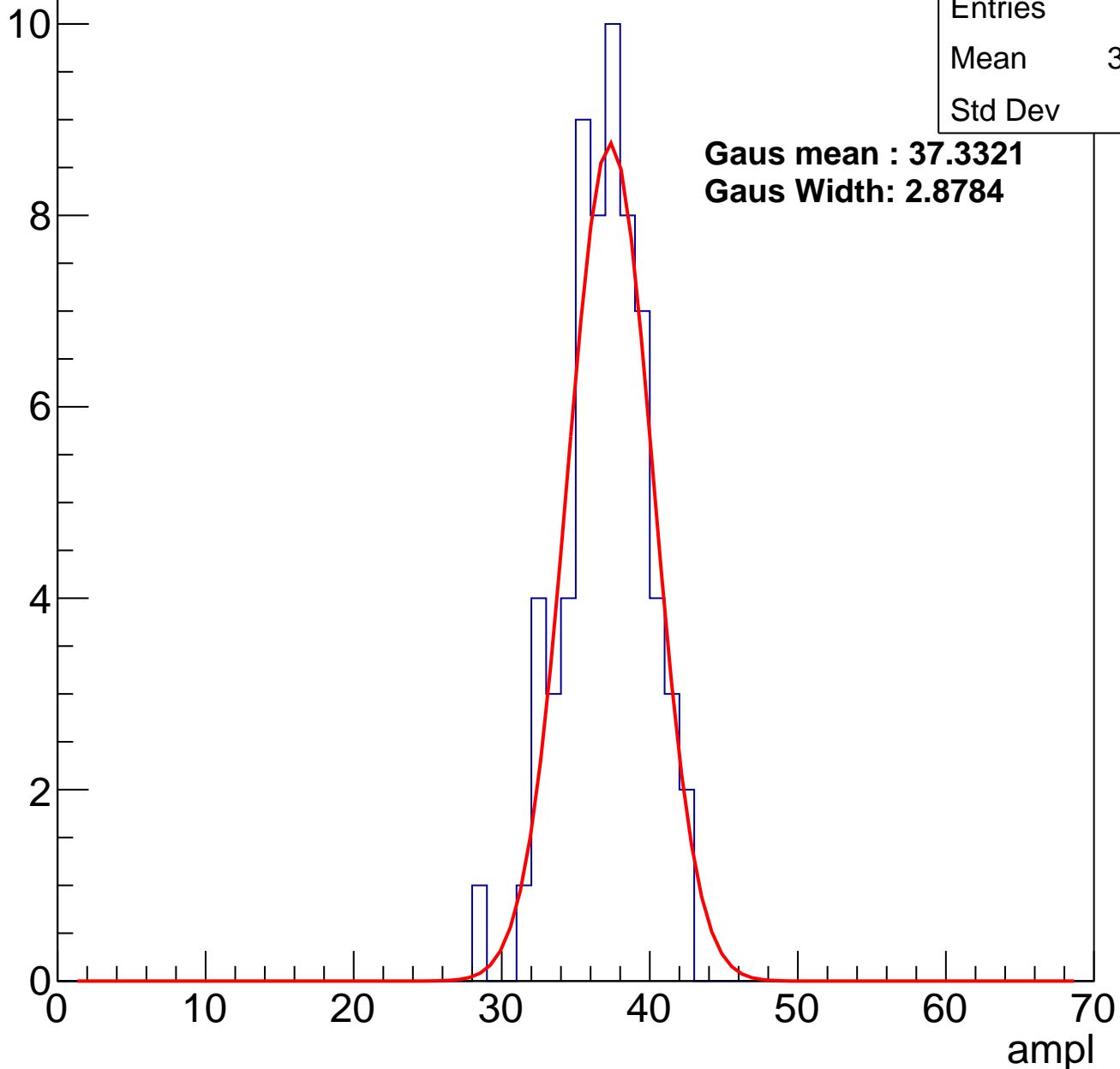
calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	36.55
Std Dev	2.8

Gaus mean : 37.3321

Gaus Width: 2.8784

Entry



B0L002S, U7-ch64, adc2

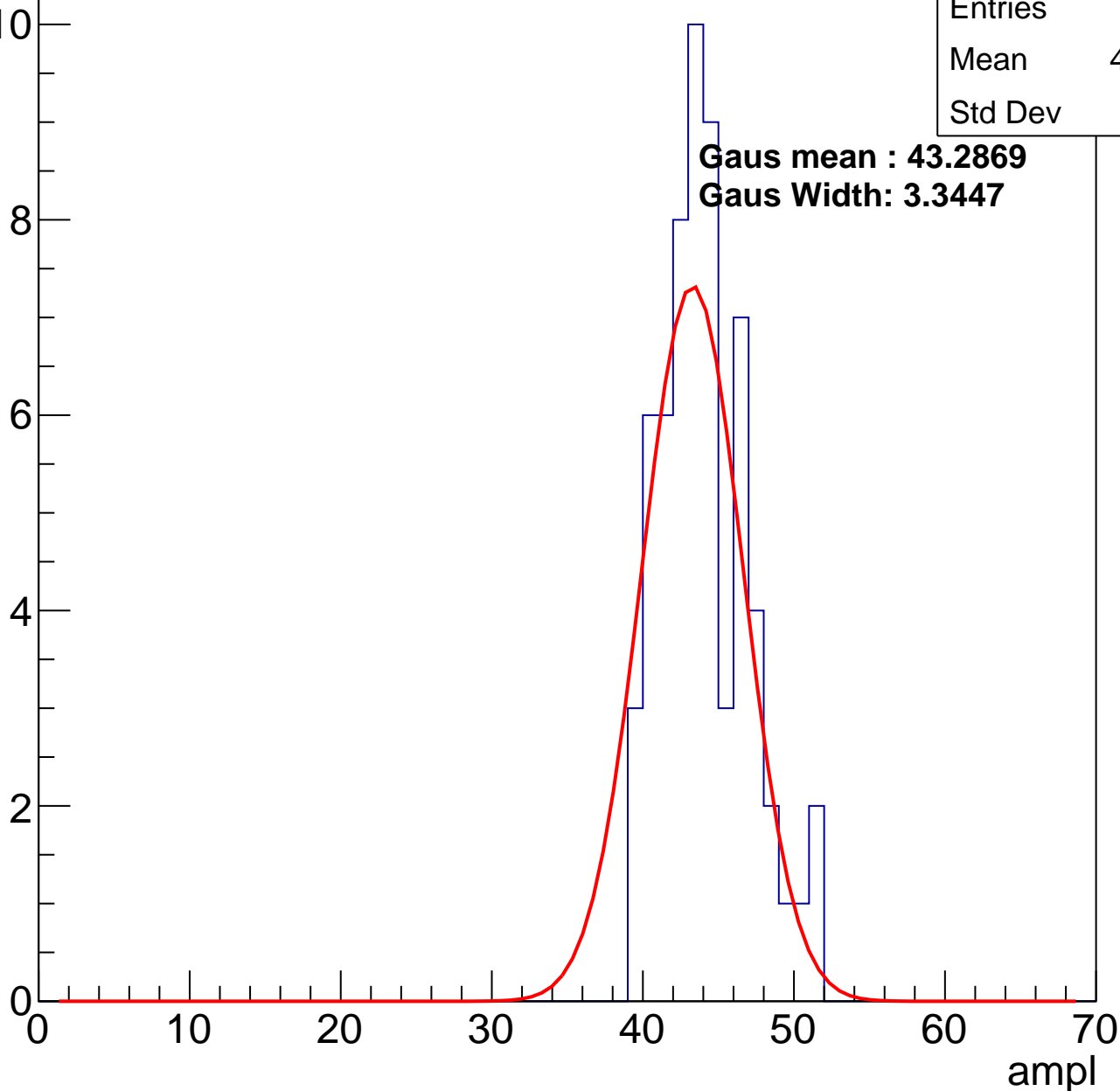
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.66
Std Dev	2.89

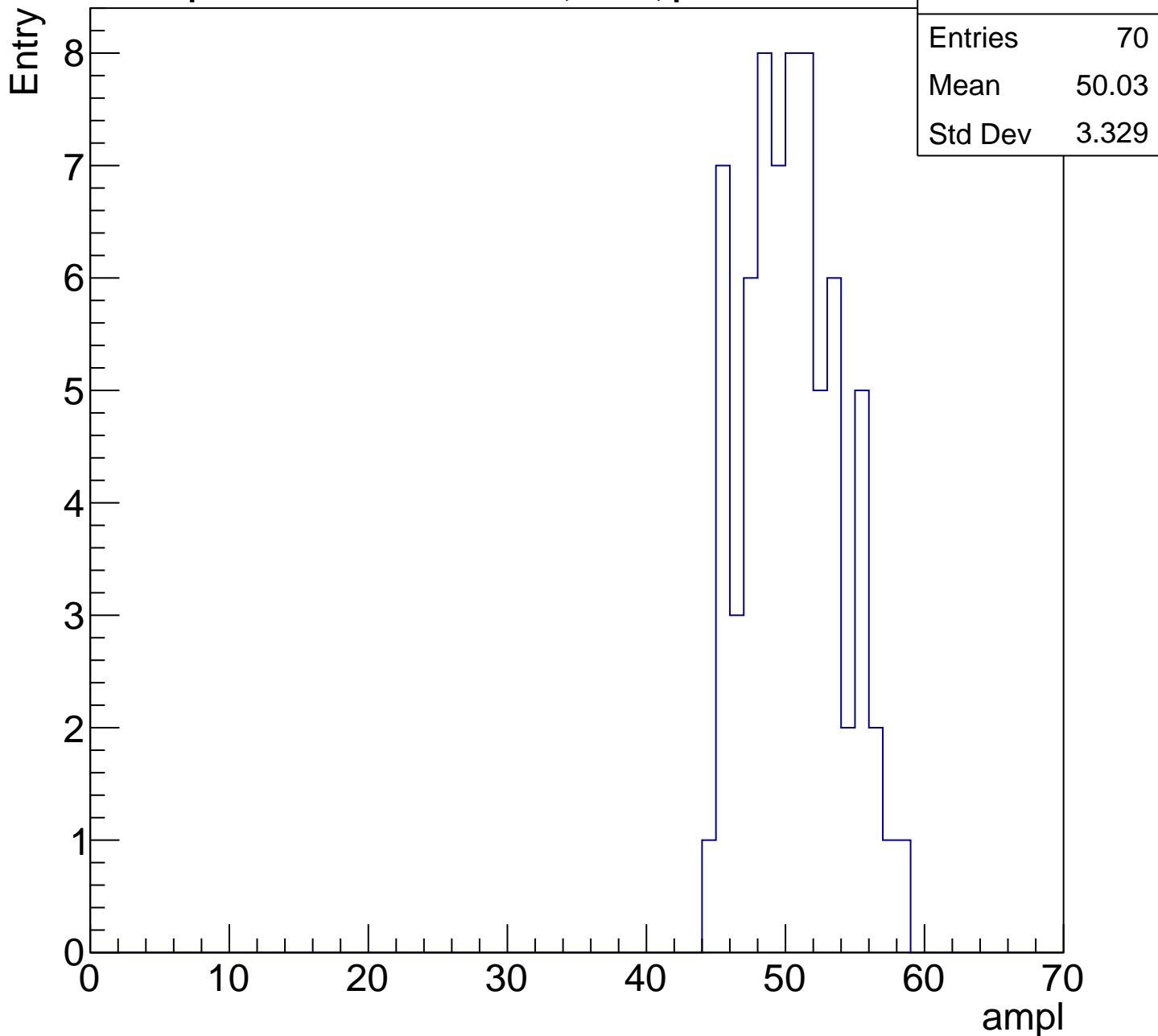
Gaus mean : 43.2869

Gaus Width: 3.3447



B0L002S, U7-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

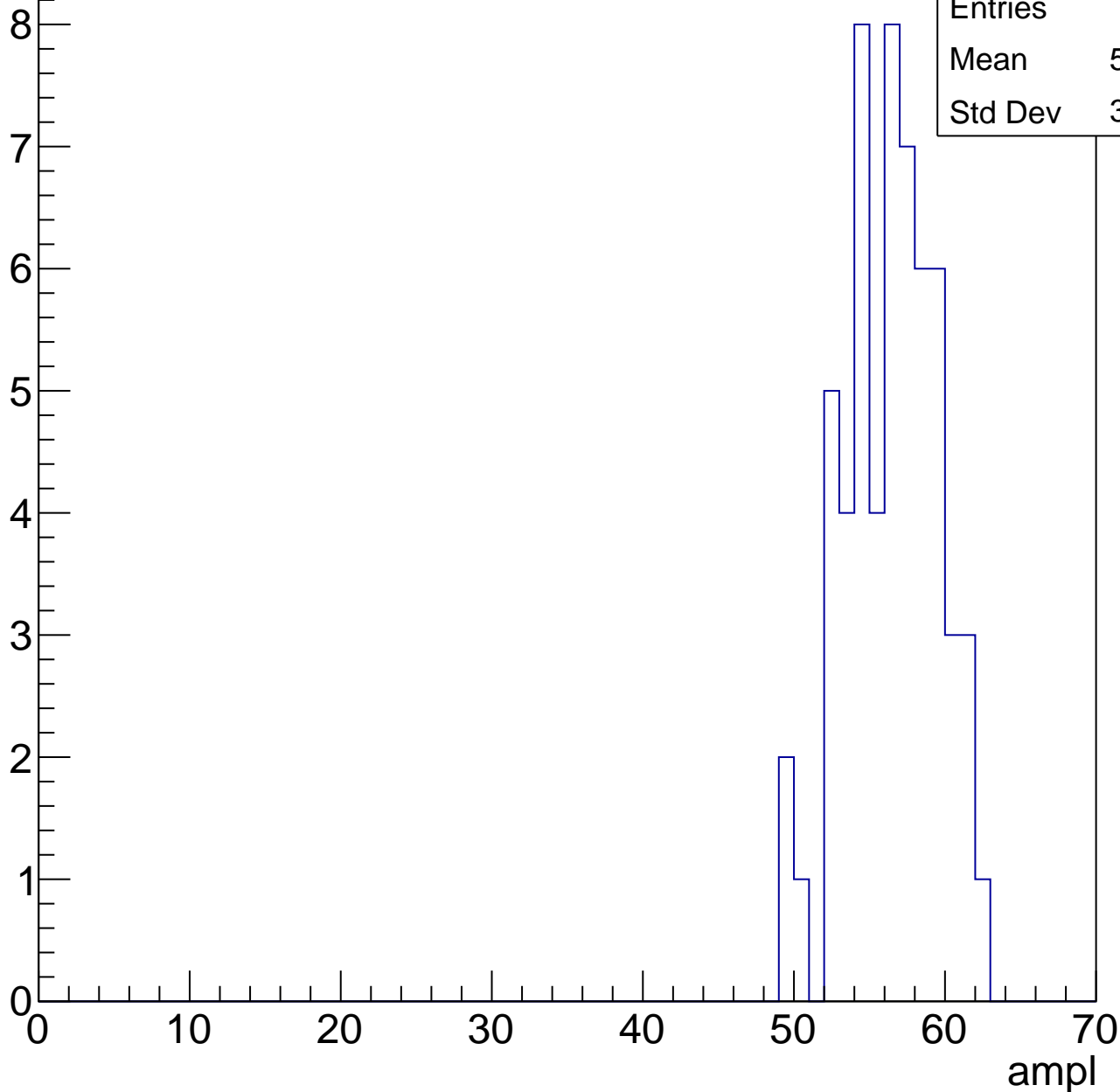


B0L002S, U7-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.97
Std Dev	3.023

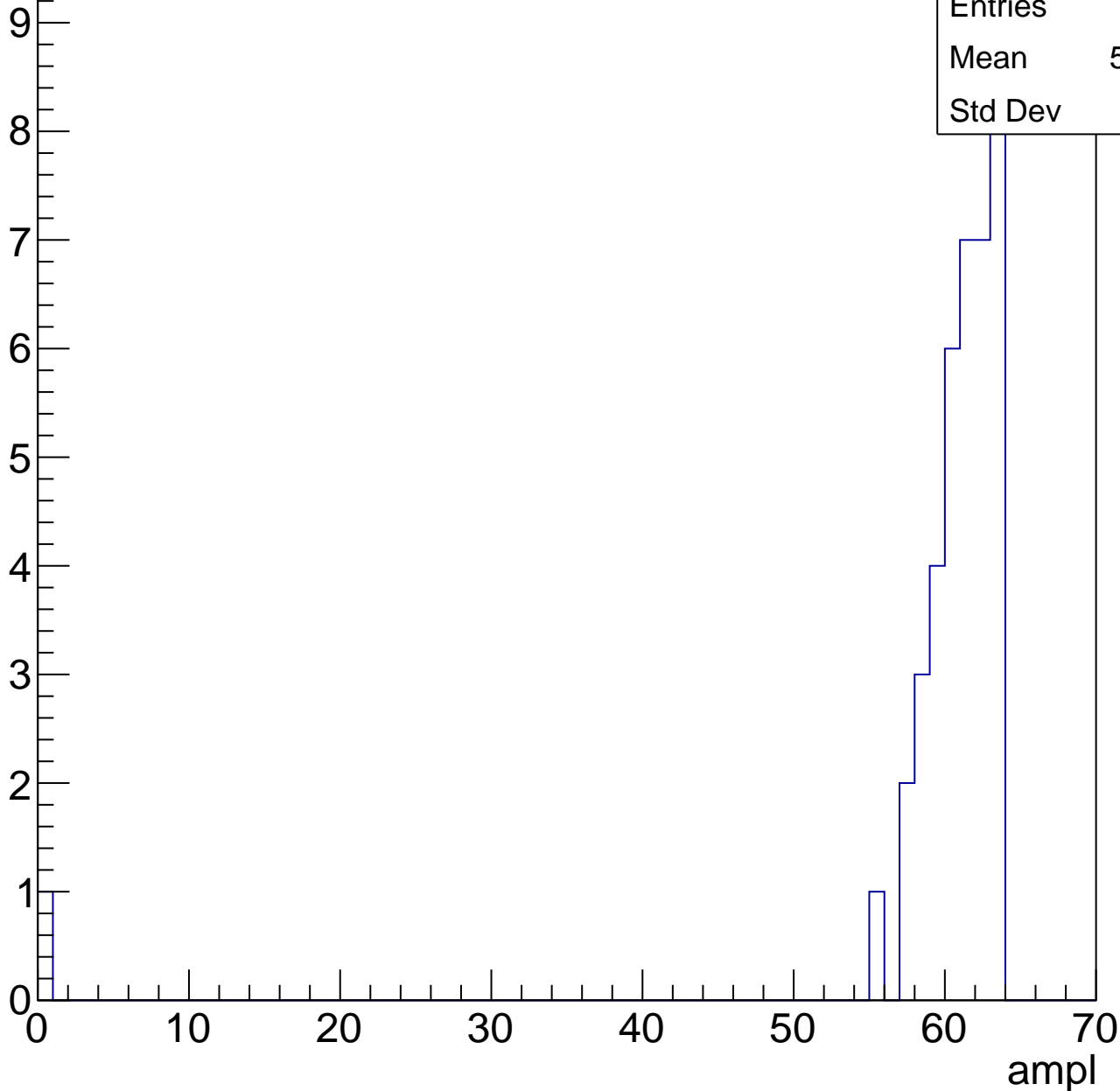


B0L002S, U7-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.17
Std Dev	9.68



B0L002S, U7-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

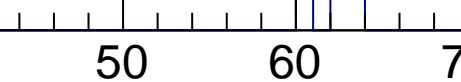
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch65, adc0

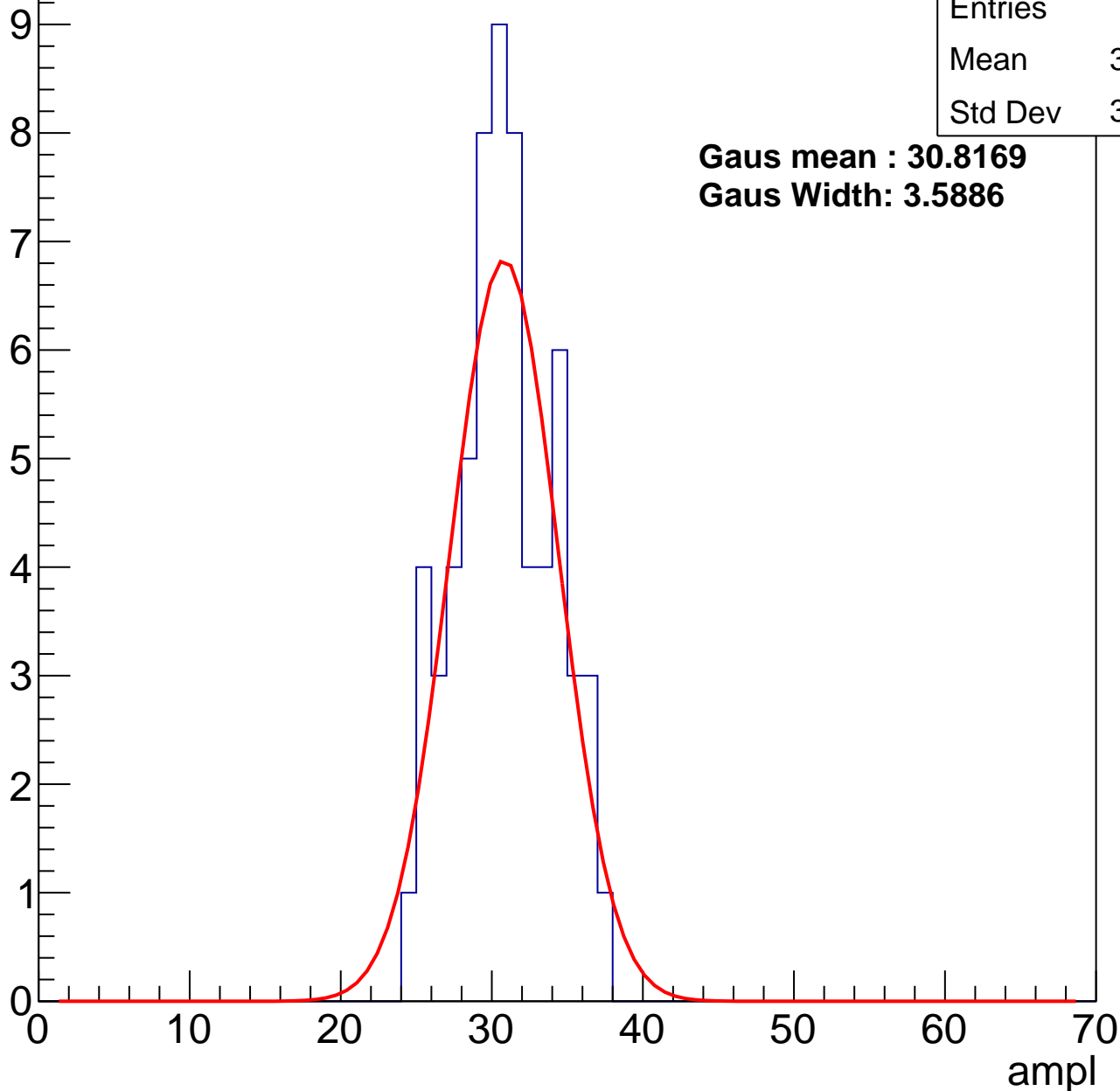
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.38
Std Dev	3.154

Gaus mean : 30.8169

Gaus Width: 3.5886



B0L002S, U7-ch65, adc1

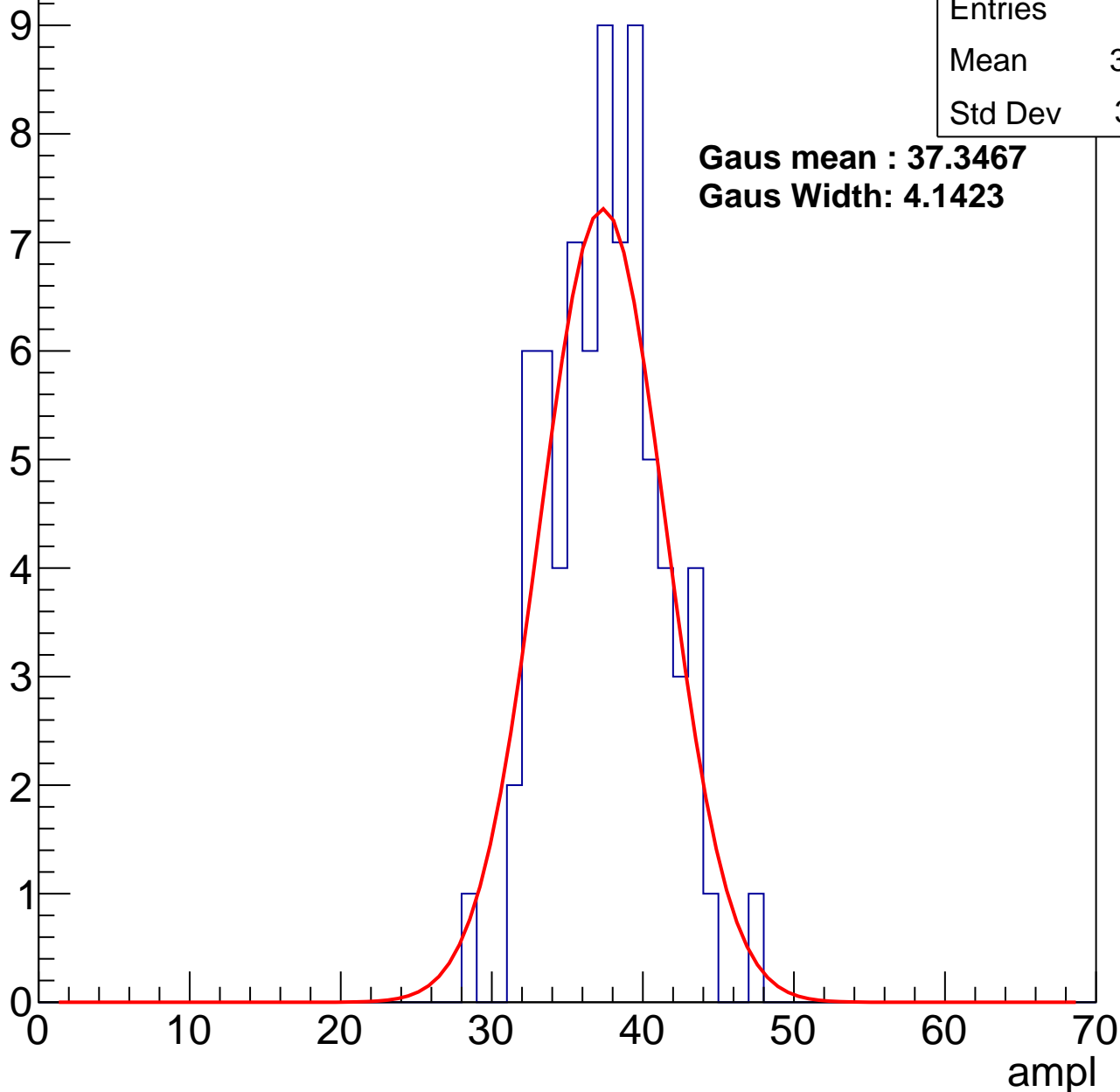
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.07
Std Dev	3.631

Gaus mean : 37.3467

Gaus Width: 4.1423



B0L002S, U7-ch65, adc2

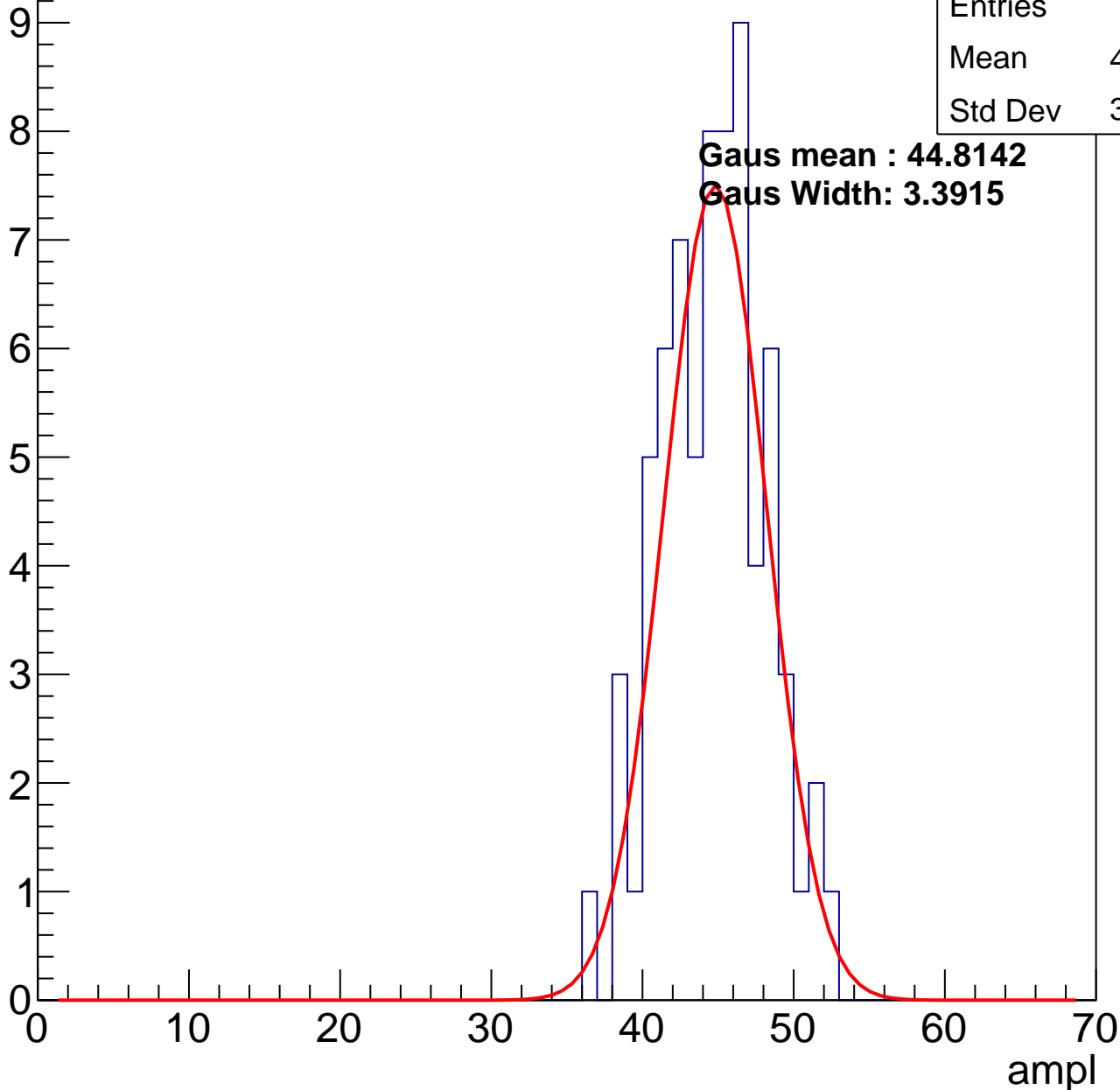
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.24
Std Dev	3.399

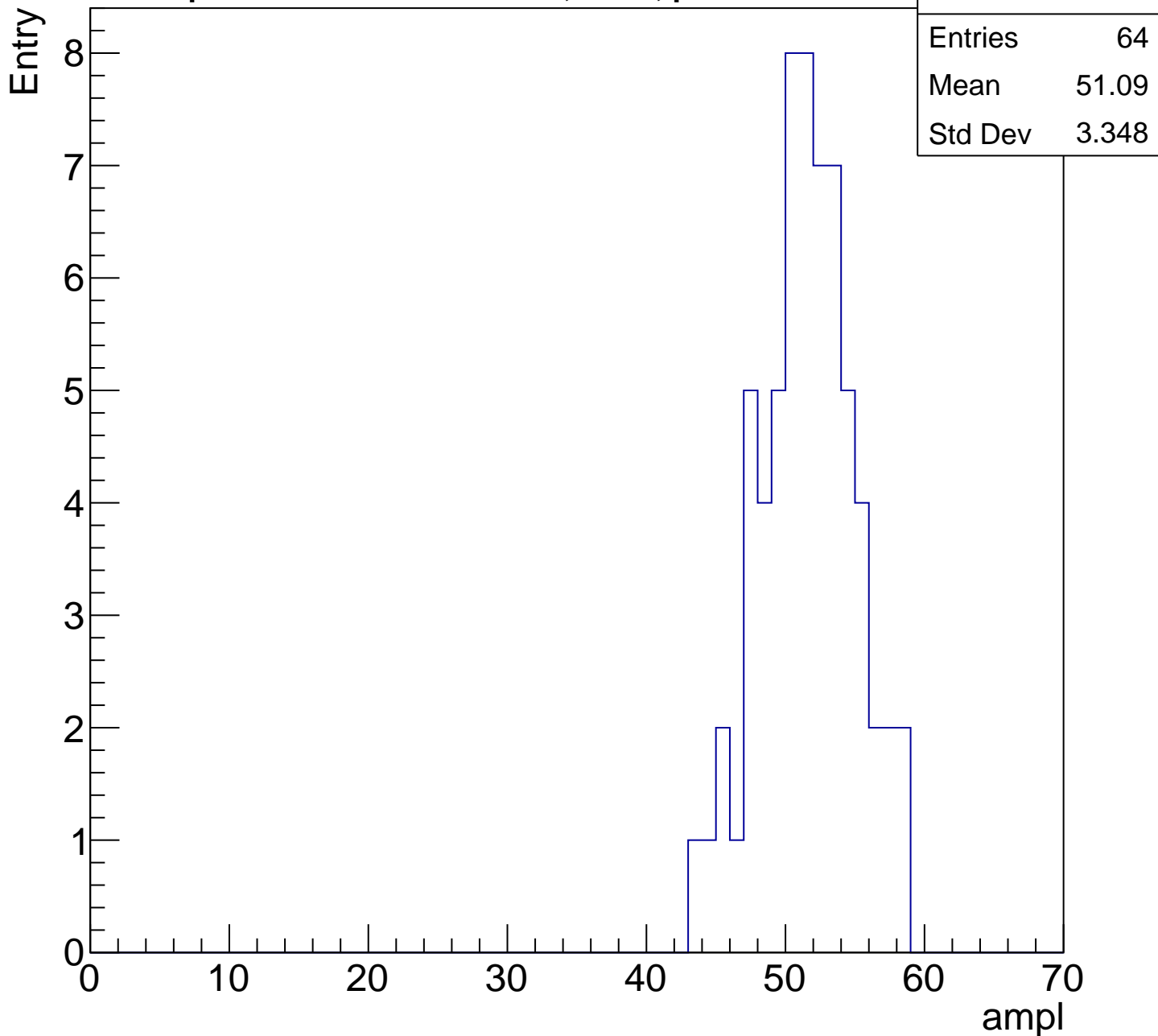
Gaus mean : 44.8142

Gaus Width: 3.3915



B0L002S, U7-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

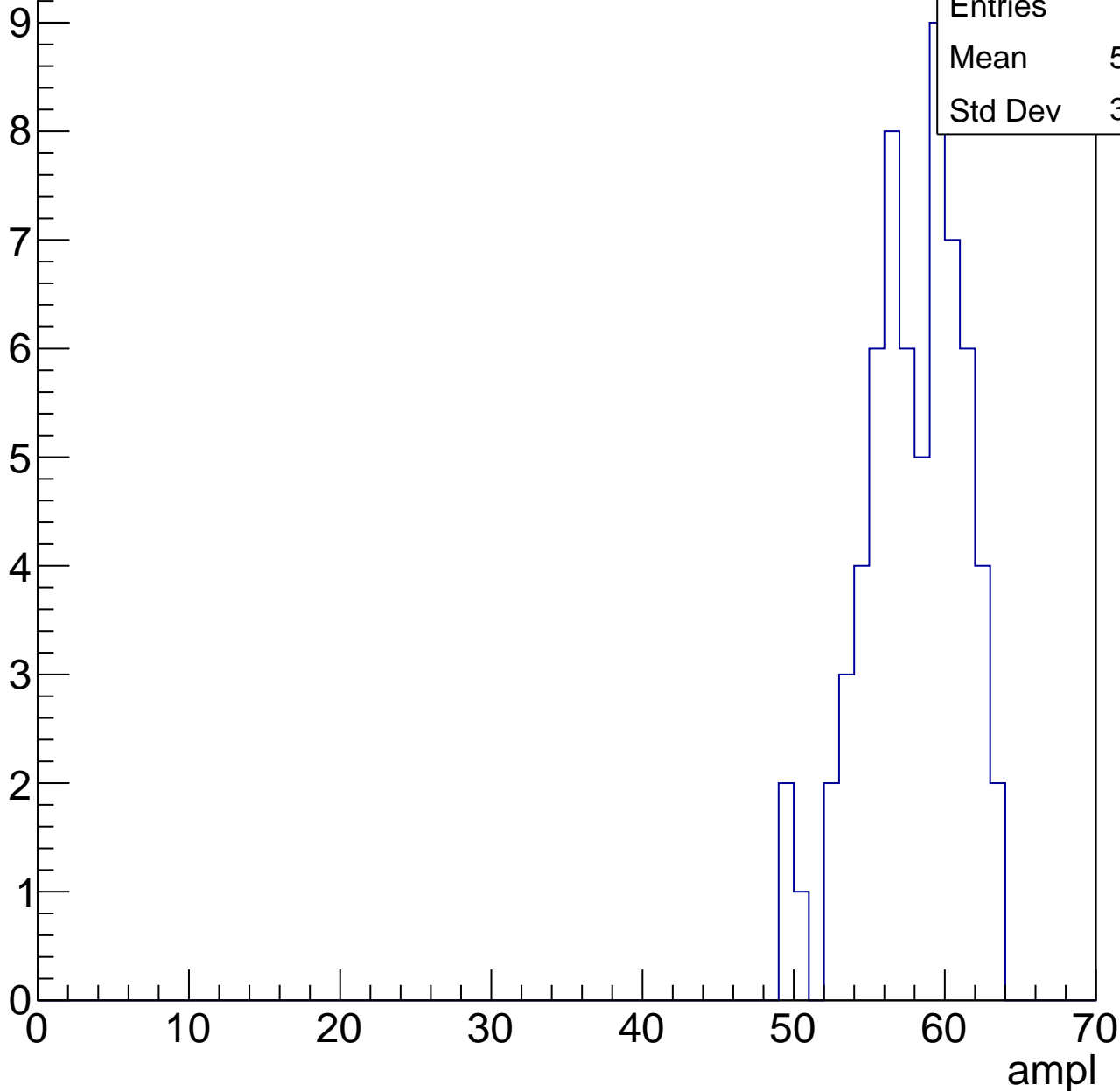


B0L002S, U7-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	57.35
Std Dev	3.298

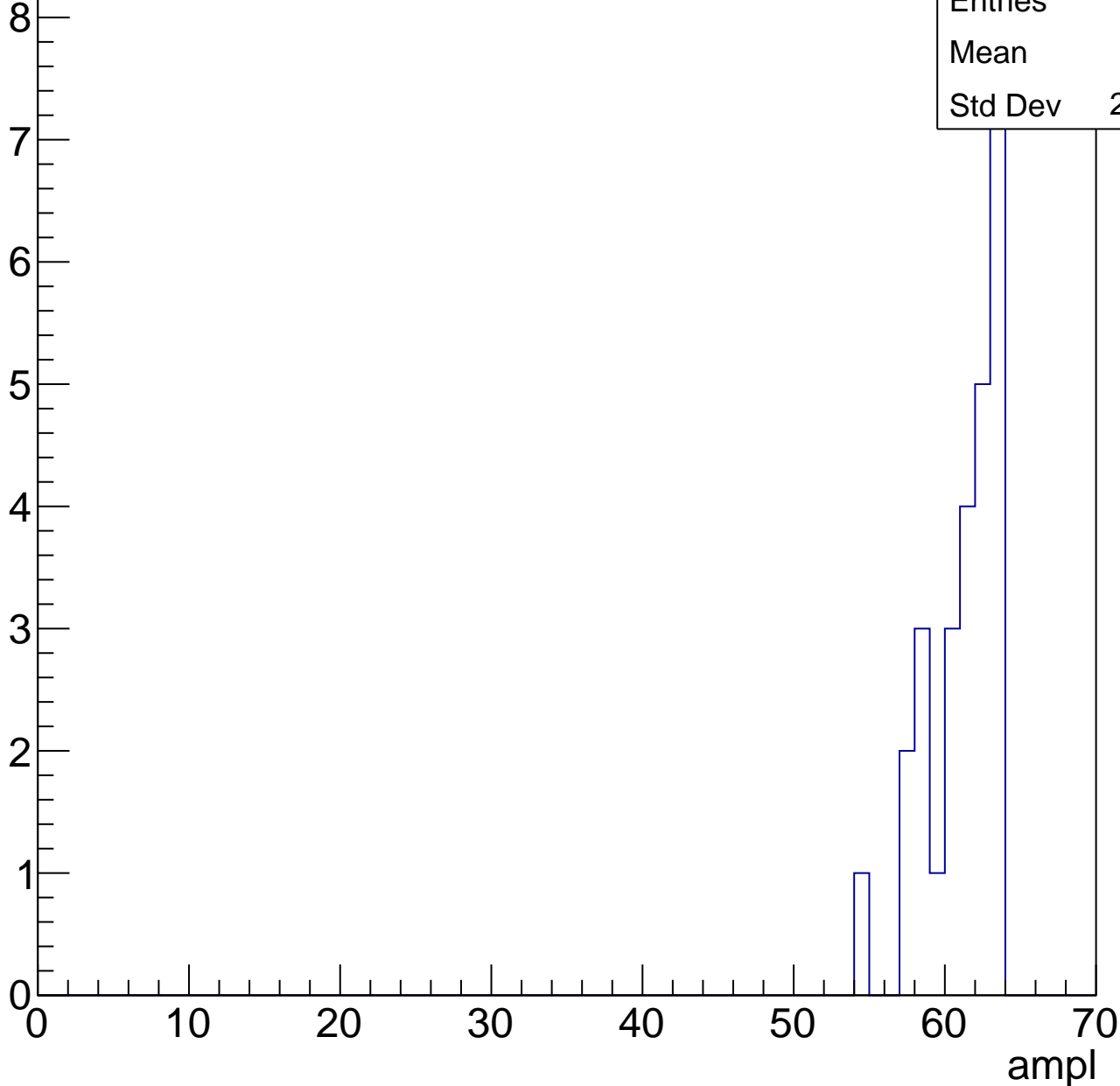


B0L002S, U7-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	60.7
Std Dev	2.354



B0L002S, U7-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U7-ch66, adc0

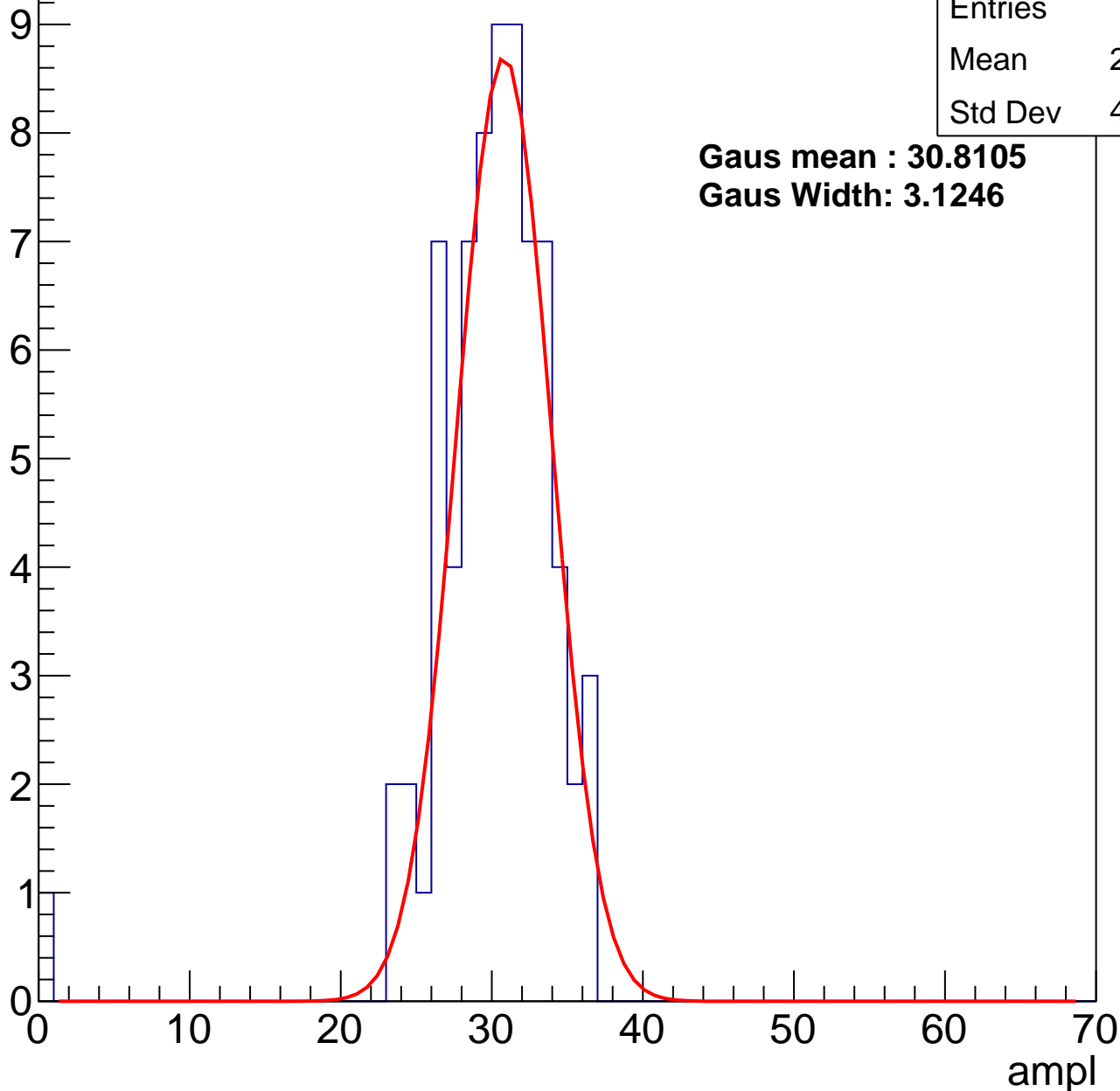
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.52
Std Dev	4.659

Gaus mean : 30.8105

Gaus Width: 3.1246



B0L002S, U7-ch66, adc1

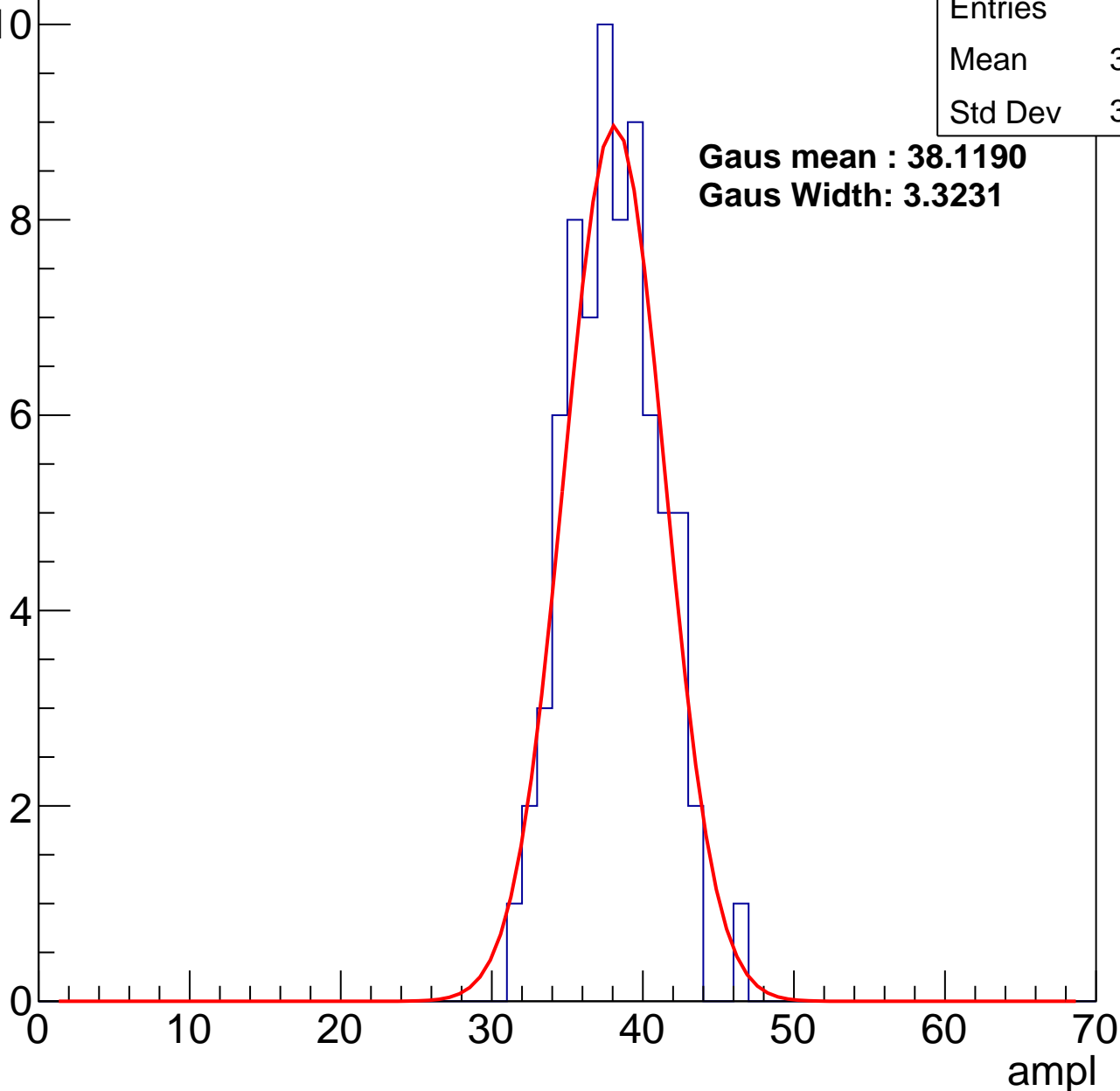
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.56
Std Dev	3.007

Gaus mean : 38.1190

Gaus Width: 3.3231



B0L002S, U7-ch66, adc2

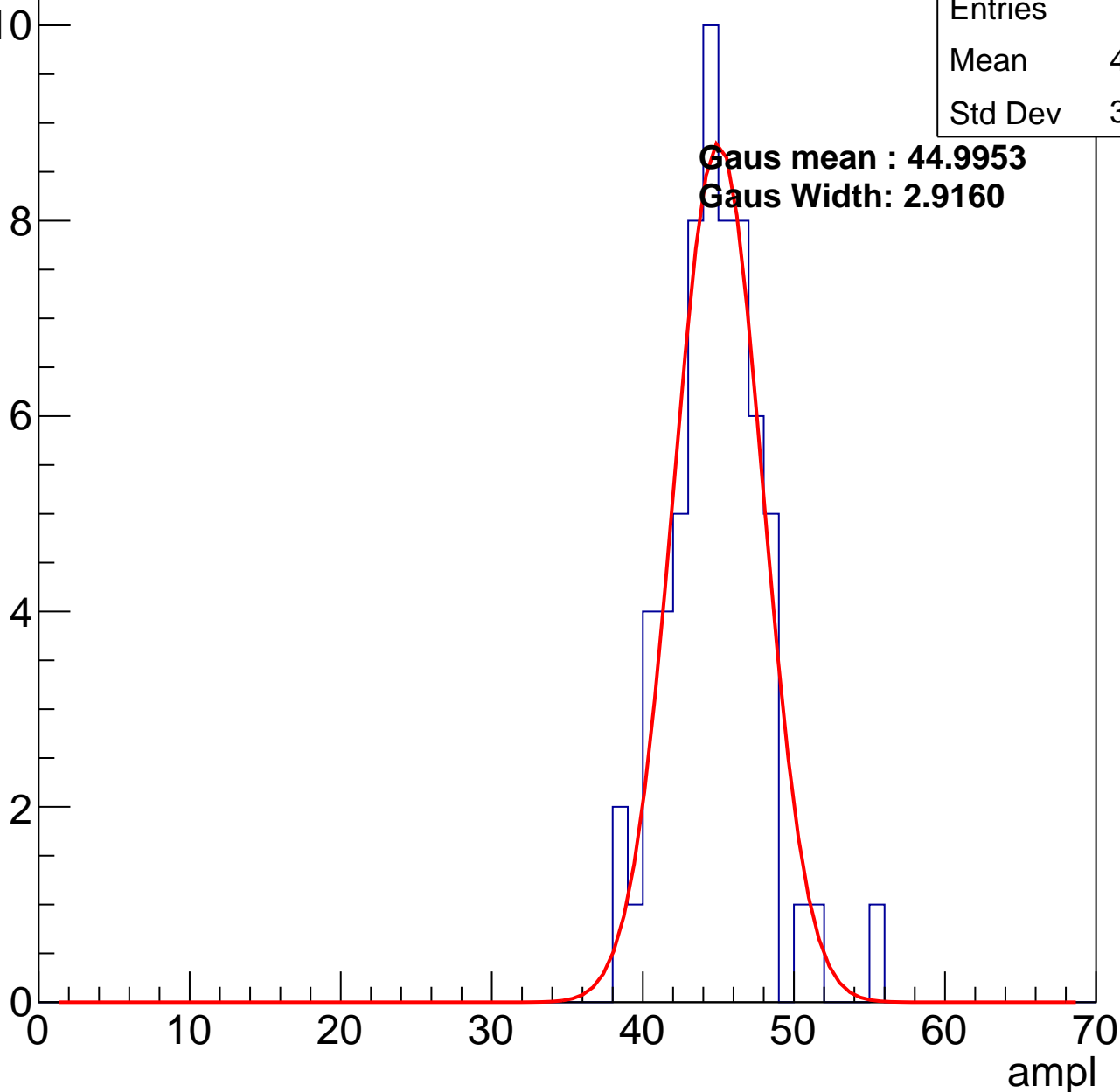
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.36
Std Dev	3.054

Gaus mean : 44.9953

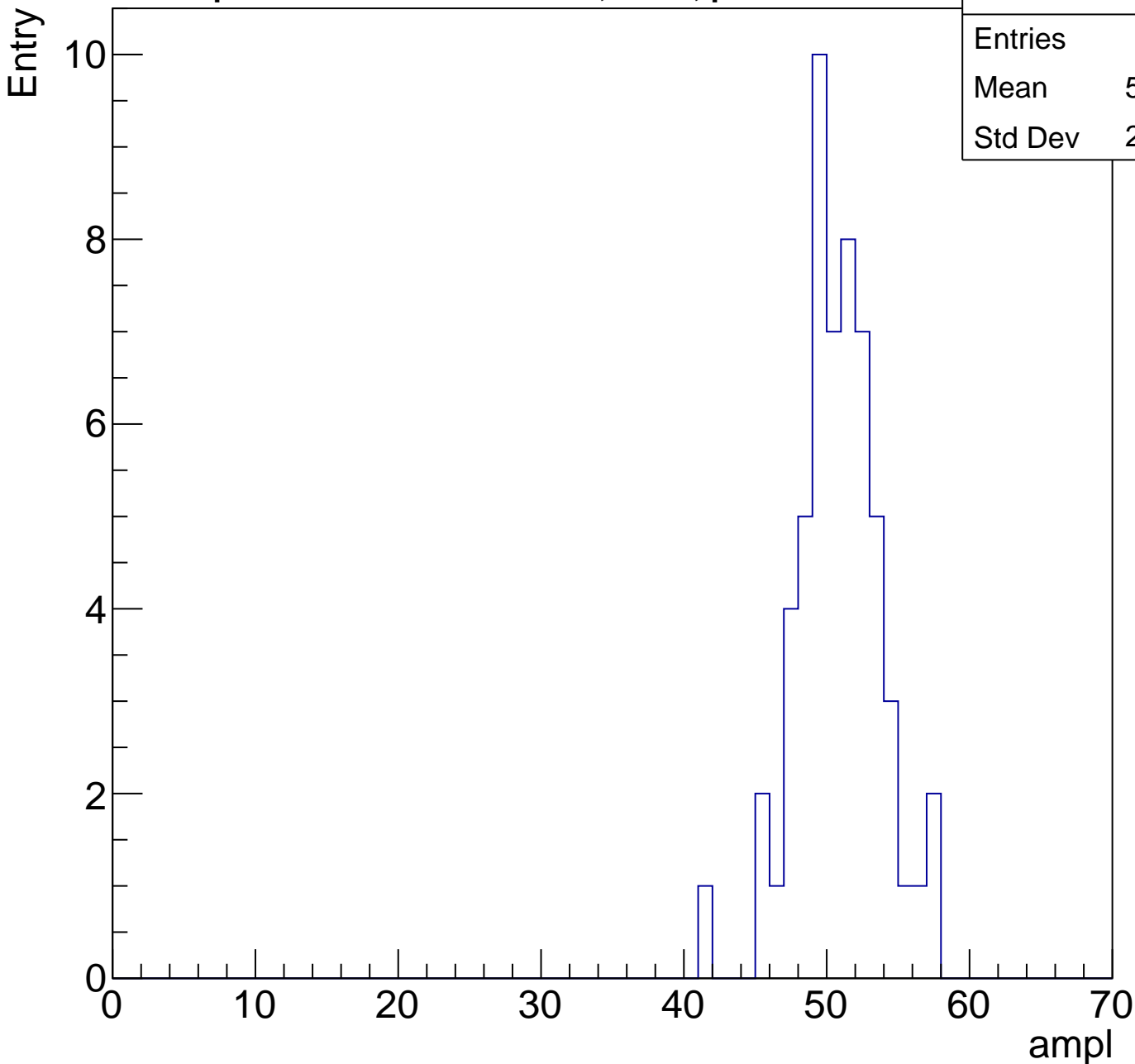
Gaus Width: 2.9160



B0L002S, U7-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	50.33
Std Dev	2.946

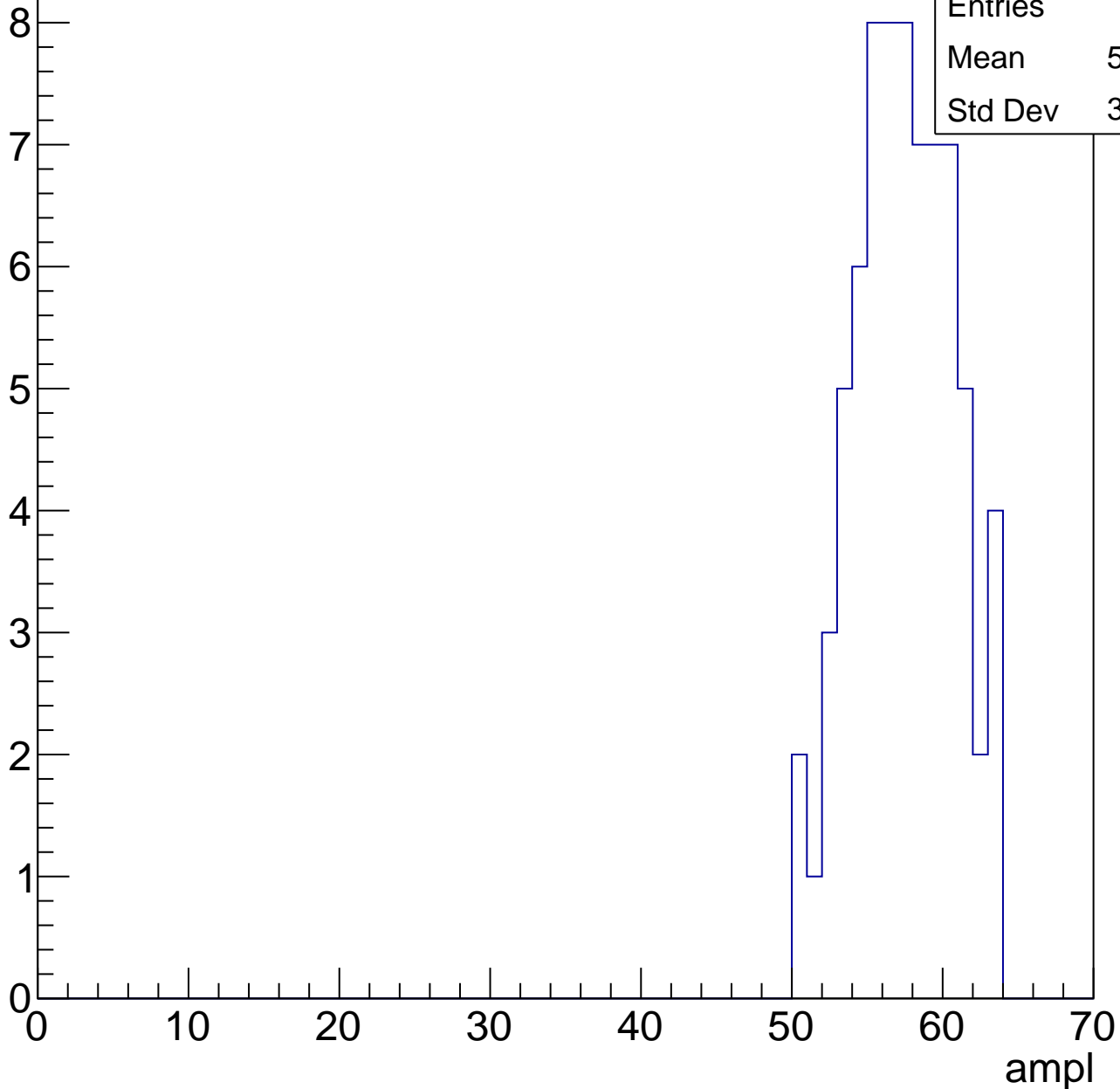


B0L002S, U7-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	56.99
Std Dev	3.216

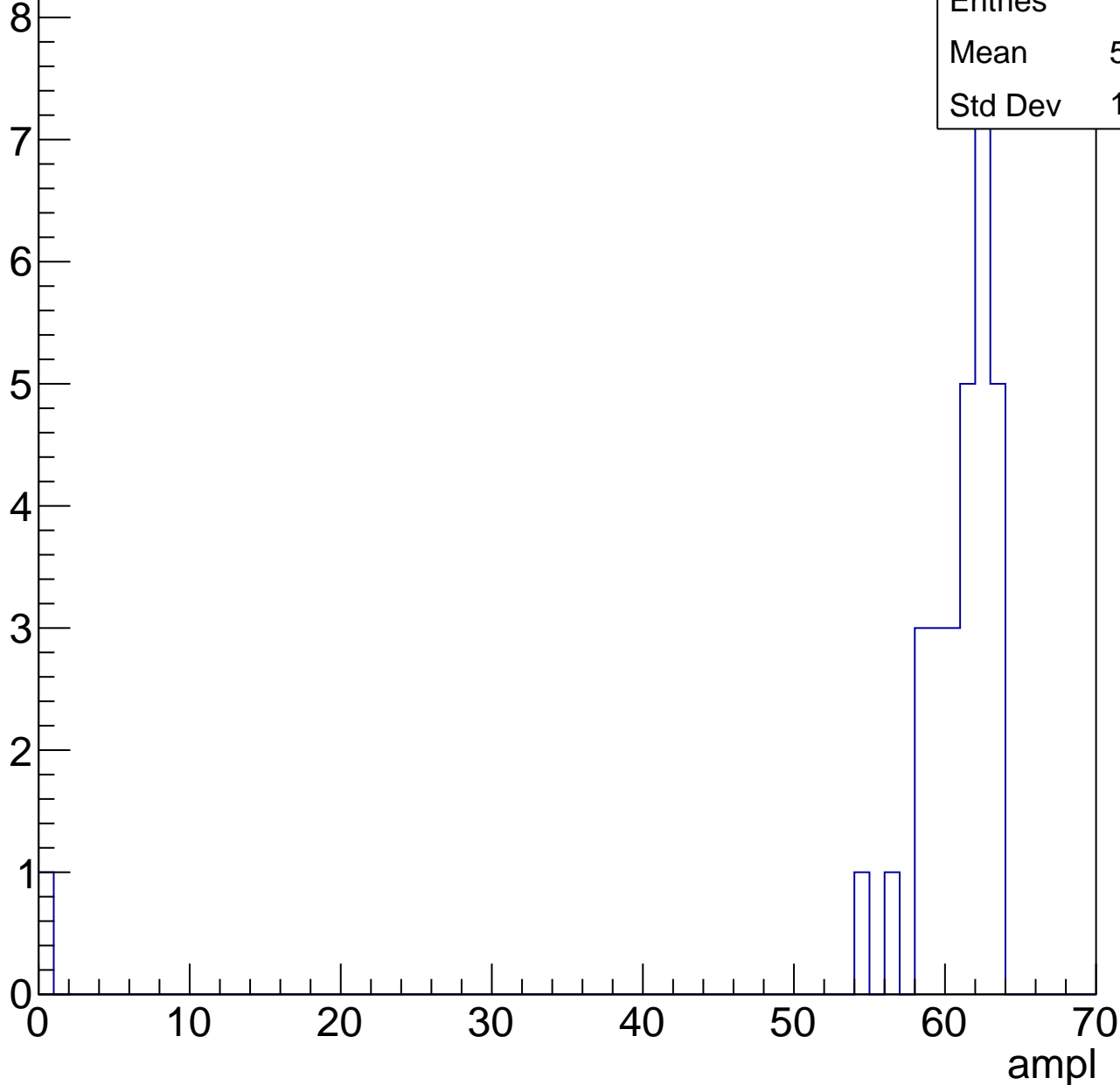


B0L002S, U7-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.57
Std Dev	11.09



B0L002S, U7-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch67, adc0

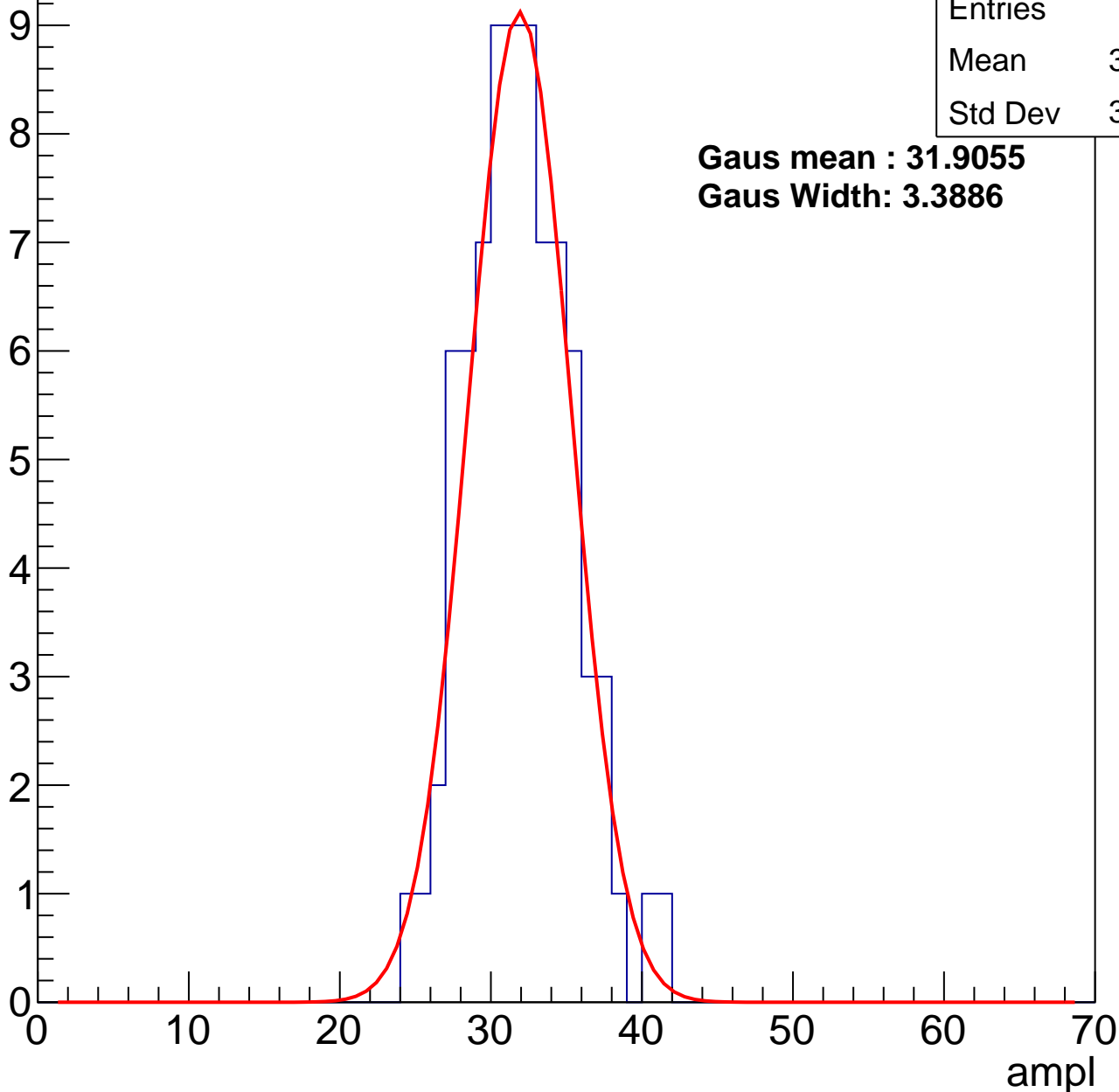
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	31.49
Std Dev	3.397

Gaus mean : 31.9055

Gaus Width: 3.3886



B0L002S, U7-ch67, adc1

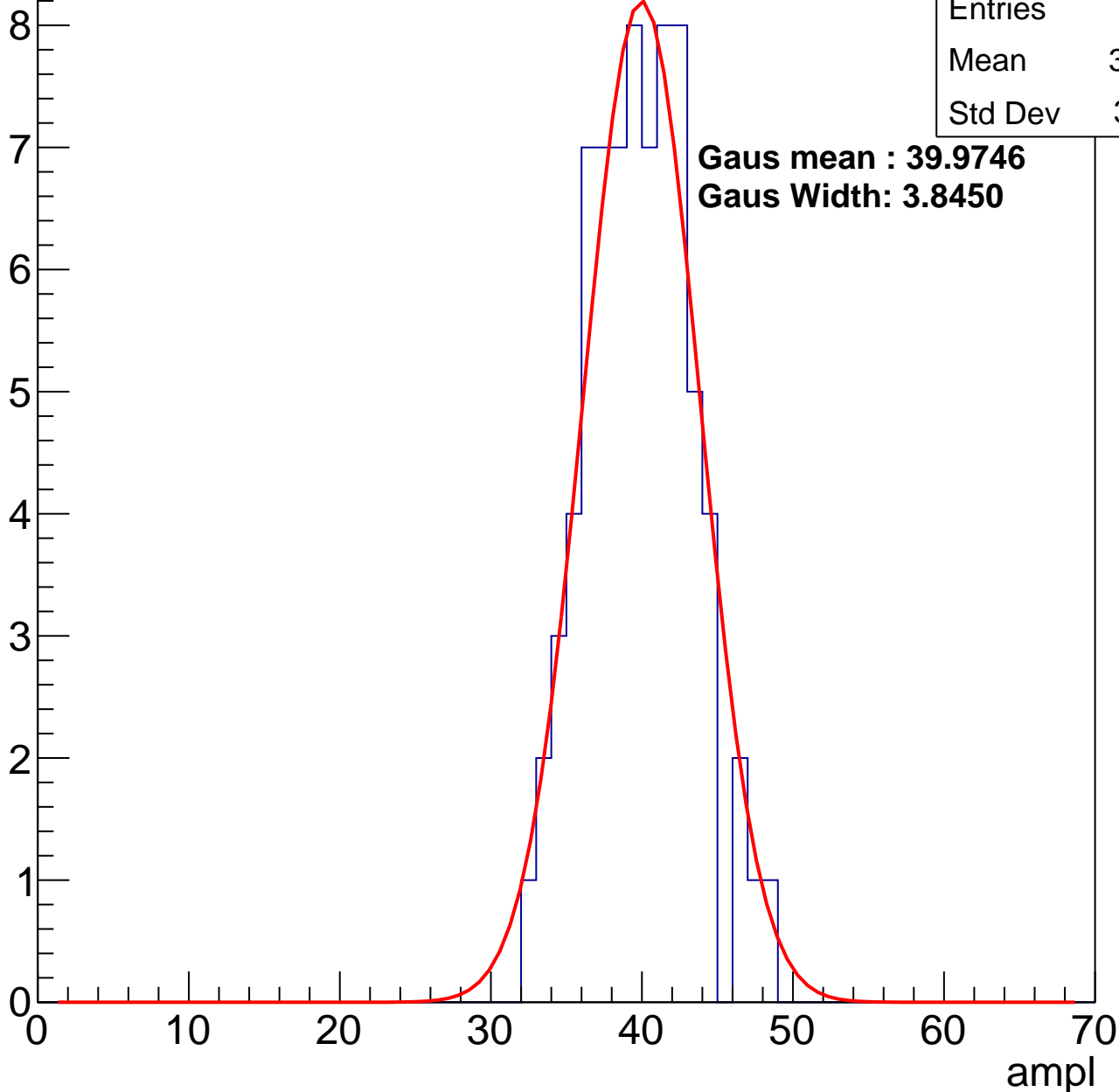
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	39.35
Std Dev	3.431

Gaus mean : 39.9746

Gaus Width: 3.8450



B0L002S, U7-ch67, adc2

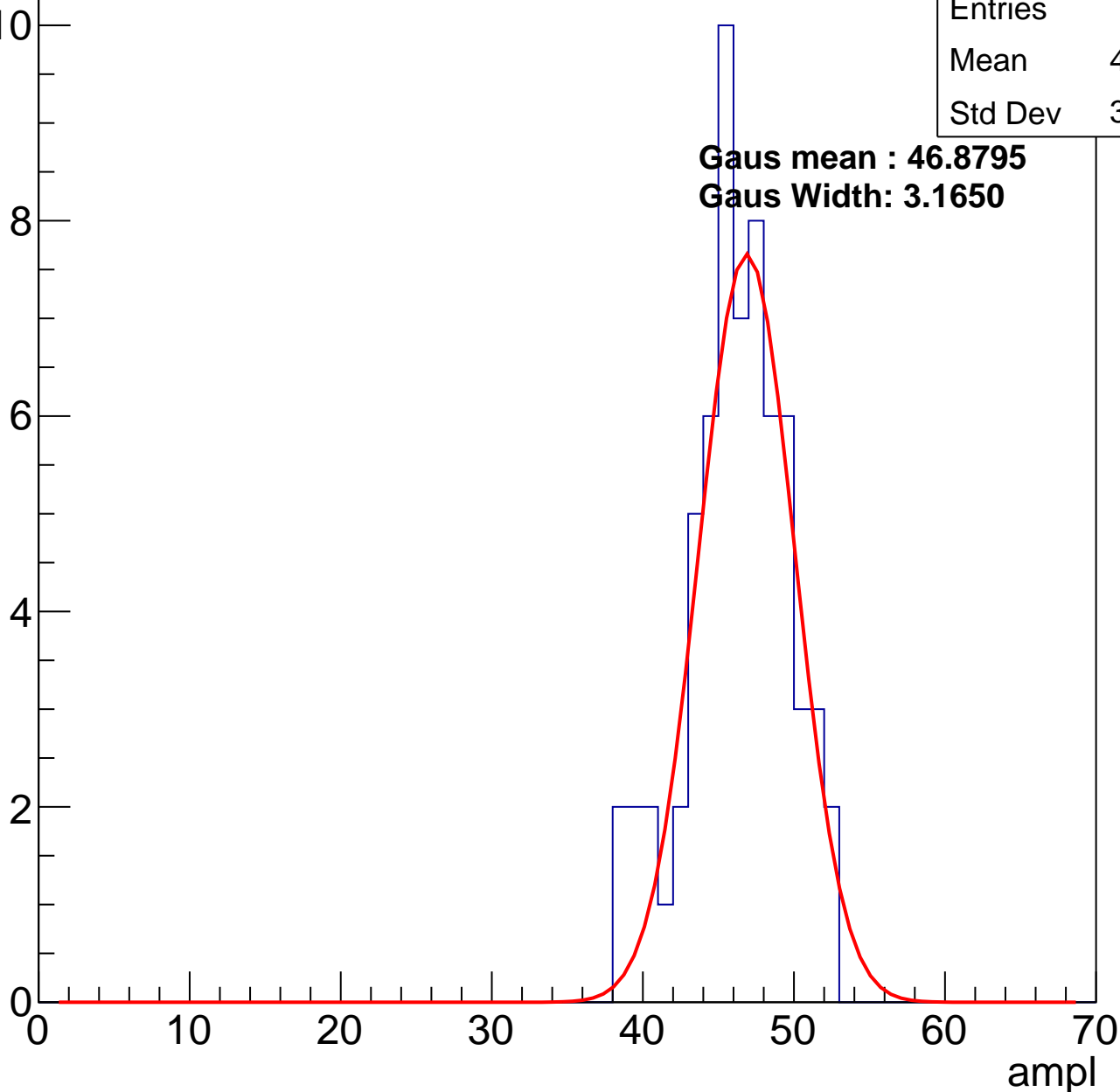
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.77
Std Dev	3.309

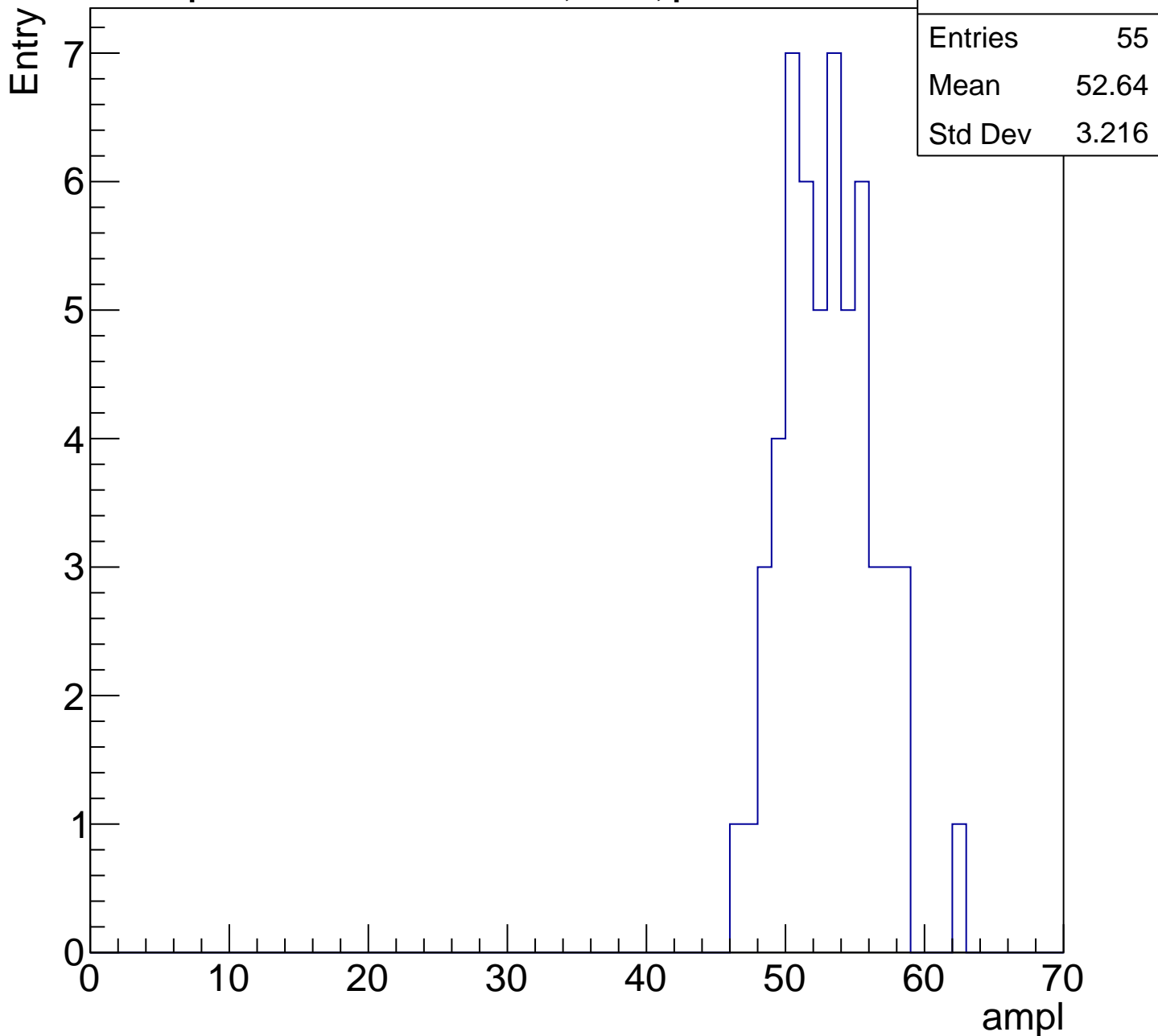
Gaus mean : 46.8795

Gaus Width: 3.1650



B0L002S, U7-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

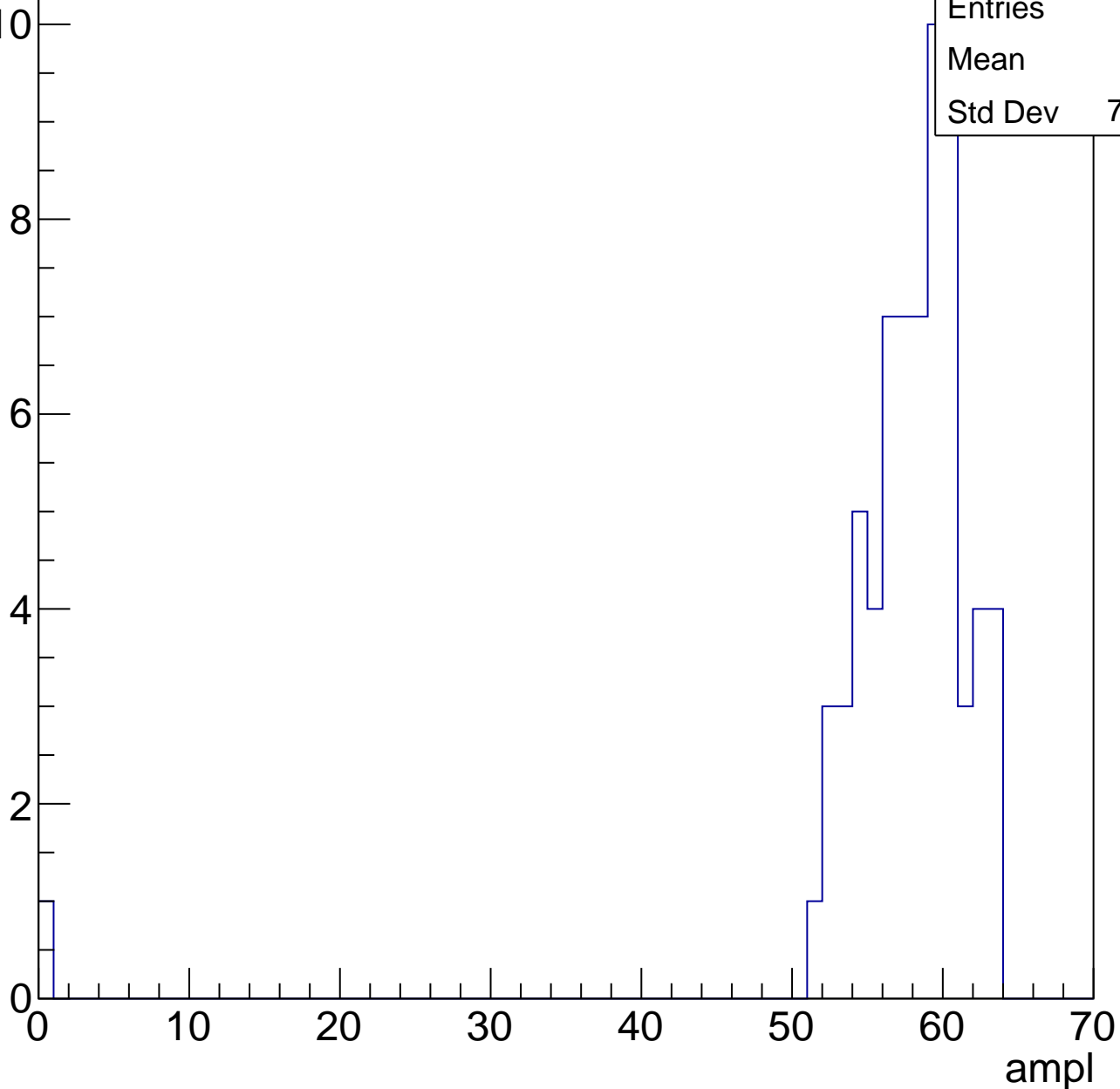


B0L002S, U7-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	56.9
Std Dev	7.522

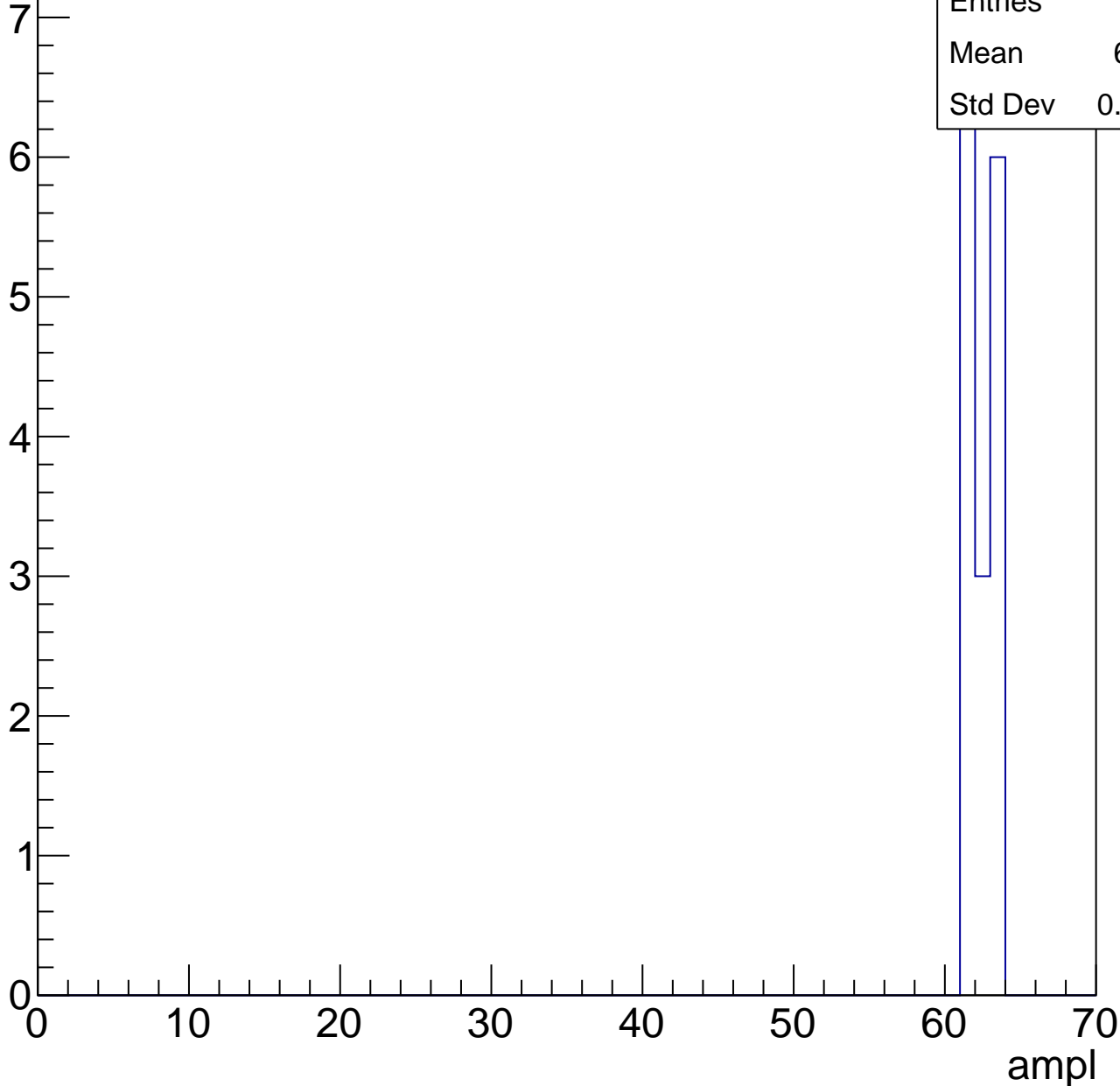


B0L002S, U7-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	61.94
Std Dev	0.8992



B0L002S, U7-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch68, adc0

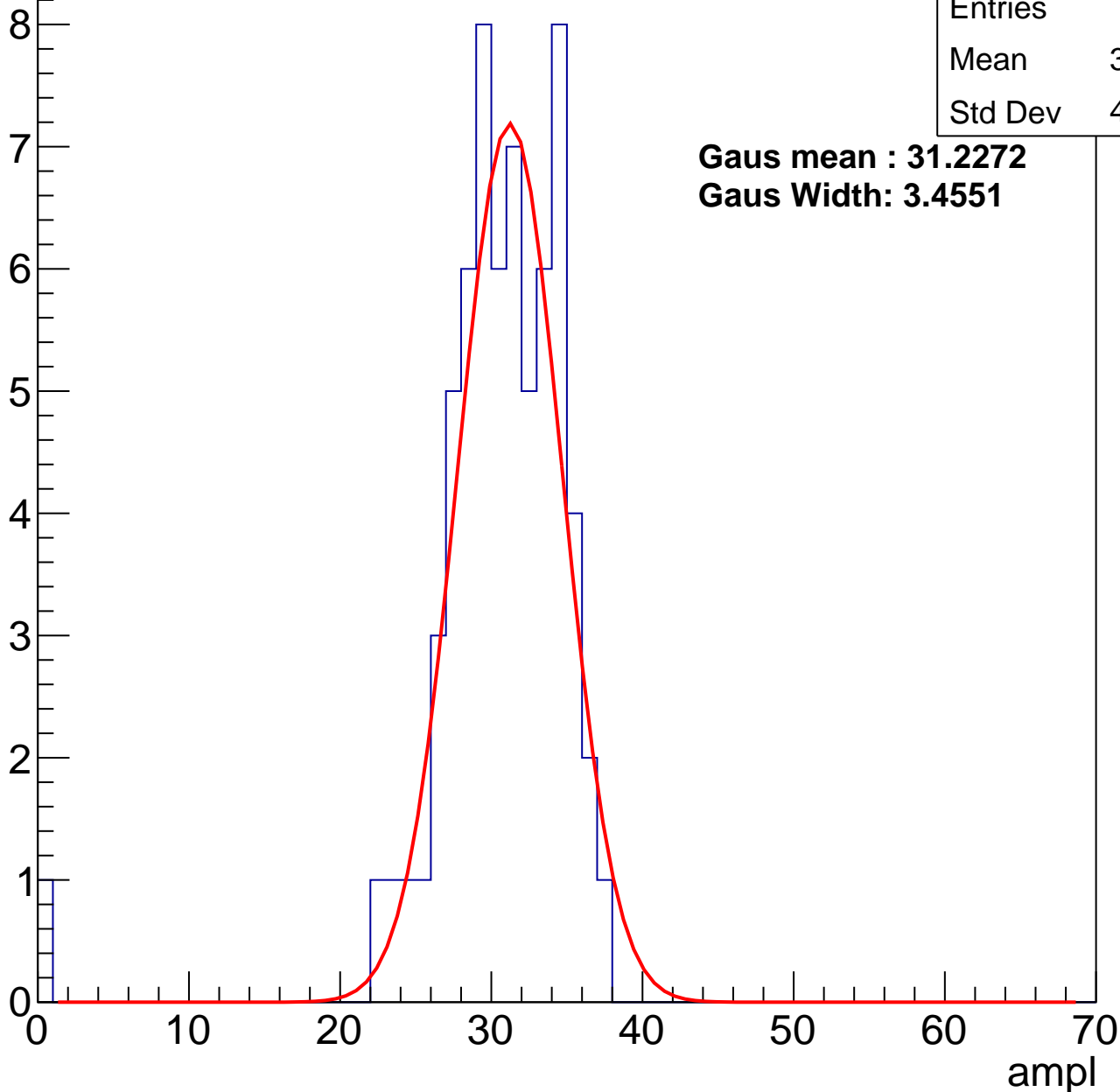
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.05
Std Dev	4.968

Gaus mean : 31.2272

Gaus Width: 3.4551



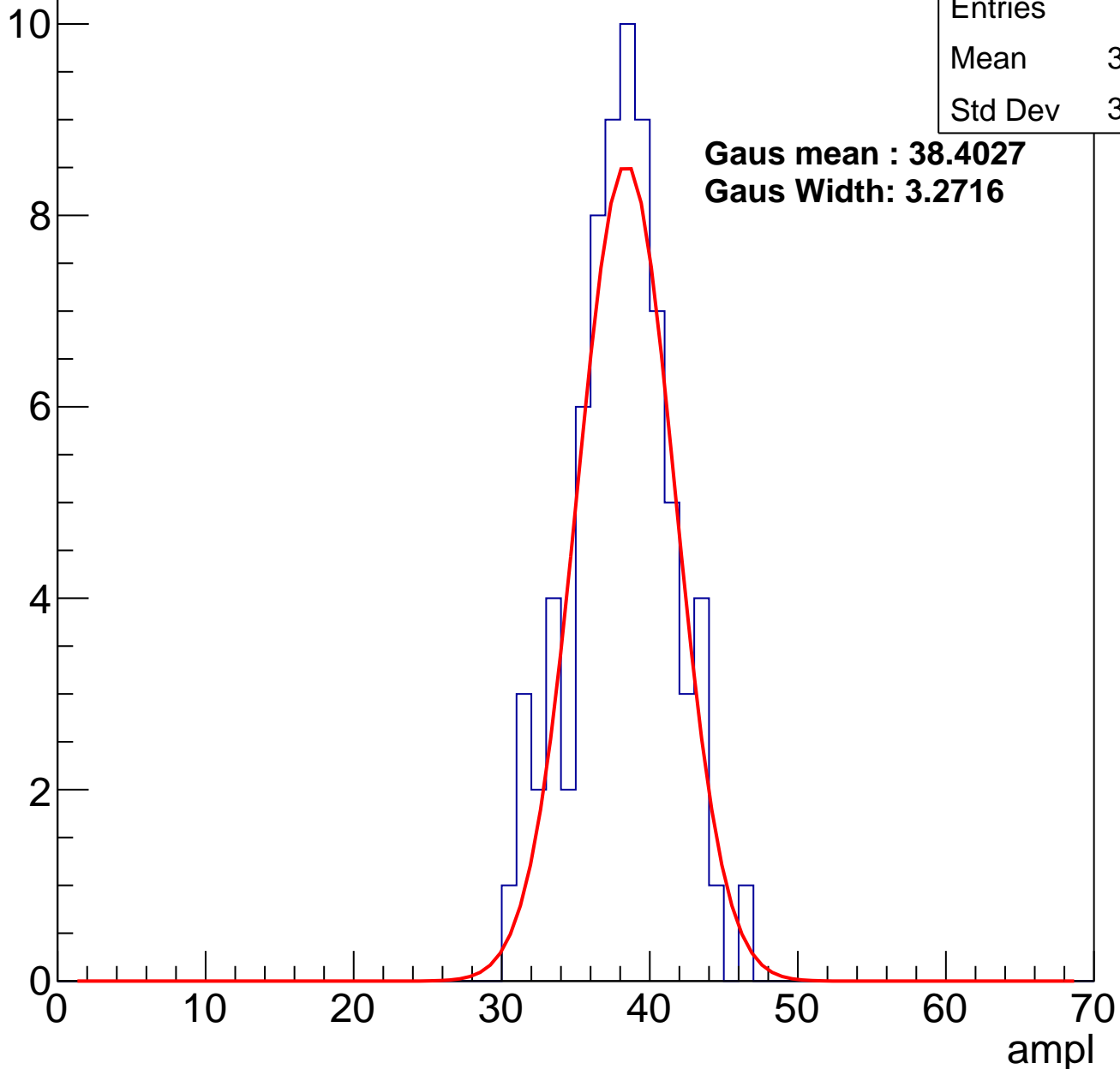
B0L002S, U7-ch68, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.63
Std Dev	3.326

Gaus mean : 38.4027
Gaus Width: 3.2716

Entry



B0L002S, U7-ch68, adc2

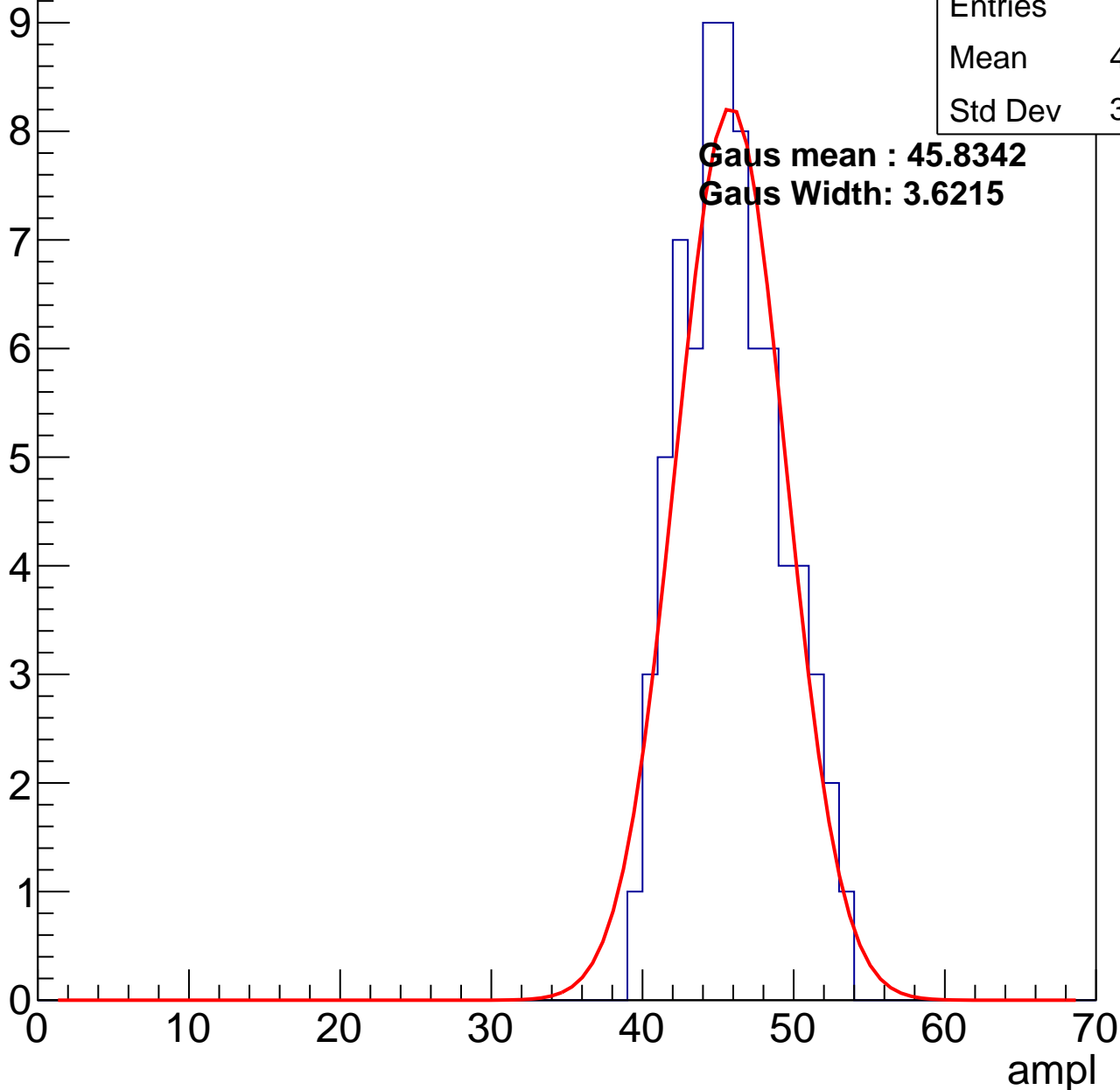
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	45.42
Std Dev	3.276

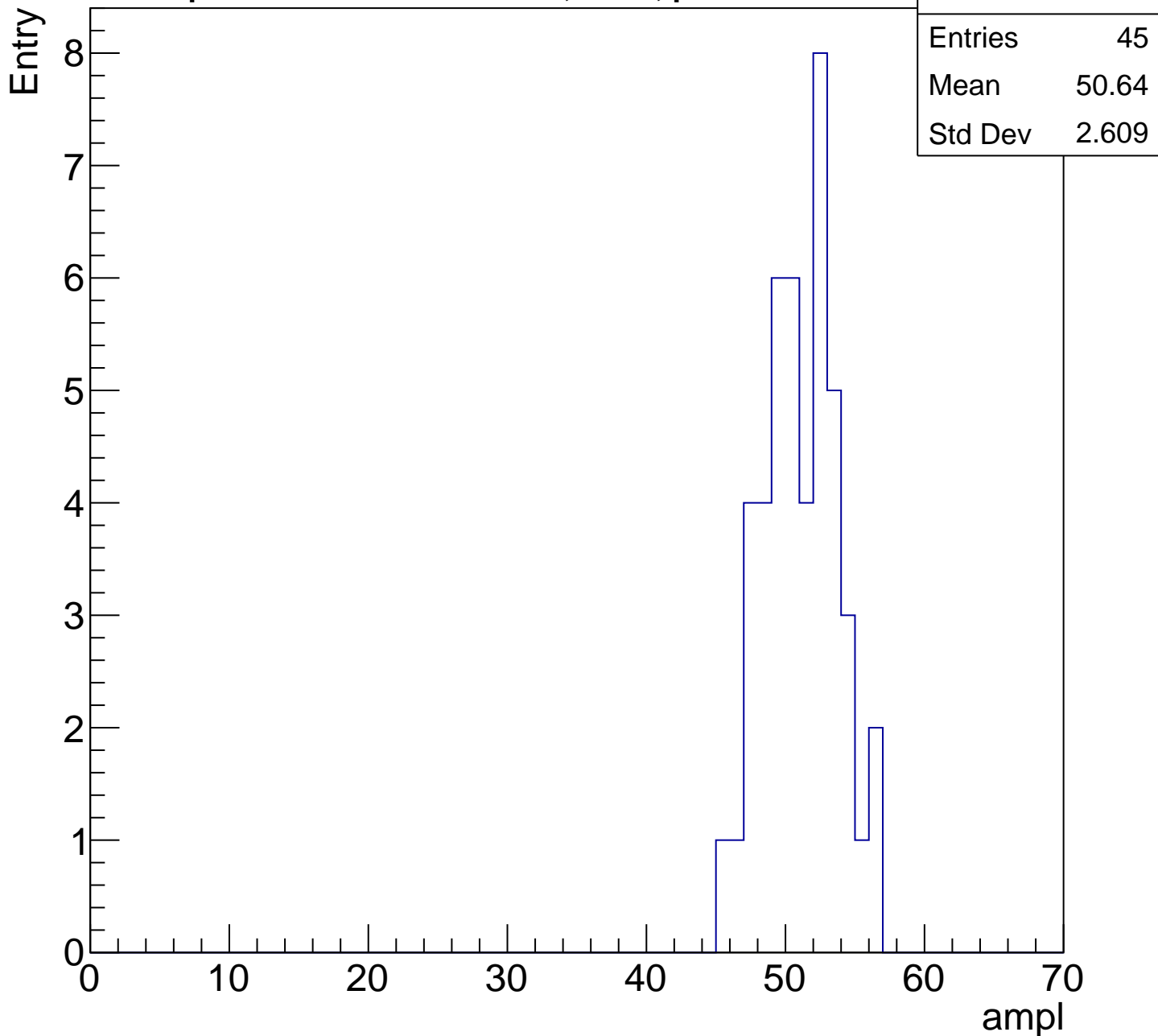
Gaus mean : 45.8342

Gaus Width: 3.6215



B0L002S, U7-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

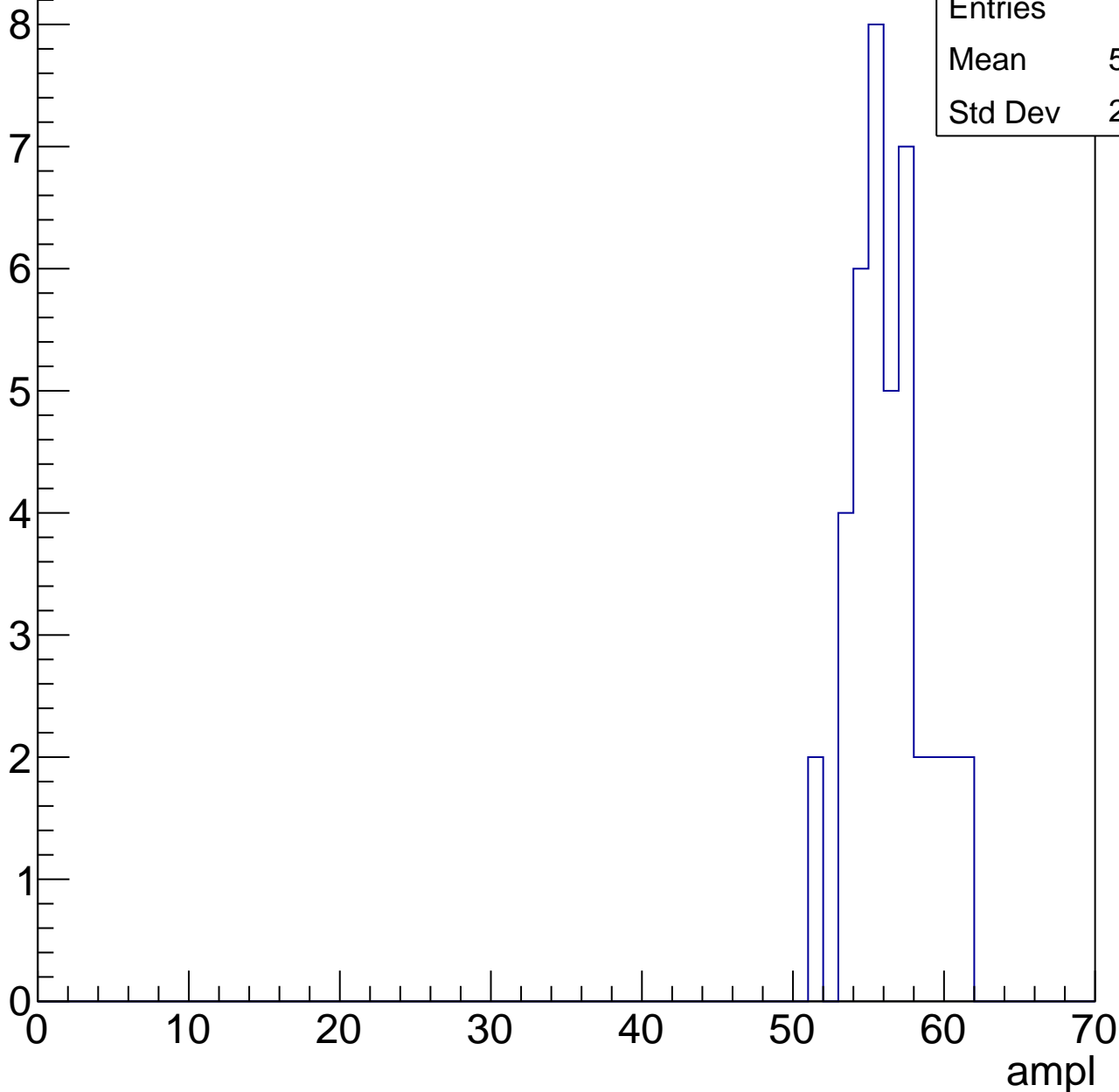


B0L002S, U7-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	55.83
Std Dev	2.407

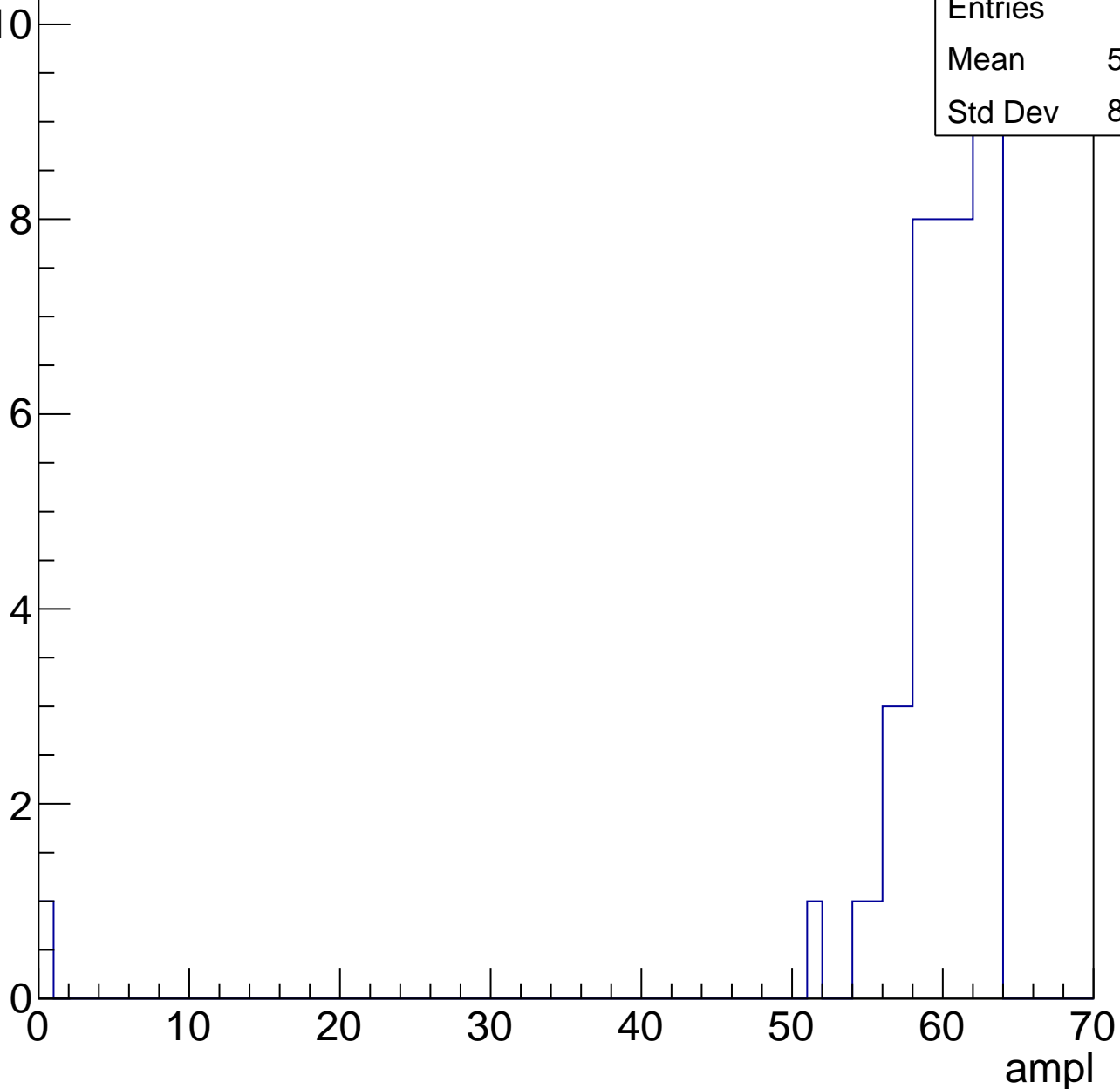


B0L002S, U7-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	58.85
Std Dev	8.002



B0L002S, U7-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch69, adc0

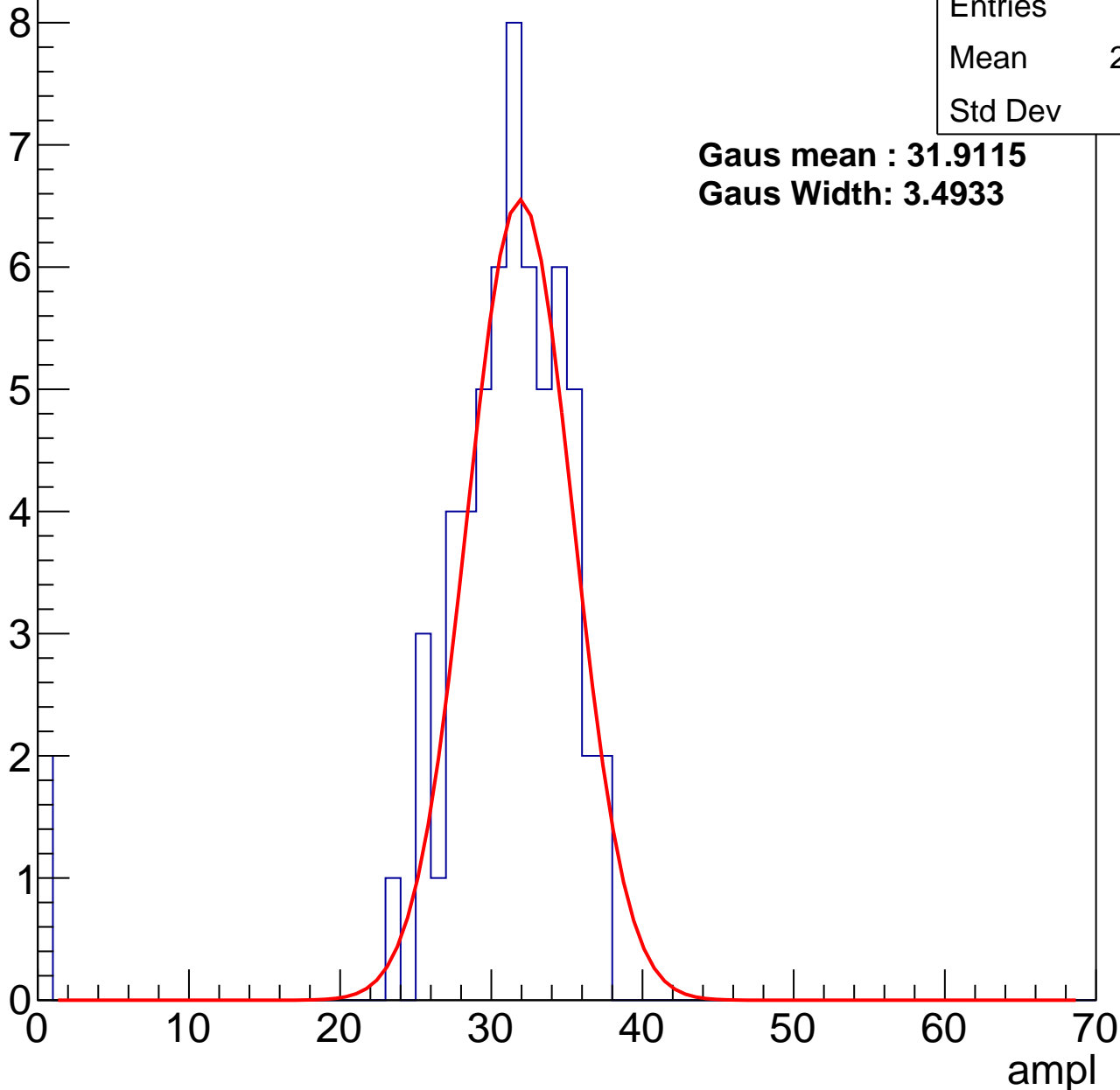
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.98
Std Dev	6.41

Gaus mean : 31.9115

Gaus Width: 3.4933



B0L002S, U7-ch69, adc1

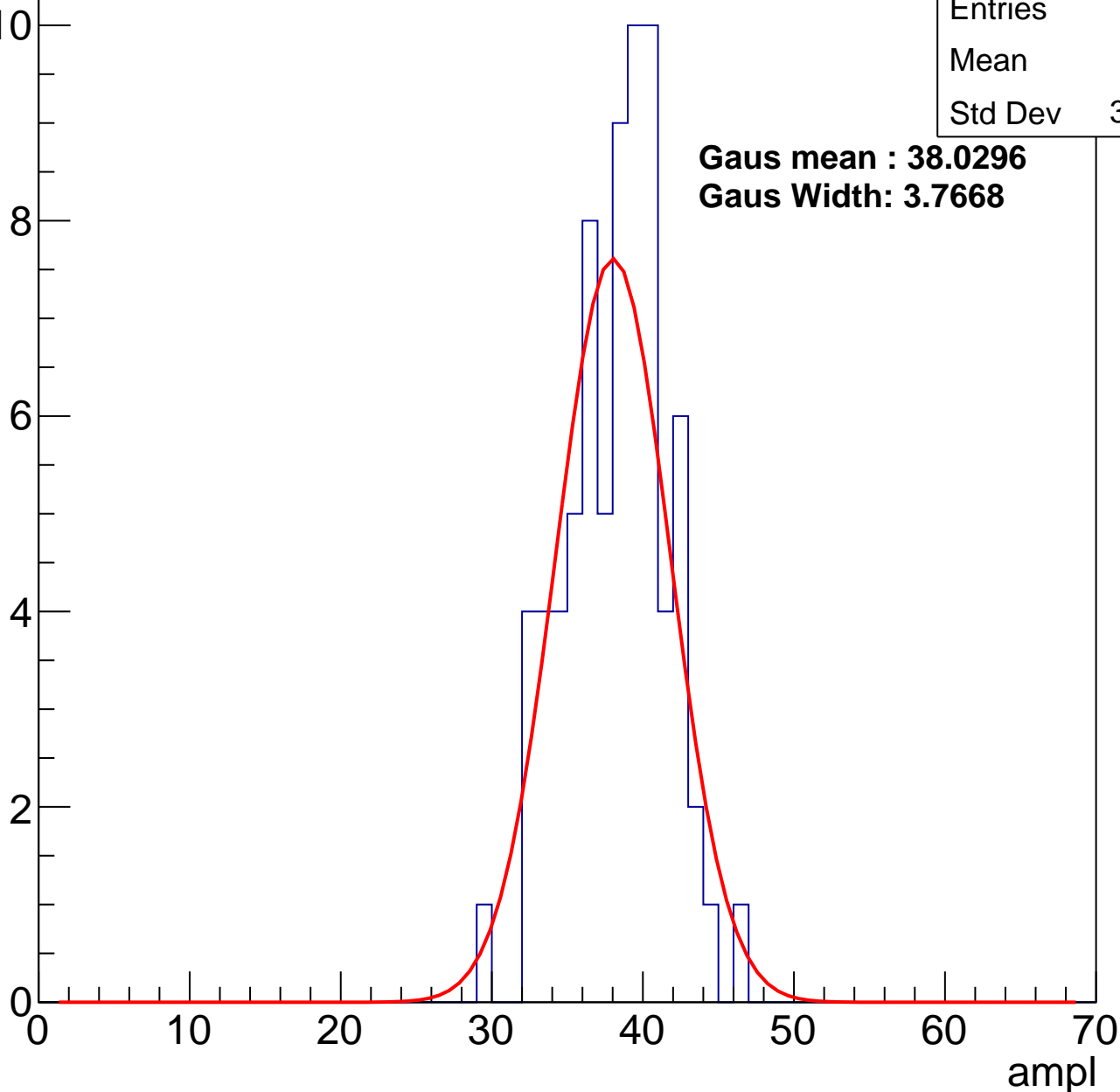
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.8
Std Dev	3.288

Gaus mean : 38.0296

Gaus Width: 3.7668



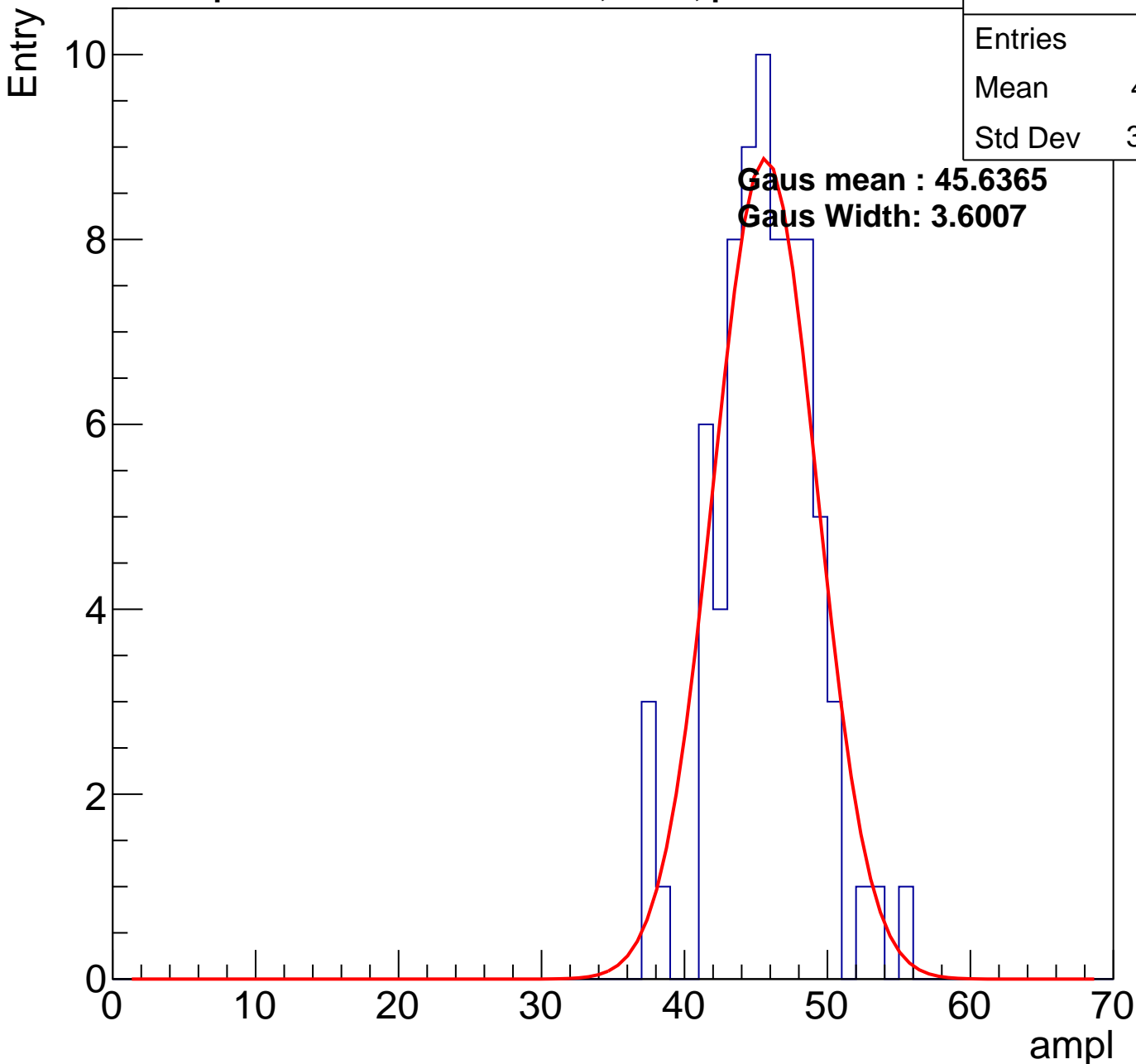
B0L002S, U7-ch69, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	45.21
Std Dev	3.423

Gaus mean : 45.6365

Gaus Width: 3.6007

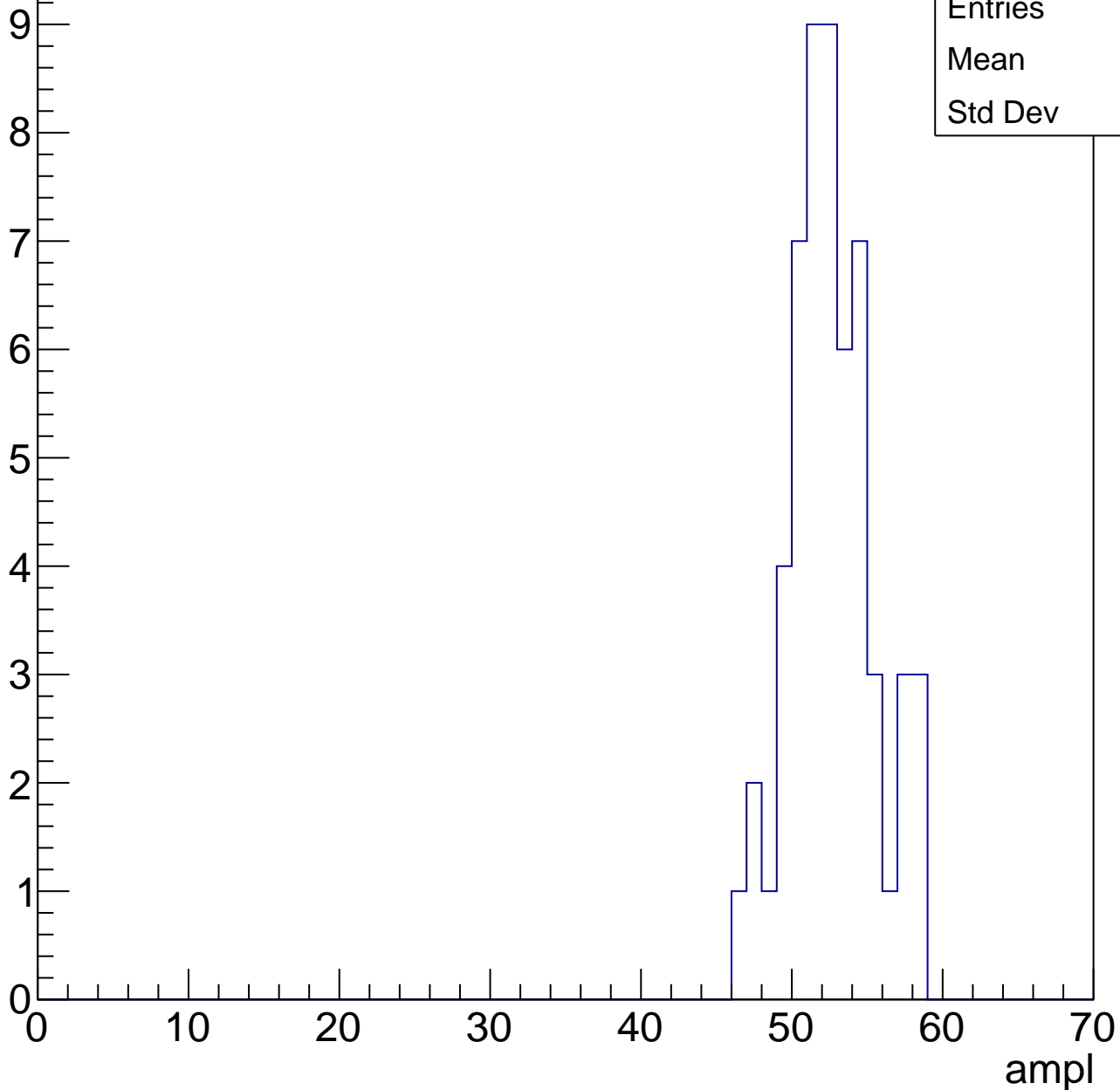


B0L002S, U7-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

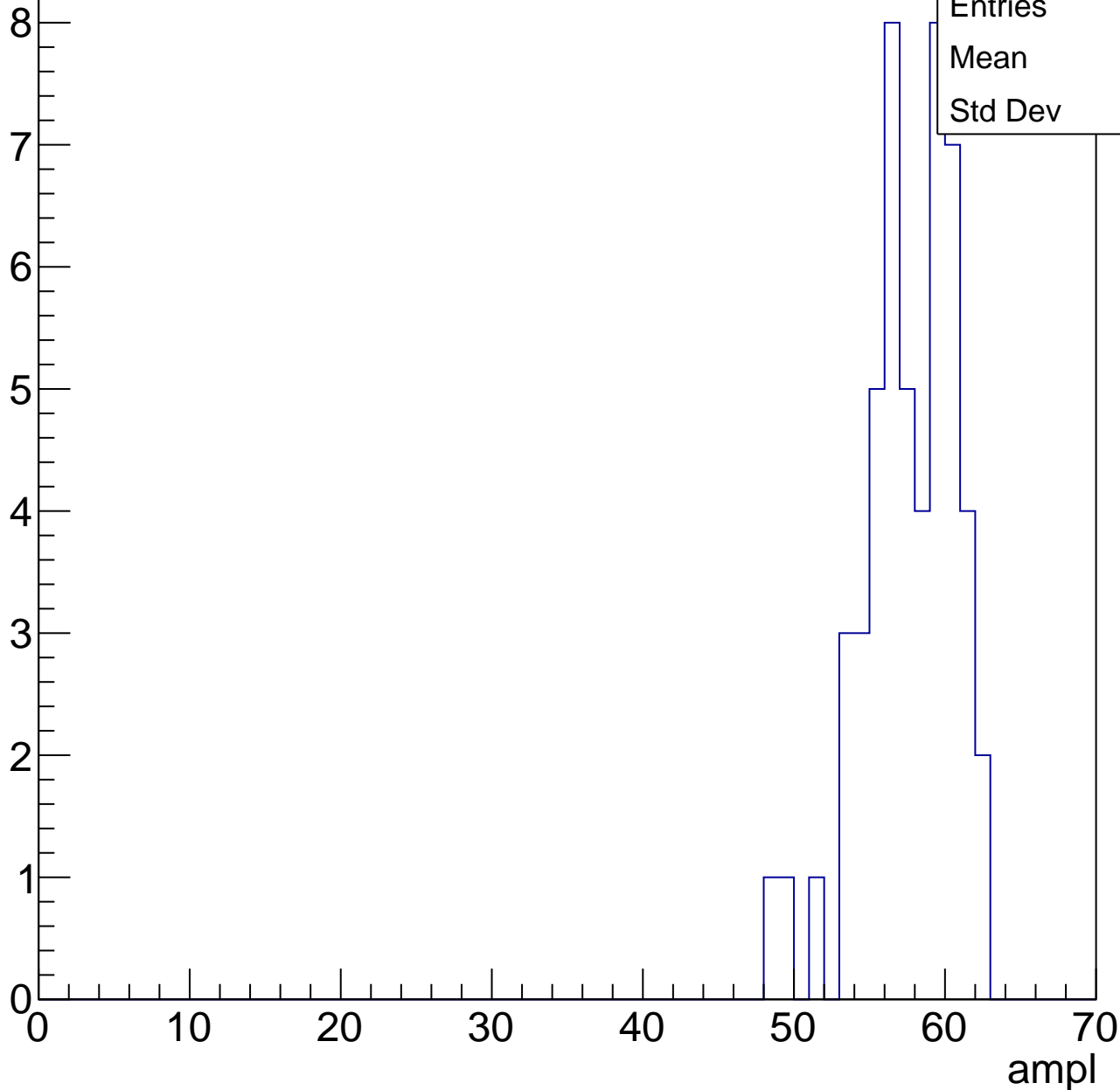
Entries	56
Mean	52.2
Std Dev	2.78



B0L002S, U7-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

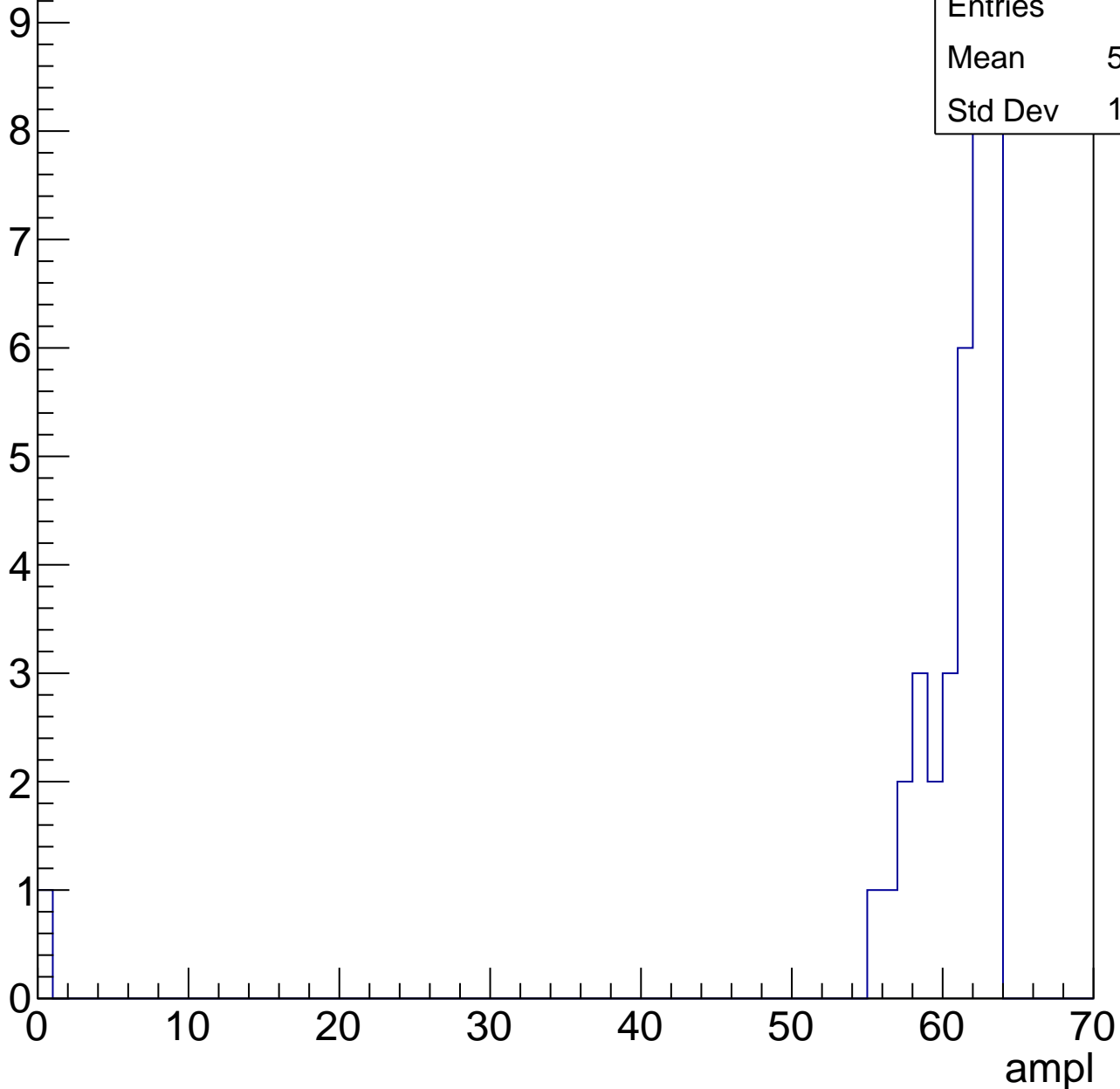


B0L002S, U7-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.06
Std Dev	10.22



B0L002S, U7-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B0L002S, U7-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch70, adc0

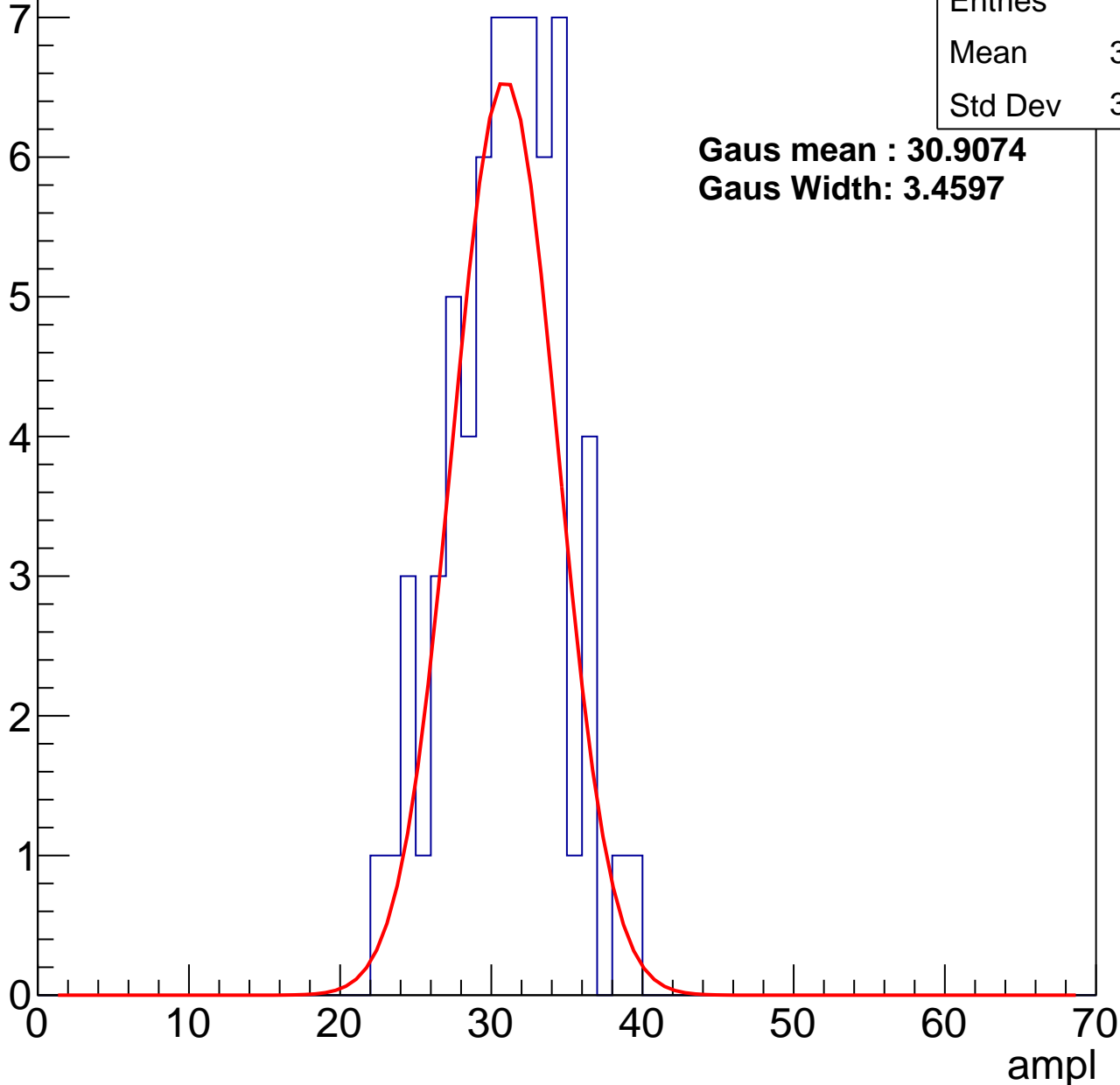
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.52
Std Dev	3.634

Gaus mean : 30.9074

Gaus Width: 3.4597



B0L002S, U7-ch70, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	37.73
Std Dev	3.551

Gaus mean : 38.0351

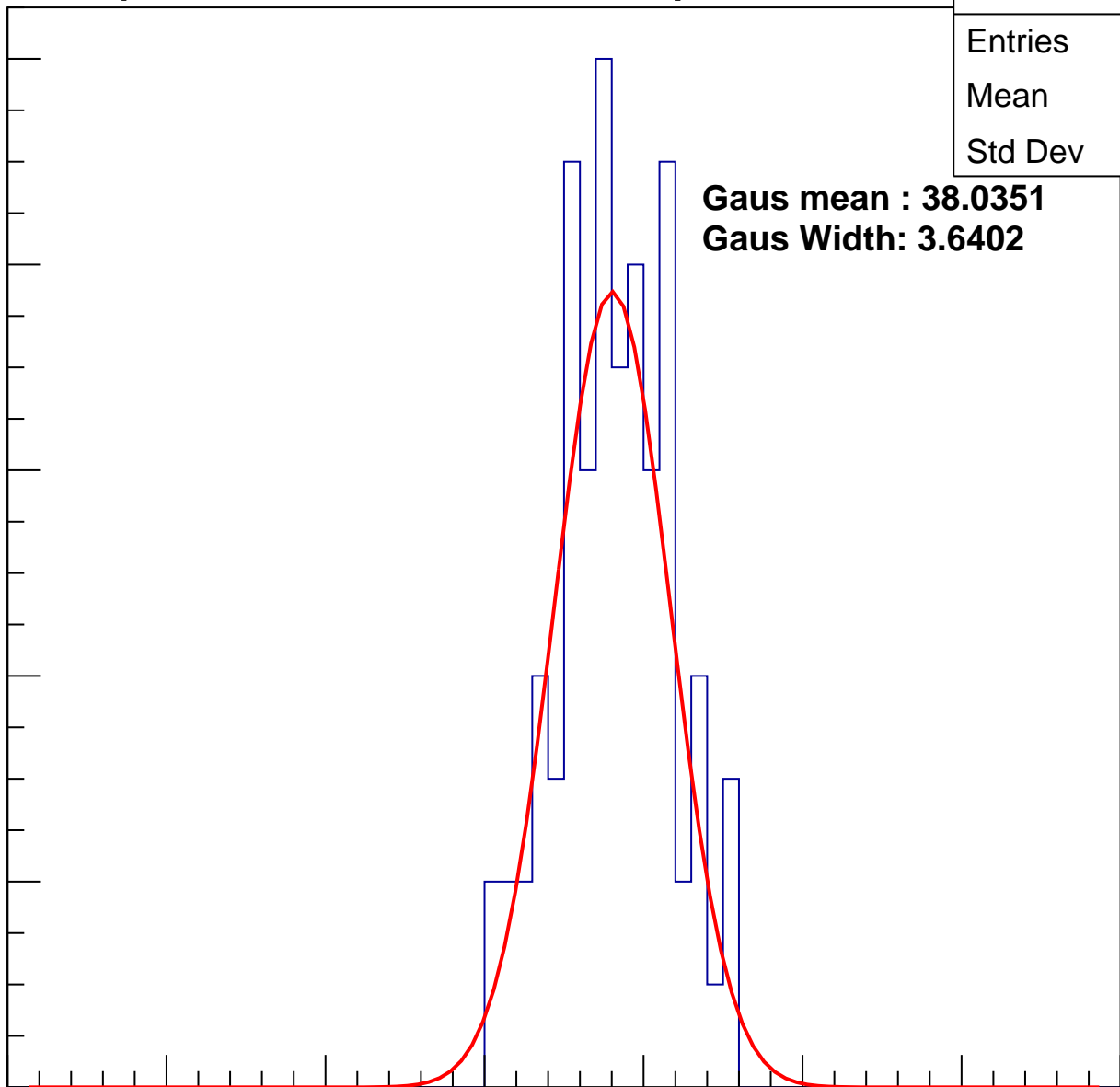
Gaus Width: 3.6402

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch70, adc2

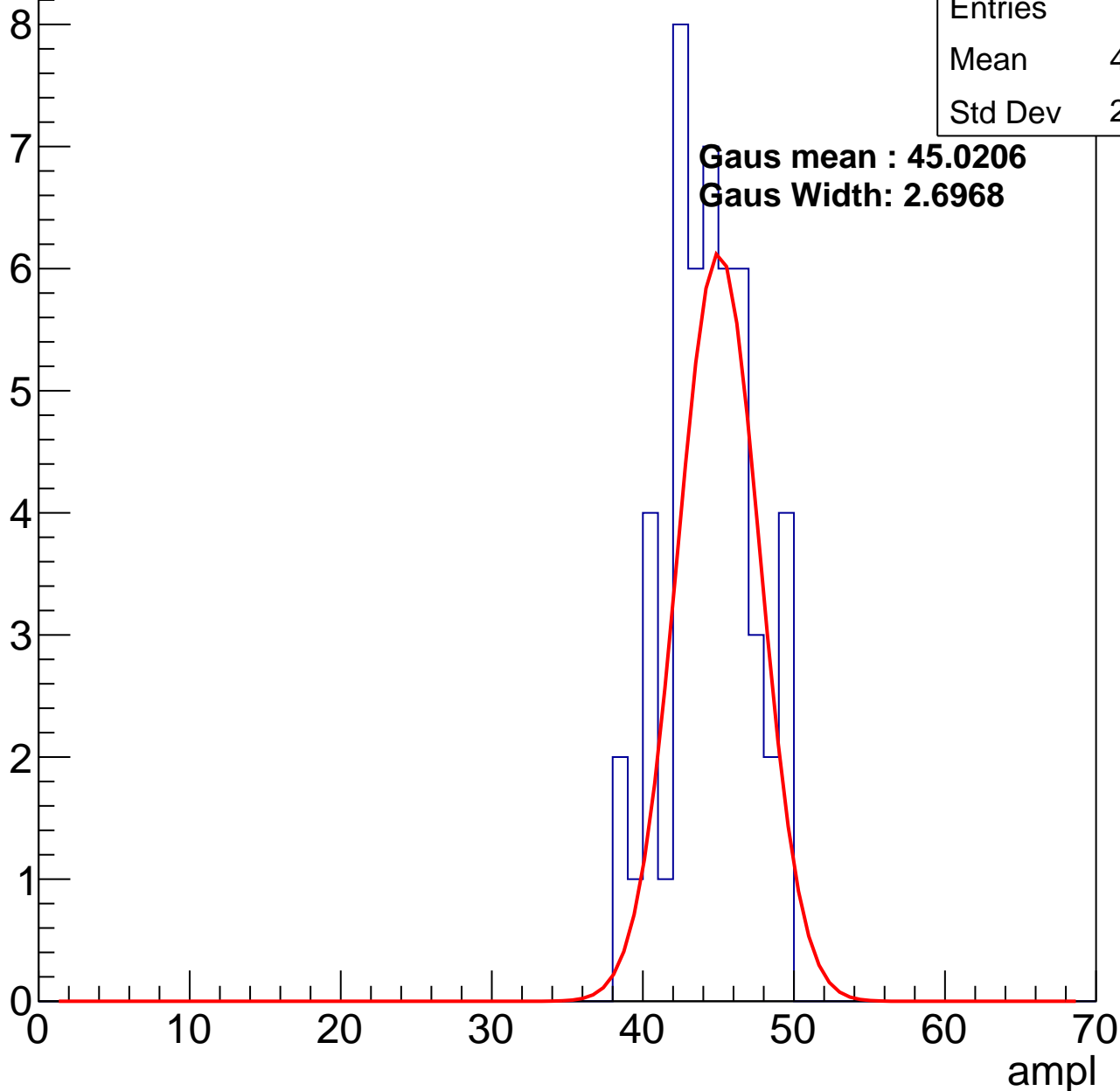
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.94
Std Dev	2.817

Gaus mean : 45.0206

Gaus Width: 2.6968

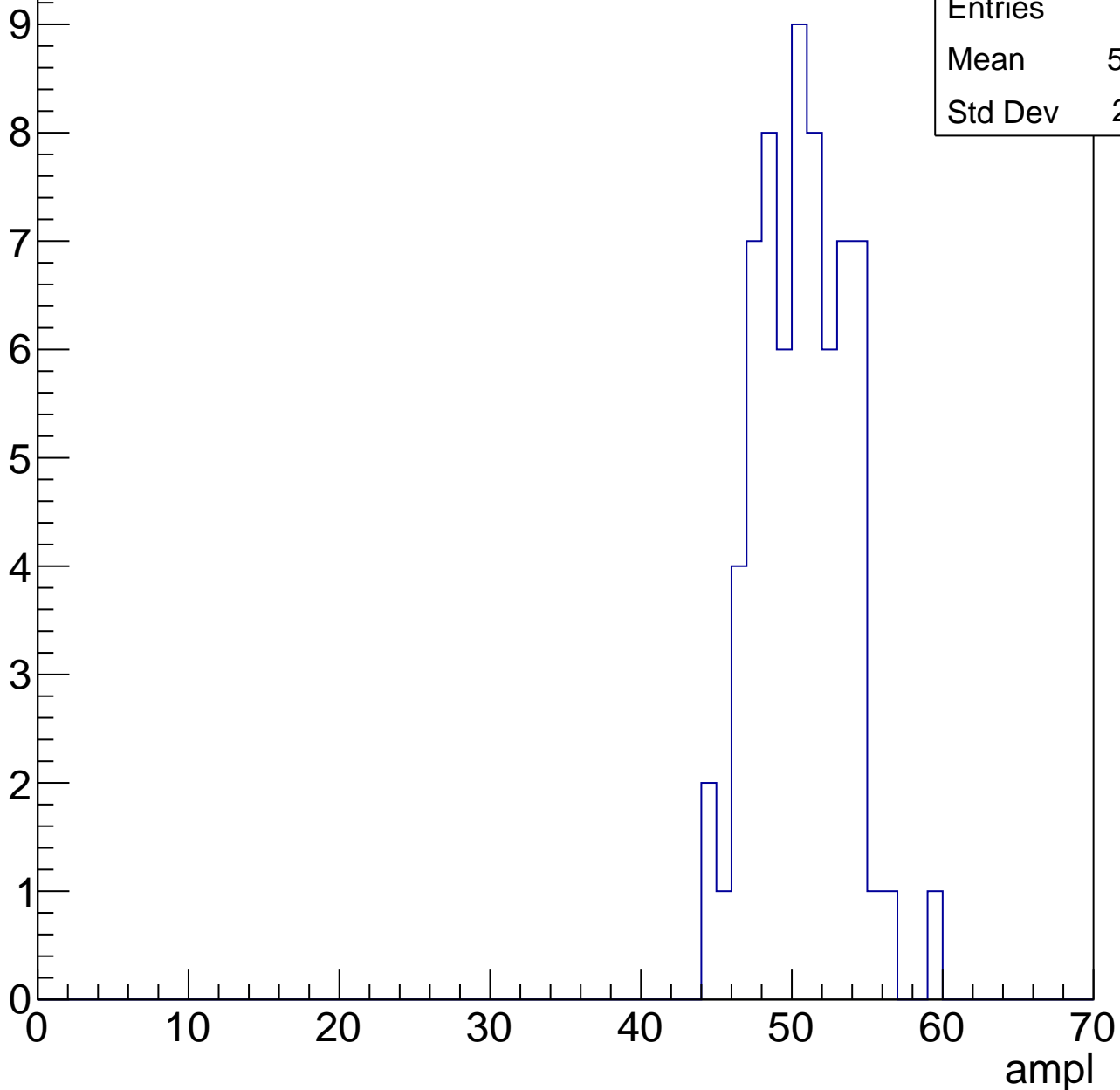


B0L002S, U7-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.19
Std Dev	2.991

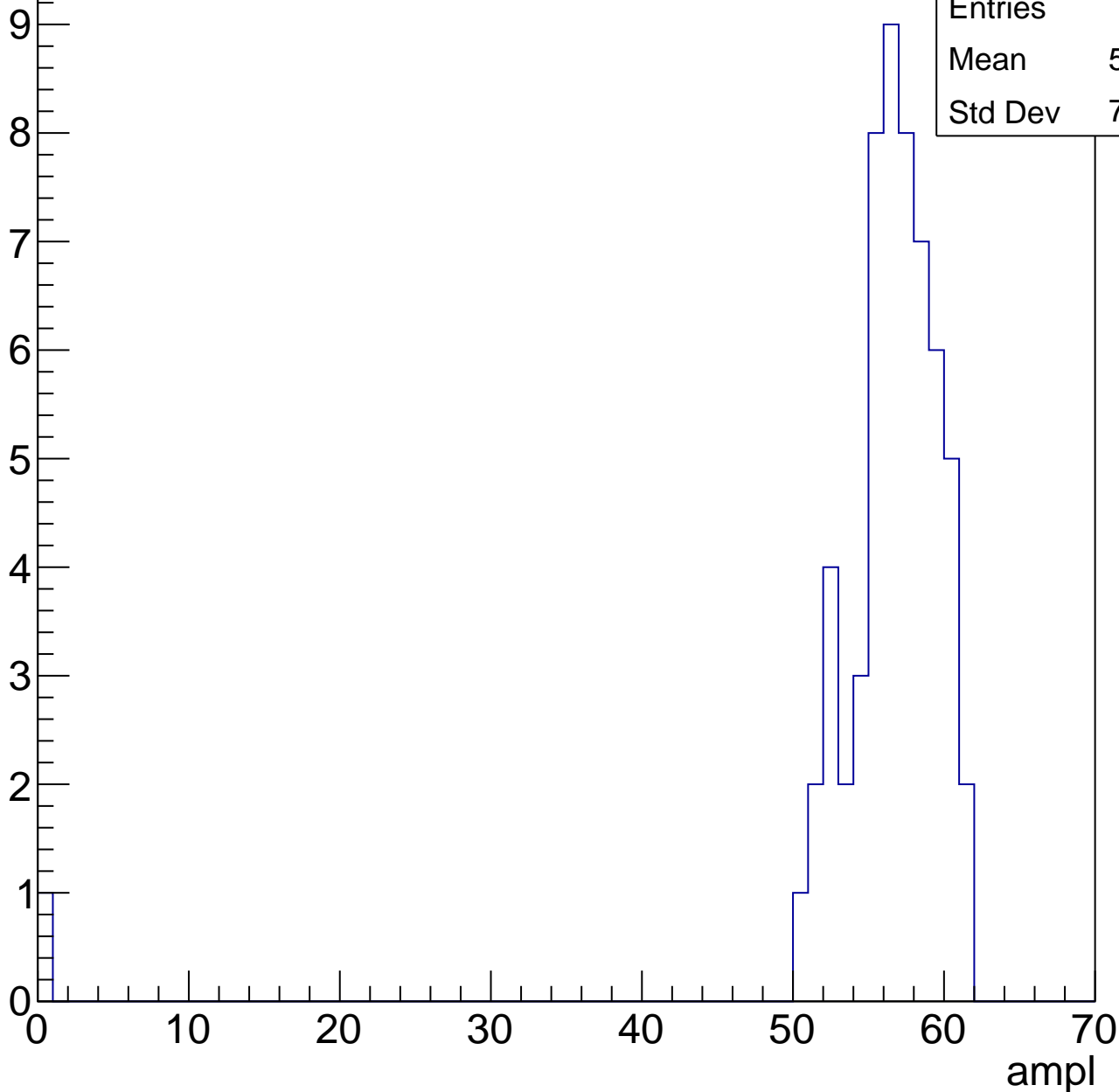


B0L002S, U7-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.34
Std Dev	7.789

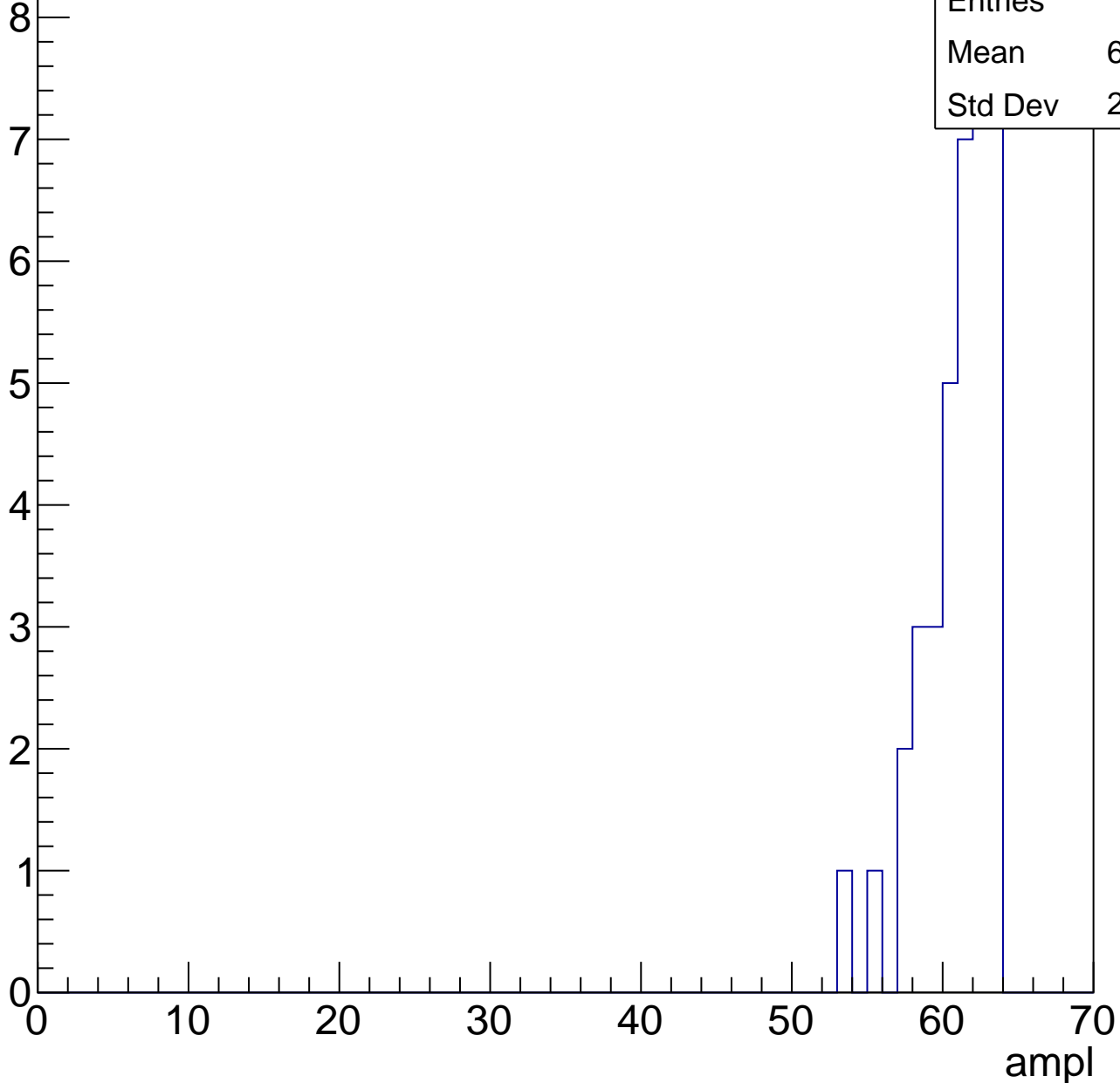


B0L002S, U7-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	60.53
Std Dev	2.337



B0L002S, U7-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L002S, U7-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch71, adc0

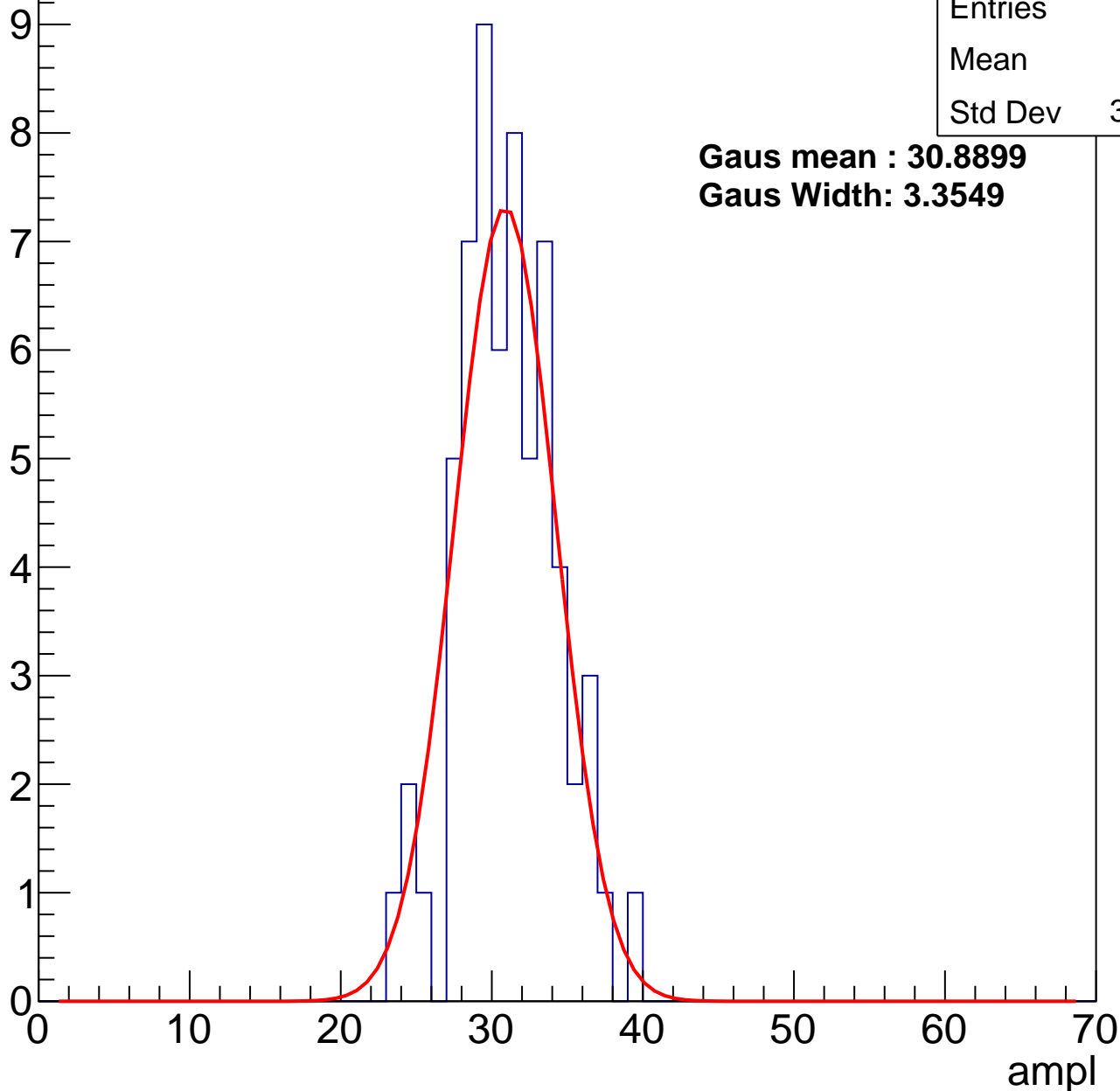
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.6
Std Dev	3.235

Gaus mean : 30.8899

Gaus Width: 3.3549



B0L002S, U7-ch71, adc1

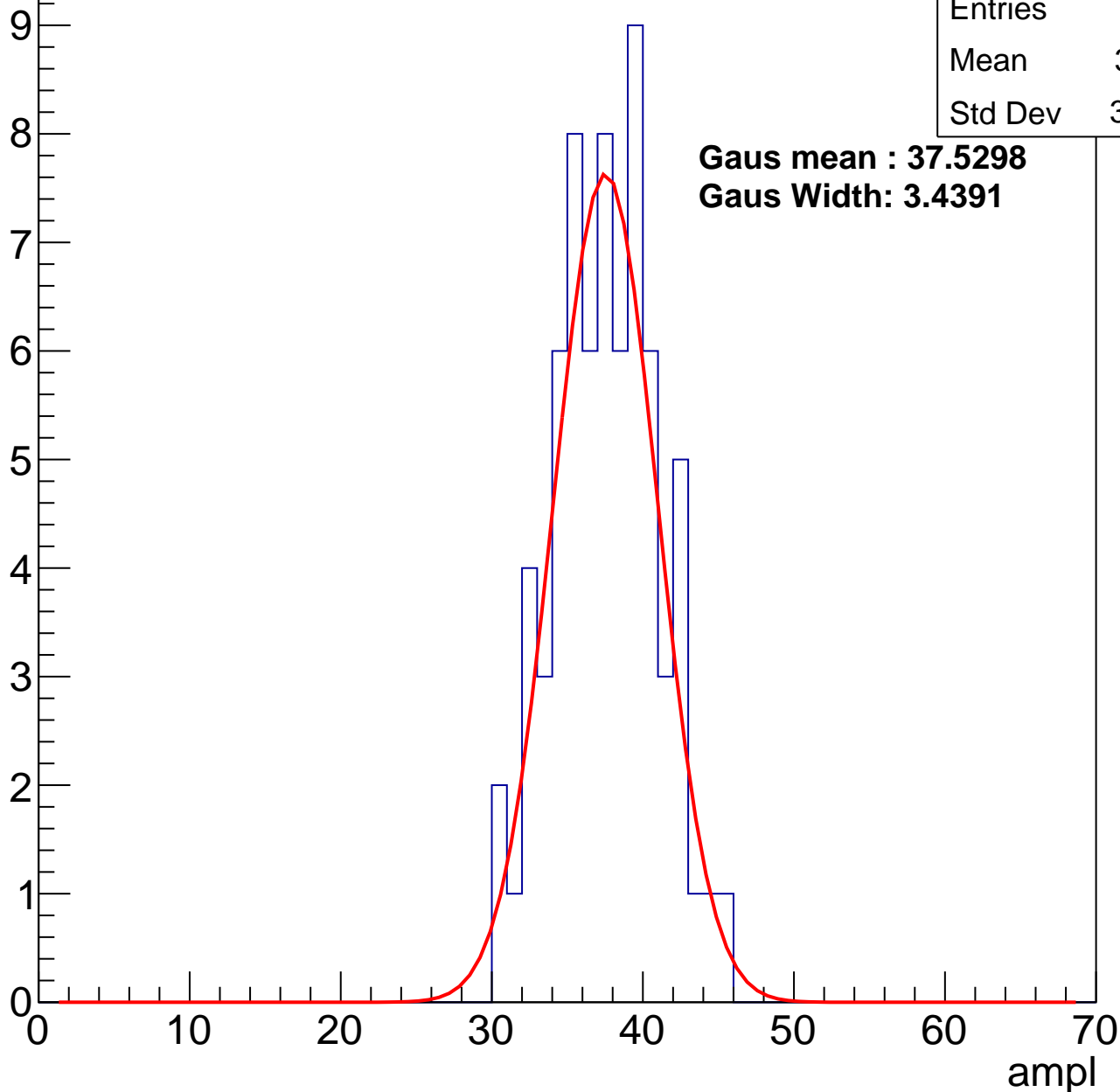
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.11
Std Dev	3.362

Gaus mean : 37.5298

Gaus Width: 3.4391



B0L002S, U7-ch71, adc2

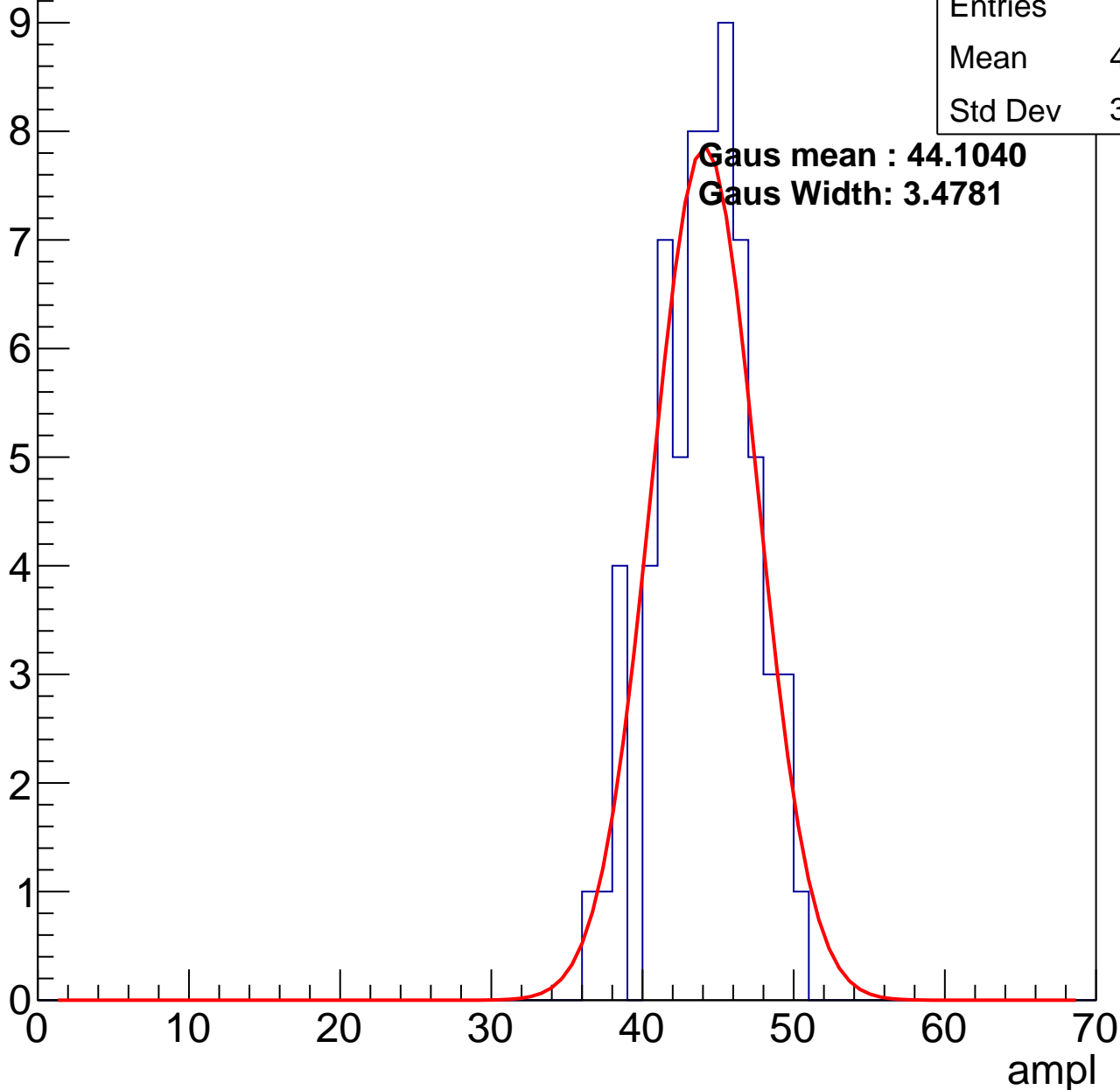
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.65
Std Dev	3.126

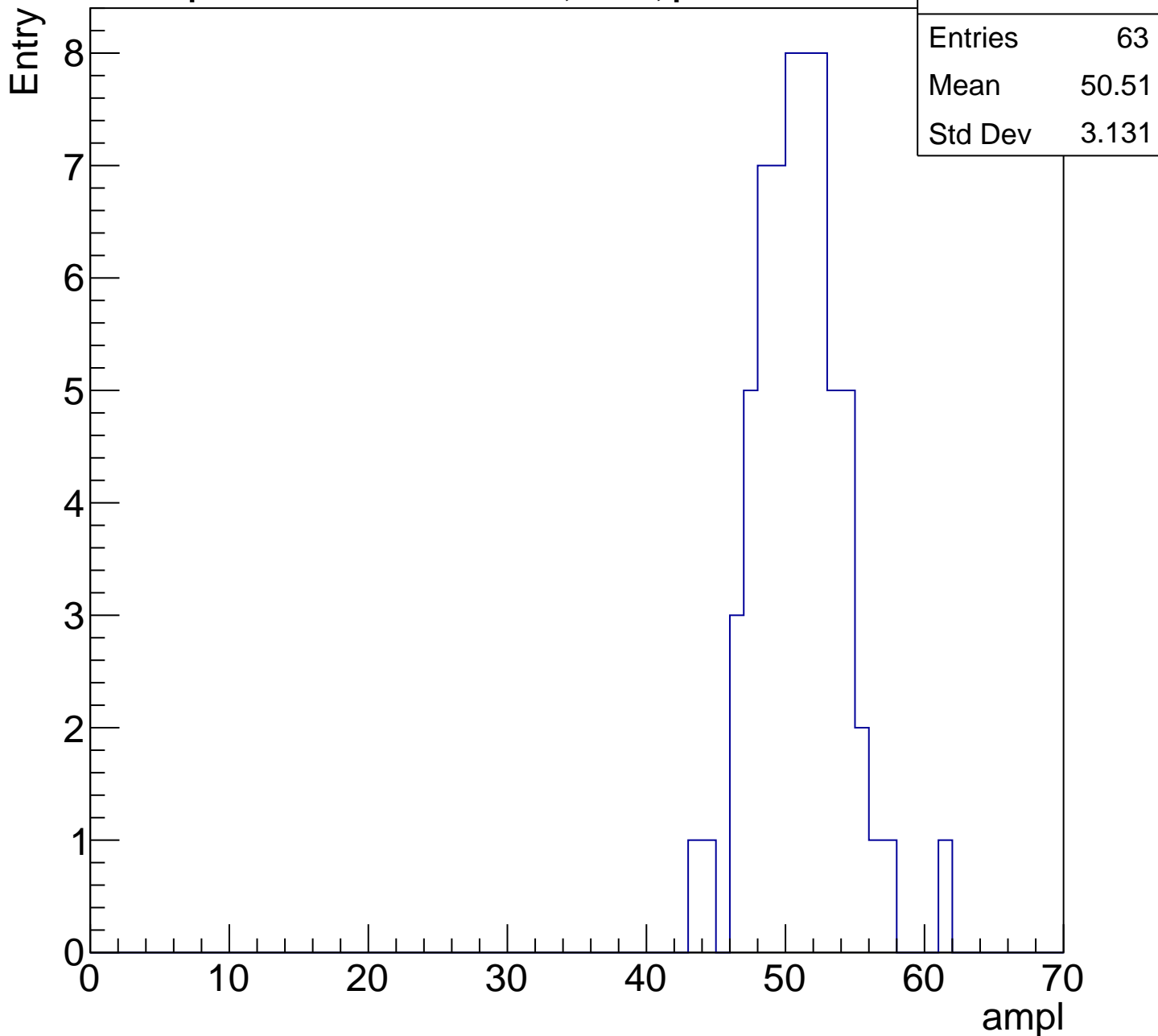
Gaus mean : 44.1040

Gaus Width: 3.4781



B0L002S, U7-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

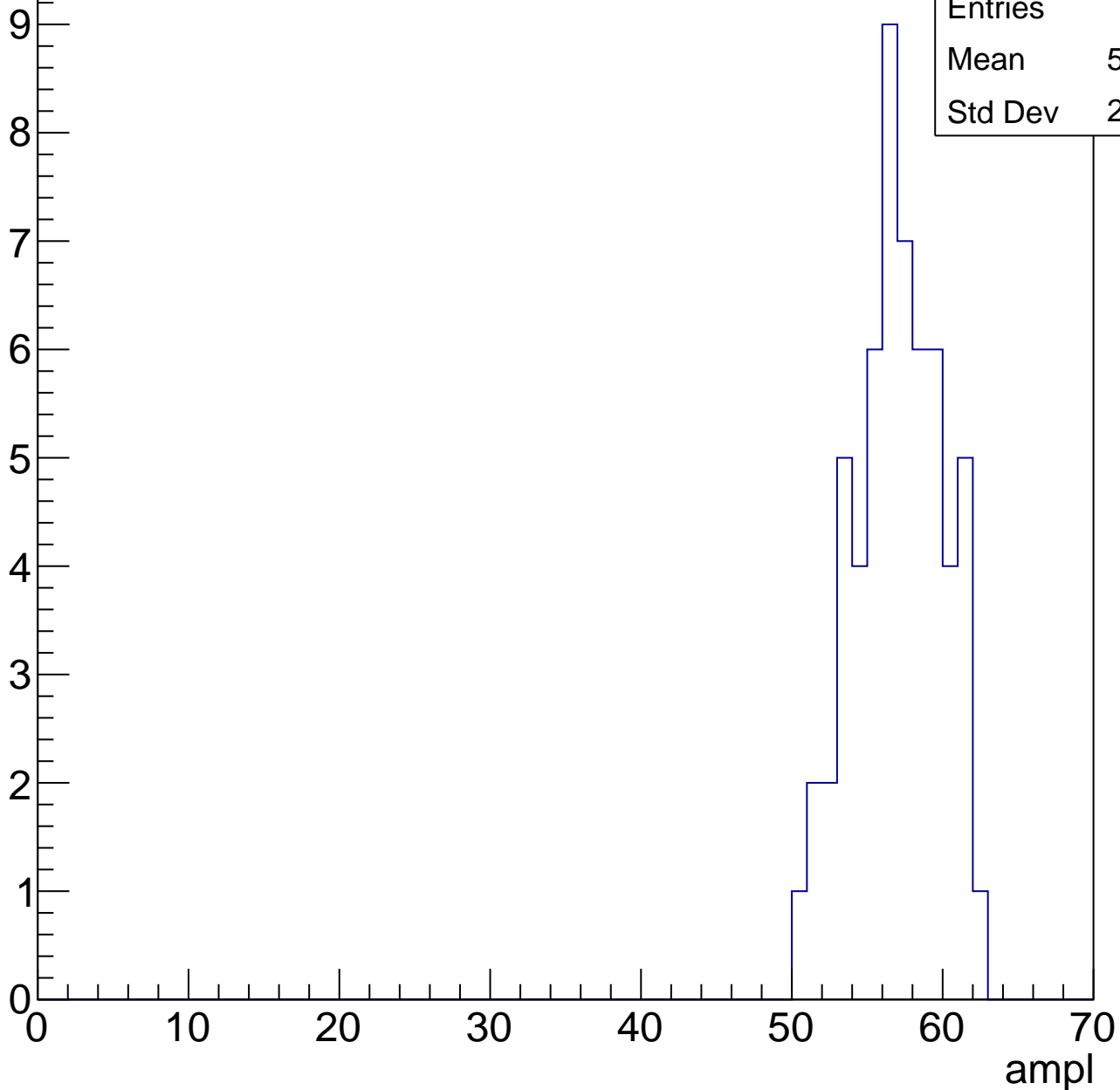


B0L002S, U7-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.53
Std Dev	2.872

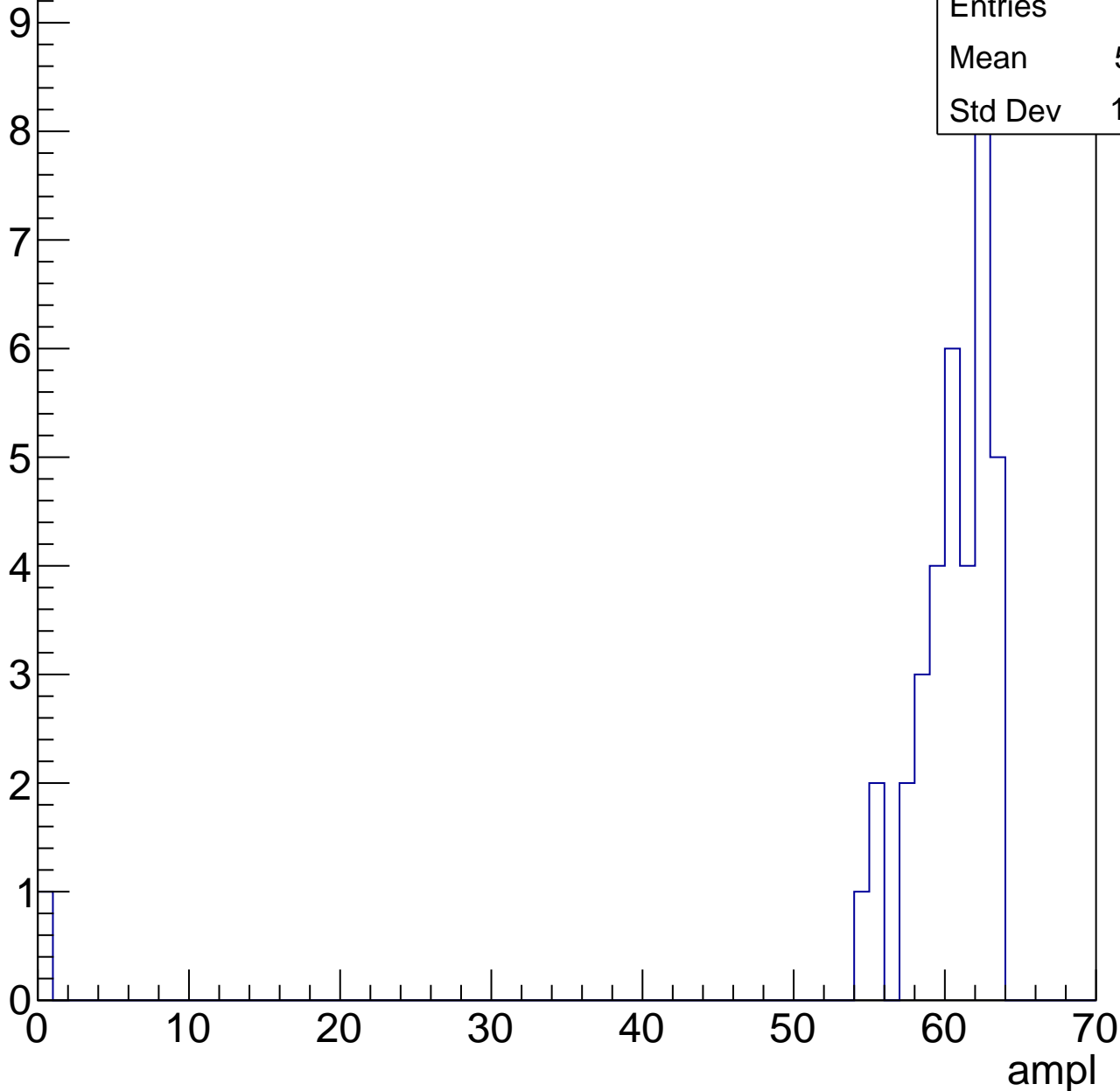


B0L002S, U7-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.51
Std Dev	10.03

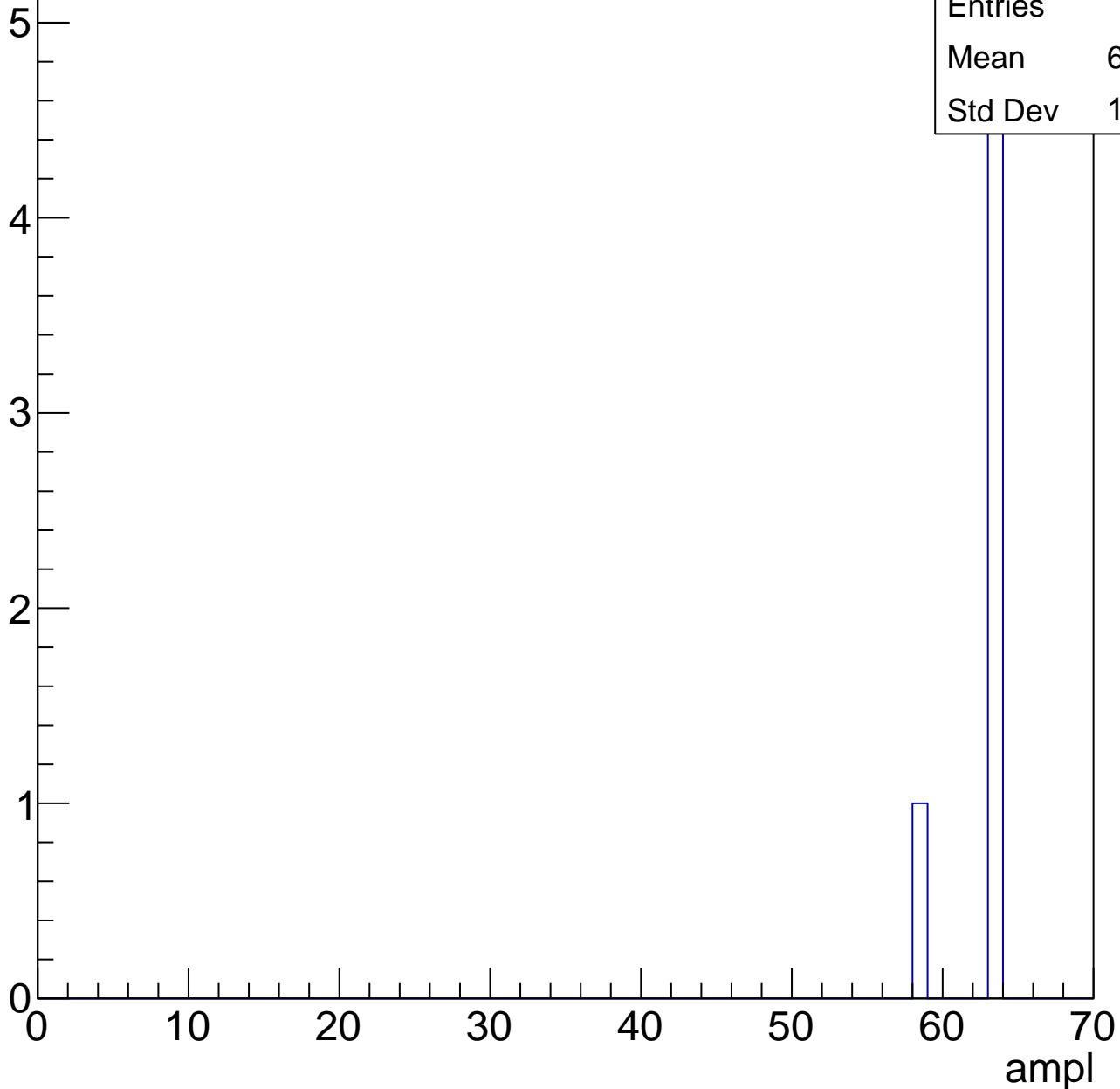


B0L002S, U7-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	6
Mean	62.17
Std Dev	1.863



B0L002S, U7-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch72, adc0

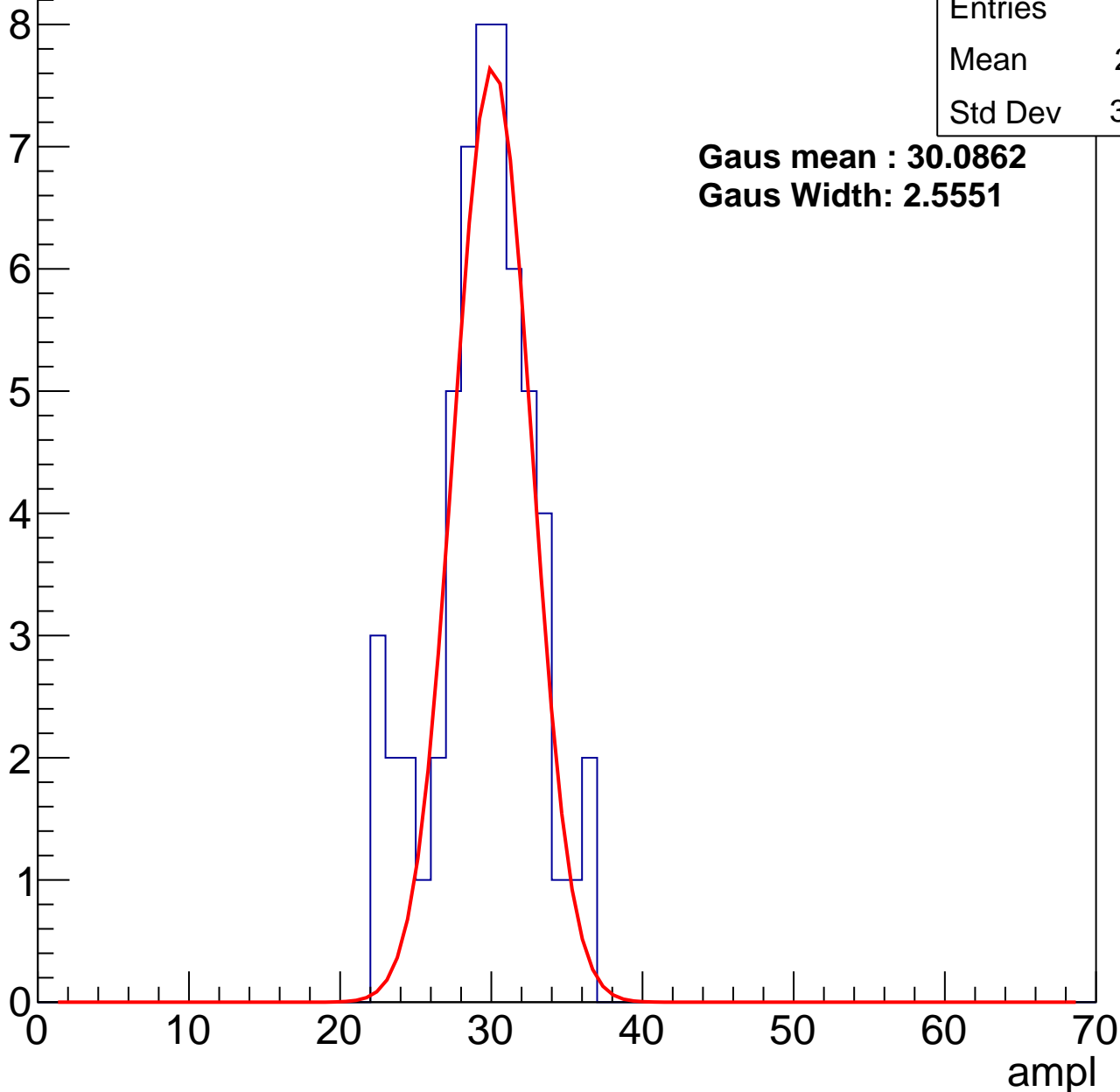
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.11
Std Dev	3.323

Gaus mean : 30.0862

Gaus Width: 2.5551



B0L002S, U7-ch72, adc1

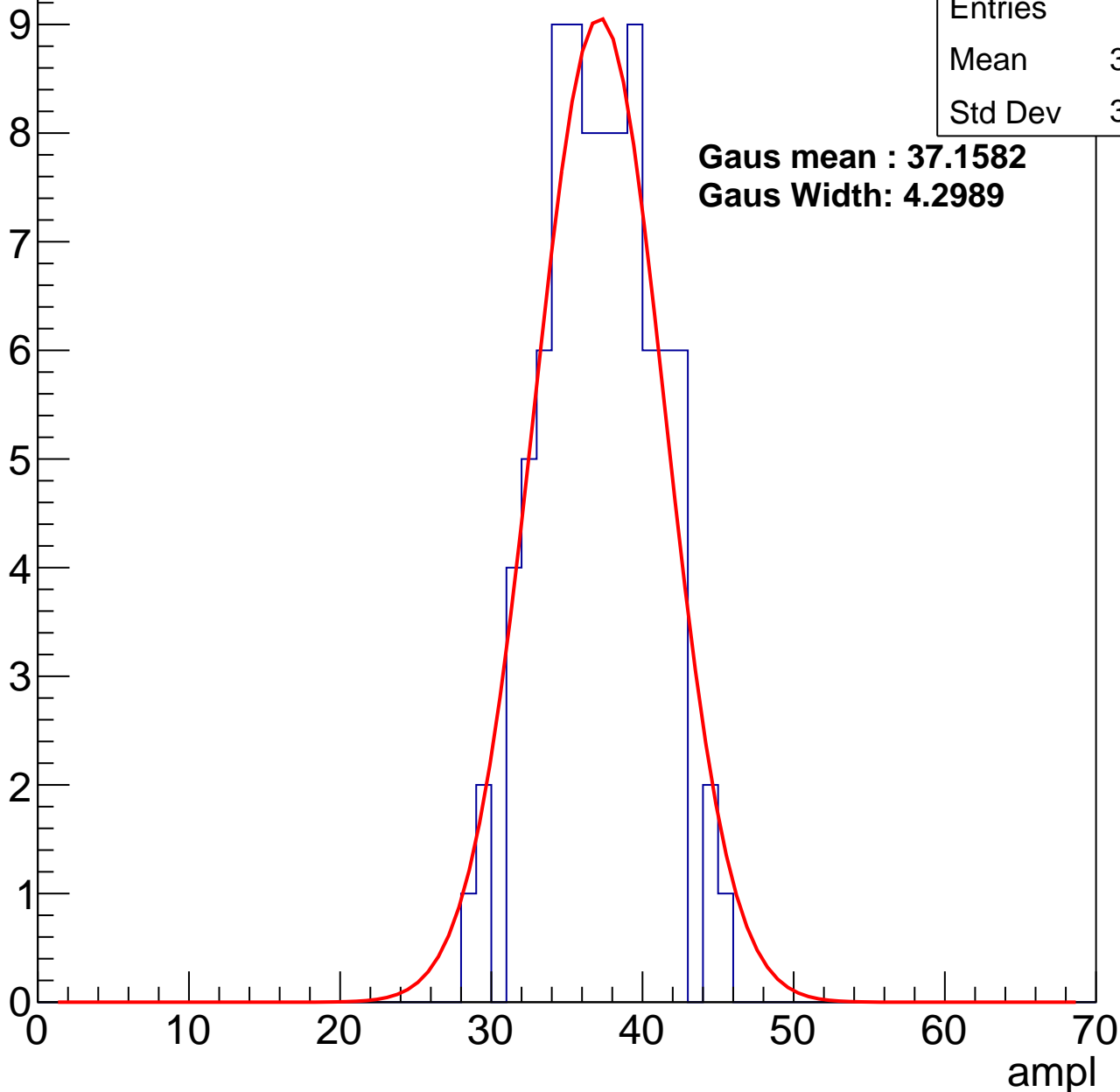
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	36.66
Std Dev	3.643

Gaus mean : 37.1582

Gaus Width: 4.2989



B0L002S, U7-ch72, adc2

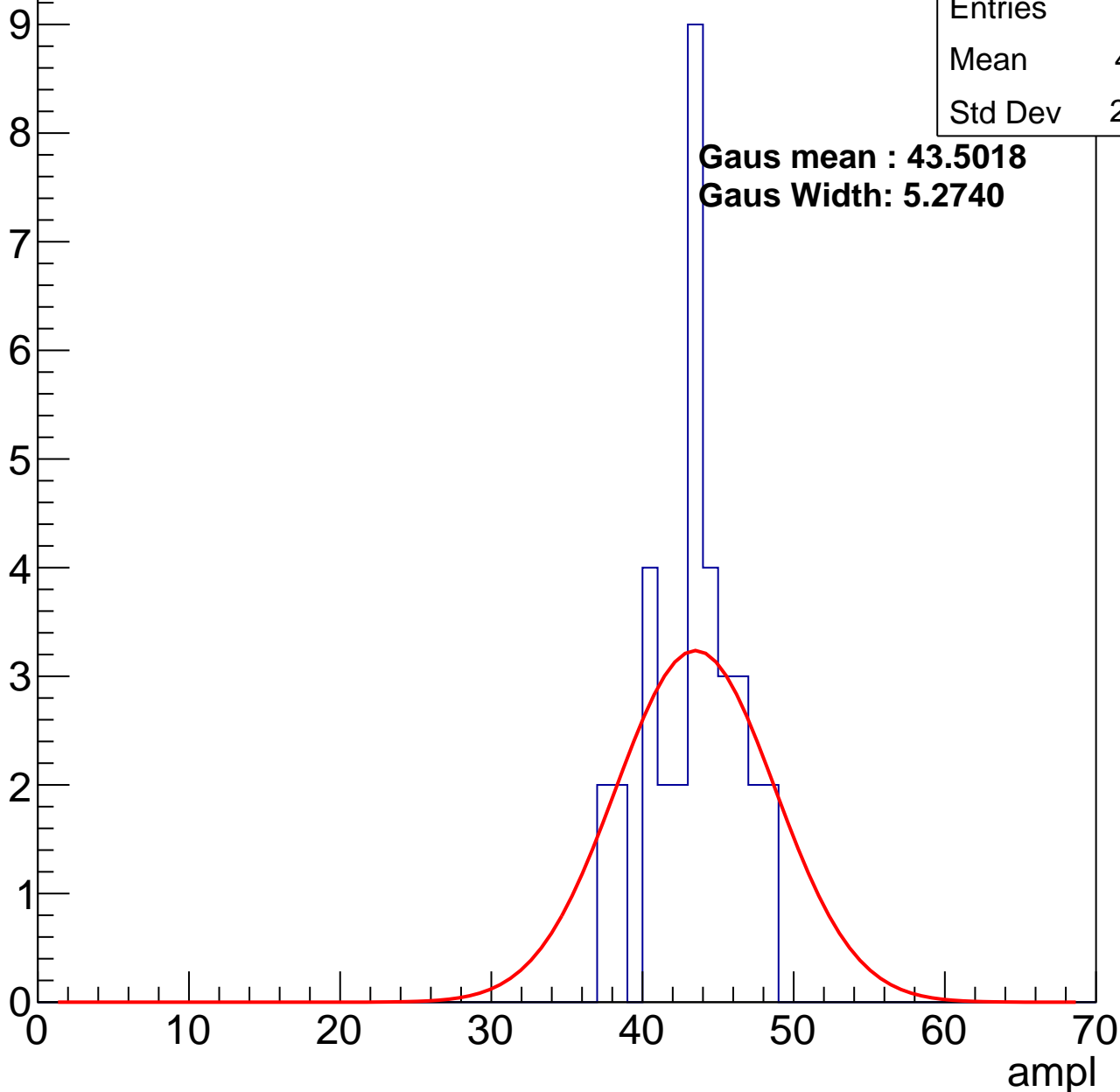
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	42.91
Std Dev	2.892

Gaus mean : 43.5018

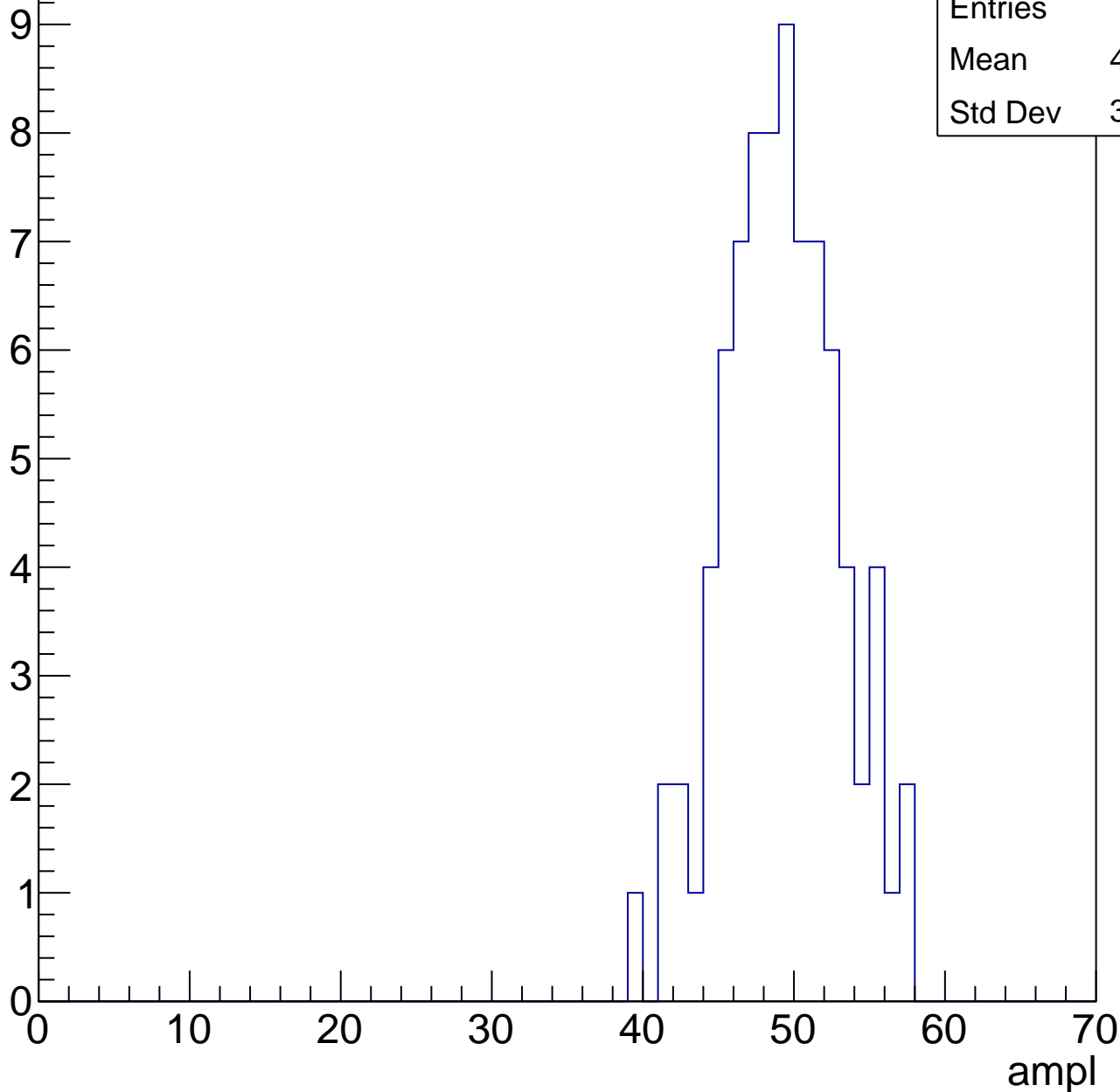
Gaus Width: 5.2740



B0L002S, U7-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



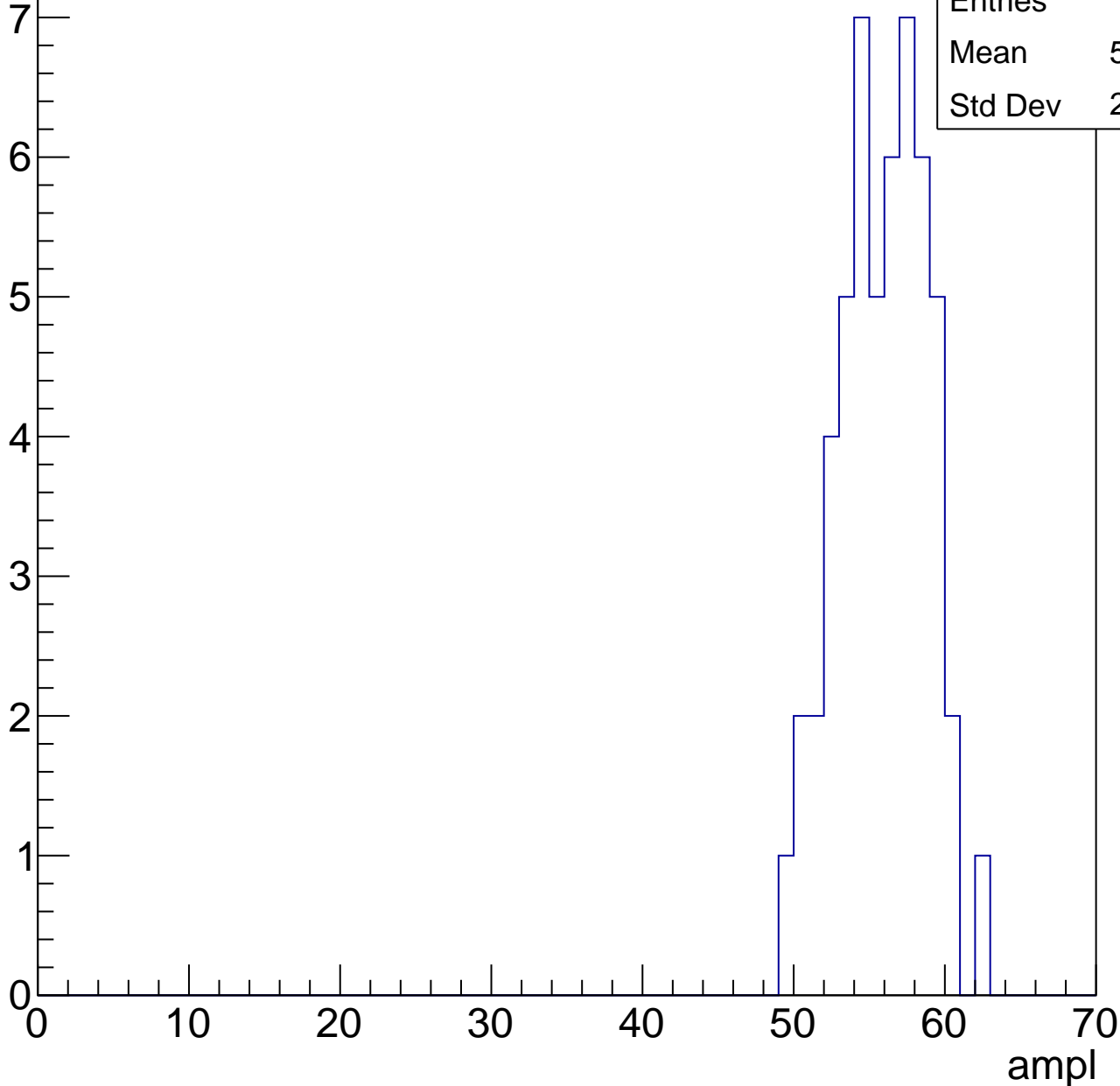
Entries	81
Mean	48.72
Std Dev	3.814

B0L002S, U7-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

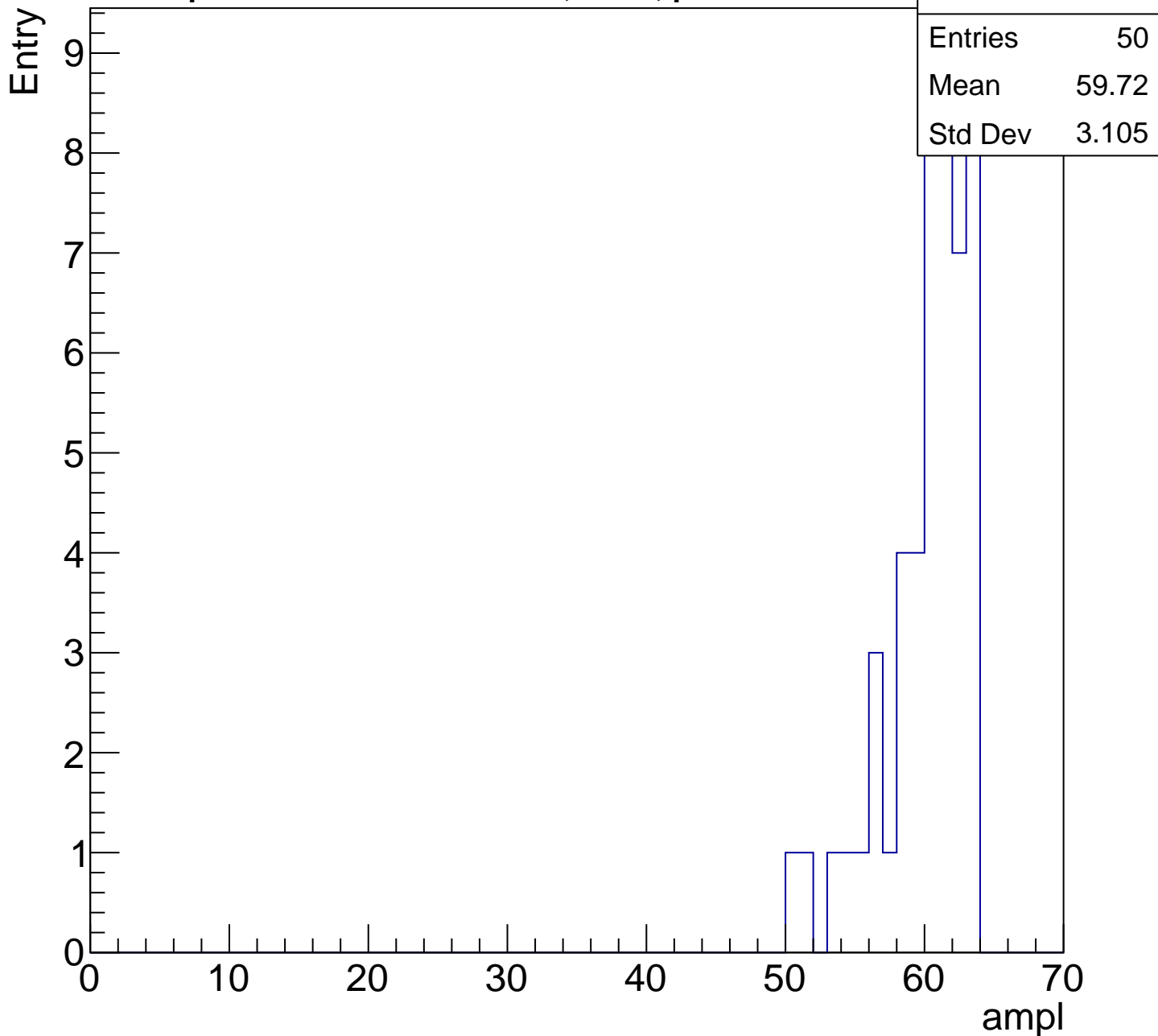
Entry

Entries	53
Mean	55.42
Std Dev	2.878



B0L002S, U7-ch72, adc5

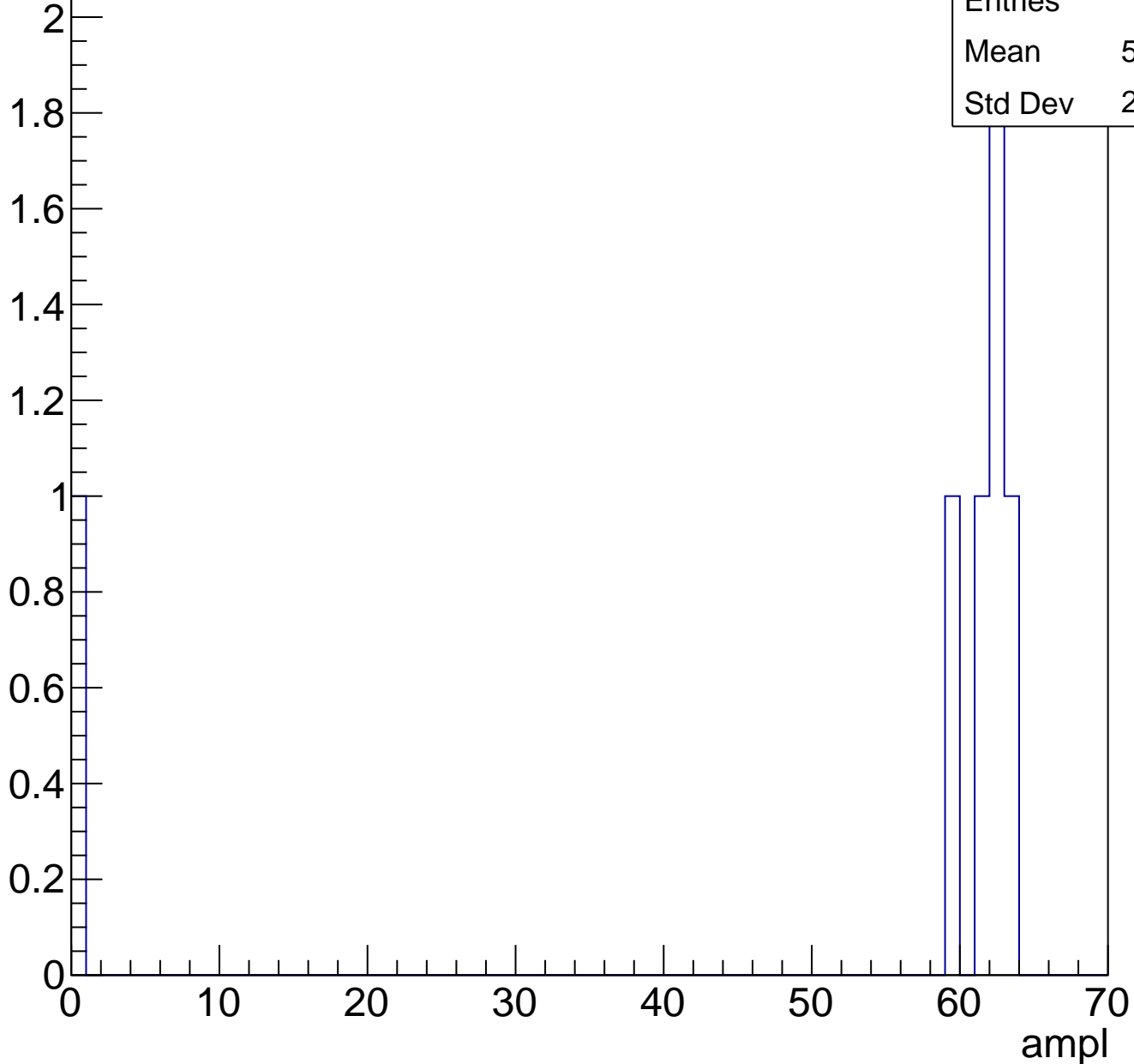
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch73, adc0

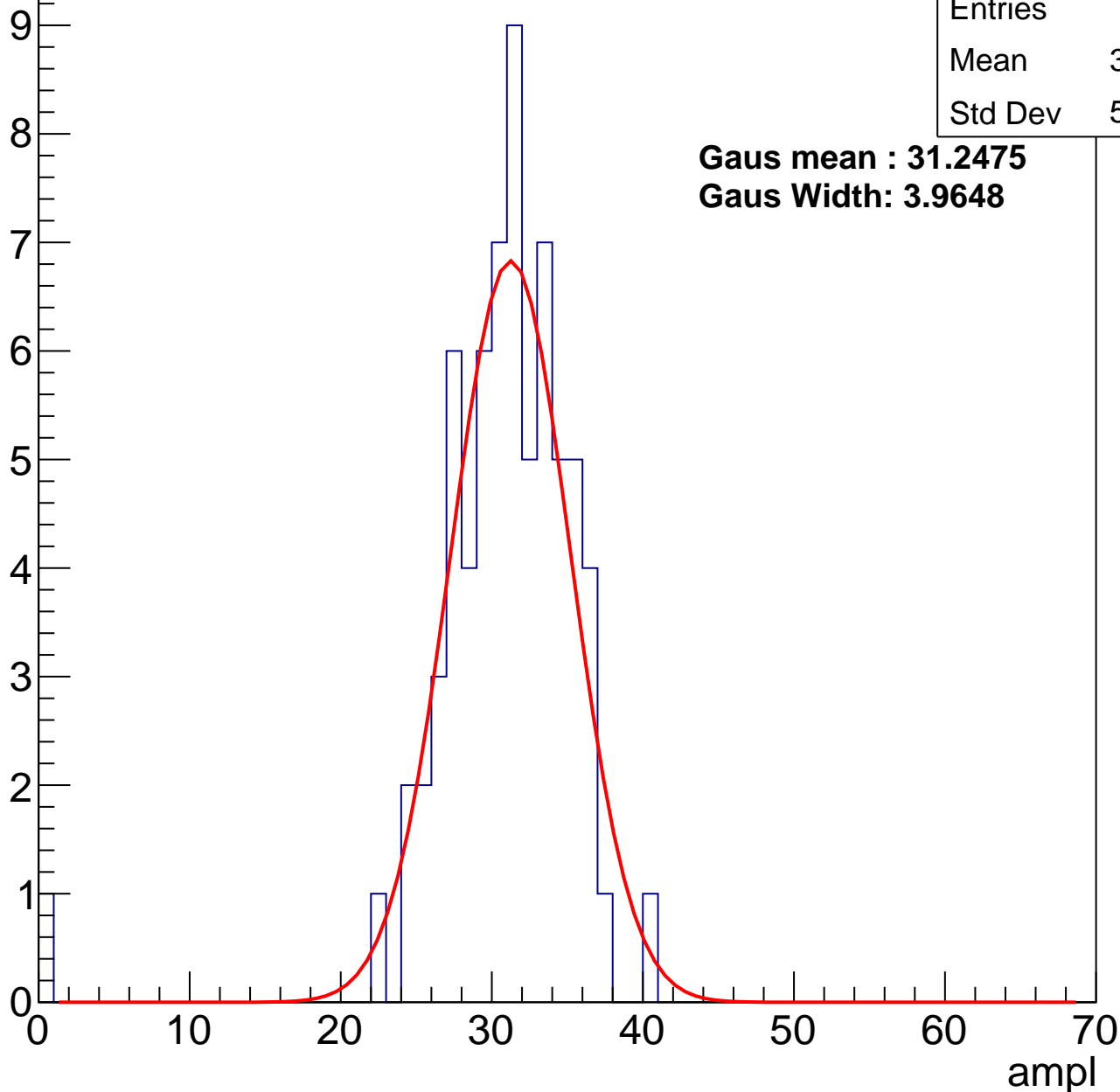
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.32
Std Dev	5.098

Gaus mean : 31.2475

Gaus Width: 3.9648



B0L002S, U7-ch73, adc1

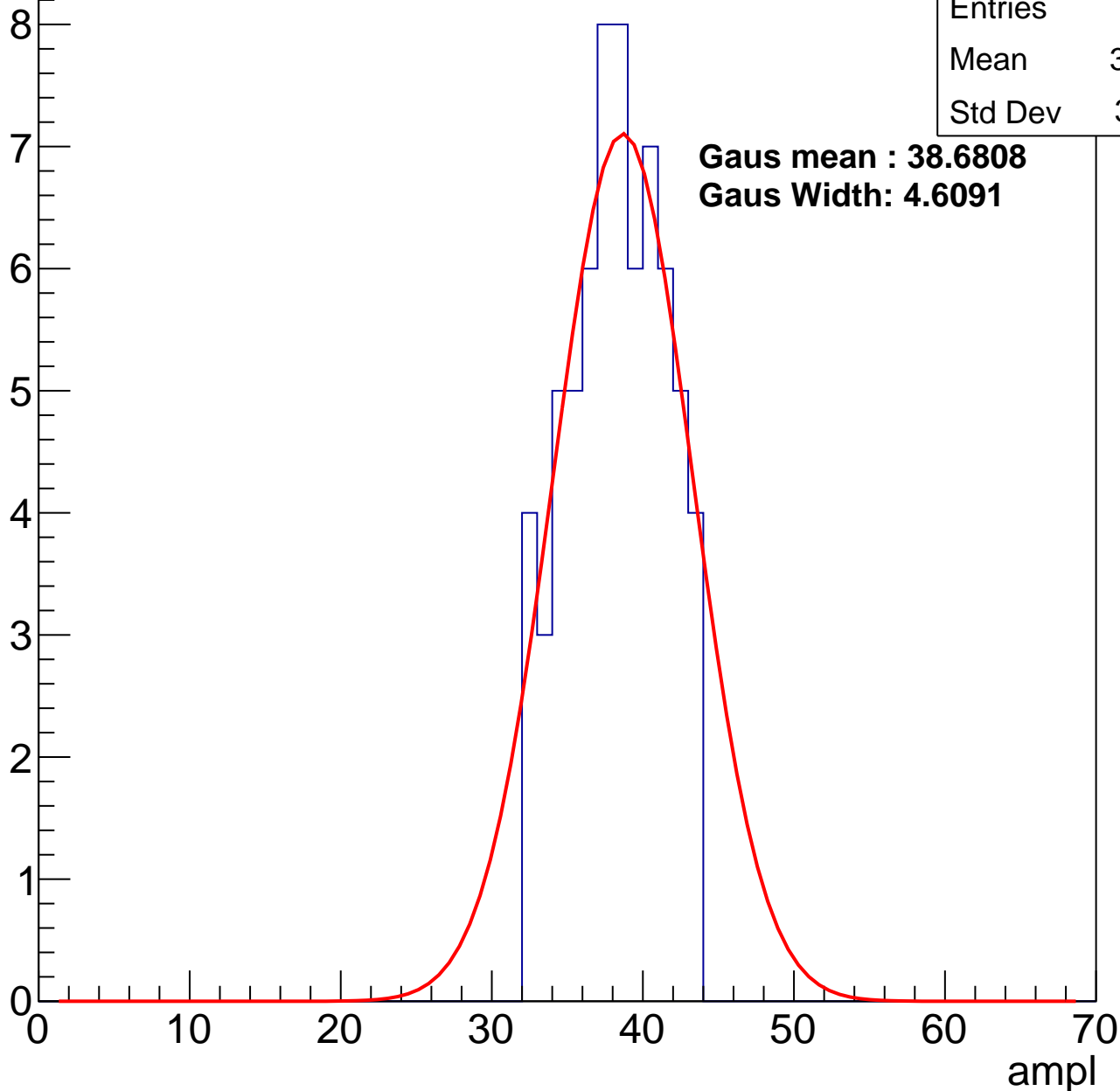
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.76
Std Dev	3.091

Gaus mean : 38.6808

Gaus Width: 4.6091



B0L002S, U7-ch73, adc2

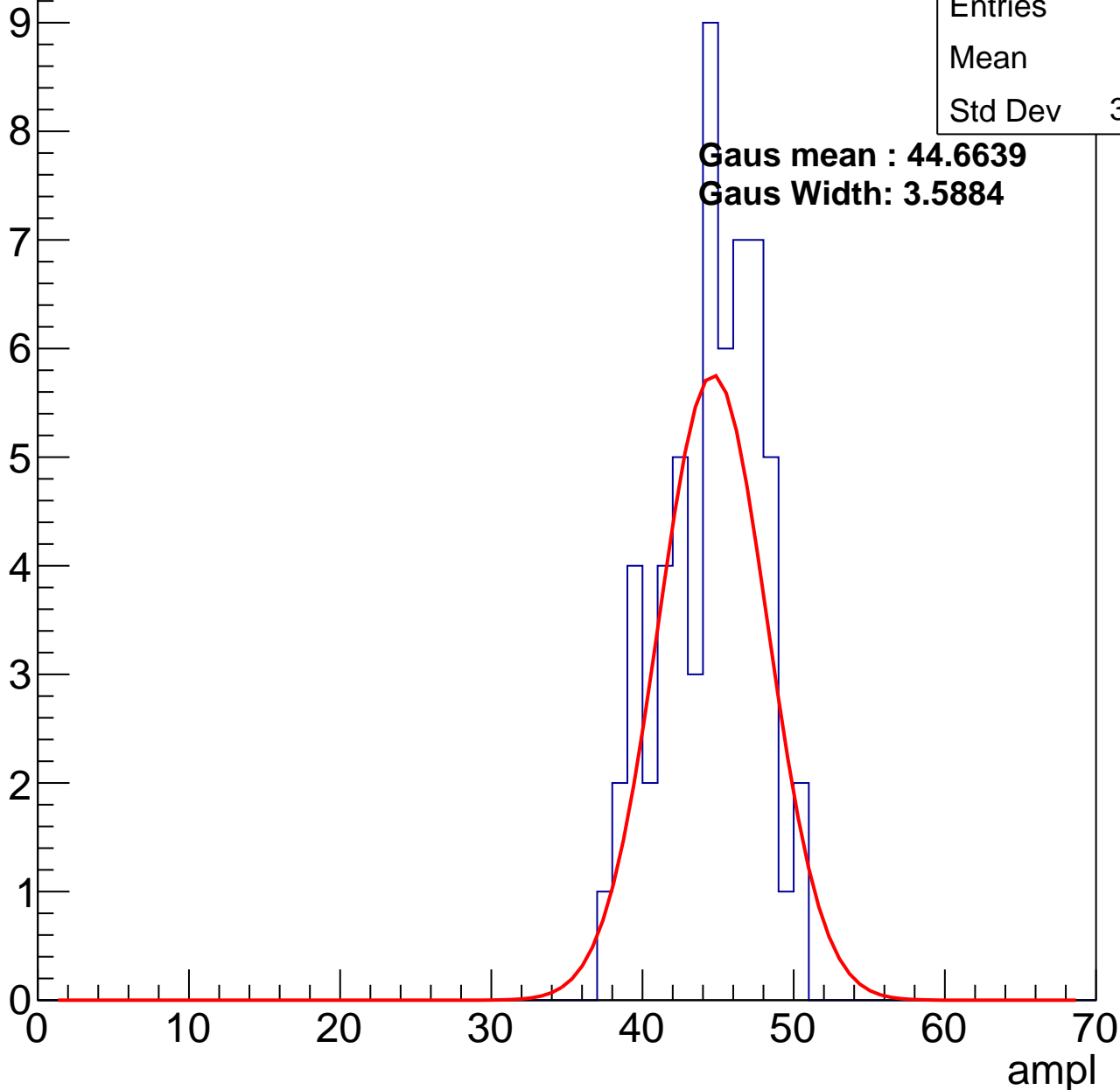
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.1
Std Dev	3.177

Gaus mean : 44.6639

Gaus Width: 3.5884

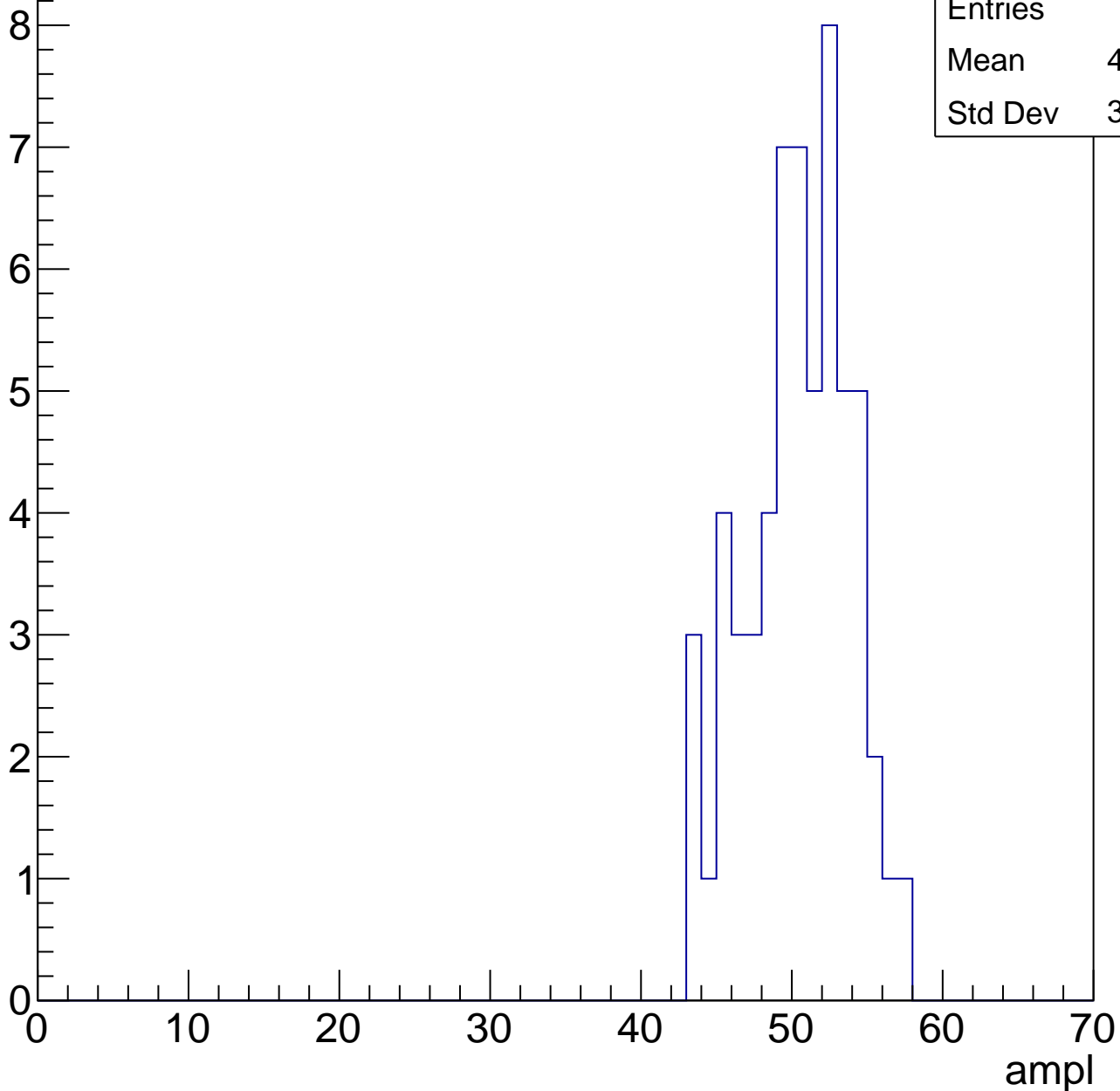


B0L002S, U7-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.93
Std Dev	3.389

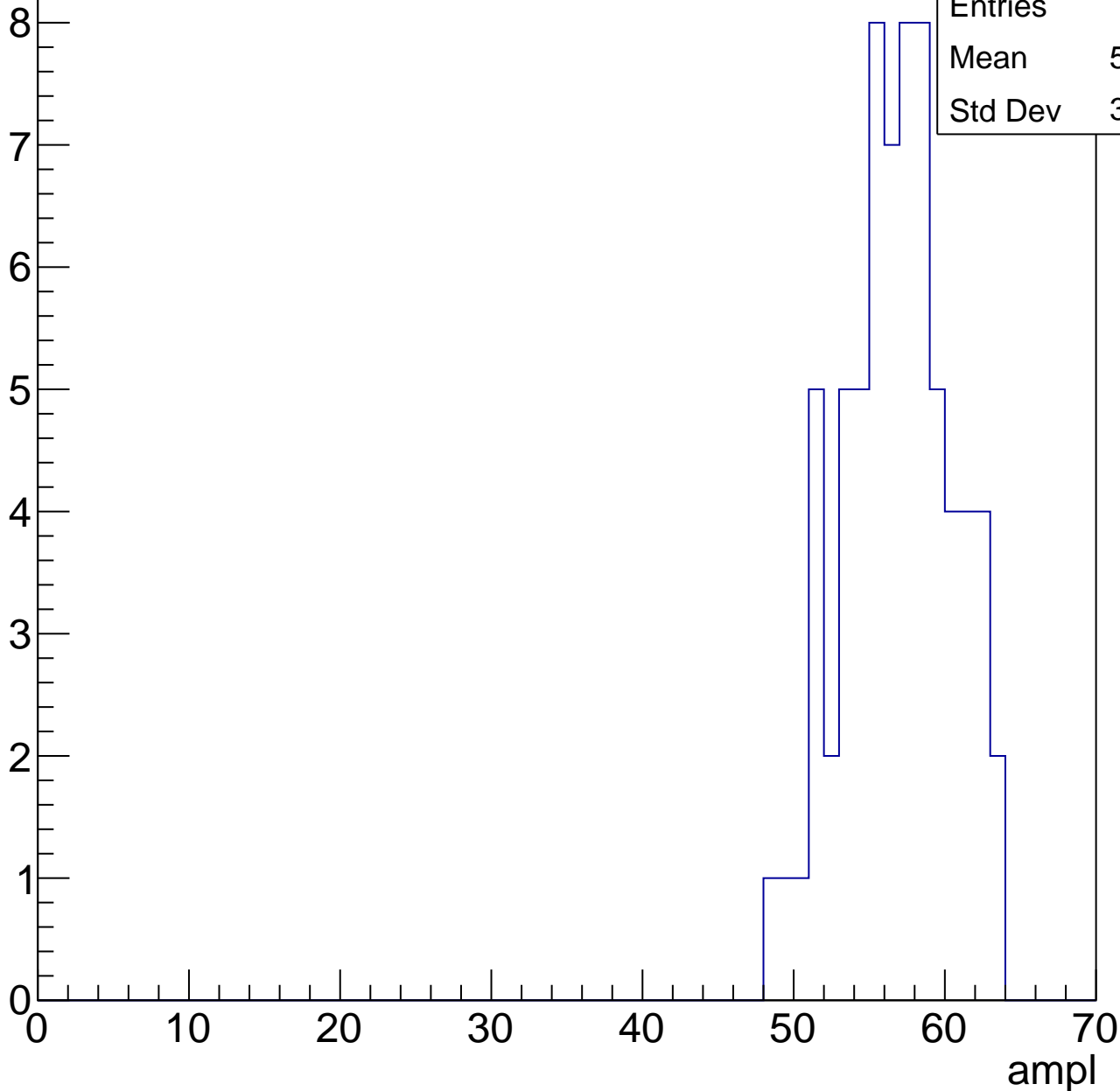


B0L002S, U7-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	56.37
Std Dev	3.506

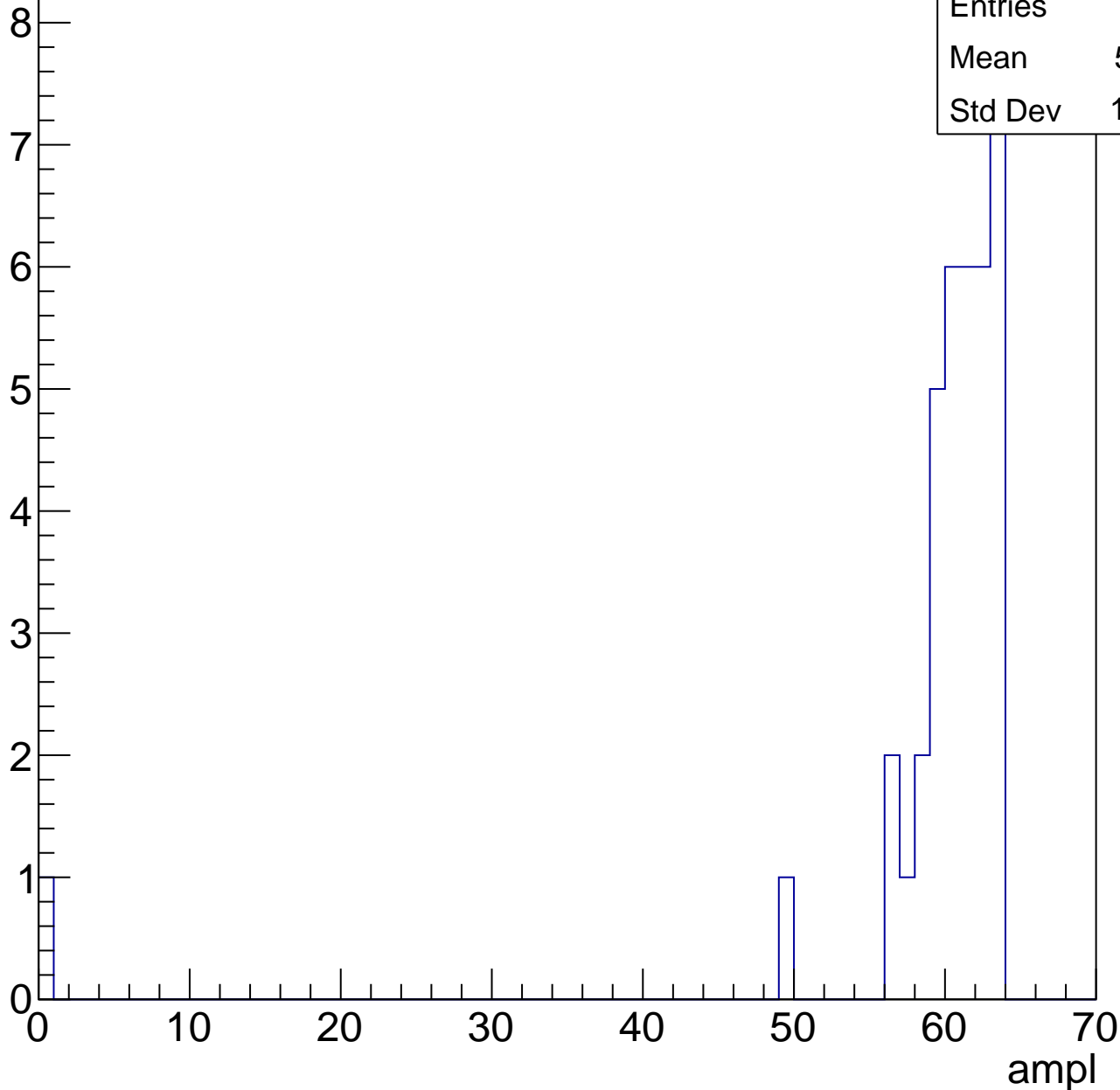


B0L002S, U7-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.71
Std Dev	10.02



B0L002S, U7-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch74, adc0

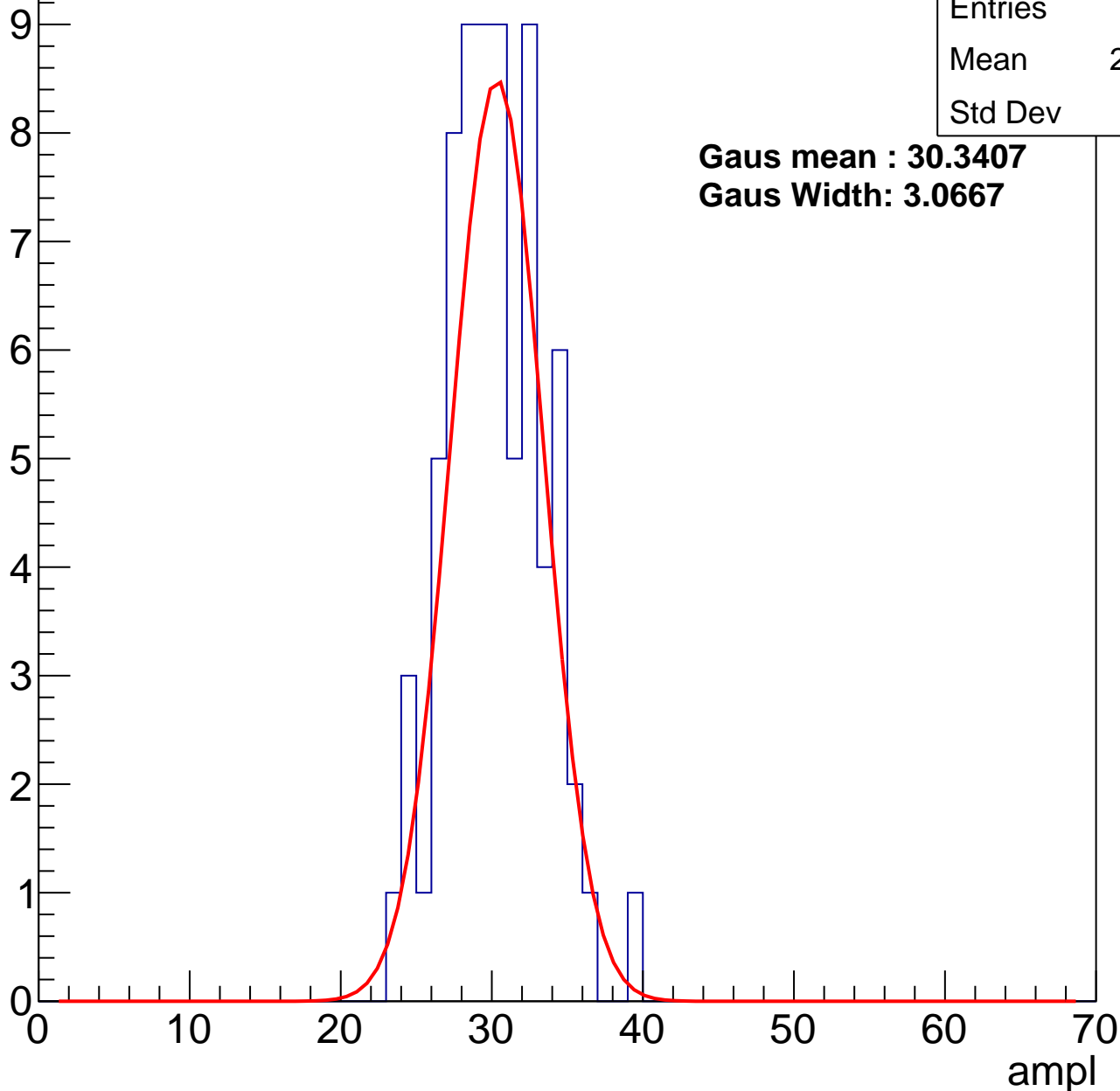
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.77
Std Dev	3.13

Gaus mean : 30.3407

Gaus Width: 3.0667



B0L002S, U7-ch74, adc1

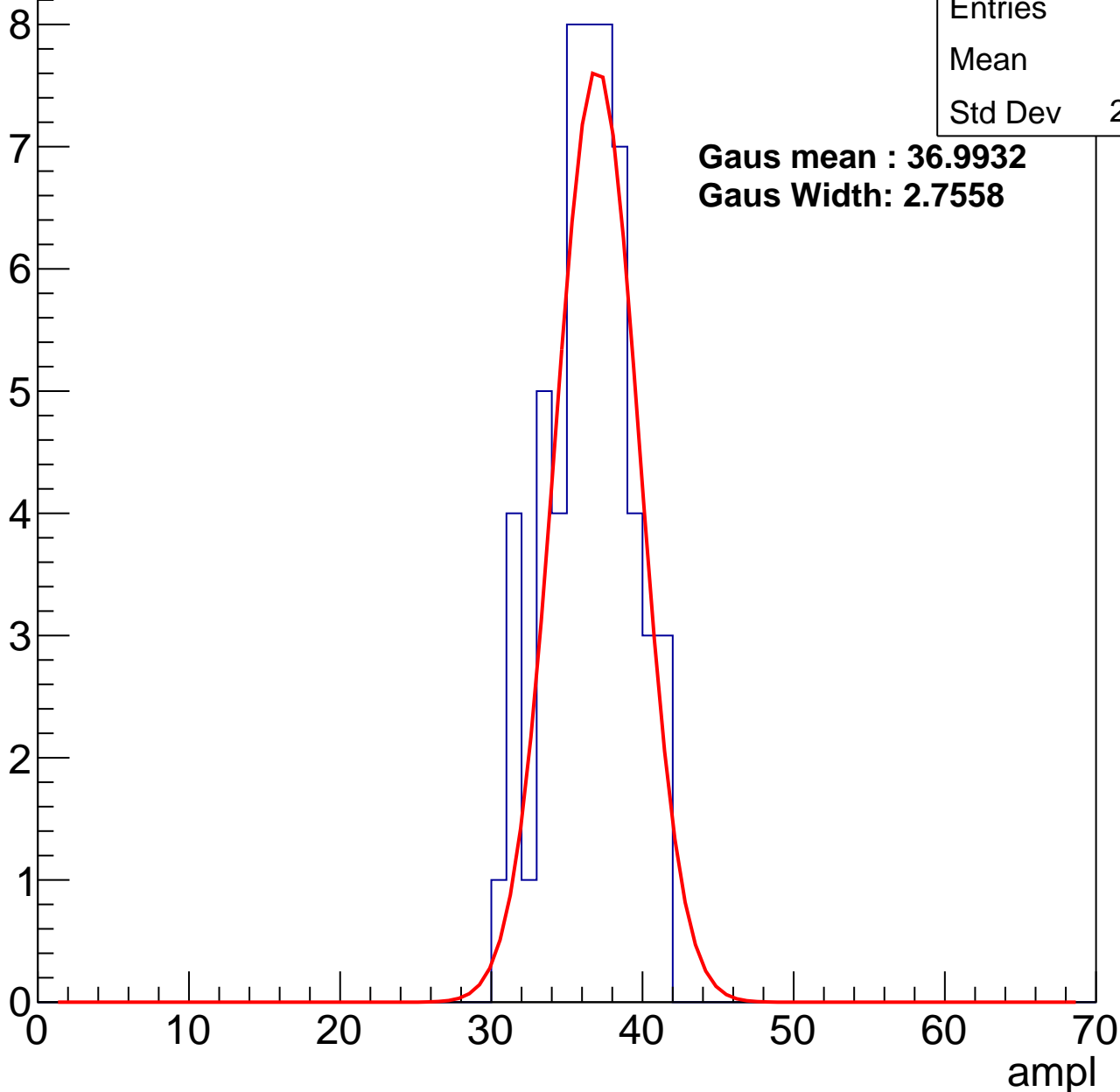
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36
Std Dev	2.726

Gaus mean : 36.9932

Gaus Width: 2.7558



B0L002S, U7-ch74, adc2

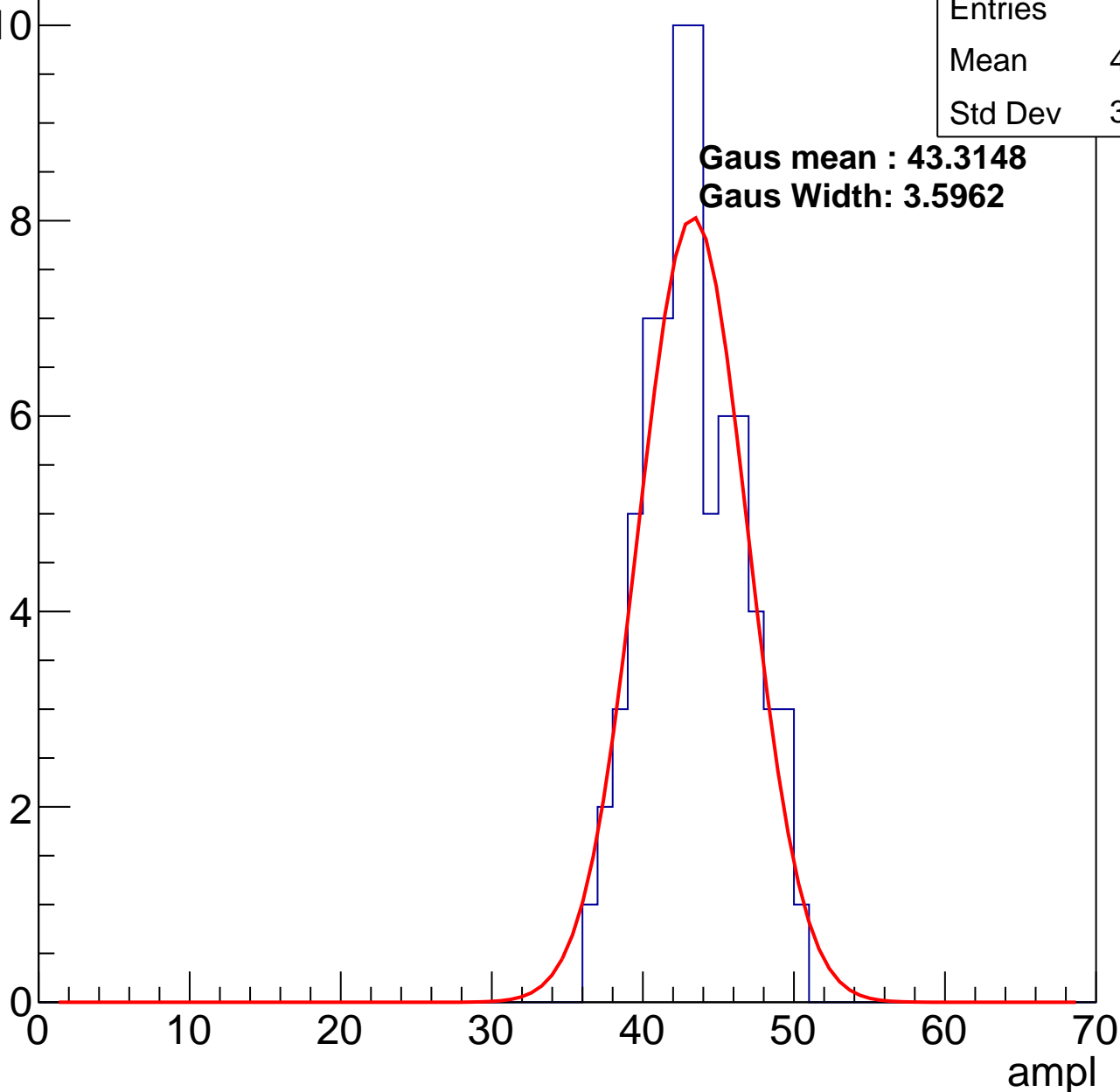
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.89
Std Dev	3.216

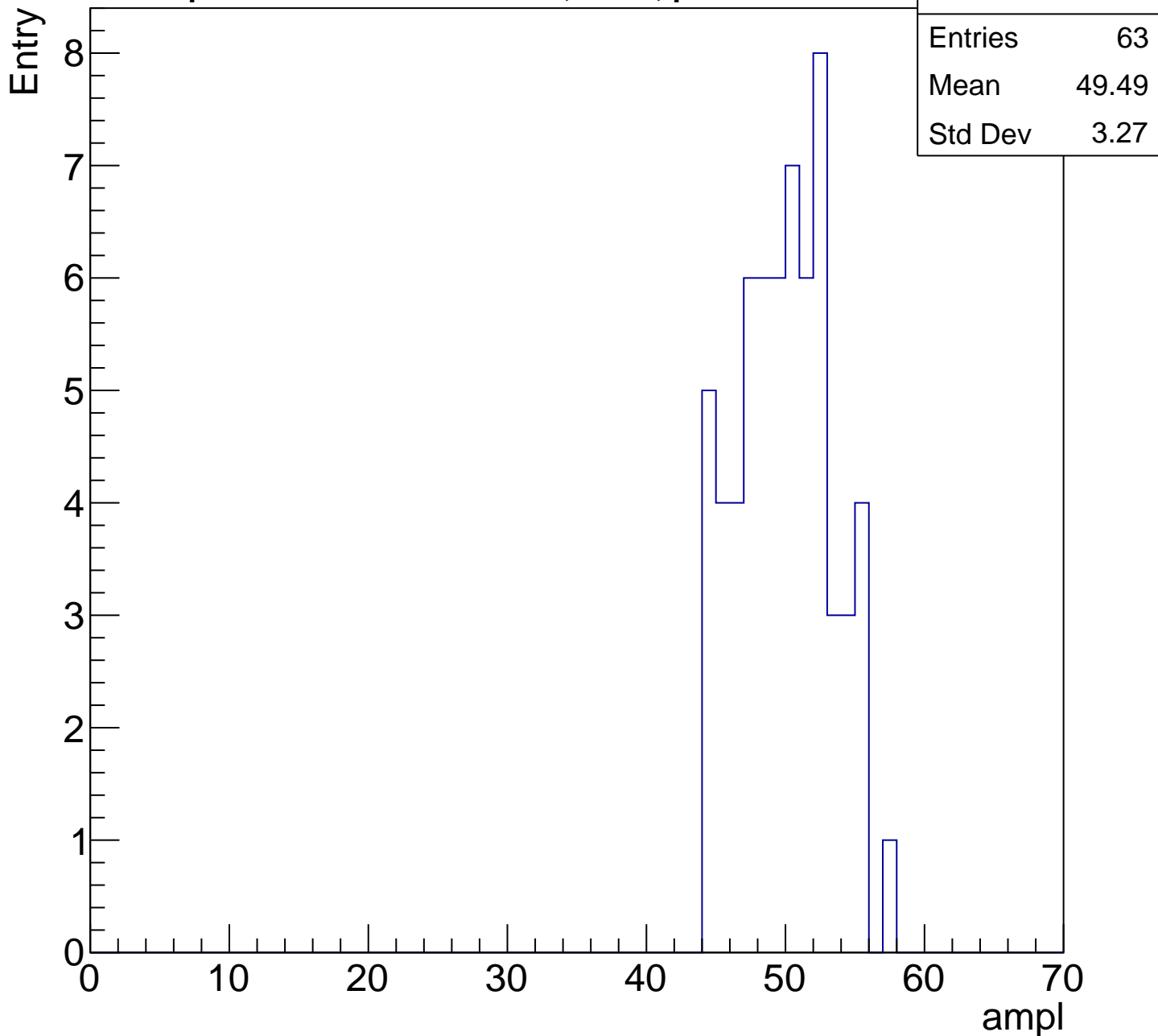
Gaus mean : 43.3148

Gaus Width: 3.5962



B0L002S, U7-ch74, adc3

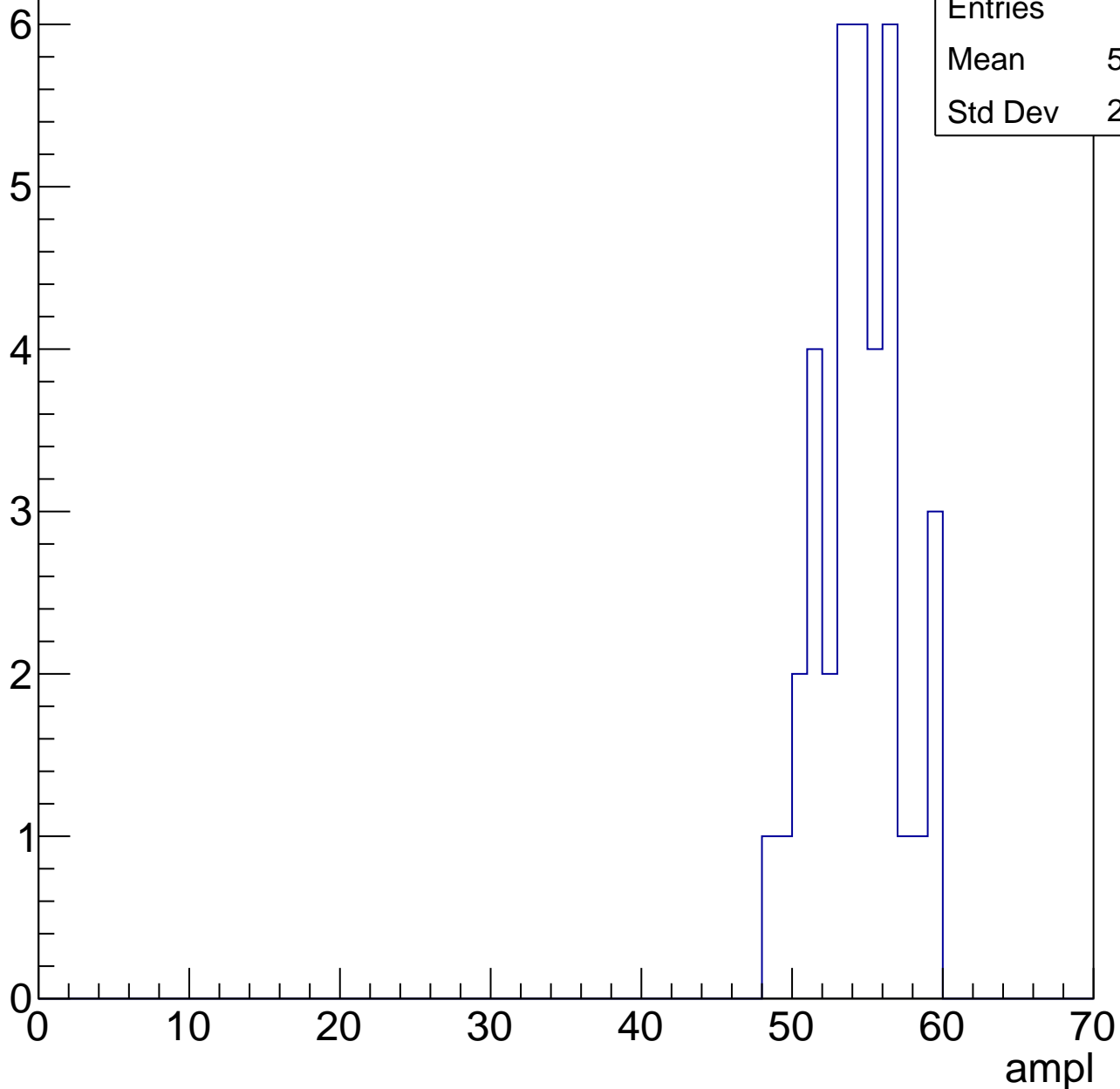
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	37
Mean	53.92
Std Dev	2.705

B0L002S, U7-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 67

Mean 58.46

Std Dev 7.585

8

6

4

2

0

0

10

20

30

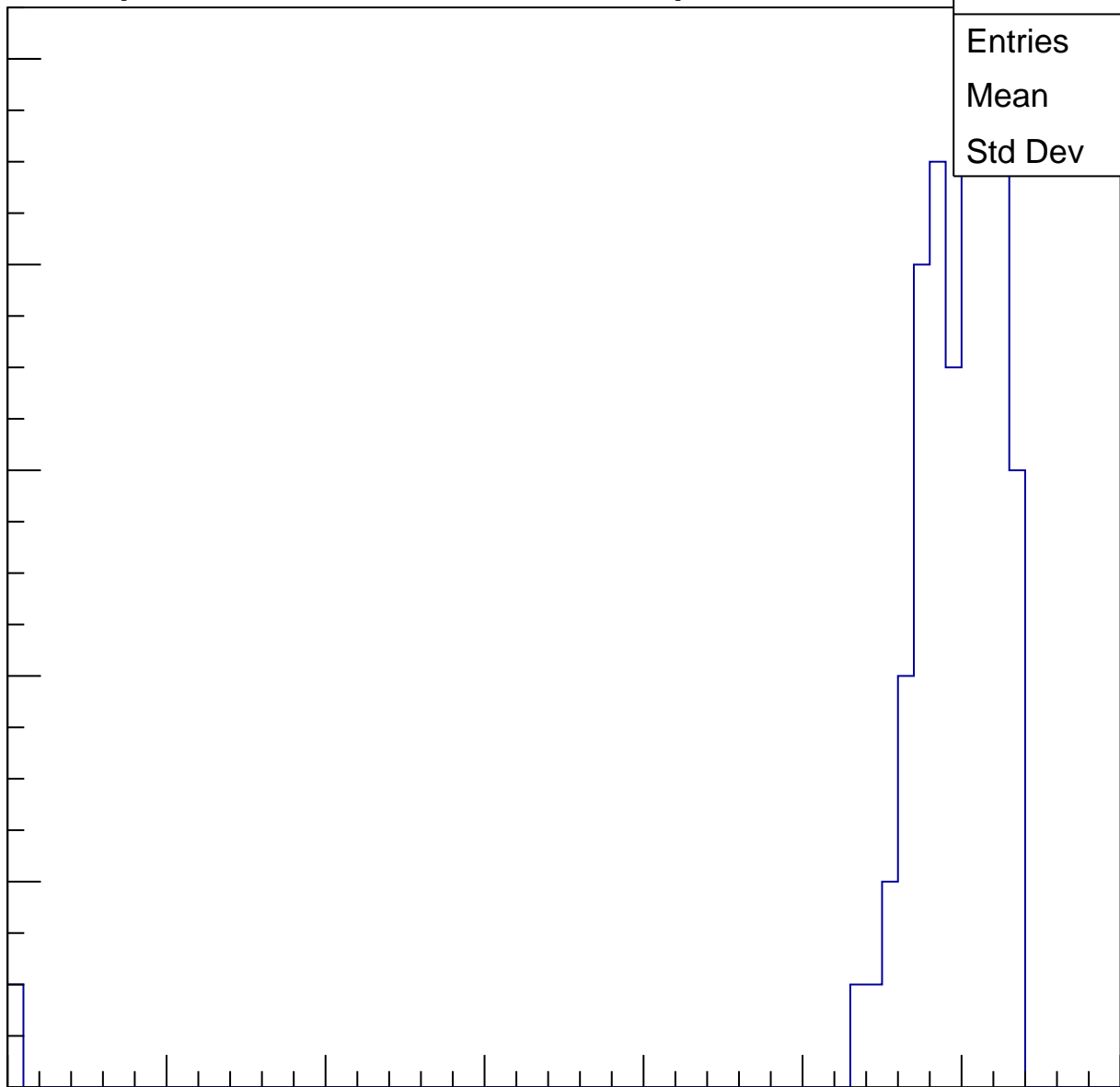
40

50

60

70

ampl



B0L002S, U7-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U7-ch75, adc0

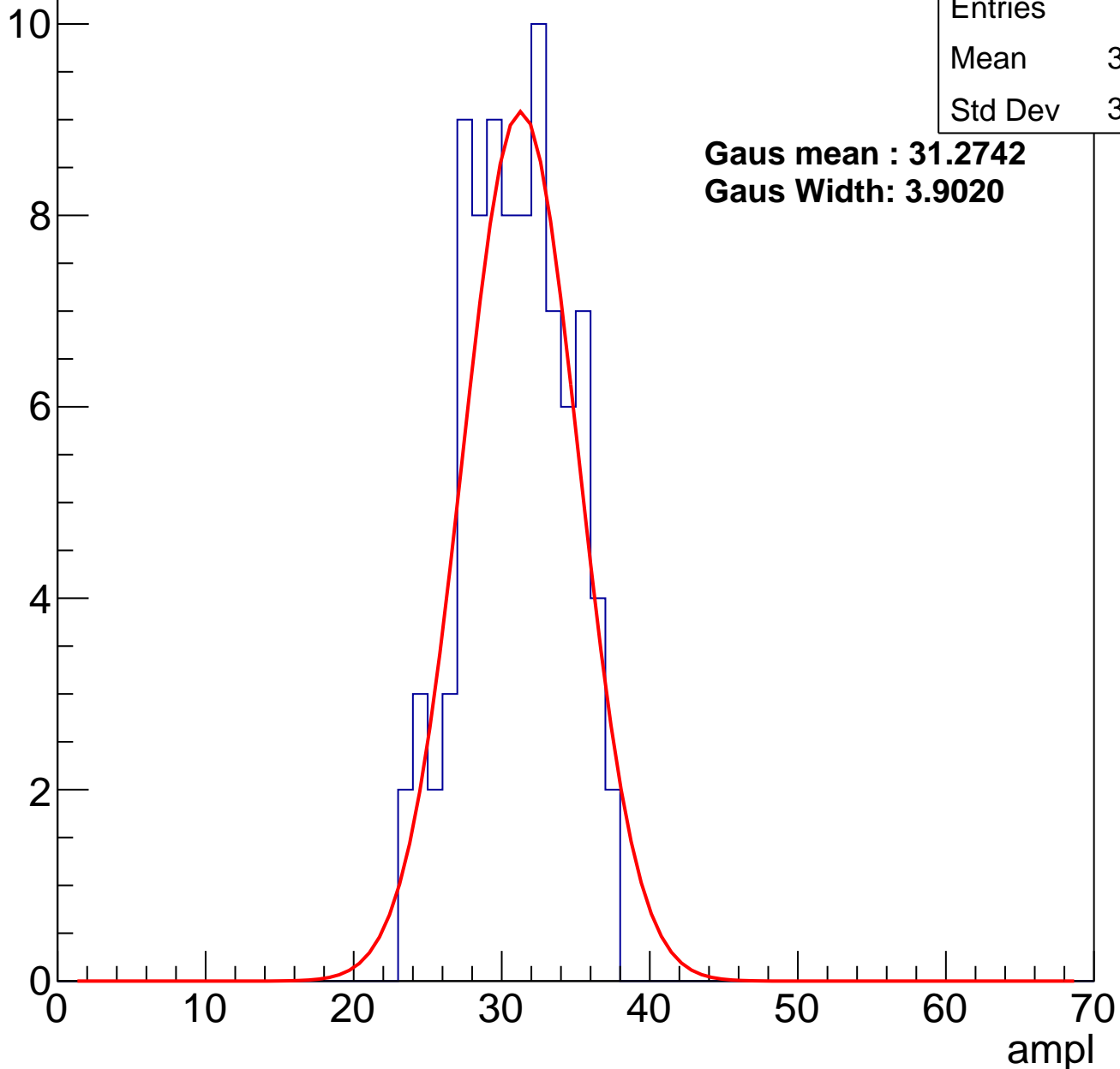
calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	30.45
Std Dev	3.424

Gaus mean : 31.2742

Gaus Width: 3.9020

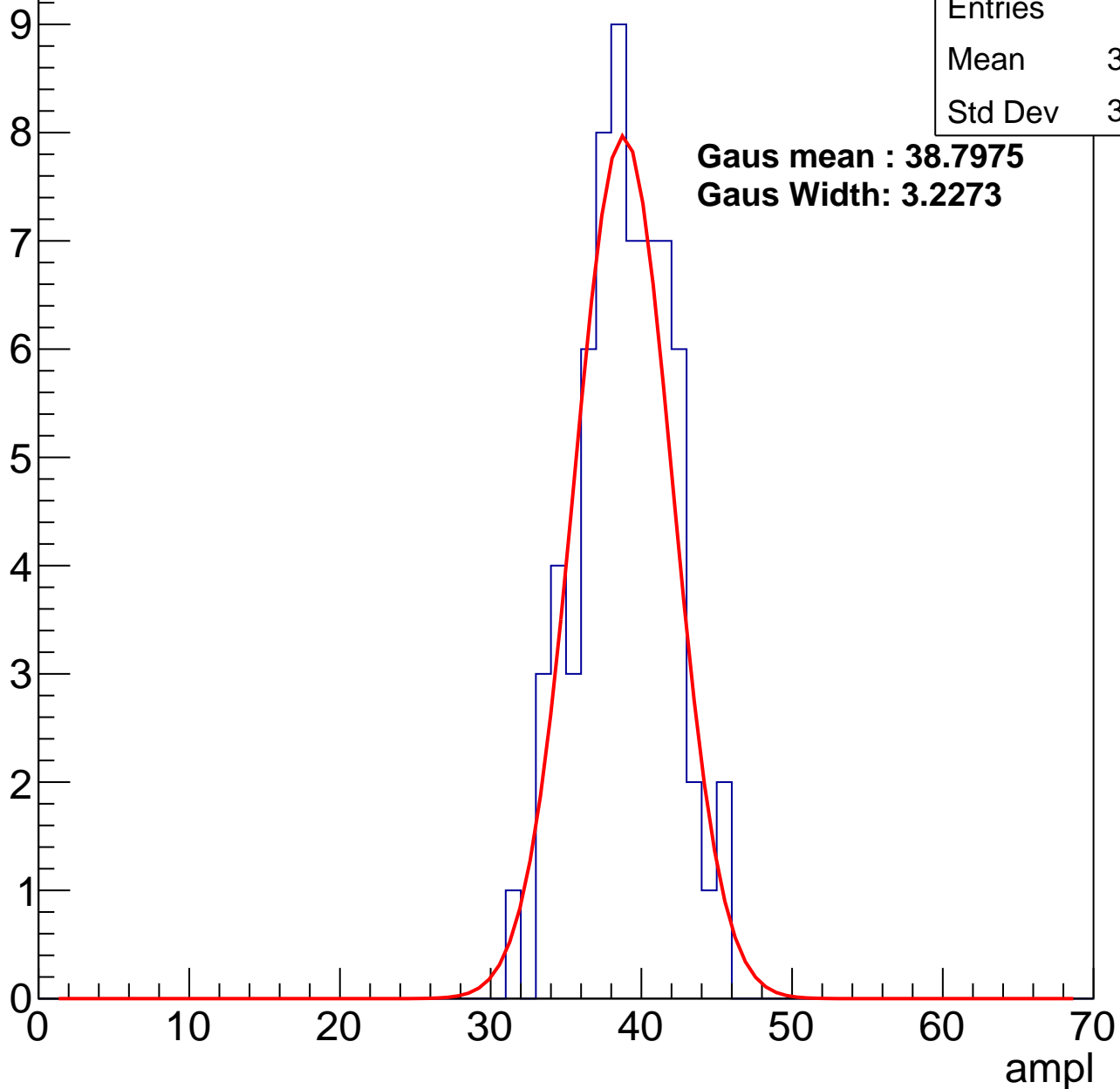
Entry



B0L002S, U7-ch75, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch75, adc2

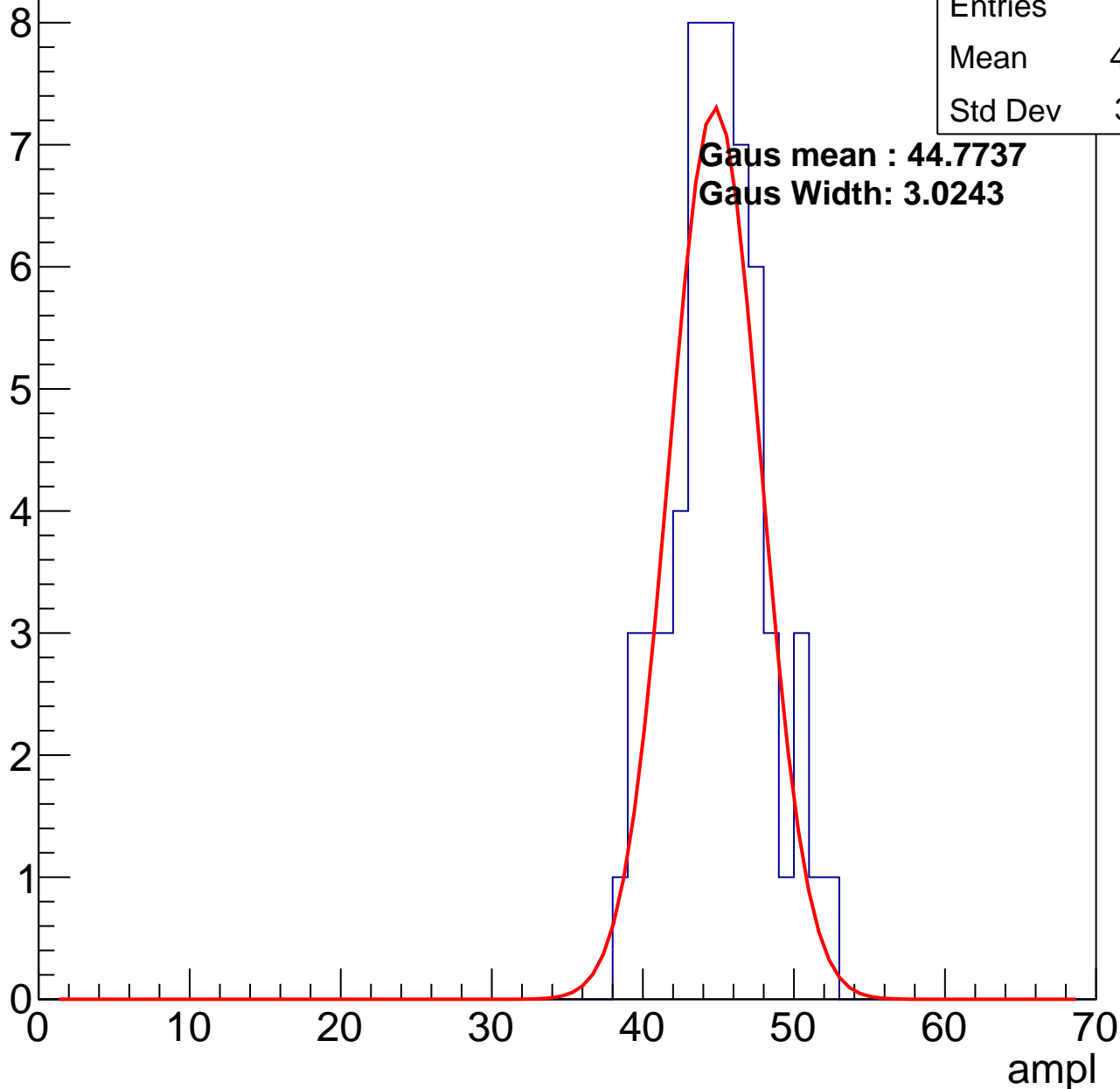
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.53
Std Dev	3.101

Gaus mean : 44.7737

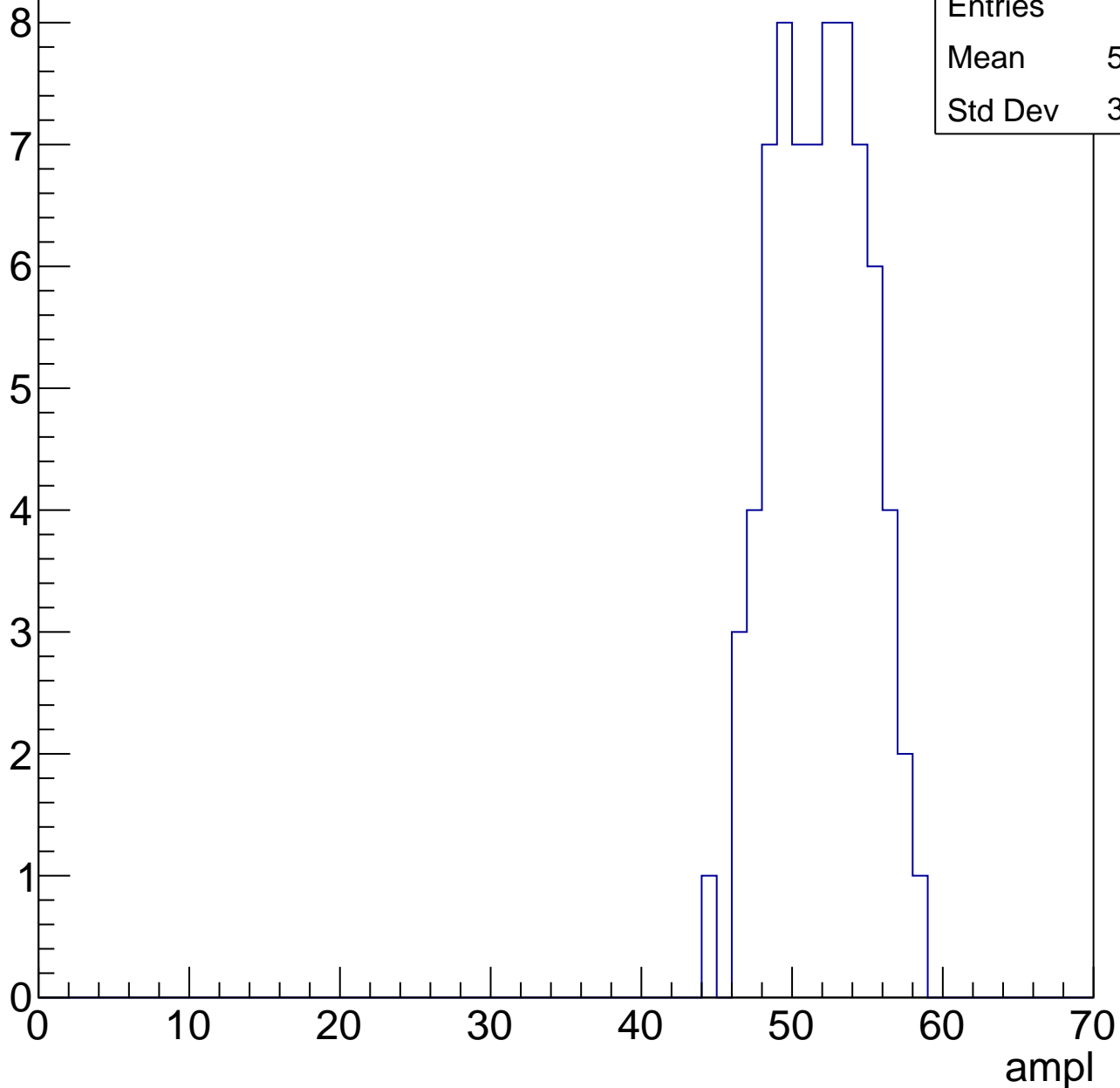
Gaus Width: 3.0243



B0L002S, U7-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

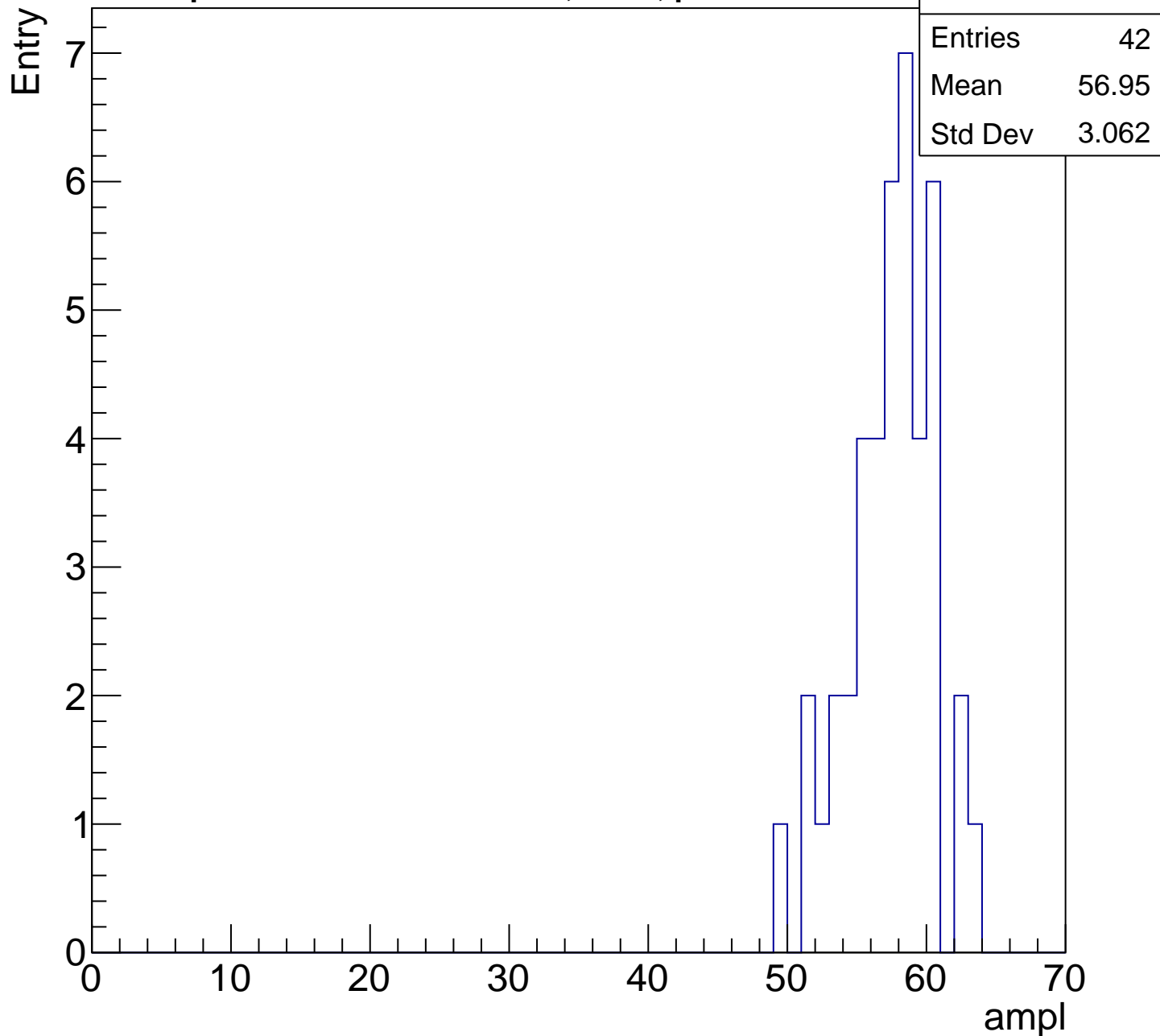
Entry



Entries	73
Mean	51.36
Std Dev	3.098

B0L002S, U7-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

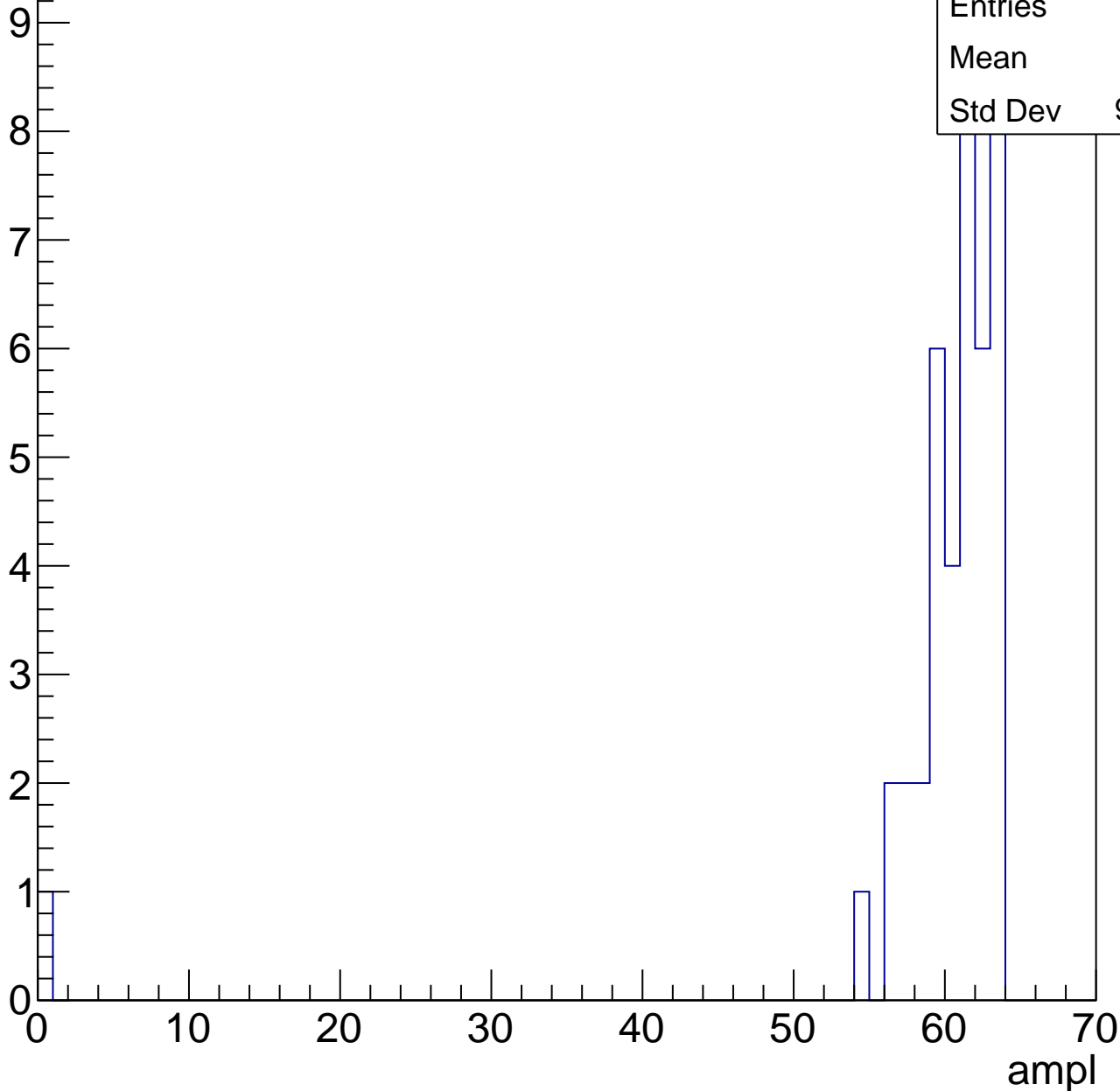


B0L002S, U7-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.9
Std Dev	9.571



B0L002S, U7-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

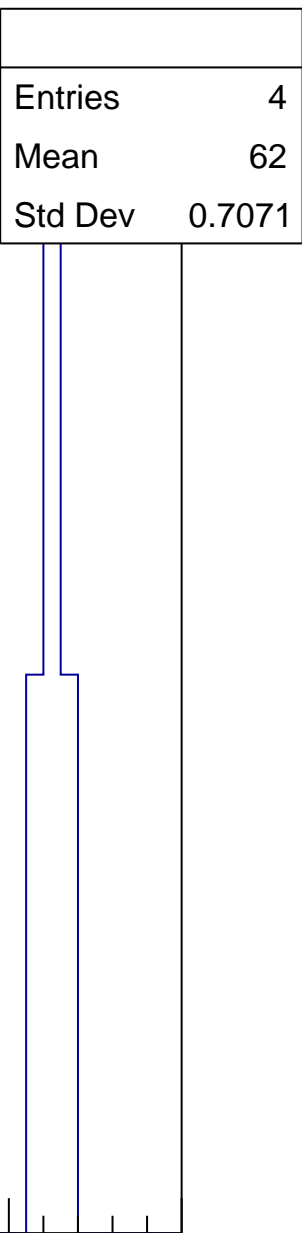
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch76, adc0

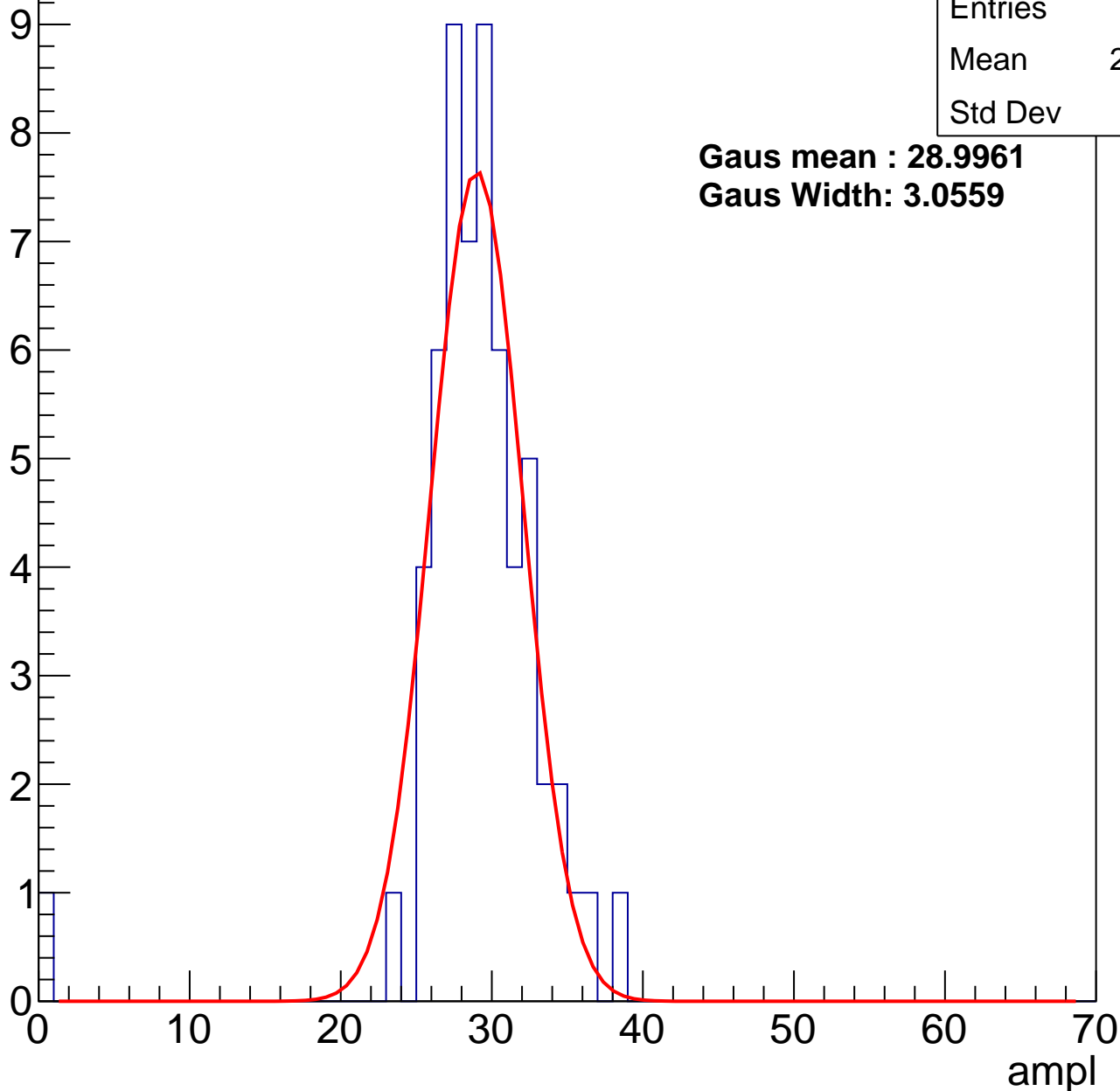
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	28.58
Std Dev	4.77

Gaus mean : 28.9961

Gaus Width: 3.0559



B0L002S, U7-ch76, adc1

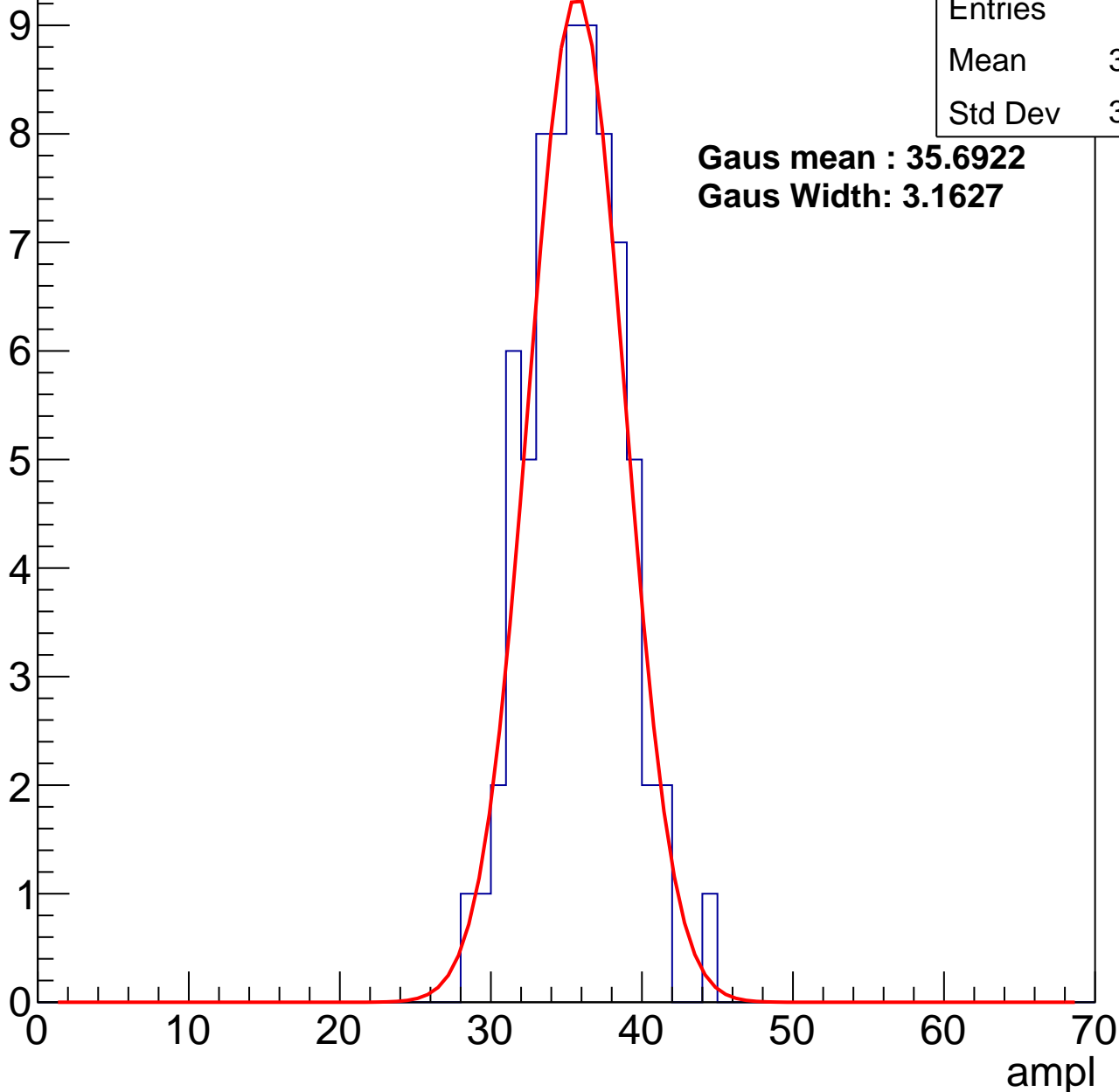
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	35.15
Std Dev	3.079

Gaus mean : 35.6922

Gaus Width: 3.1627



B0L002S, U7-ch76, adc2

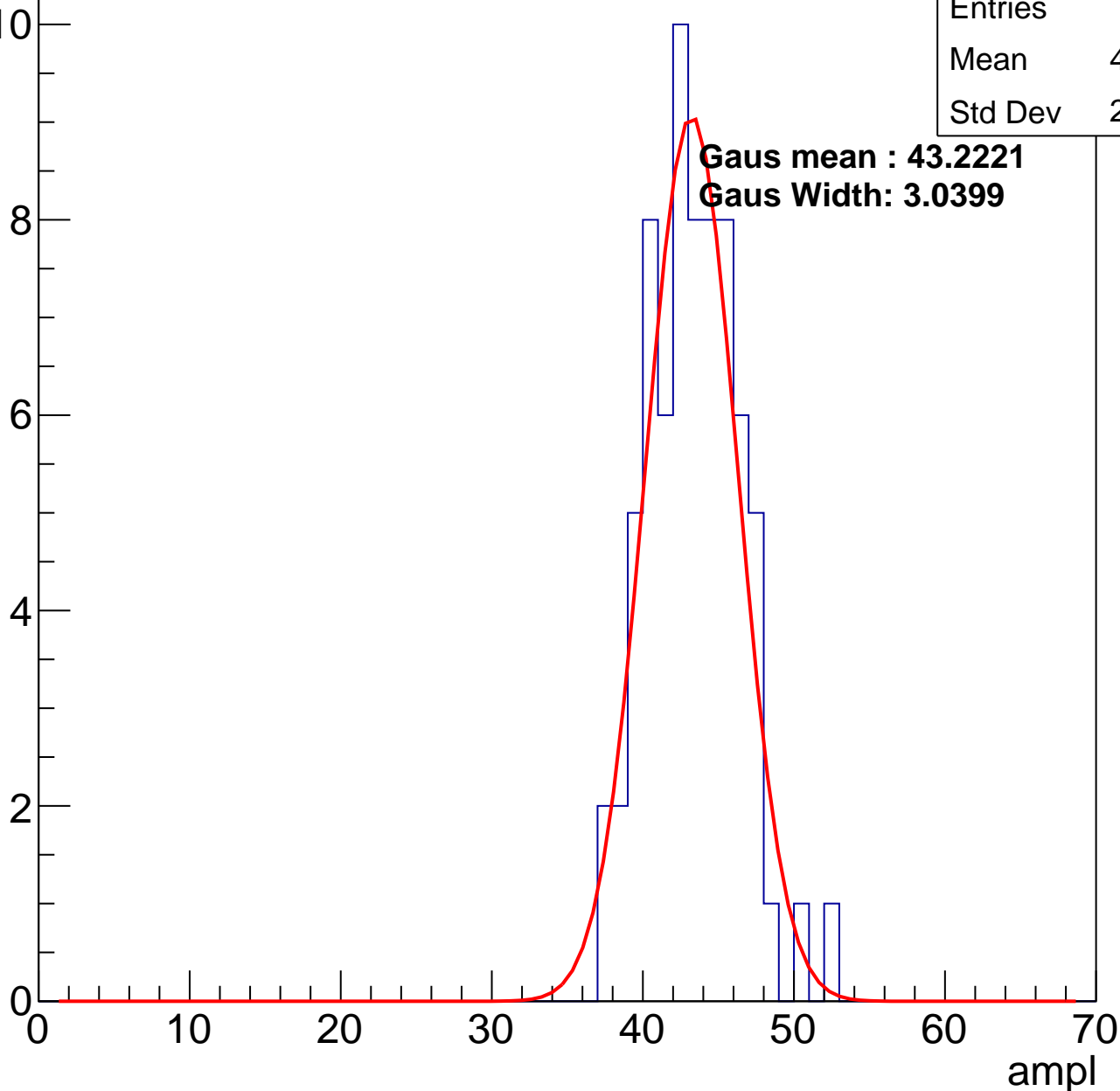
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.93
Std Dev	2.994

Gaus mean : 43.2221

Gaus Width: 3.0399

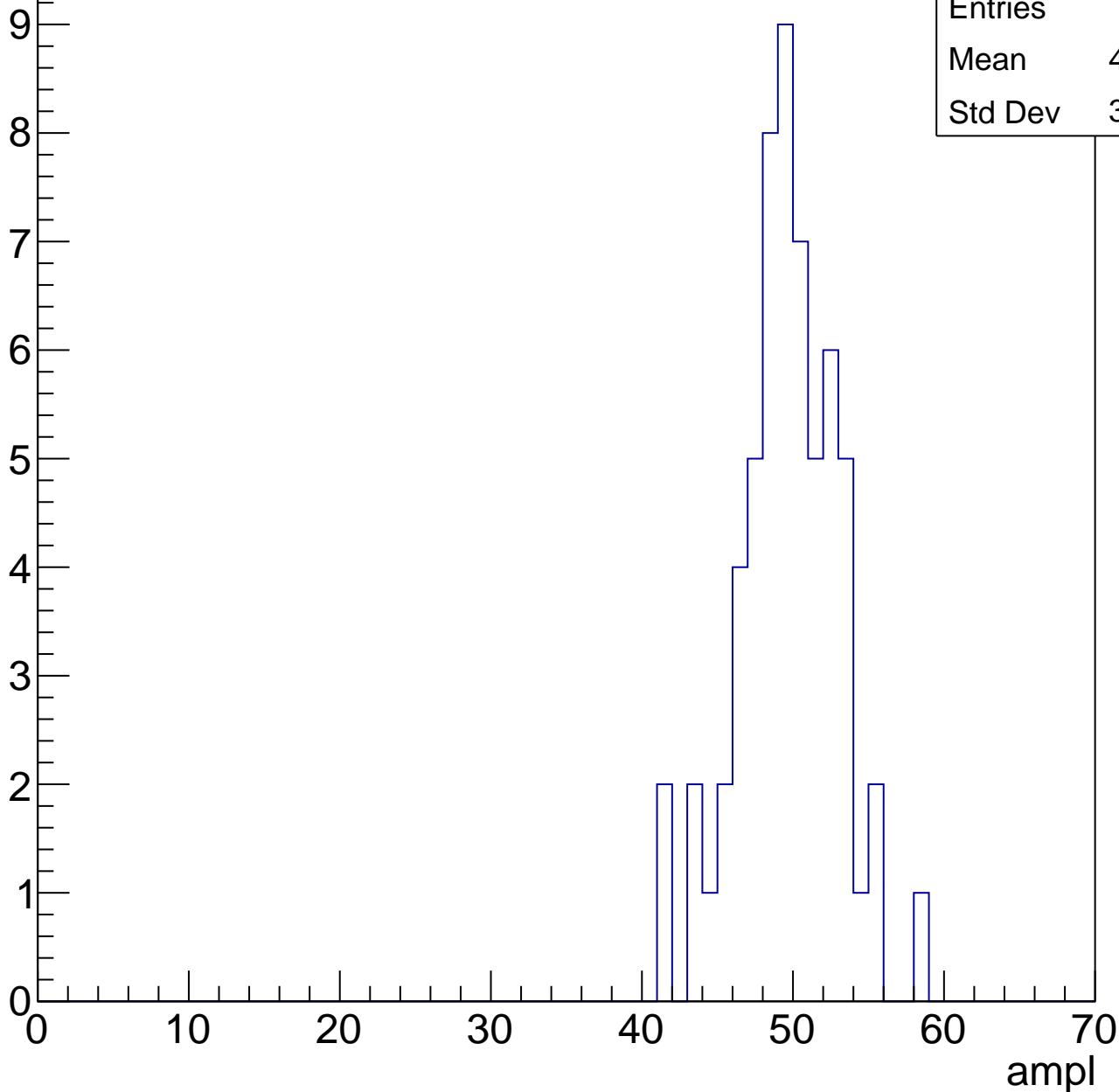


B0L002S, U7-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

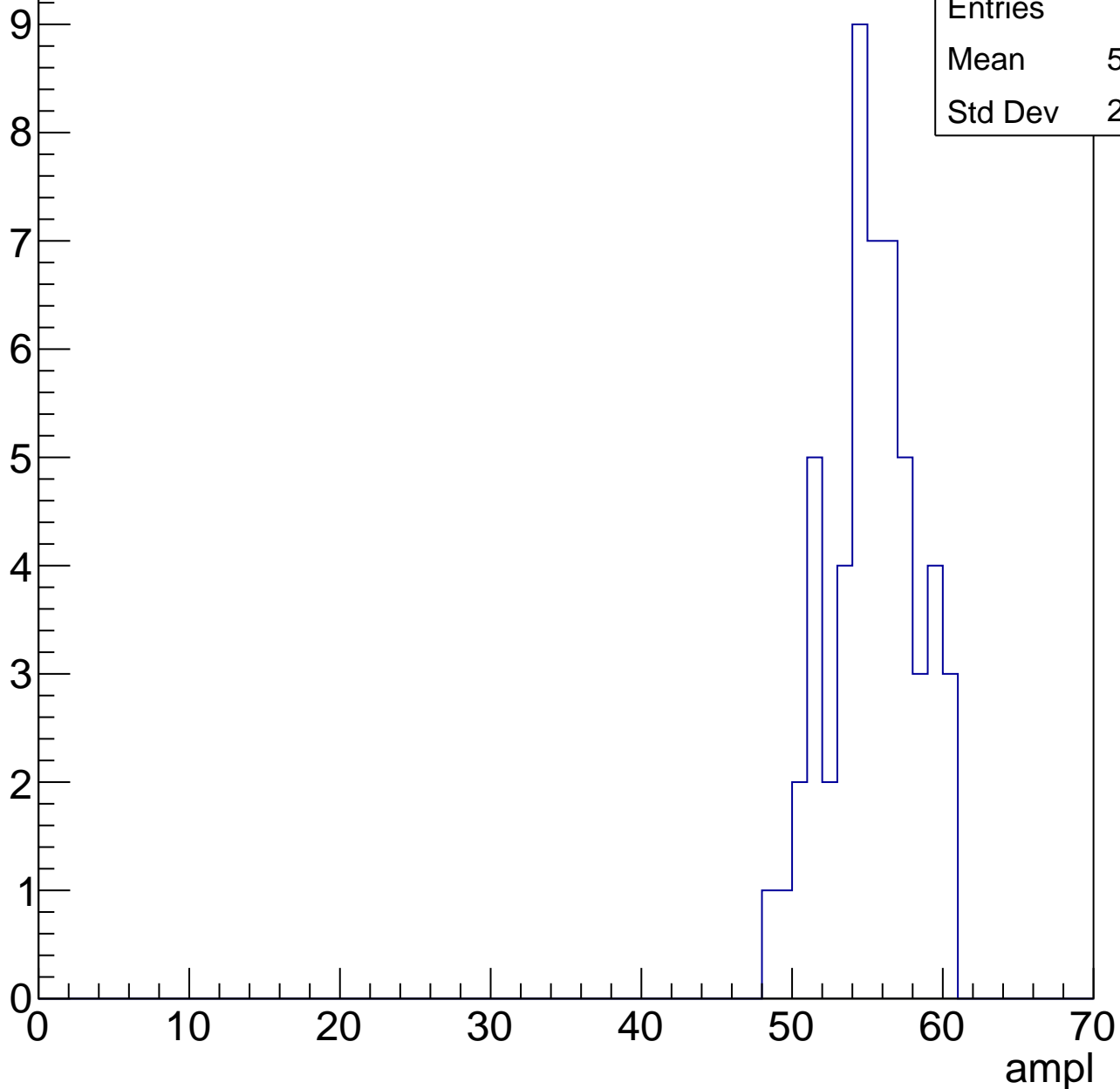
Entries	60
Mean	49.17
Std Dev	3.312



B0L002S, U7-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

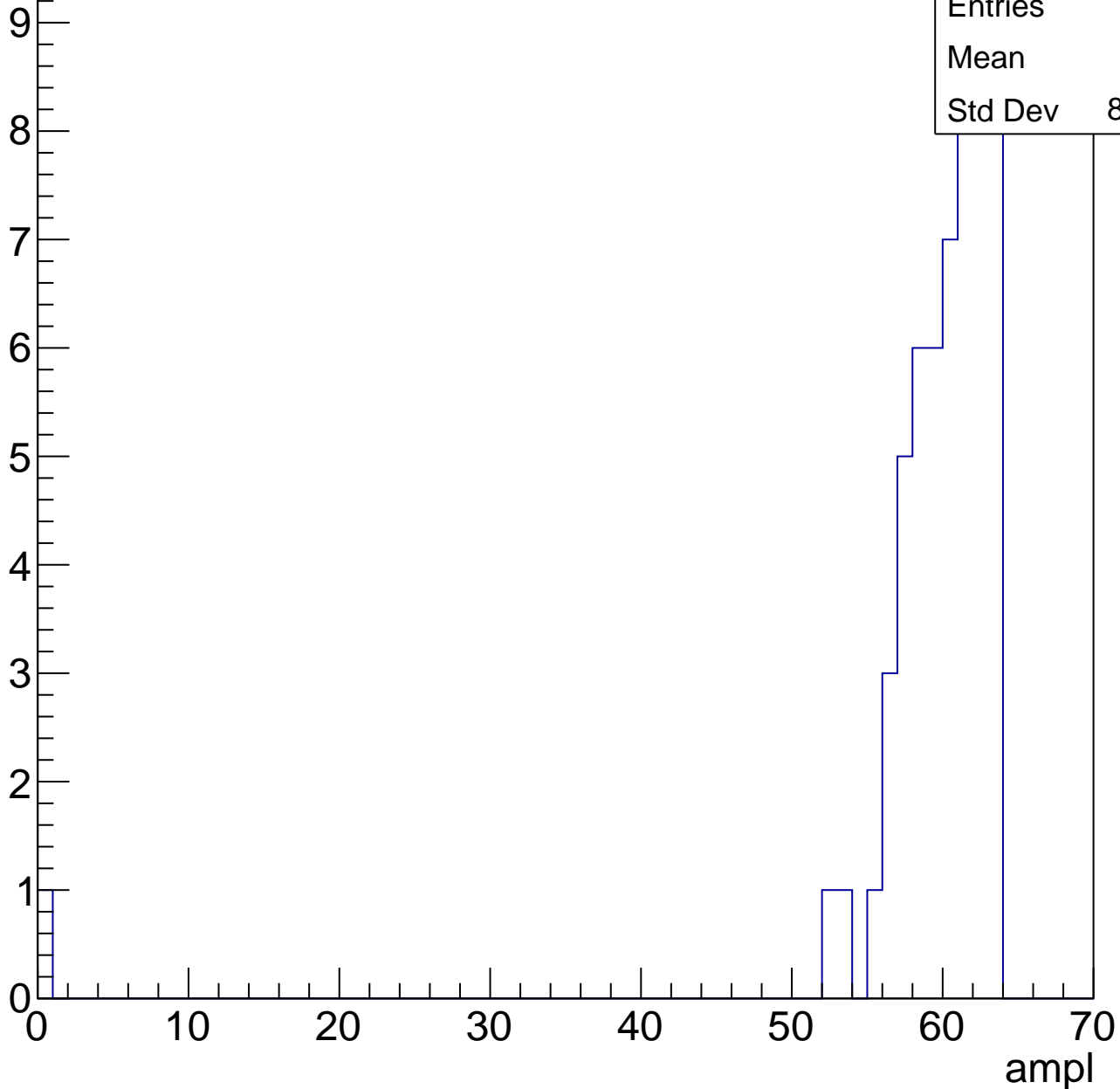


B0L002S, U7-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.7
Std Dev	8.253



B0L002S, U7-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

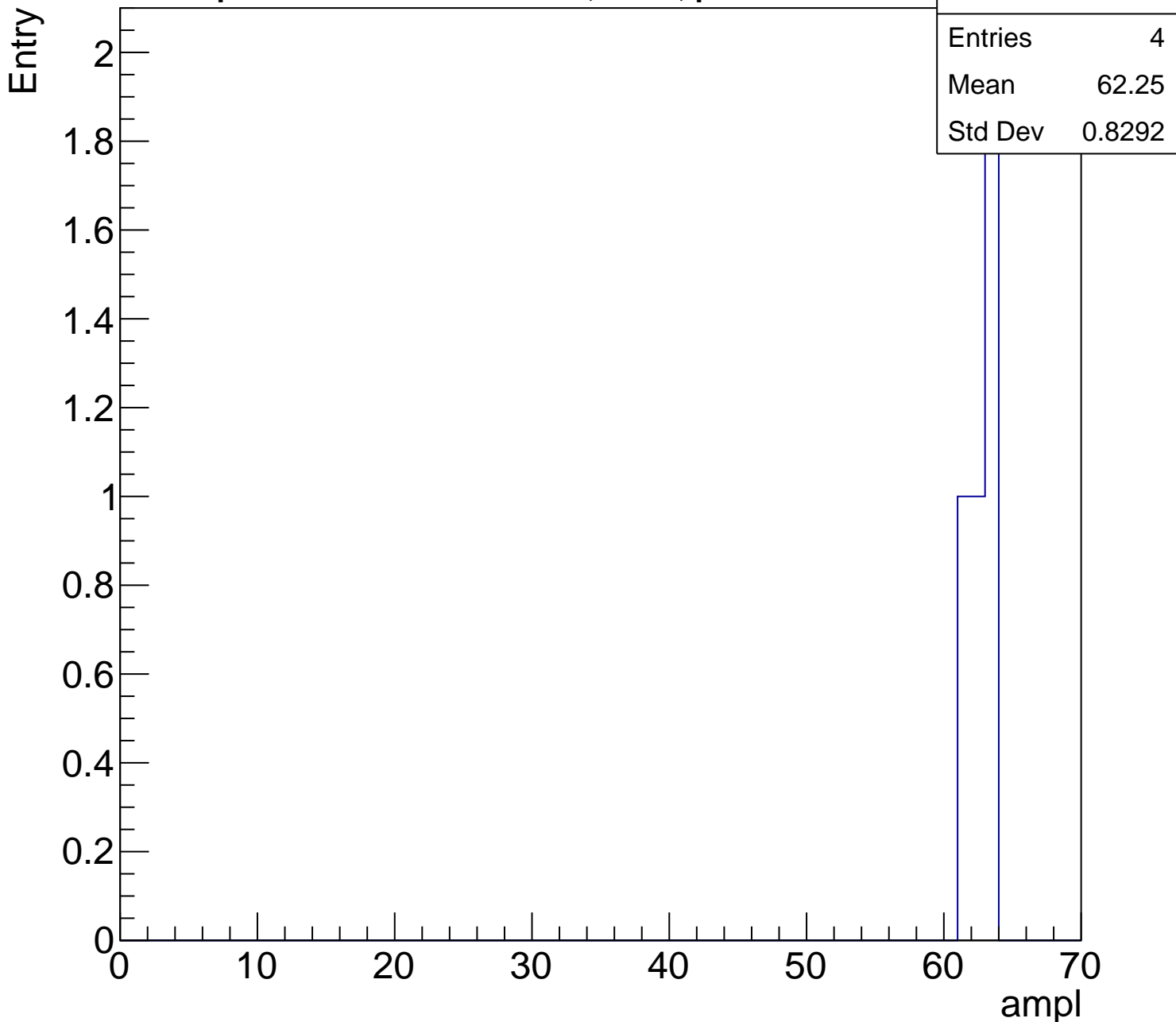
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

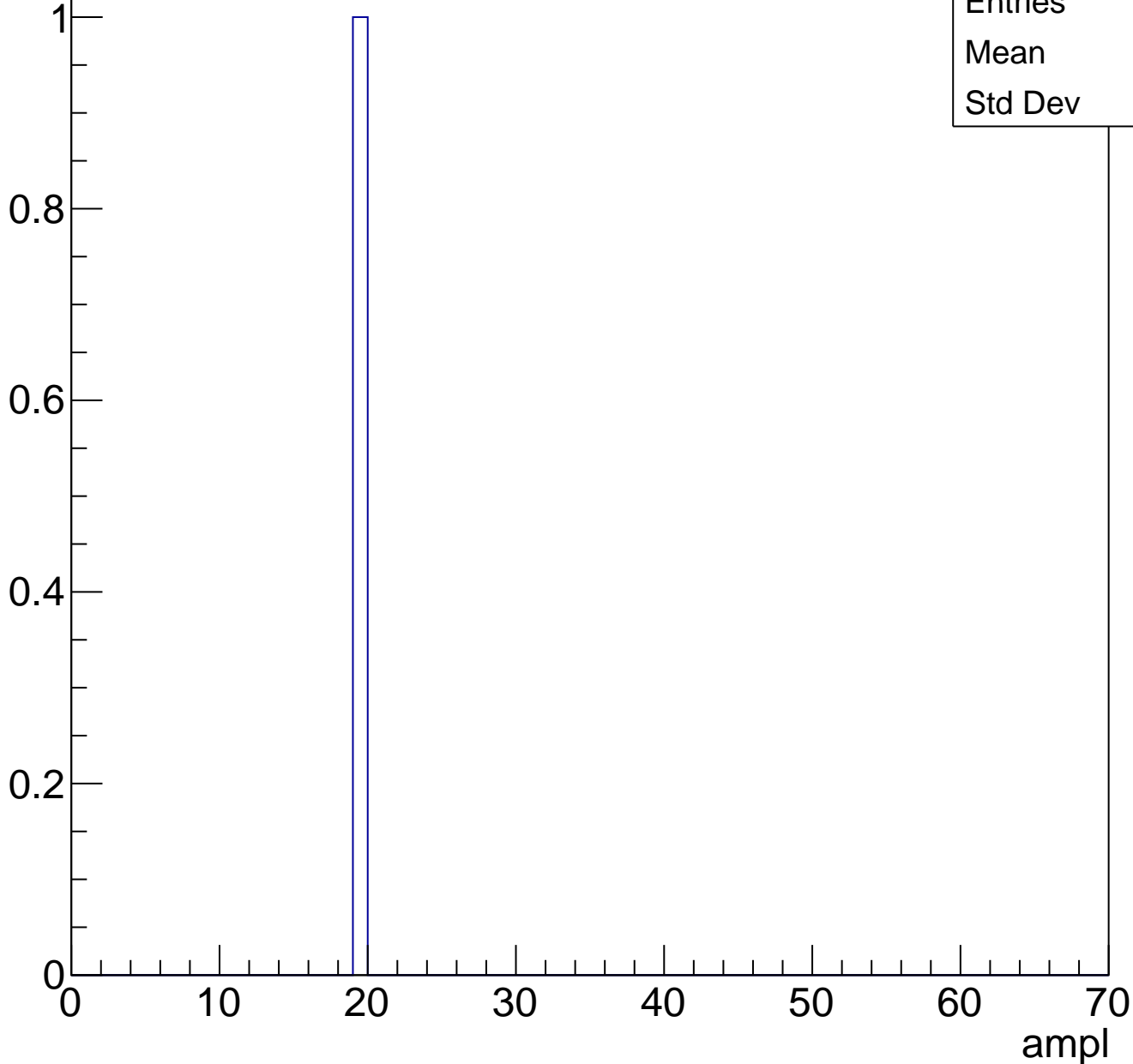
ampl



B0L002S, U7-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	19
Std Dev	0

B0L002S, U7-ch77, adc0

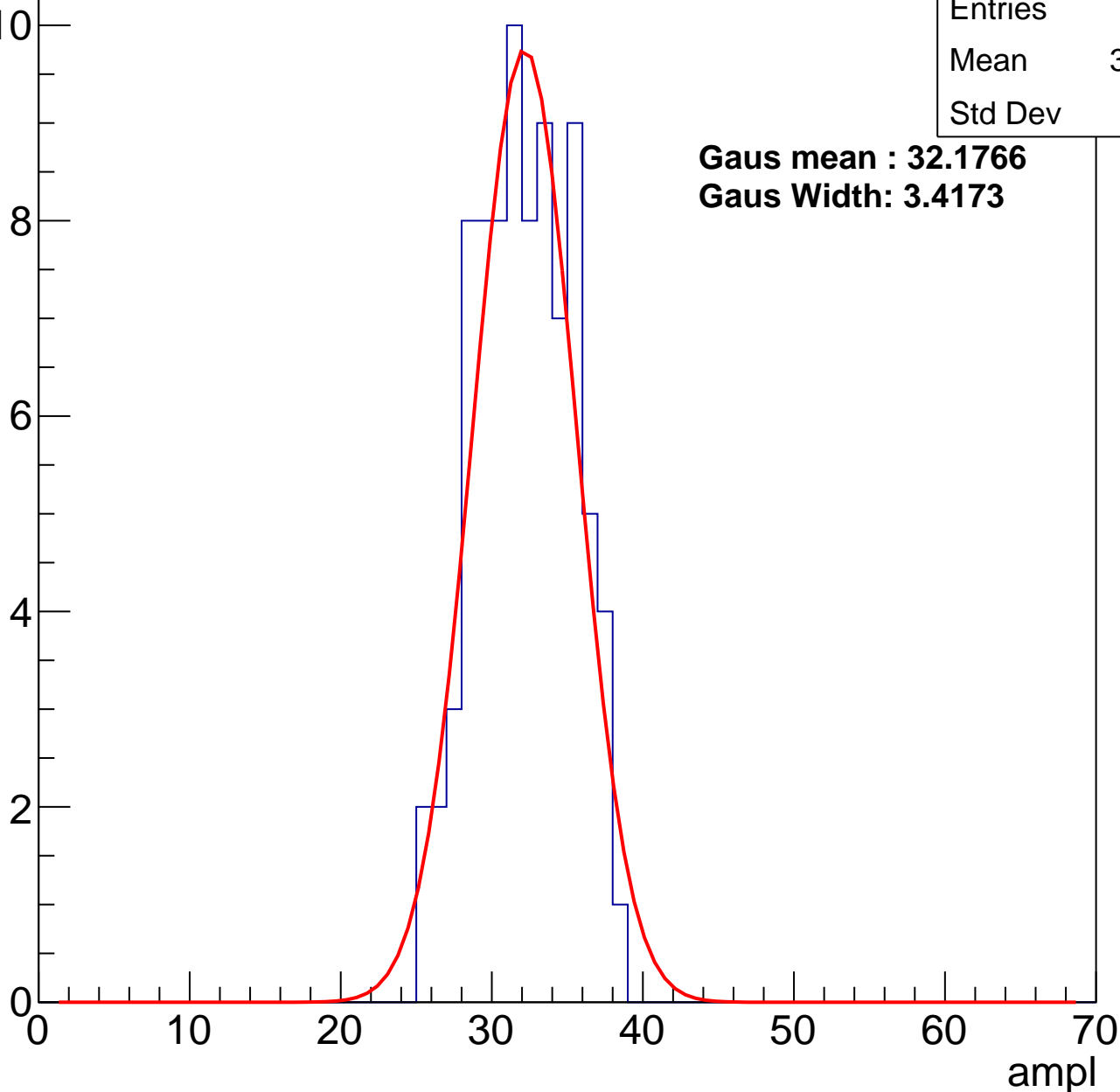
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	31.68
Std Dev	3.11

Gaus mean : 32.1766

Gaus Width: 3.4173



B0L002S, U7-ch77, adc1

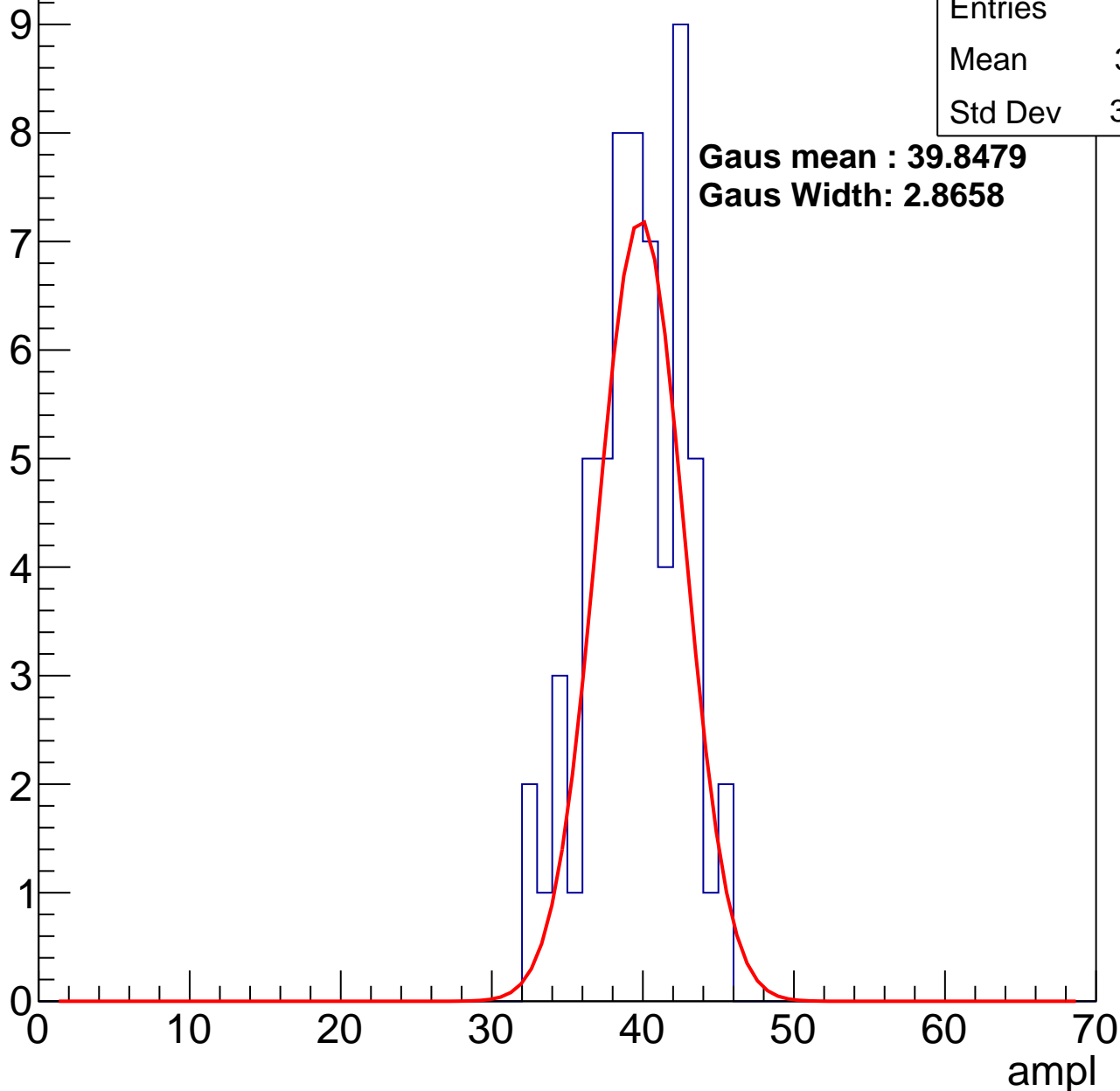
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	39.11
Std Dev	3.079

Gaus mean : 39.8479

Gaus Width: 2.8658



B0L002S, U7-ch77, adc2

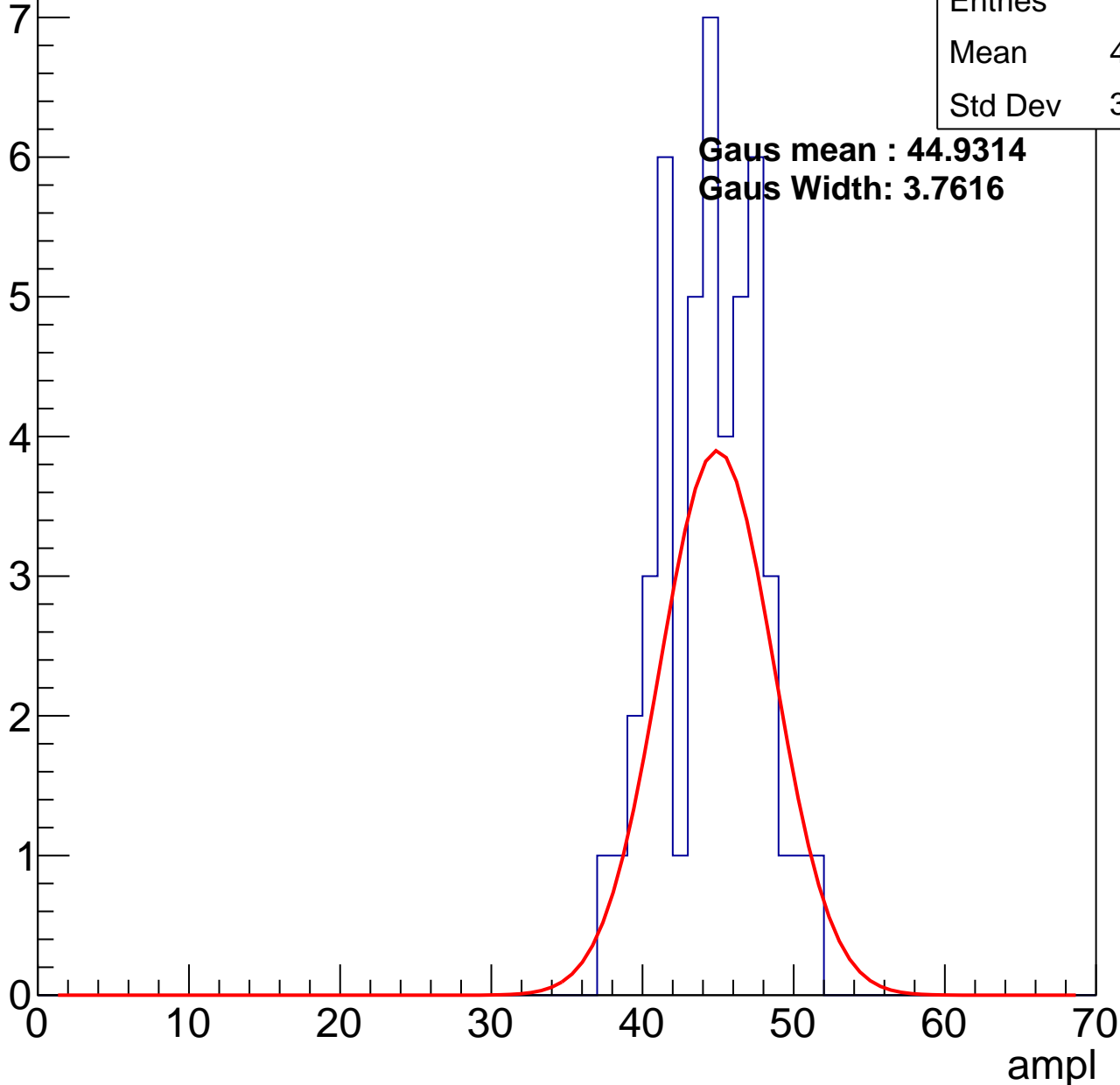
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.04
Std Dev	3.202

Gaus mean : 44.9314

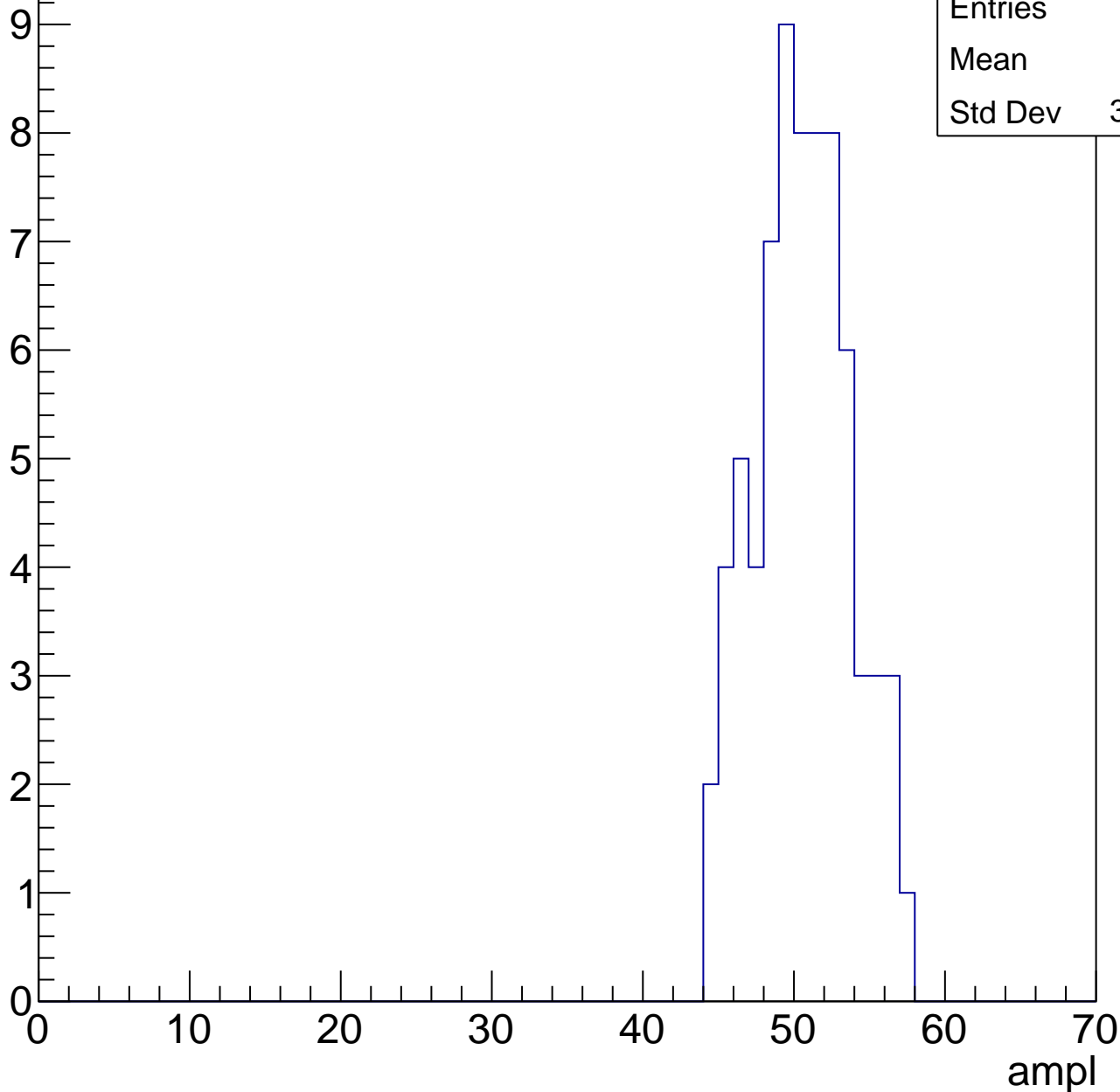
Gaus Width: 3.7616



B0L002S, U7-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

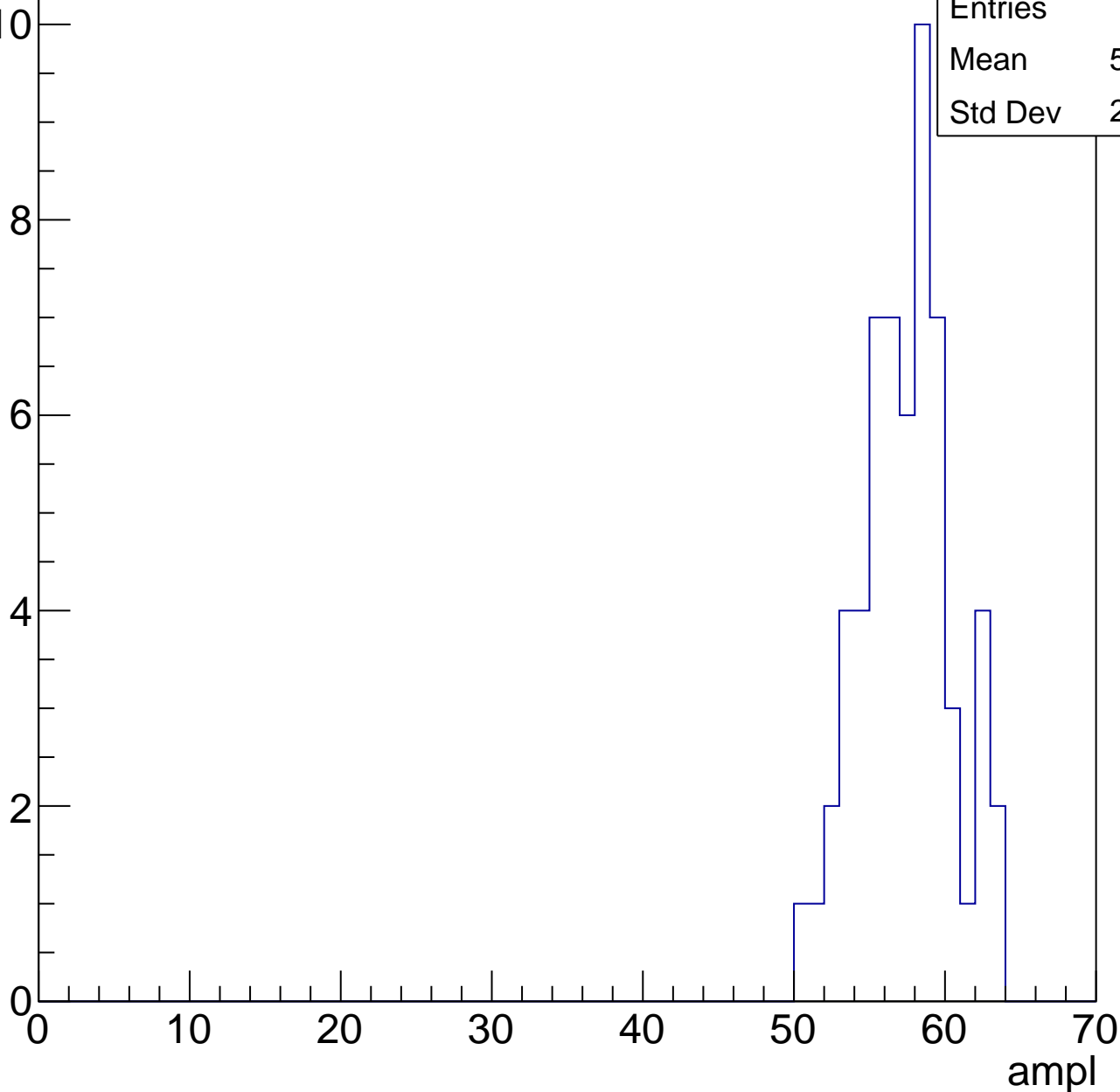


B0L002S, U7-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

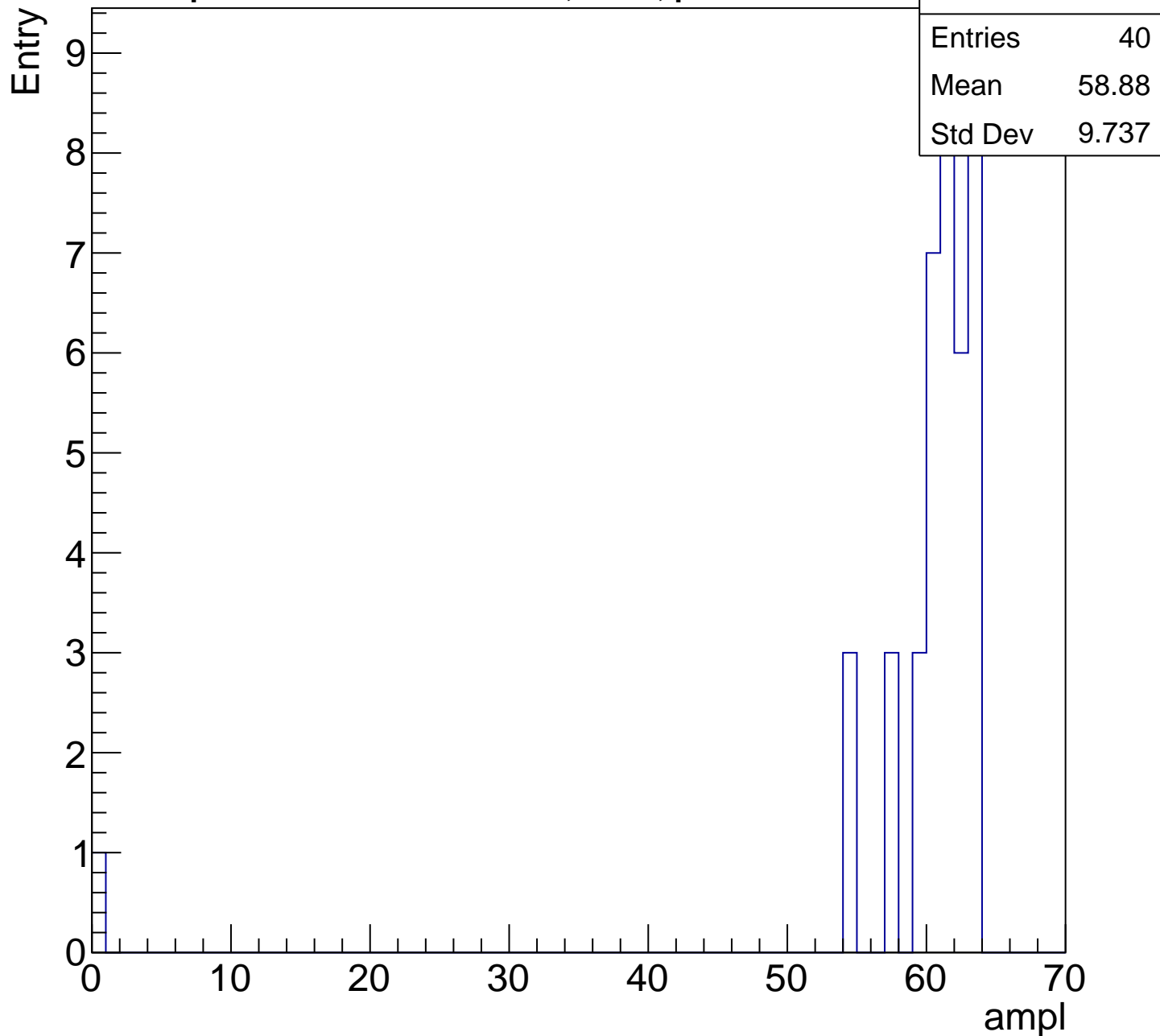
Entry

Entries	59
Mean	56.95
Std Dev	2.977



B0L002S, U7-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch77, adc7

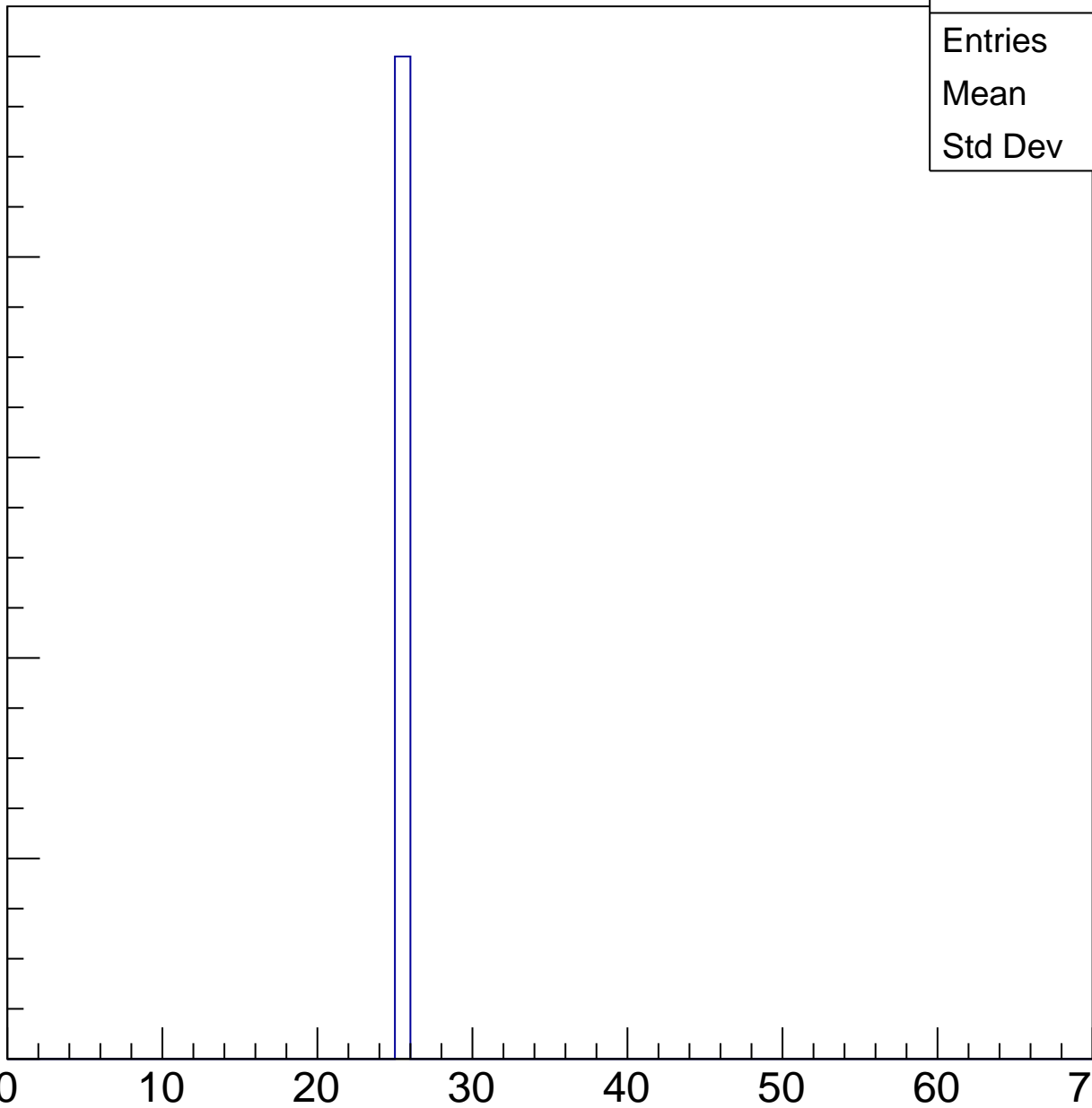
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L002S, U7-ch78, adc0

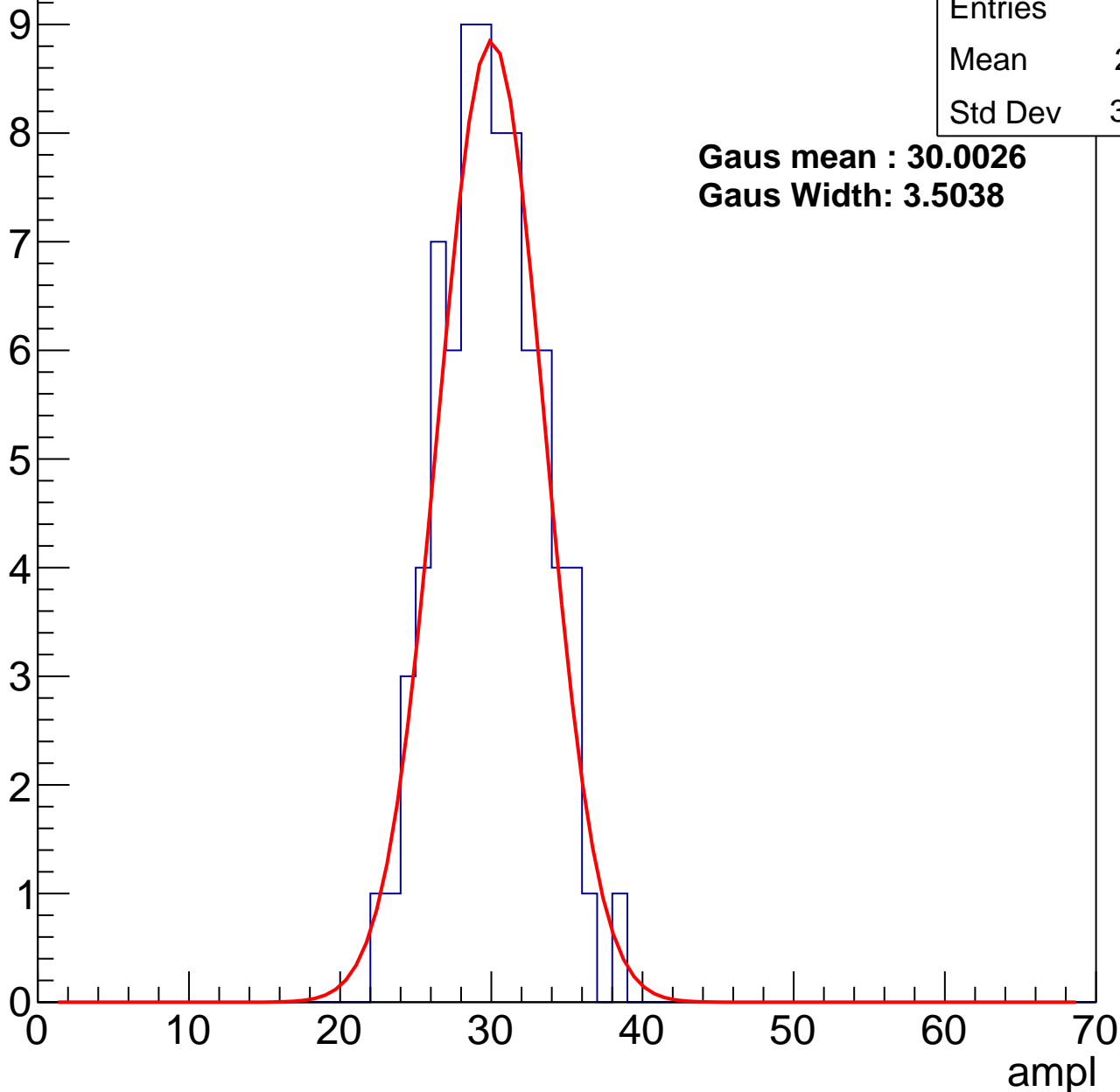
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.51
Std Dev	3.327

Gaus mean : 30.0026

Gaus Width: 3.5038



B0L002S, U7-ch78, adc1

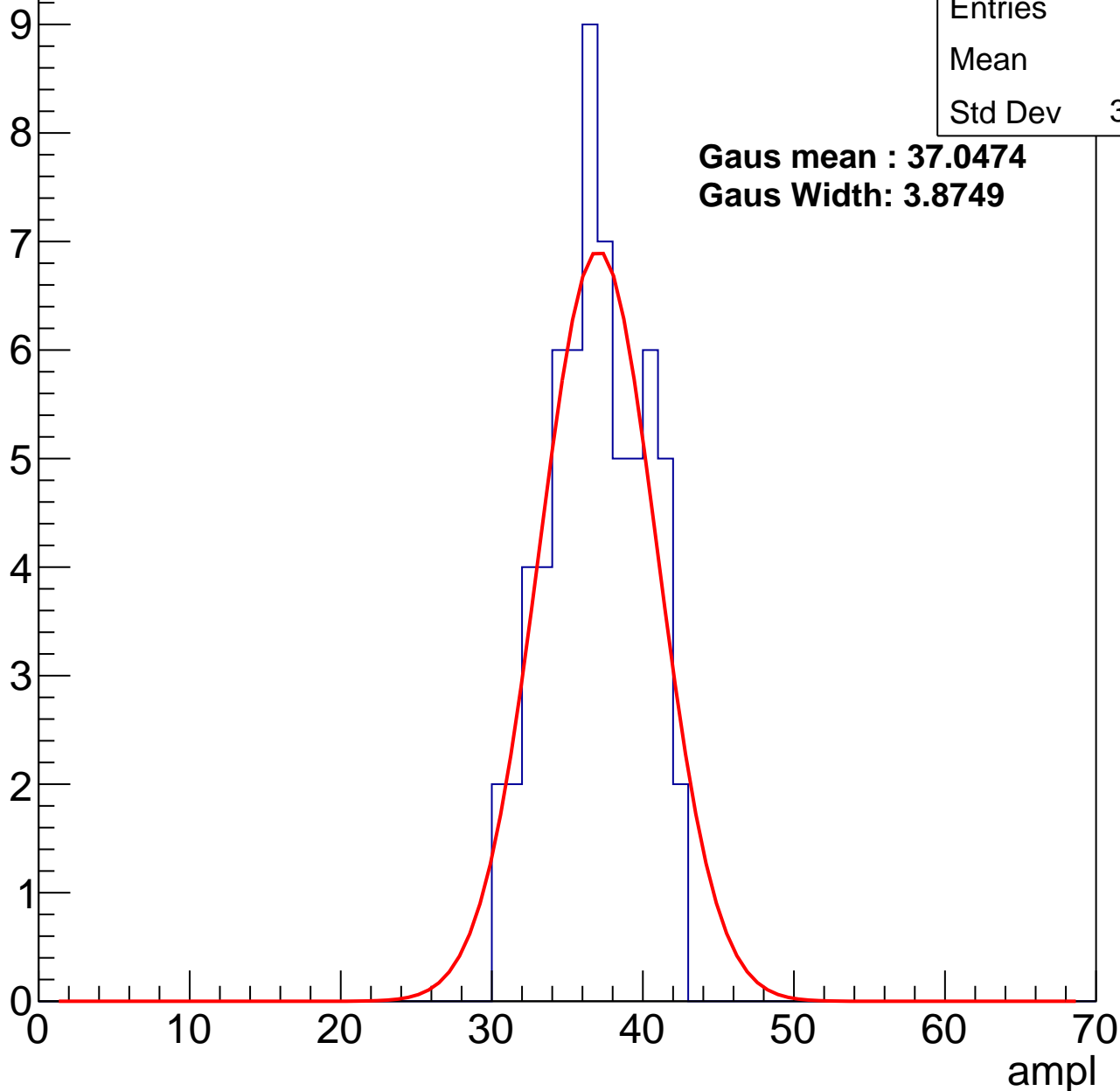
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.4
Std Dev	3.104

Gaus mean : 37.0474

Gaus Width: 3.8749



B0L002S, U7-ch78, adc2

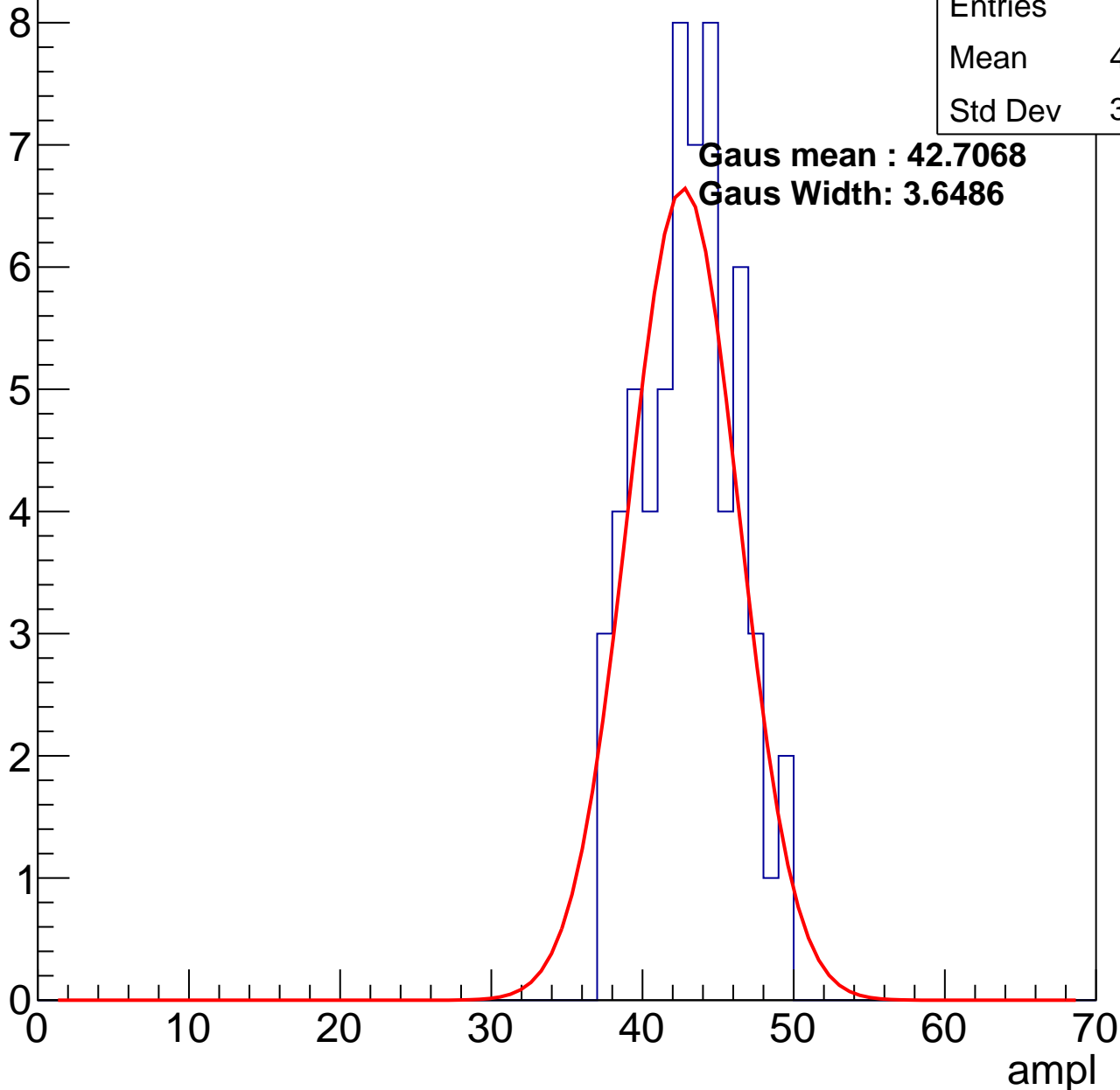
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.58
Std Dev	3.068

Gaus mean : 42.7068

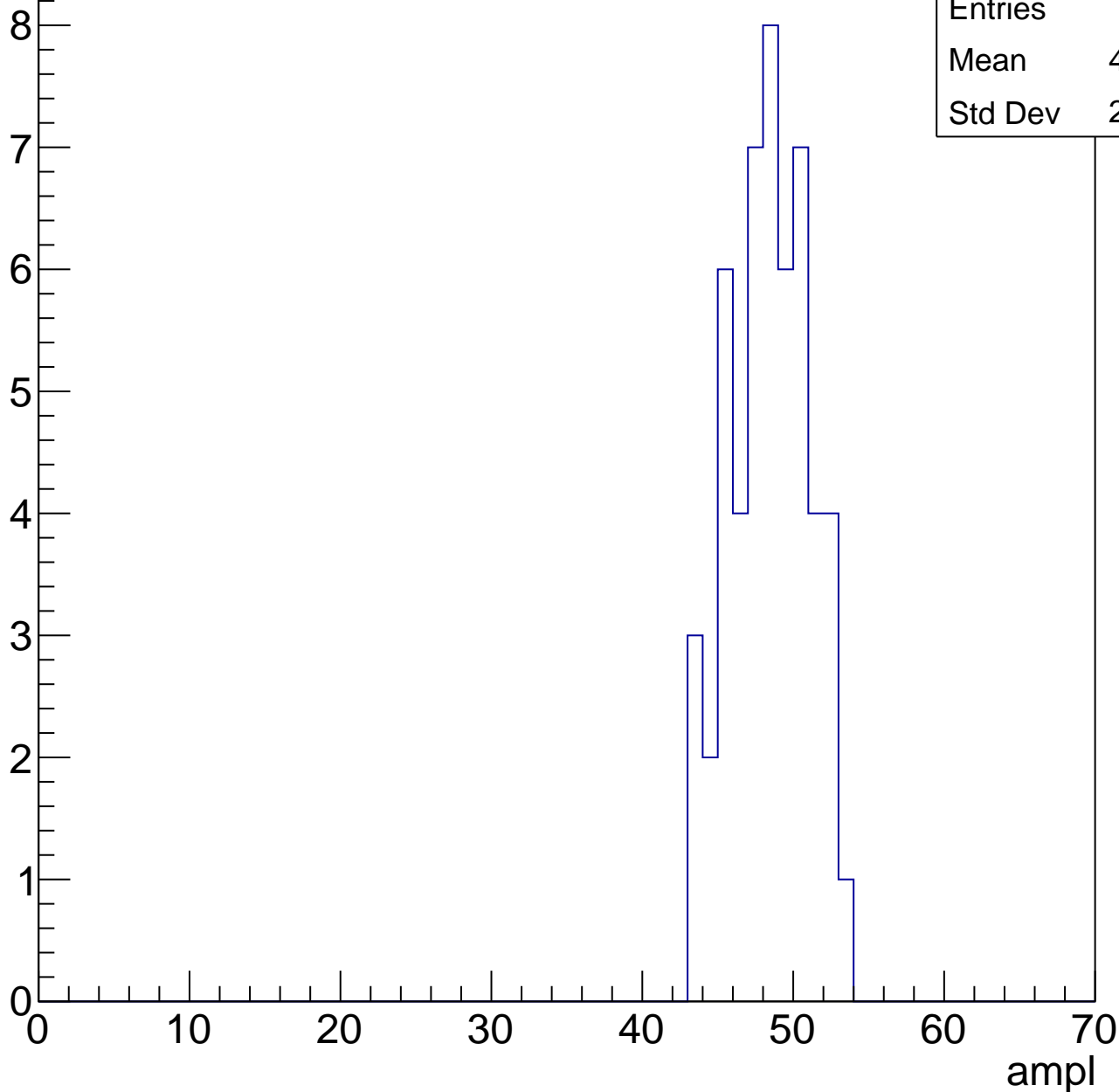
Gaus Width: 3.6486



B0L002S, U7-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

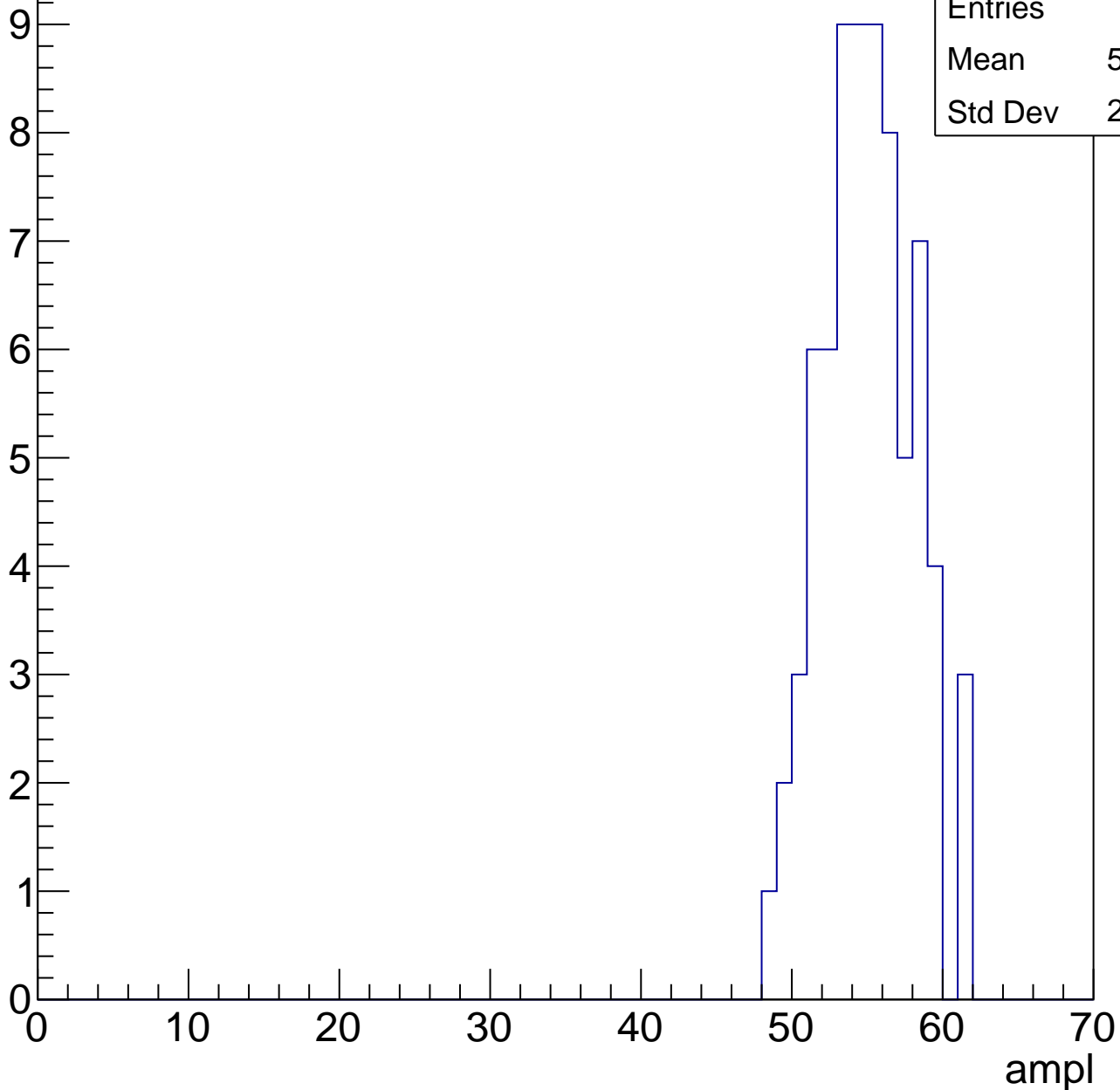
Entry



B0L002S, U7-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



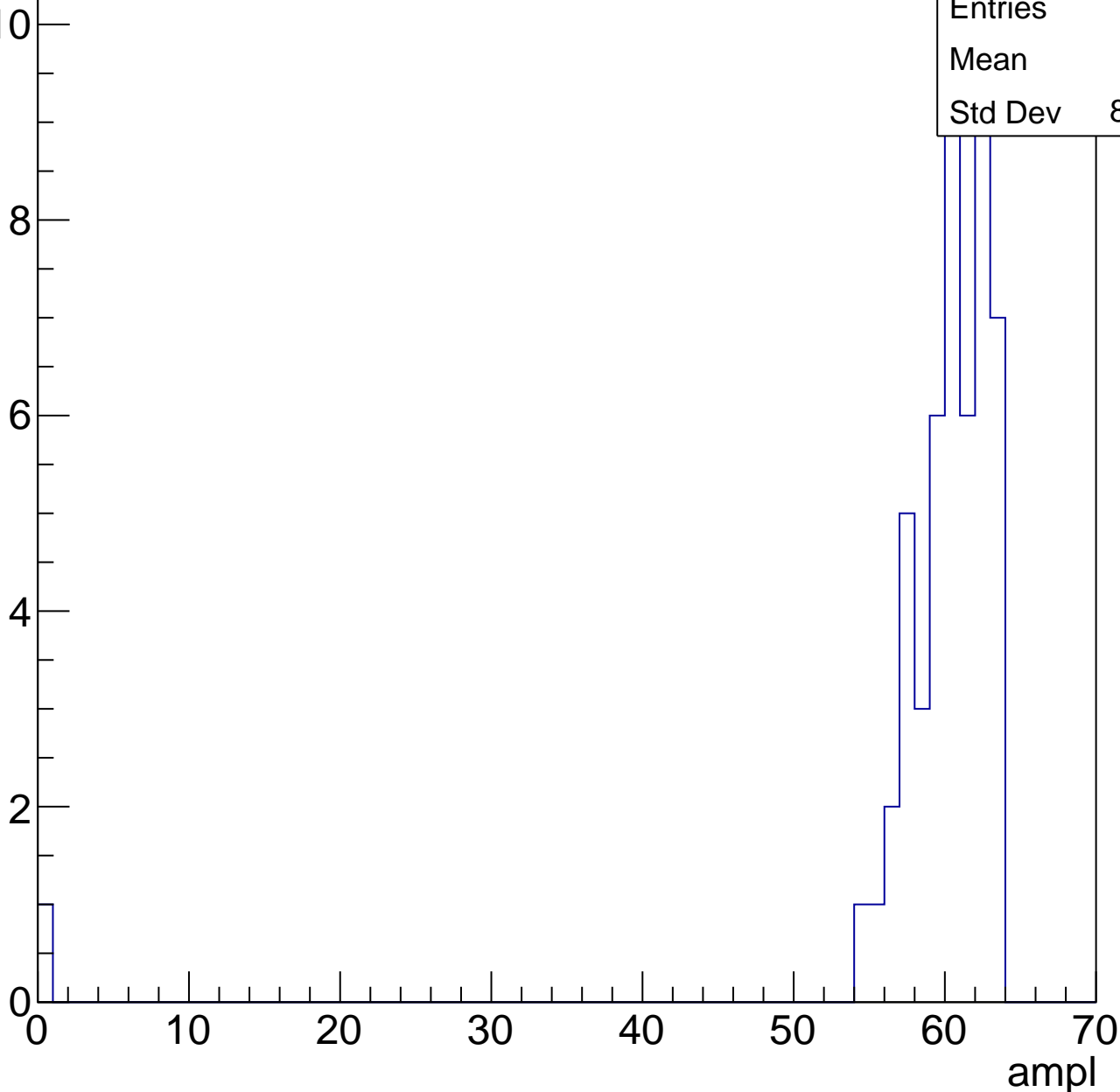
Entries	72
Mean	54.58
Std Dev	2.985

B0L002S, U7-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	58.8
Std Dev	8.618



B0L002S, U7-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B0L002S, U7-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch79, adc0

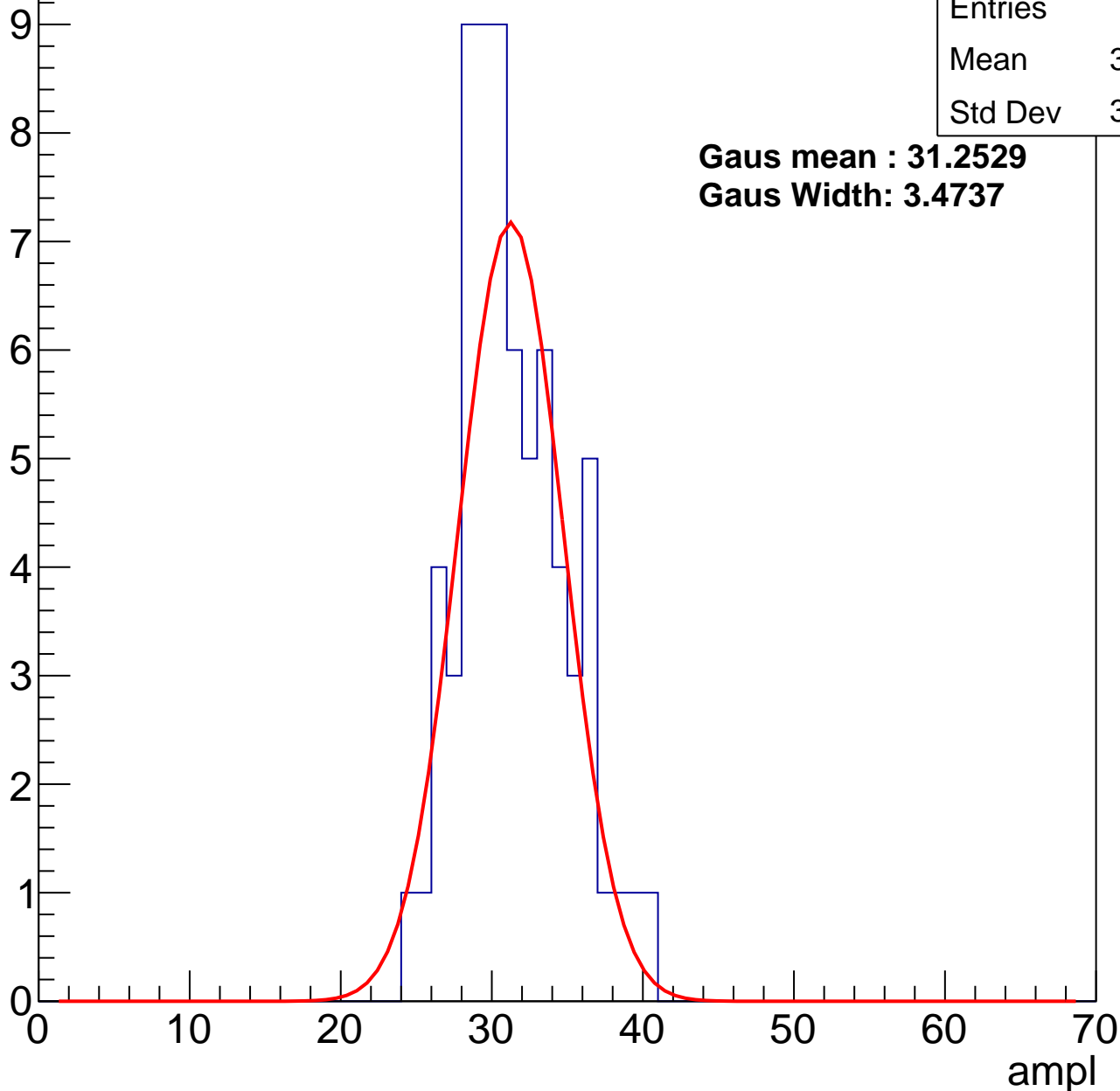
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.96
Std Dev	3.462

Gaus mean : 31.2529

Gaus Width: 3.4737



B0L002S, U7-ch79, adc1

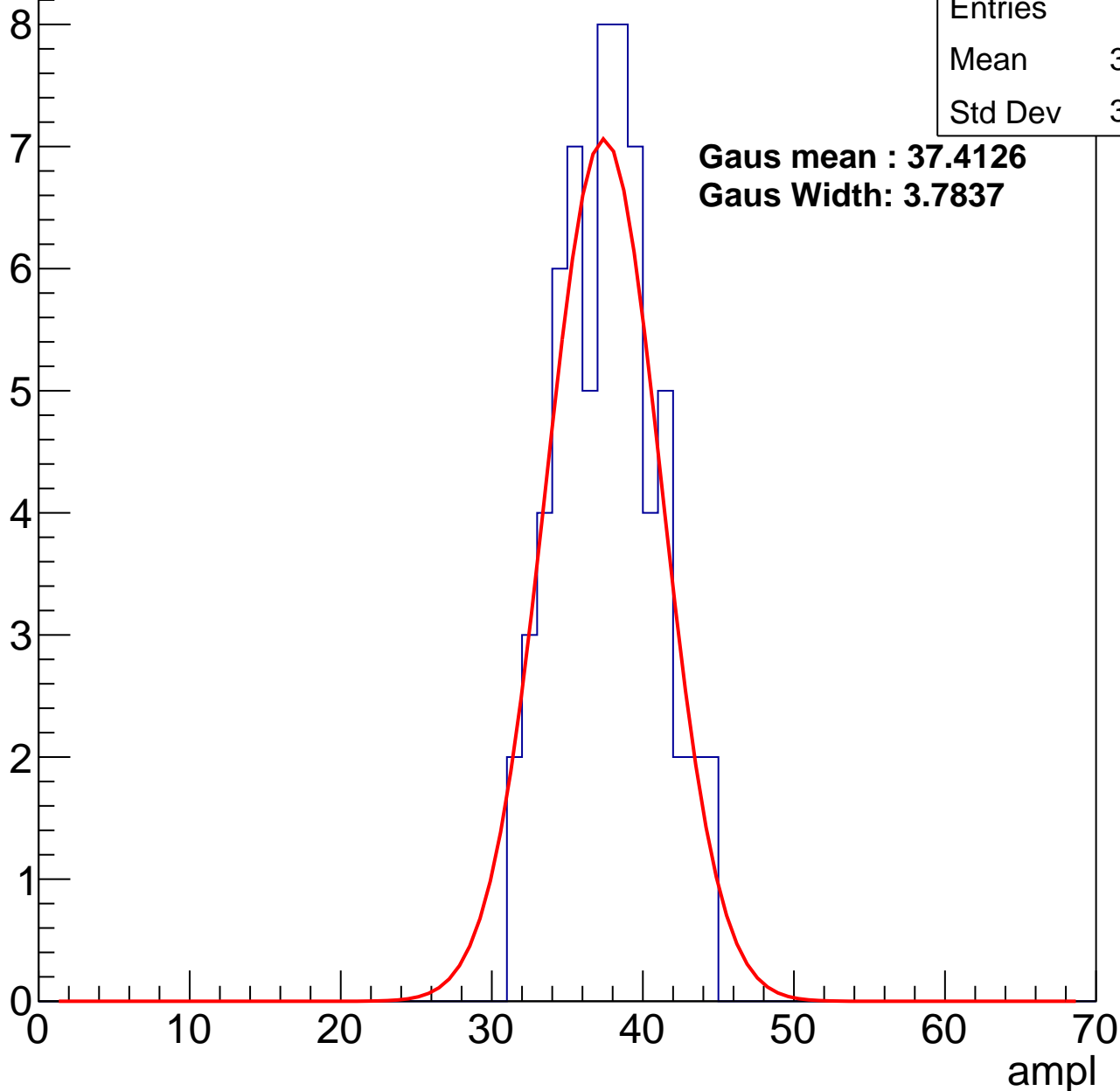
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.15
Std Dev	3.207

Gaus mean : 37.4126

Gaus Width: 3.7837



B0L002S, U7-ch79, adc2

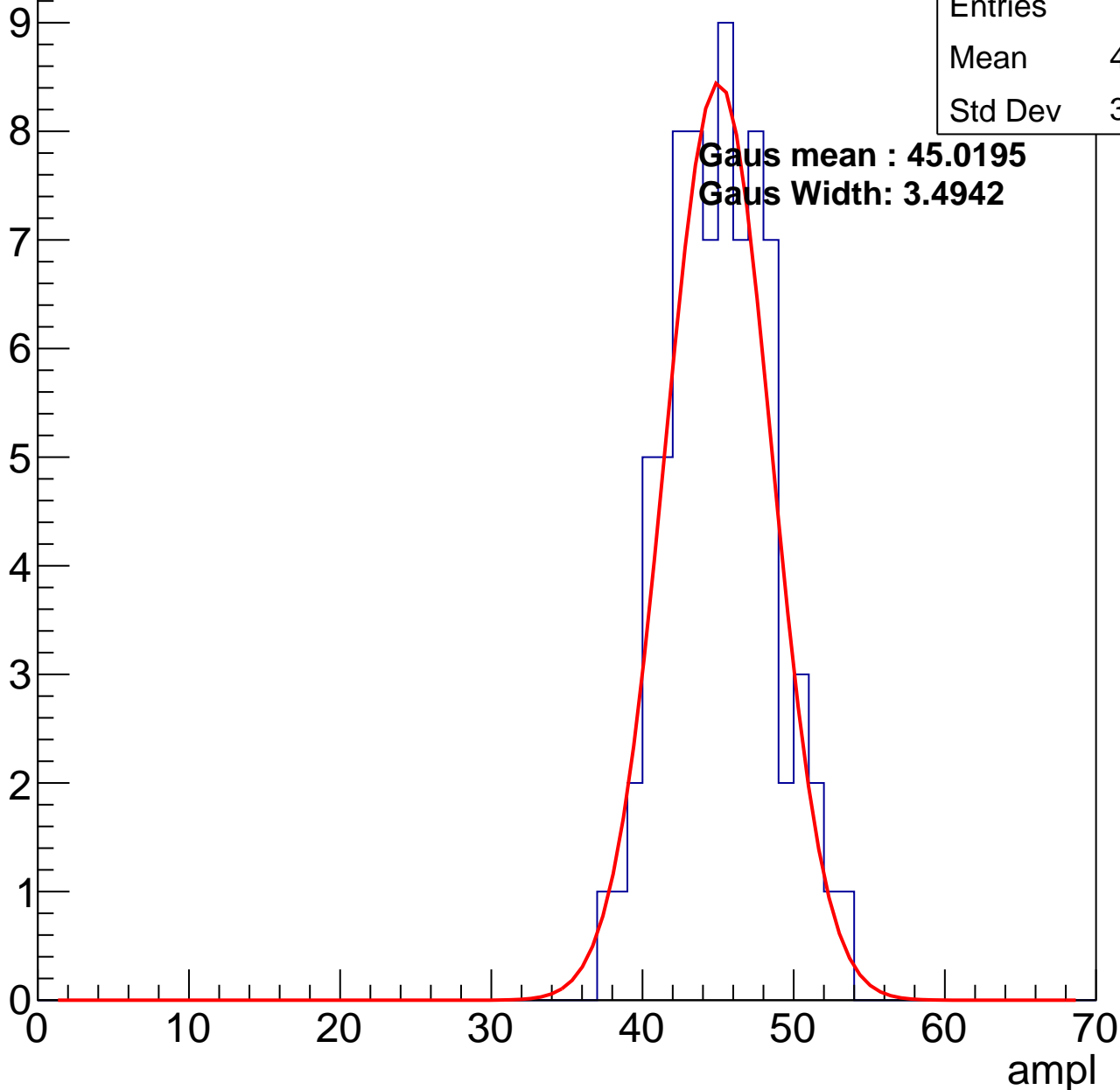
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.68
Std Dev	3.394

Gaus mean : 45.0195

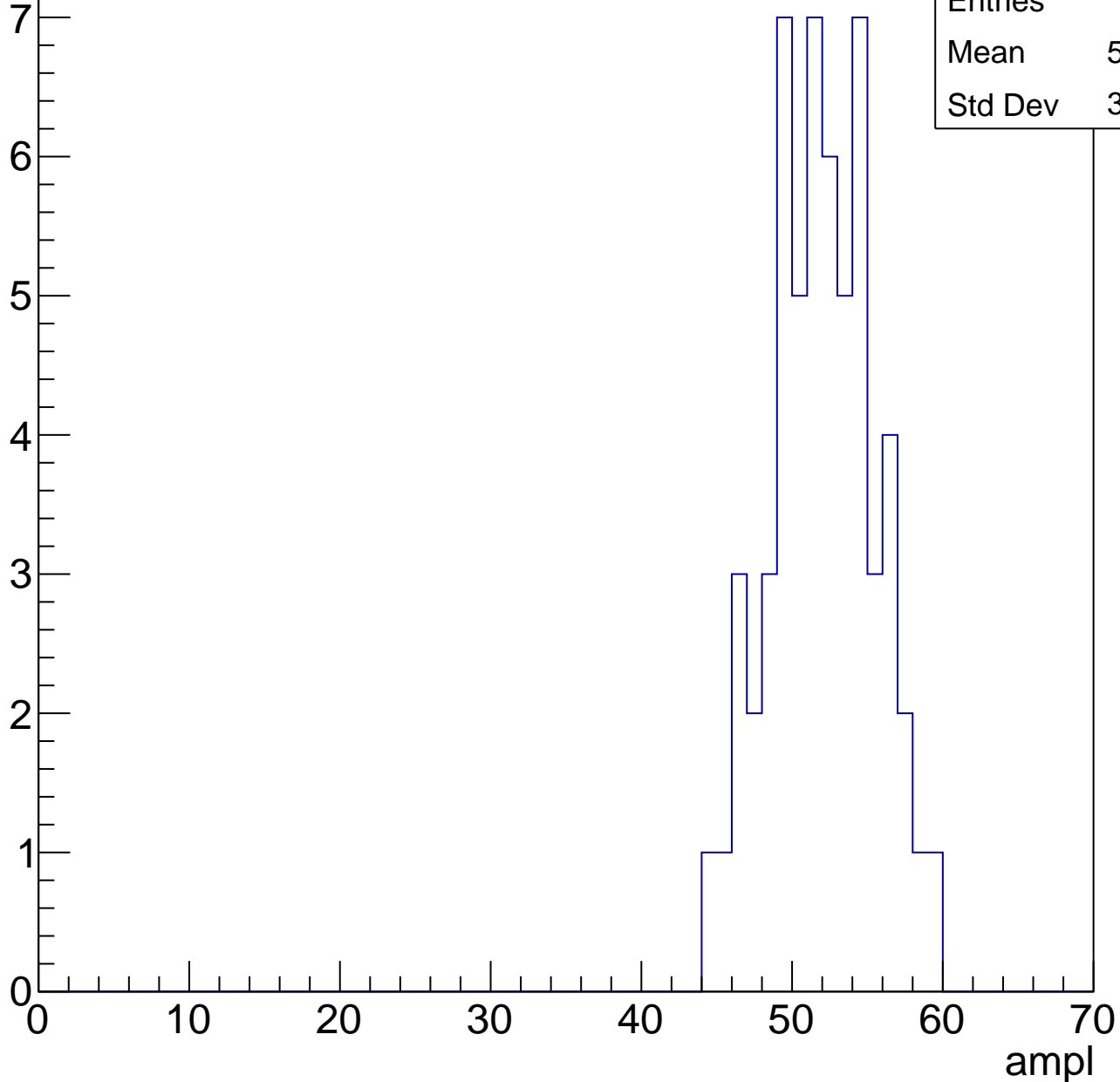
Gaus Width: 3.4942



B0L002S, U7-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

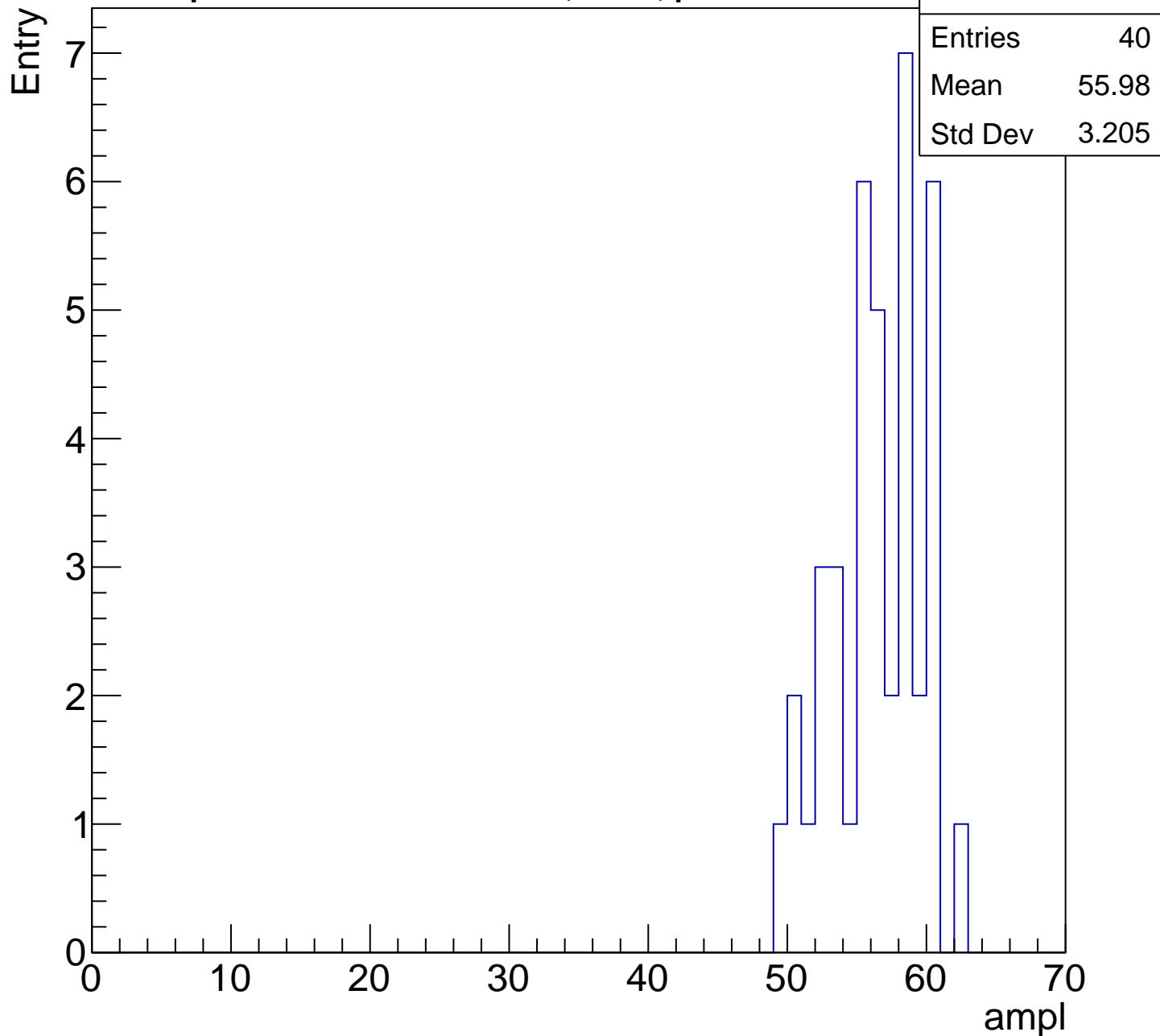
Entry



Entries	58
Mean	51.55
Std Dev	3.364

B0L002S, U7-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

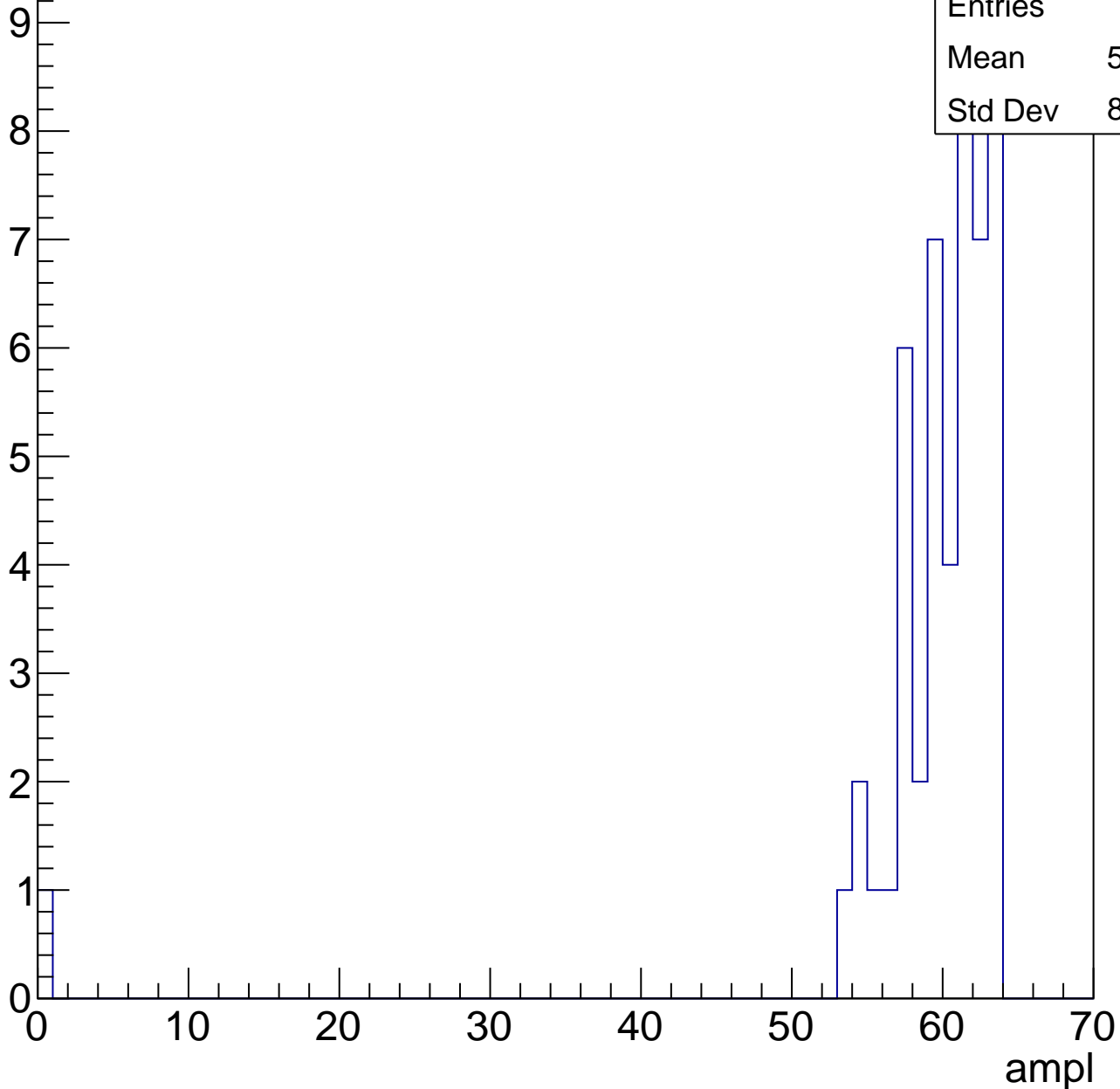


B0L002S, U7-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.57
Std Dev	8.853



B0L002S, U7-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L002S, U7-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch80, adc0

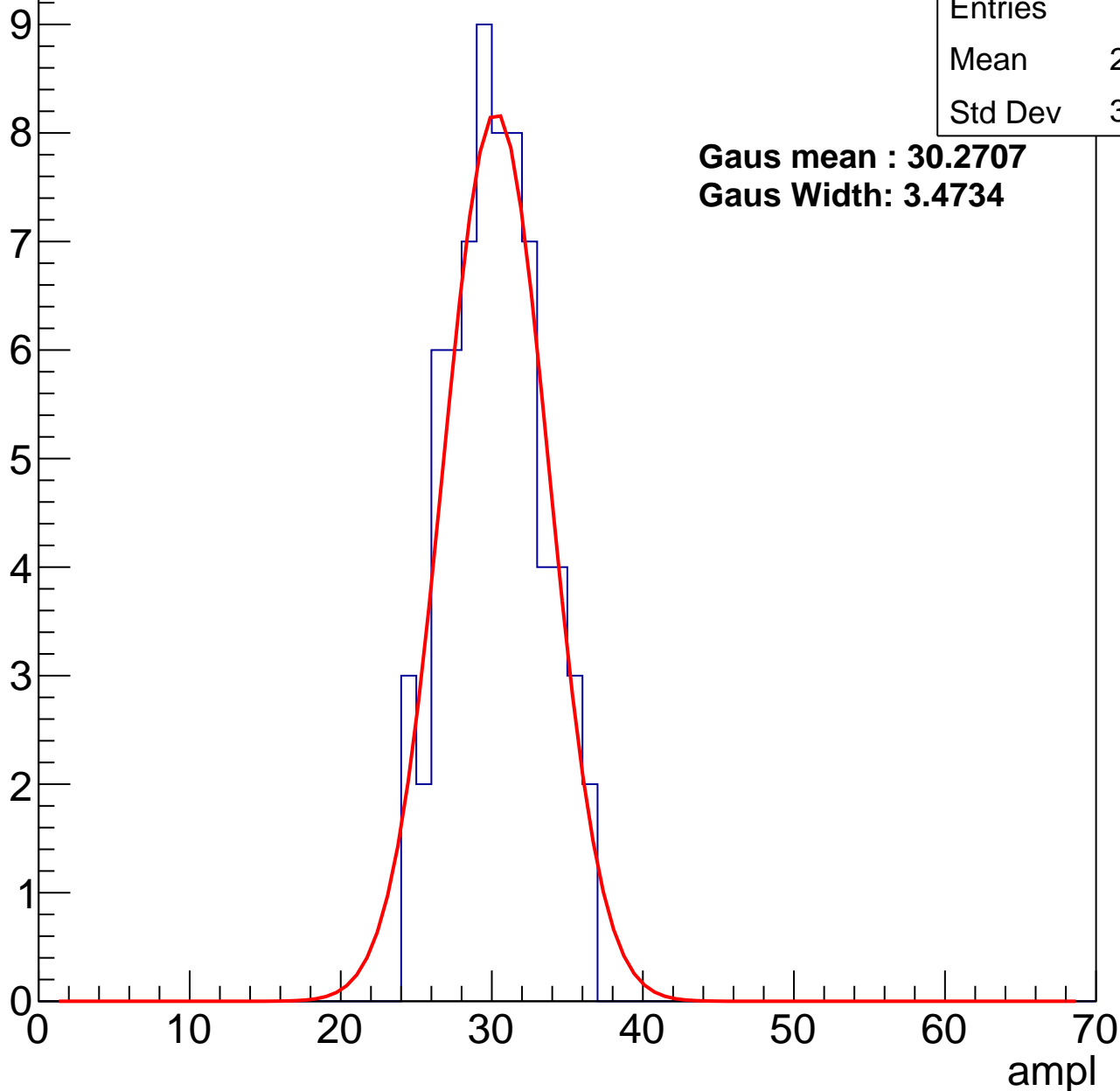
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.77
Std Dev	3.008

Gaus mean : 30.2707

Gaus Width: 3.4734



B0L002S, U7-ch80, adc1

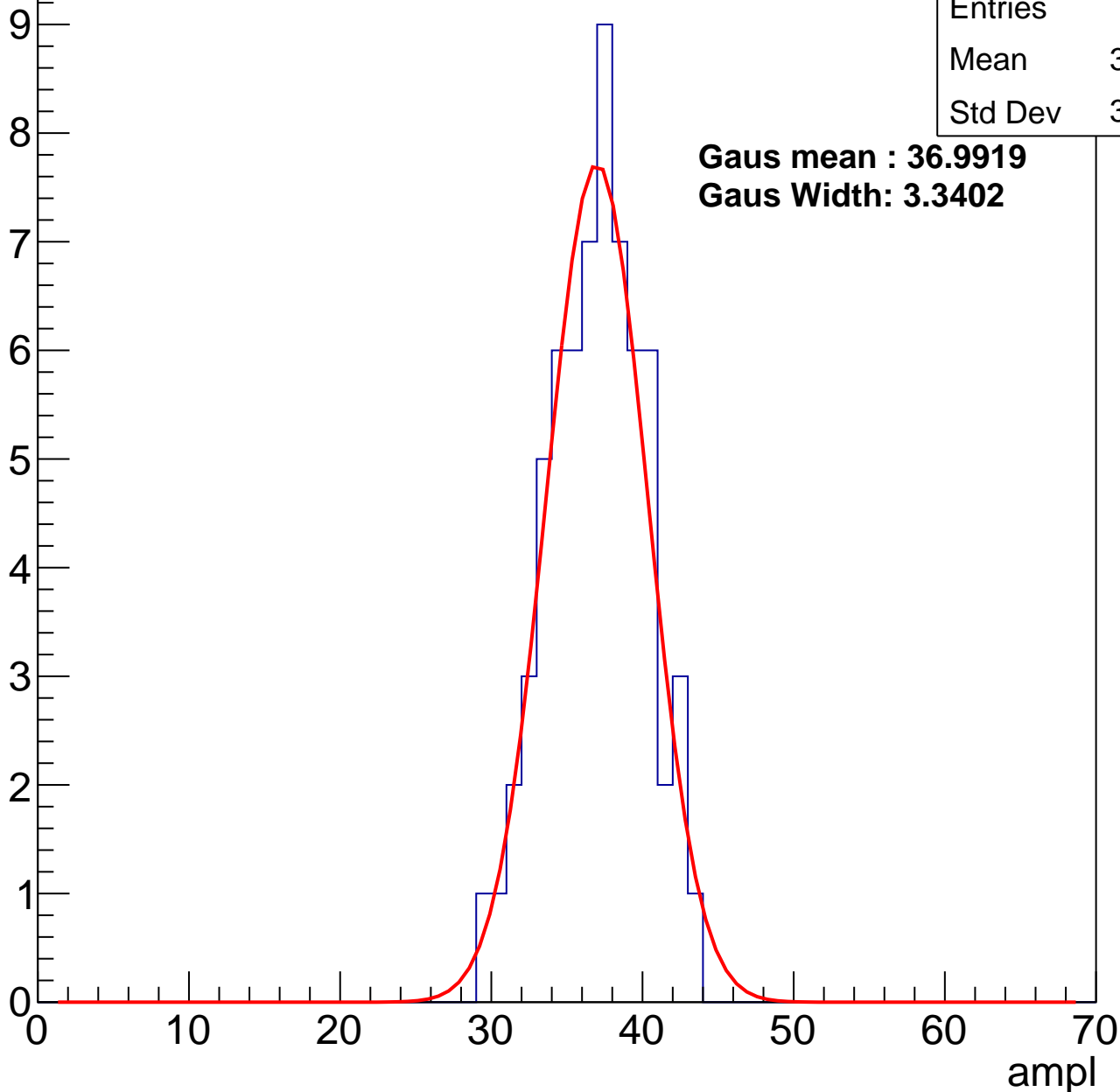
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.49
Std Dev	3.129

Gaus mean : 36.9919

Gaus Width: 3.3402



B0L002S, U7-ch80, adc2

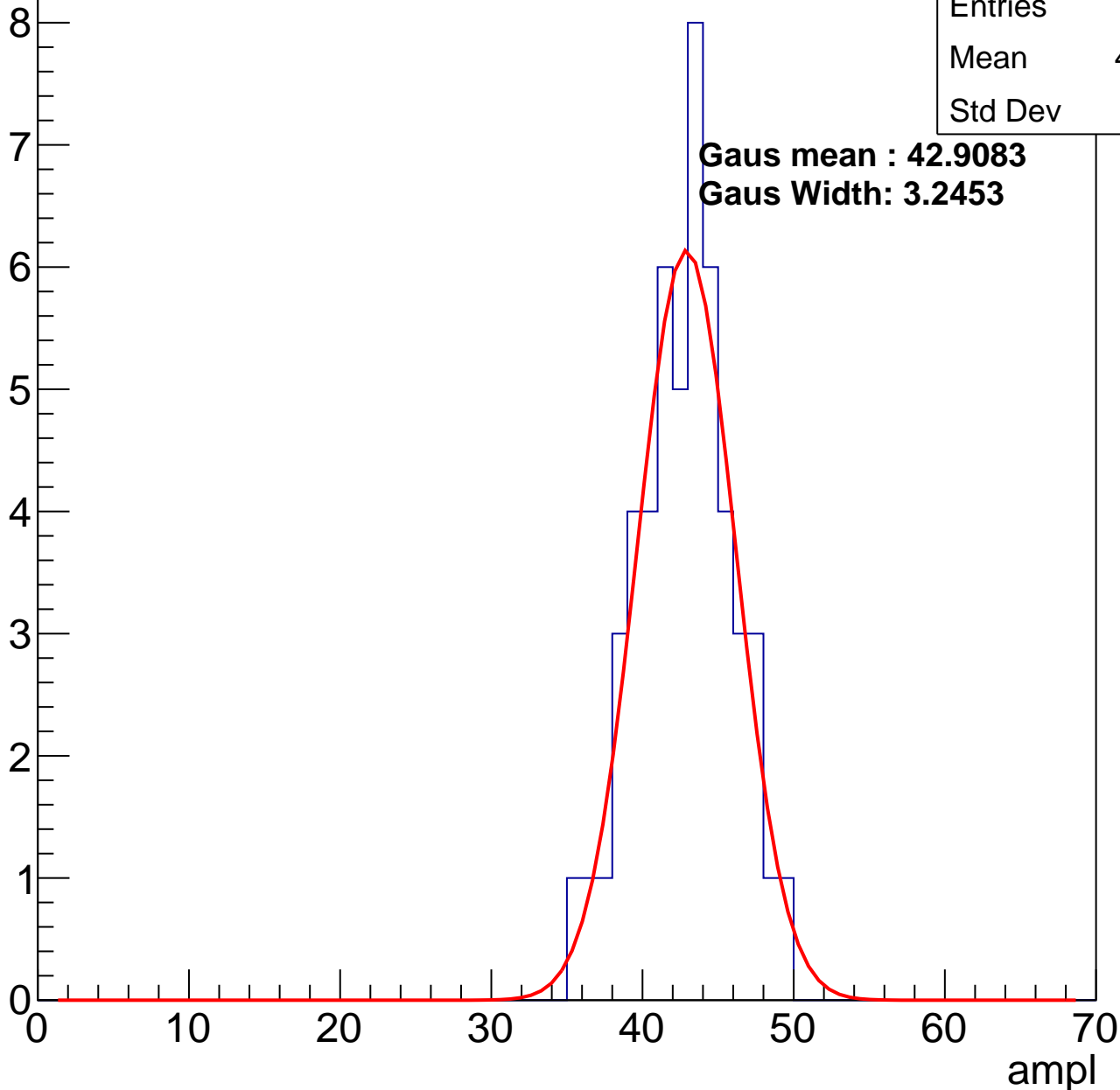
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.31
Std Dev	3.09

Gaus mean : 42.9083

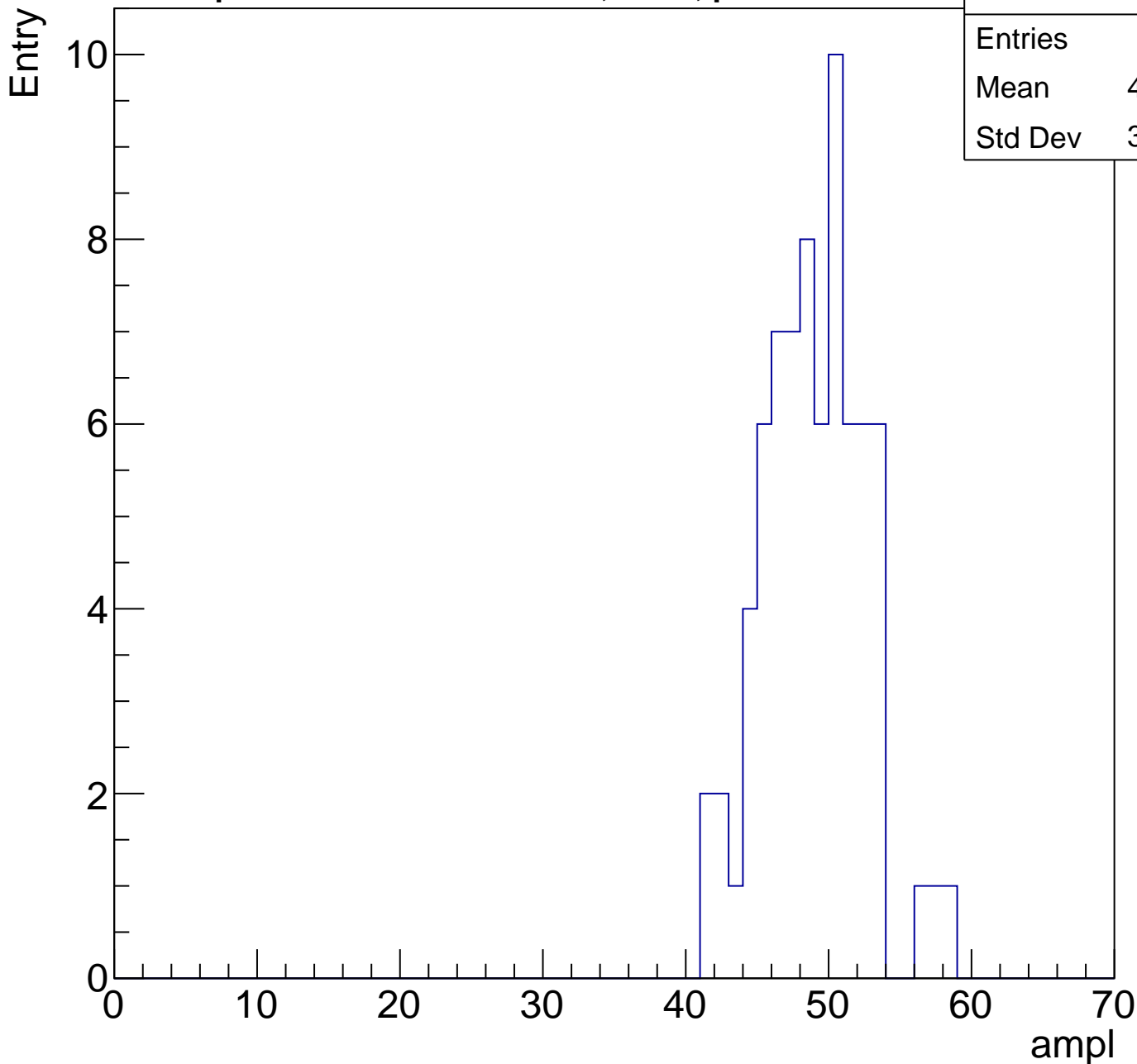
Gaus Width: 3.2453



B0L002S, U7-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	48.53
Std Dev	3.523

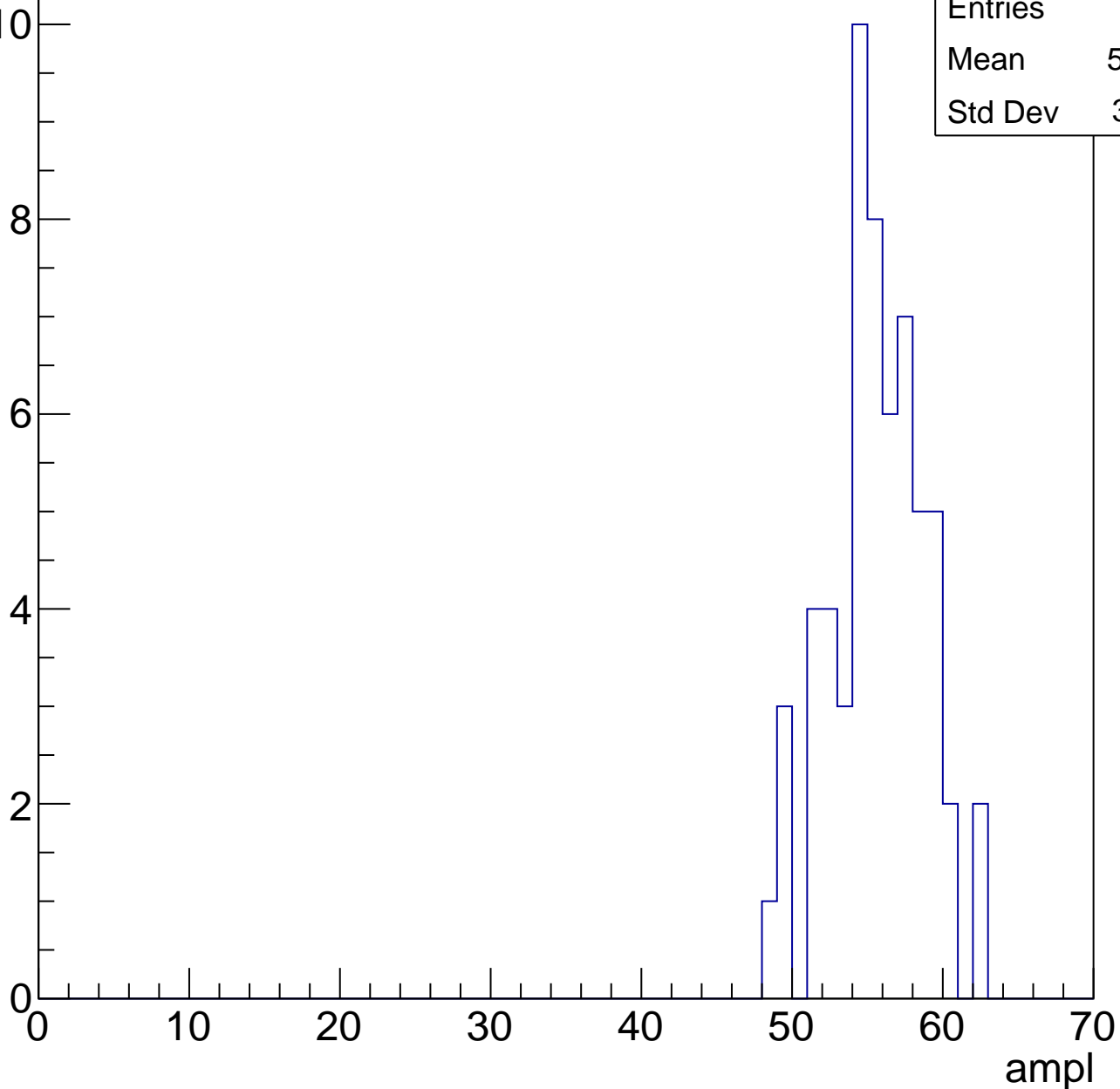


B0L002S, U7-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

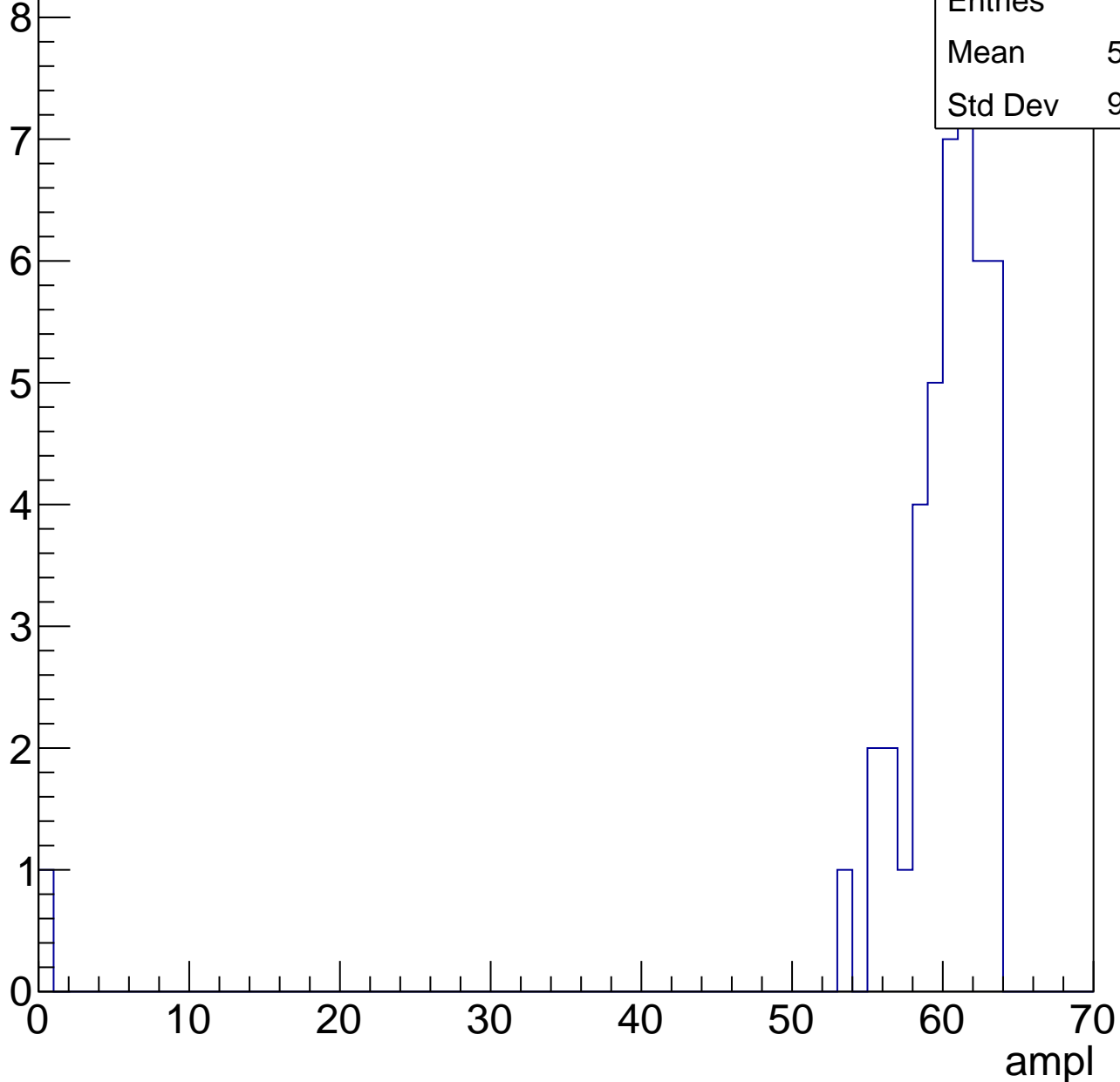
Entries	60
Mean	55.17
Std Dev	3.121



B0L002S, U7-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

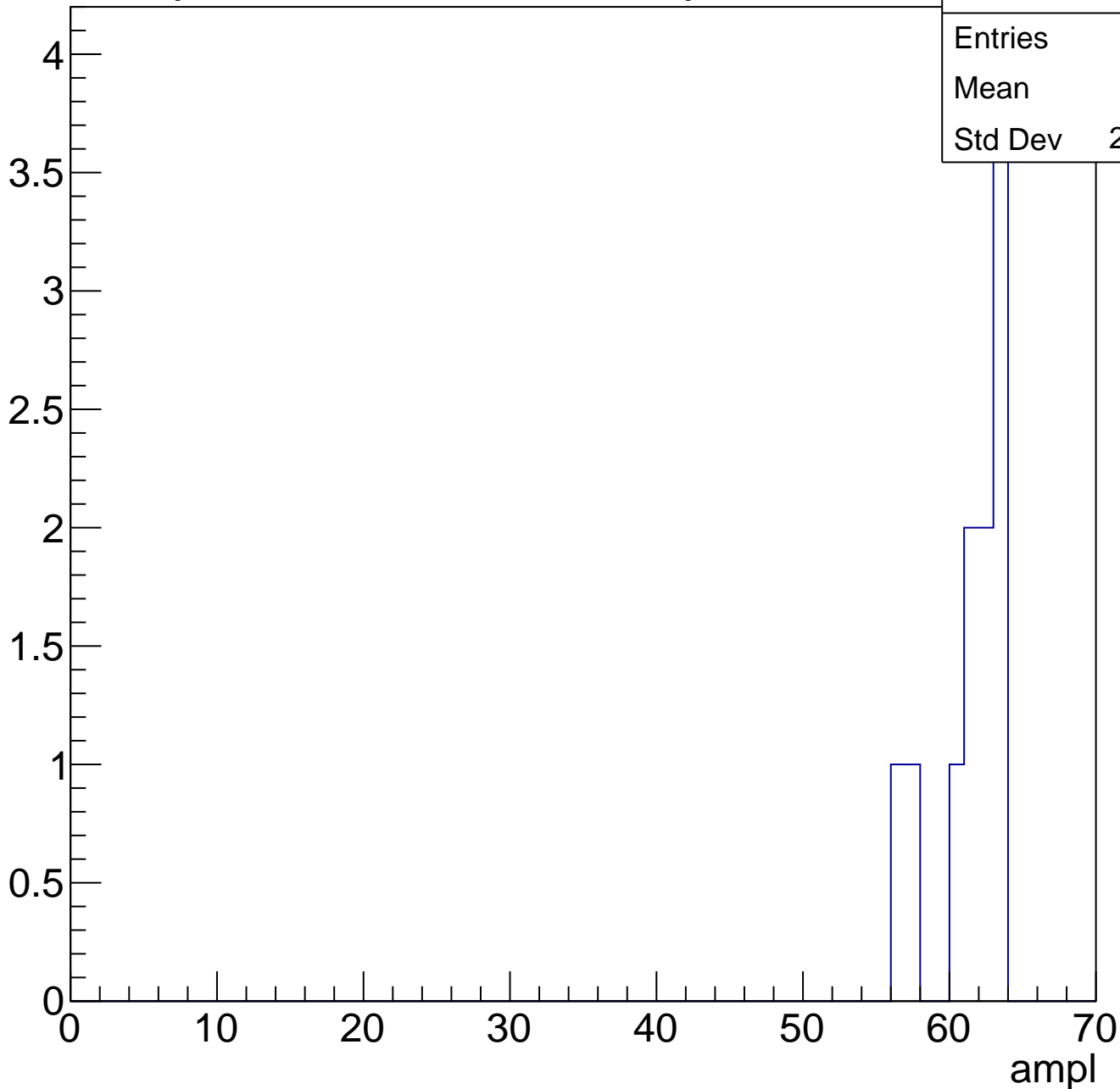
Entry



B0L002S, U7-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch81, adc0

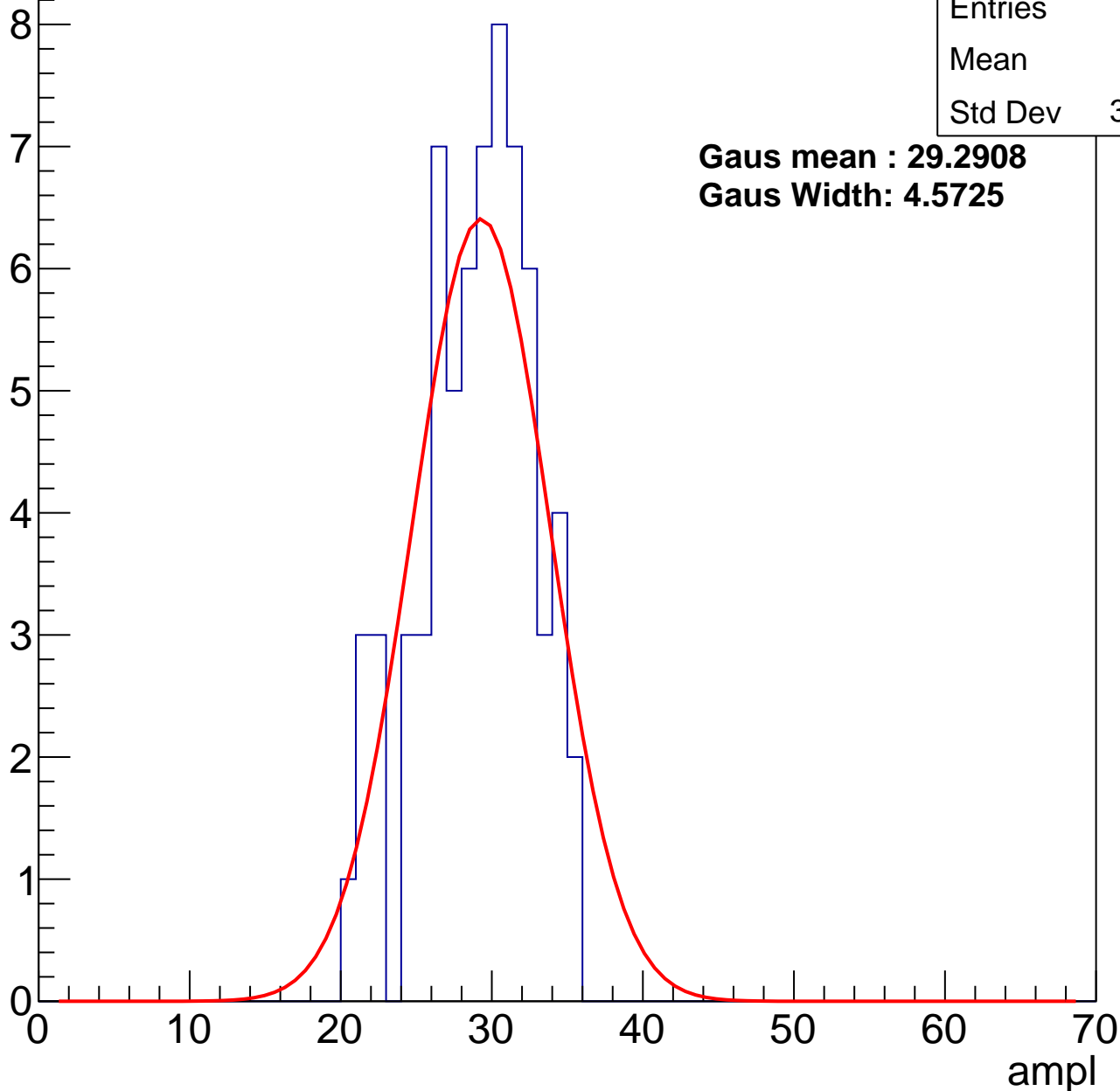
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.5
Std Dev	3.688

Gaus mean : 29.2908

Gaus Width: 4.5725



B0L002S, U7-ch81, adc1

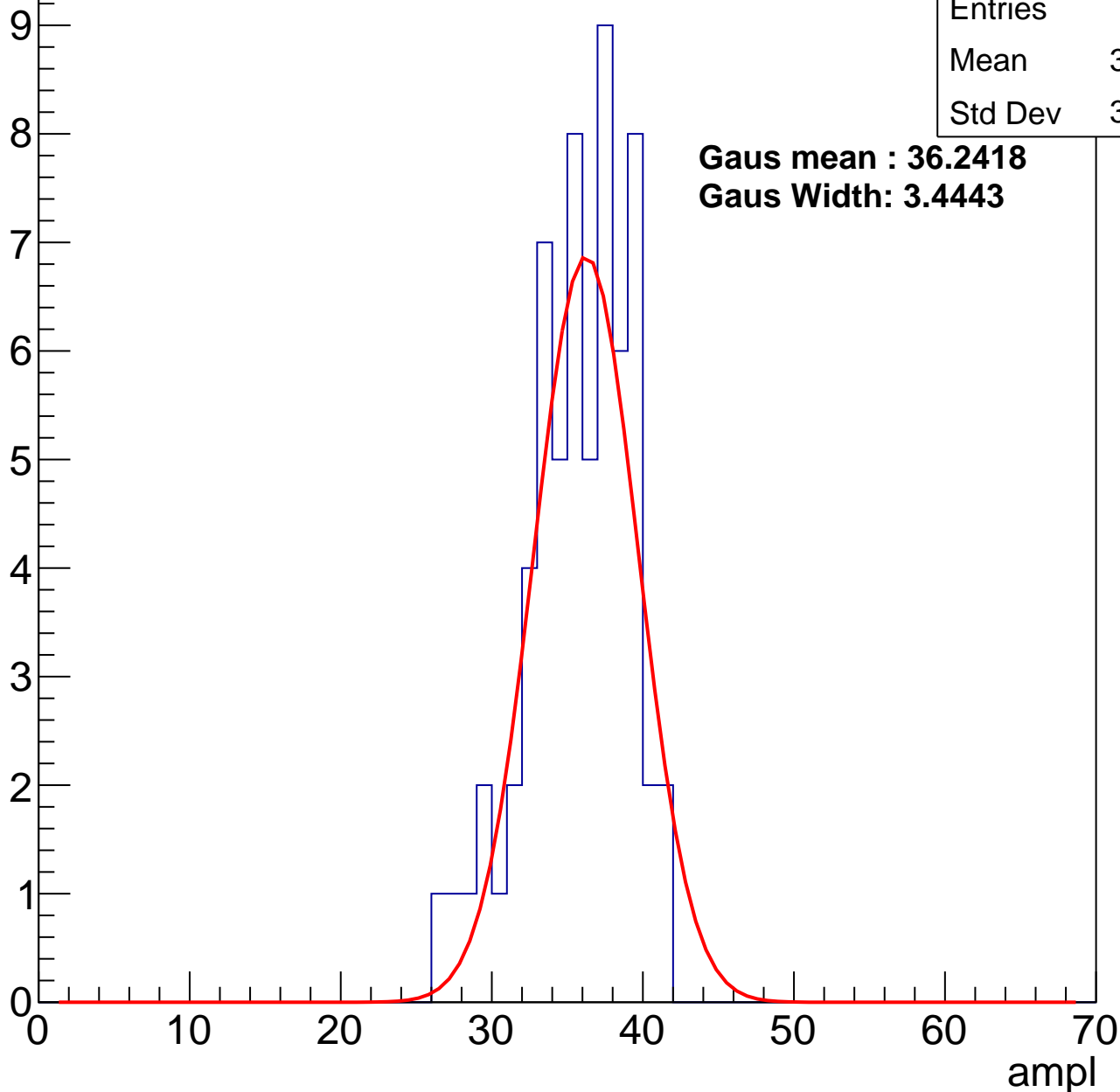
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	35.23
Std Dev	3.385

Gaus mean : 36.2418

Gaus Width: 3.4443



B0L002S, U7-ch81, adc2

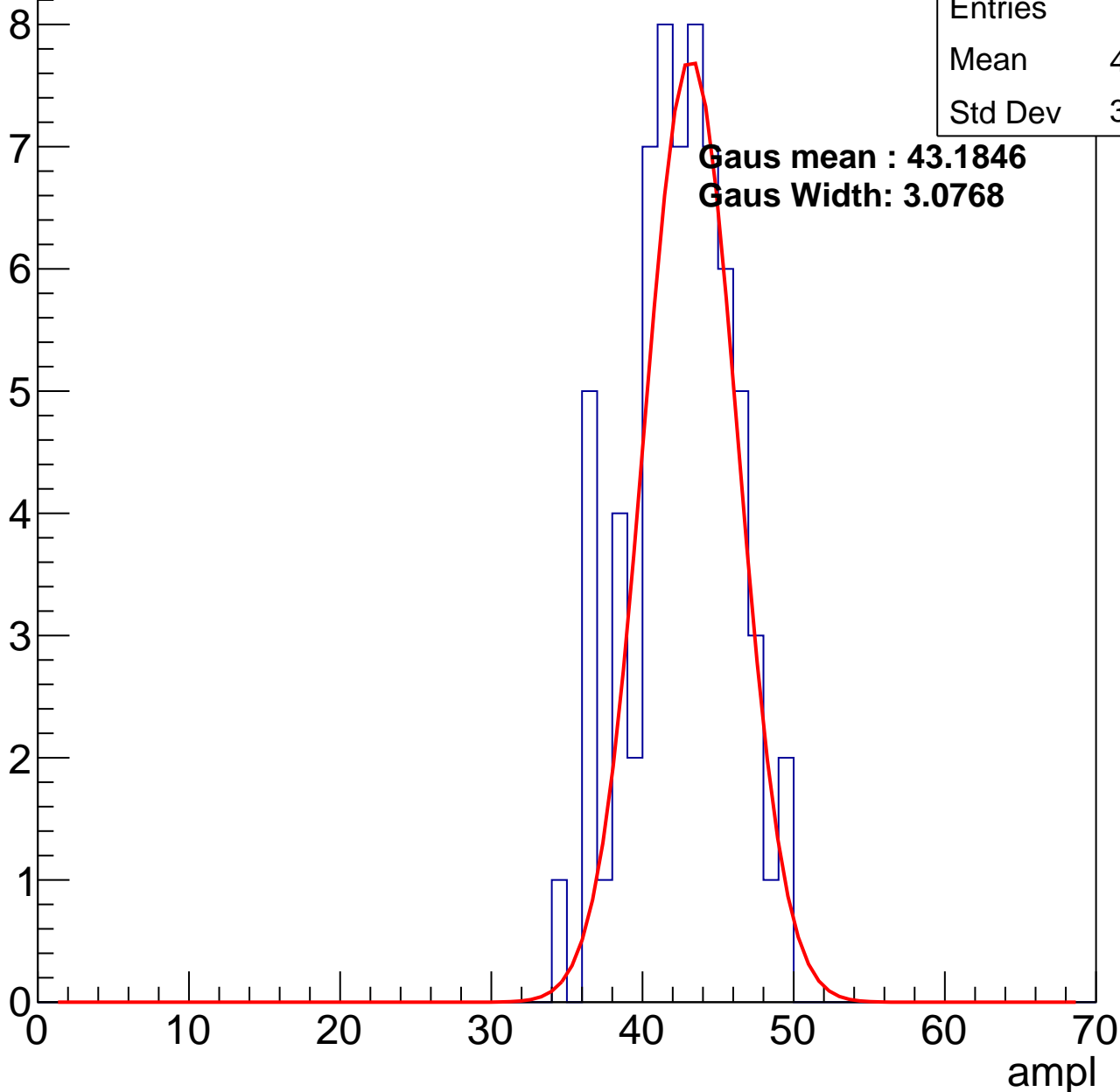
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.12
Std Dev	3.379

Gaus mean : 43.1846

Gaus Width: 3.0768

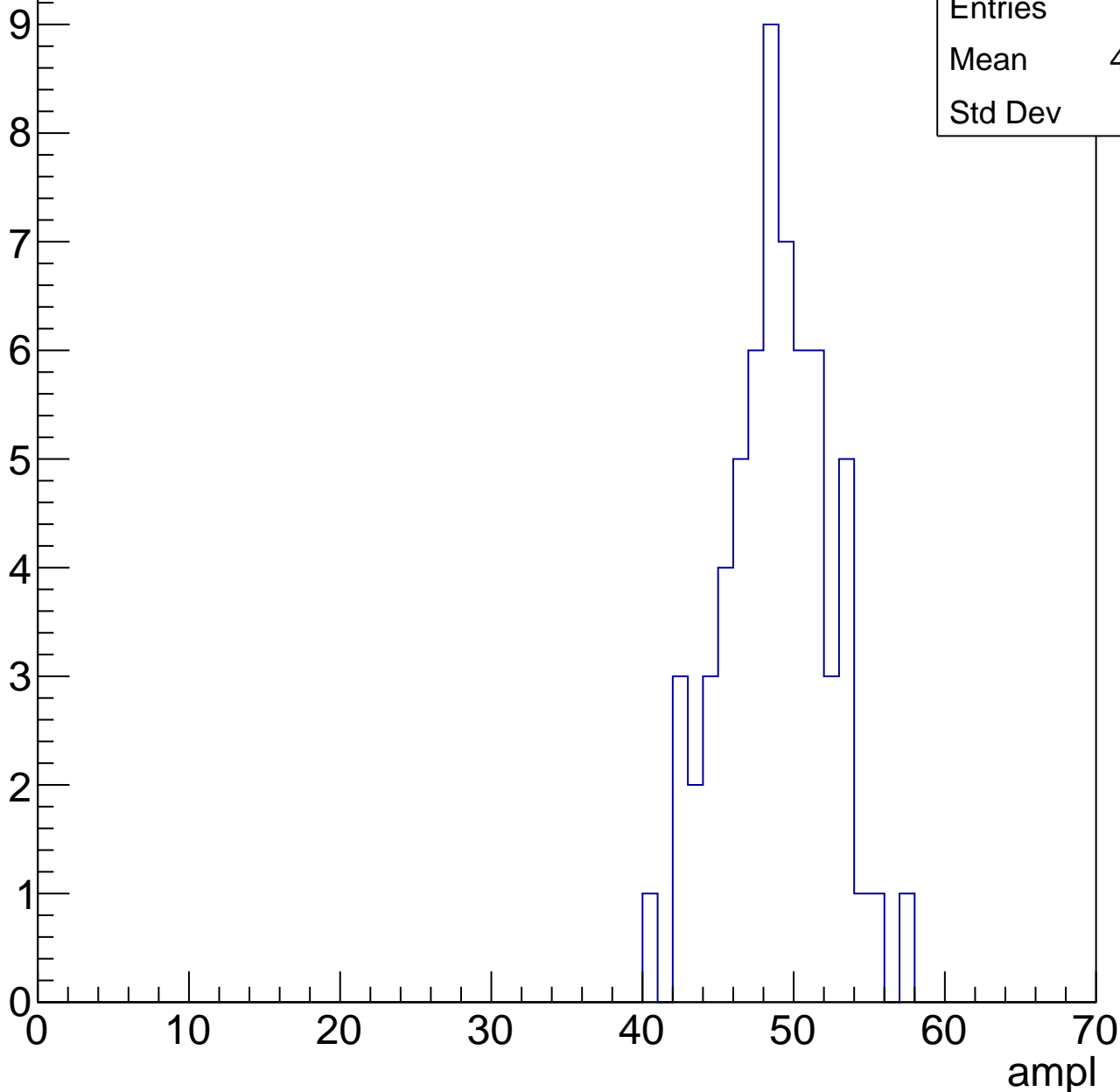


B0L002S, U7-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	48.32
Std Dev	3.45

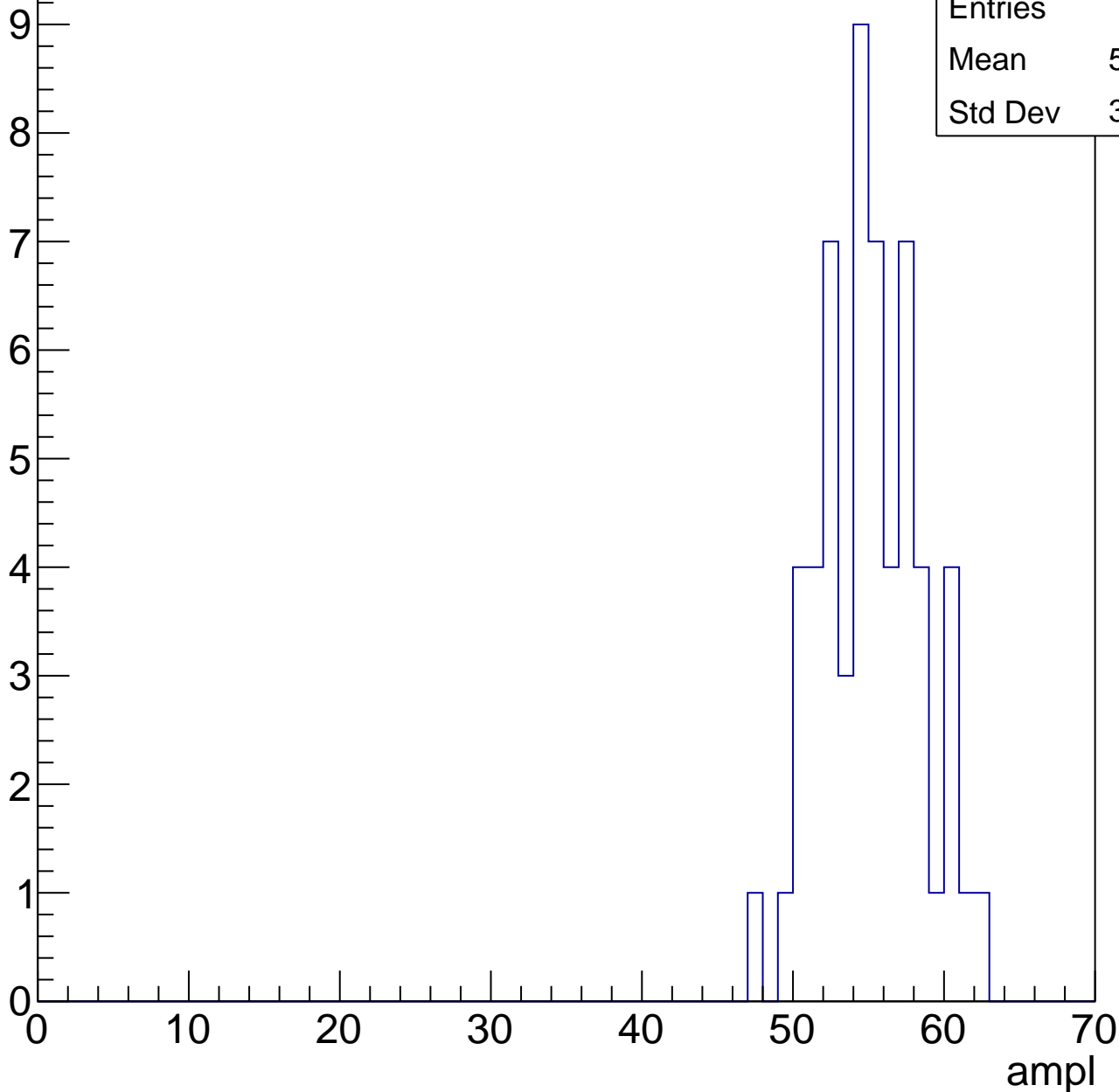


B0L002S, U7-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

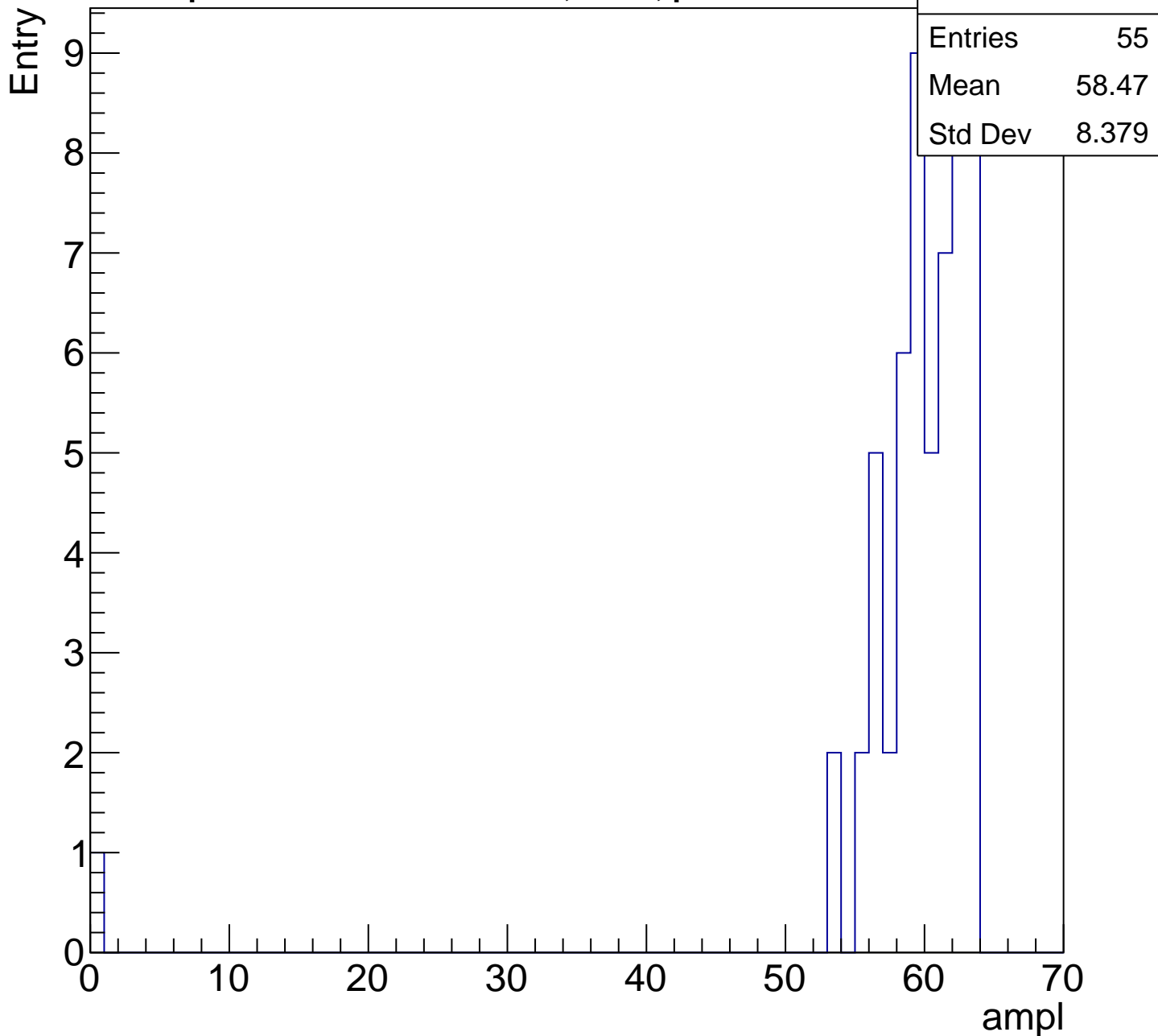
Entry

Entries	58
Mean	54.67
Std Dev	3.245



B0L002S, U7-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

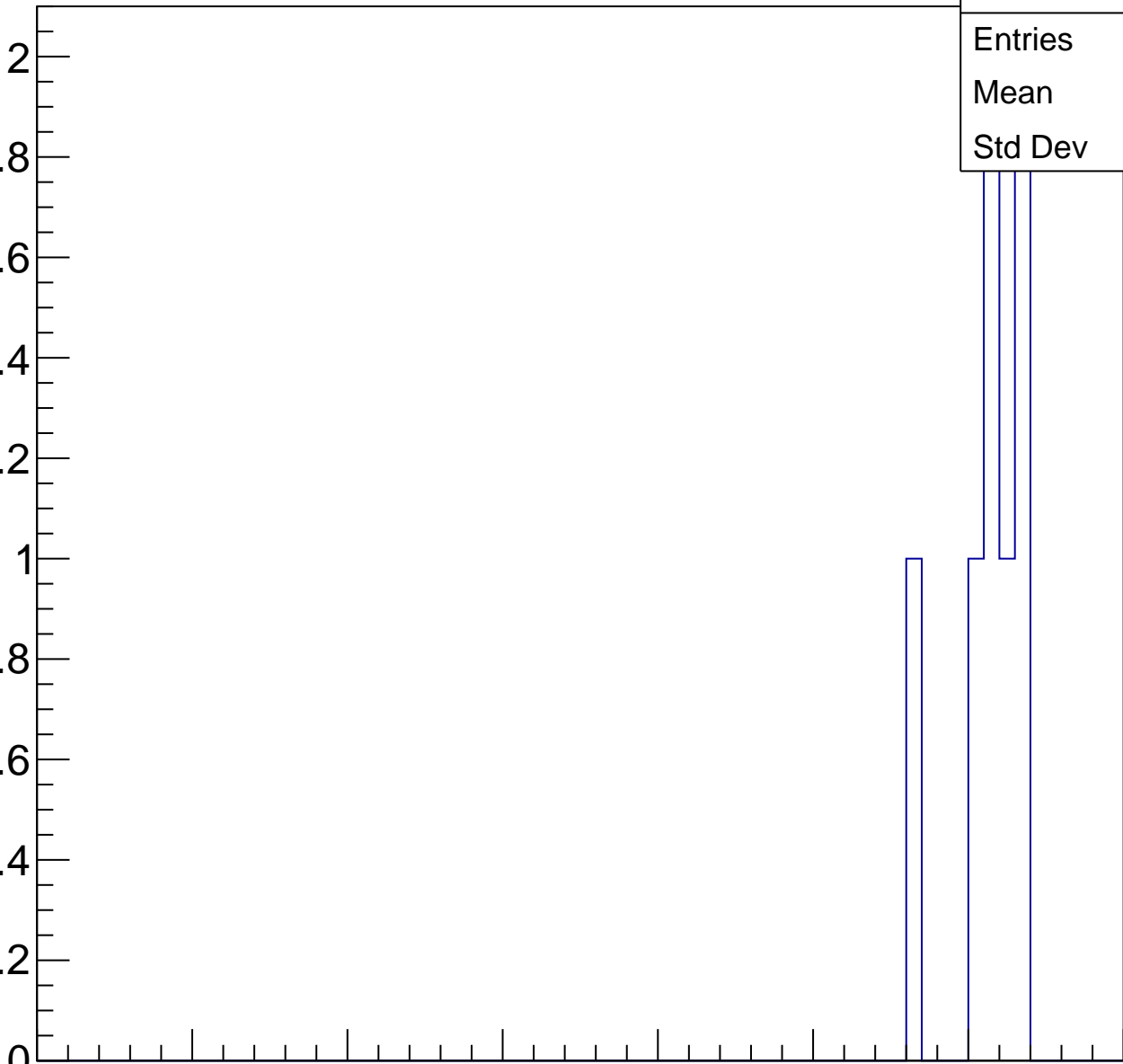
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.86
Std Dev	2.231

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch82, adc0

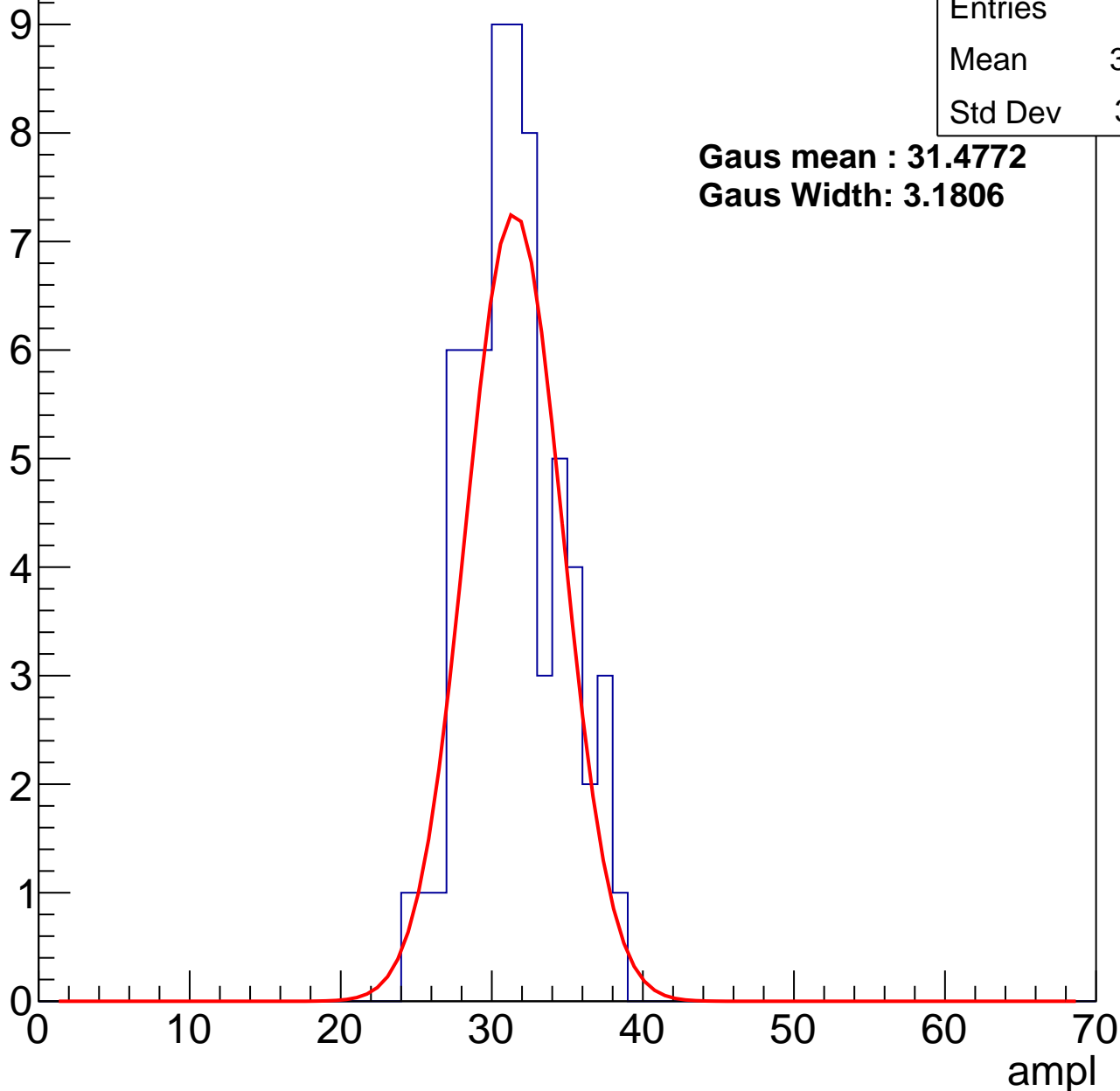
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.98
Std Dev	3.111

Gaus mean : 31.4772

Gaus Width: 3.1806



B0L002S, U7-ch82, adc1

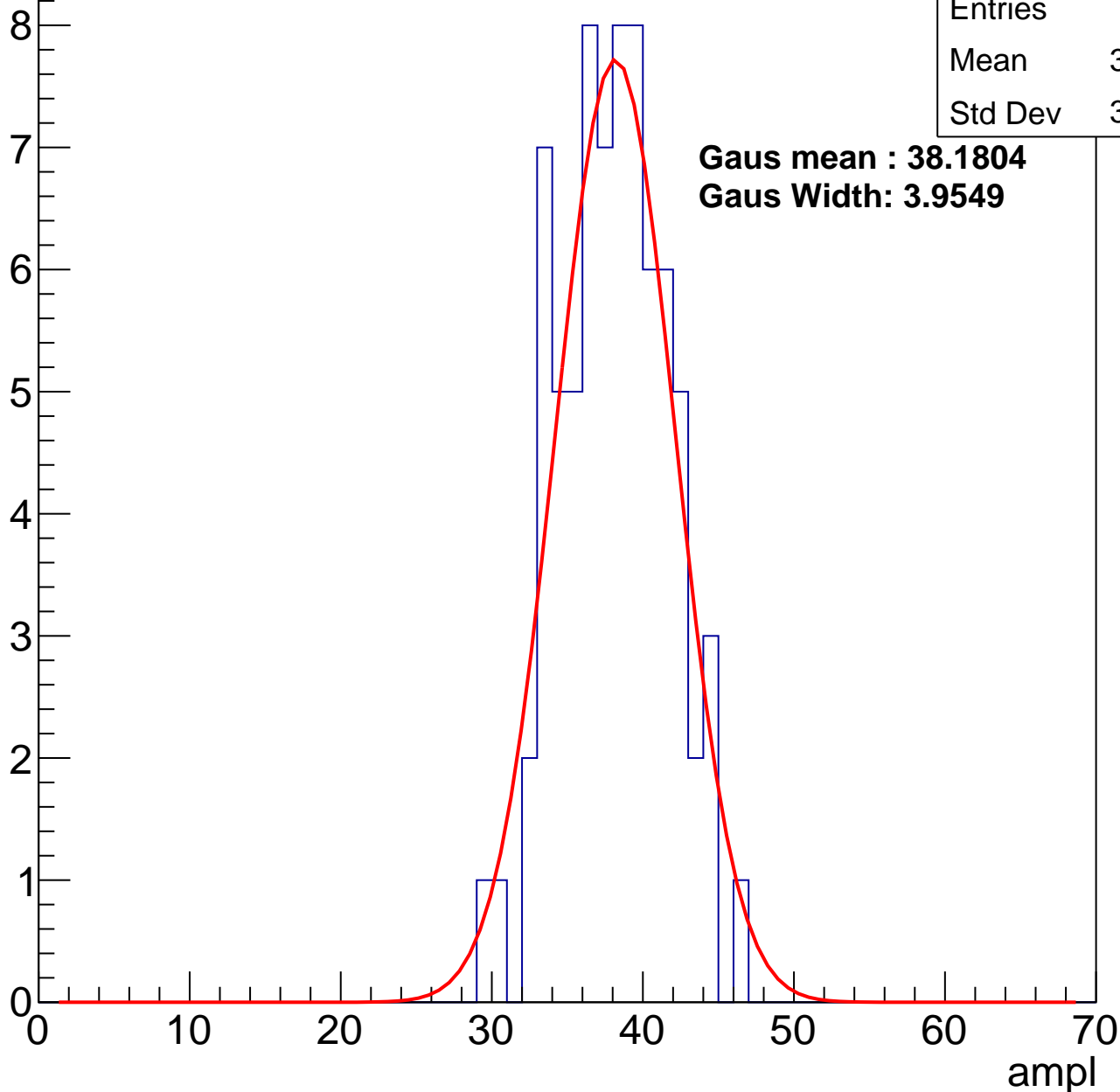
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.63
Std Dev	3.528

Gaus mean : 38.1804

Gaus Width: 3.9549



B0L002S, U7-ch82, adc2

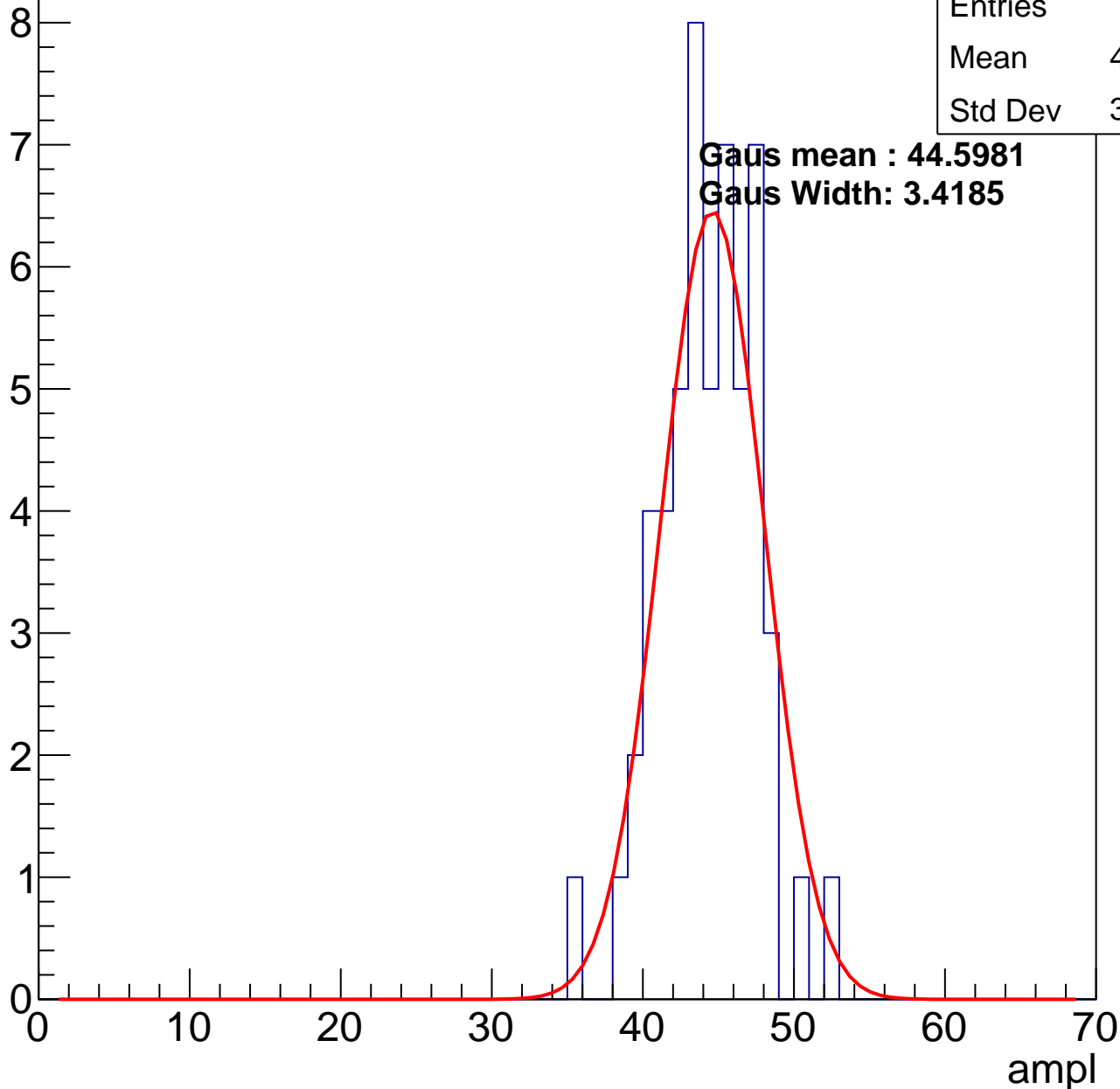
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.87
Std Dev	3.139

Gaus mean : 44.5981

Gaus Width: 3.4185

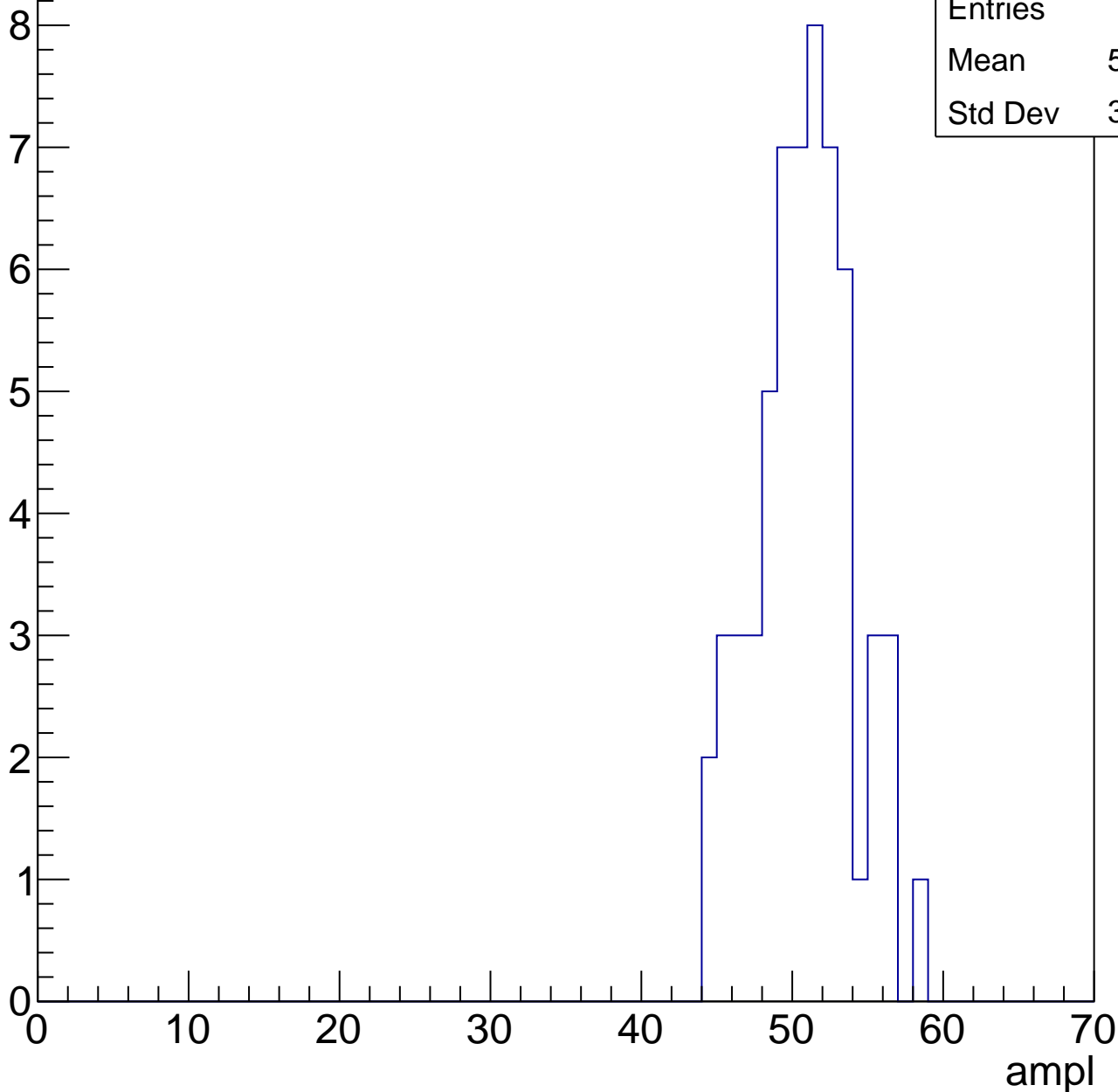


B0L002S, U7-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.34
Std Dev	3.176

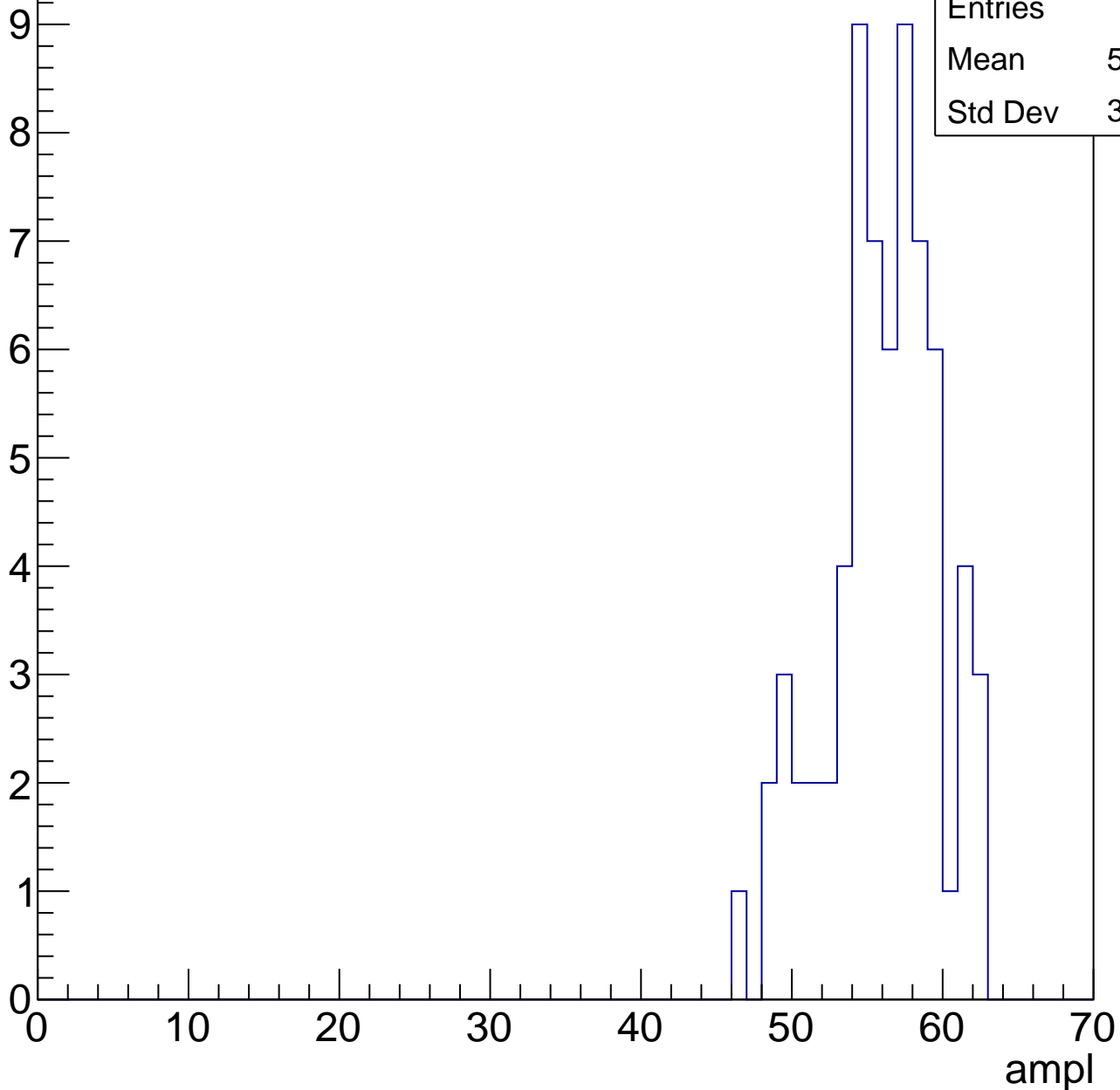


B0L002S, U7-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	55.54
Std Dev	3.656



B0L002S, U7-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

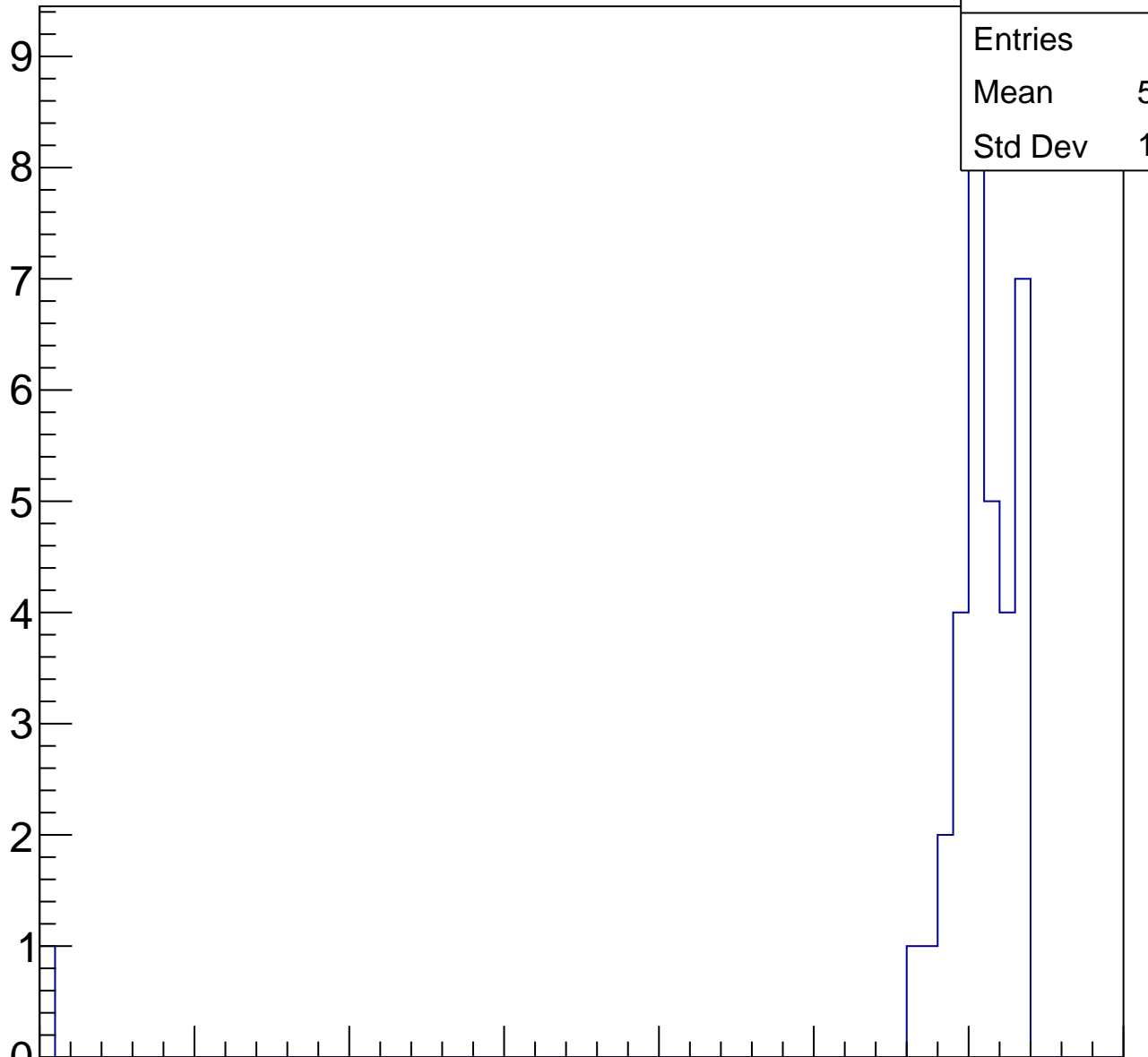
Entry

9
8
7
6
5
4
3
2
1
0

Entries	34
Mean	58.79
Std Dev	10.39

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch83, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.94
Std Dev	4.798

Gaus mean : 31.6782

Gaus Width: 3.2307

10

8

6

4

2

0

0

10

20

30

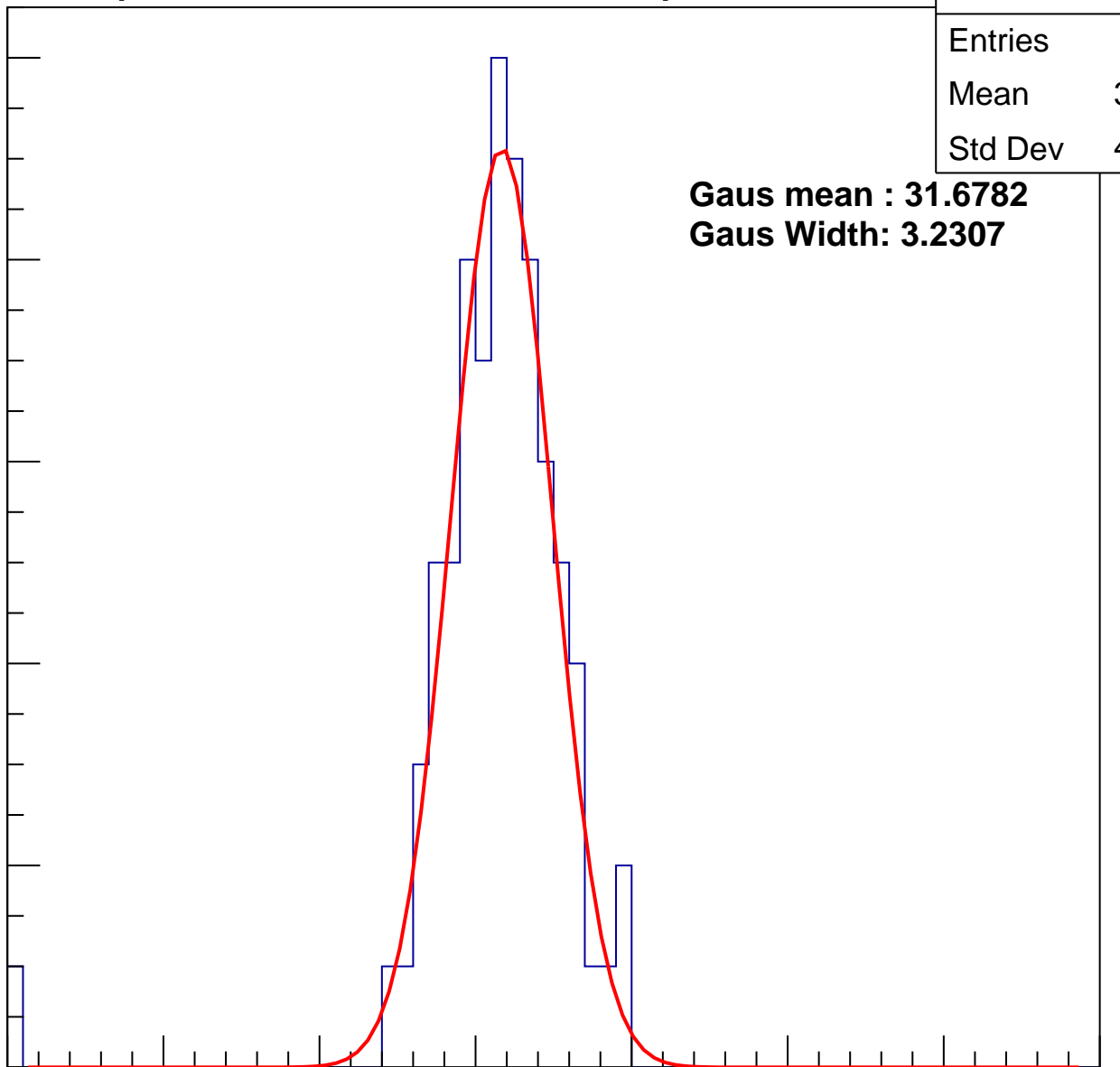
40

50

60

70

ampl



B0L002S, U7-ch83, adc1

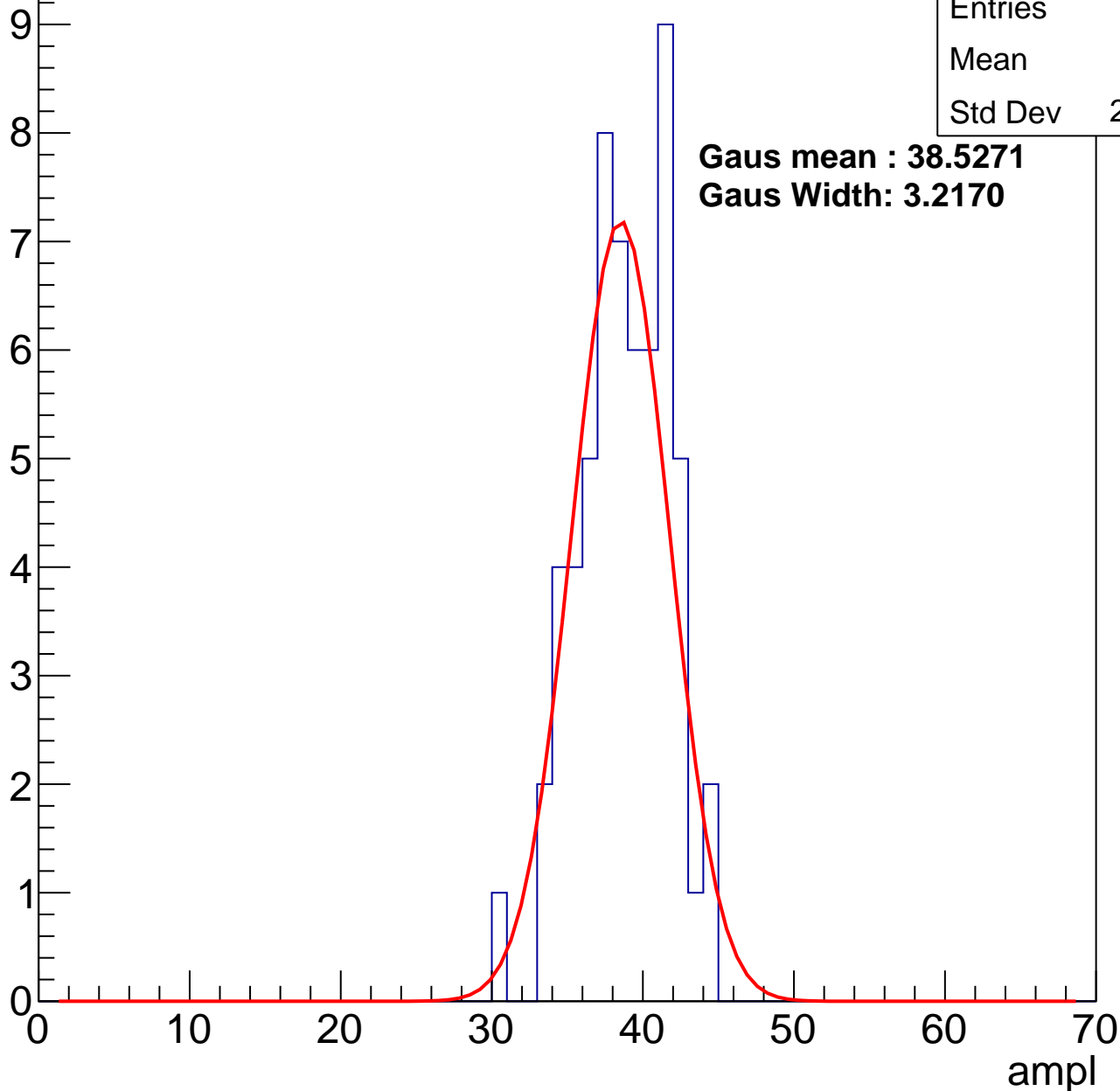
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38.3
Std Dev	2.957

Gaus mean : 38.5271

Gaus Width: 3.2170



B0L002S, U7-ch83, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	44.68
Std Dev	3.522

Gaus mean : 46.0991

Gaus Width: 2.8037

Entry

10

8

6

4

2

0

0

10

20

30

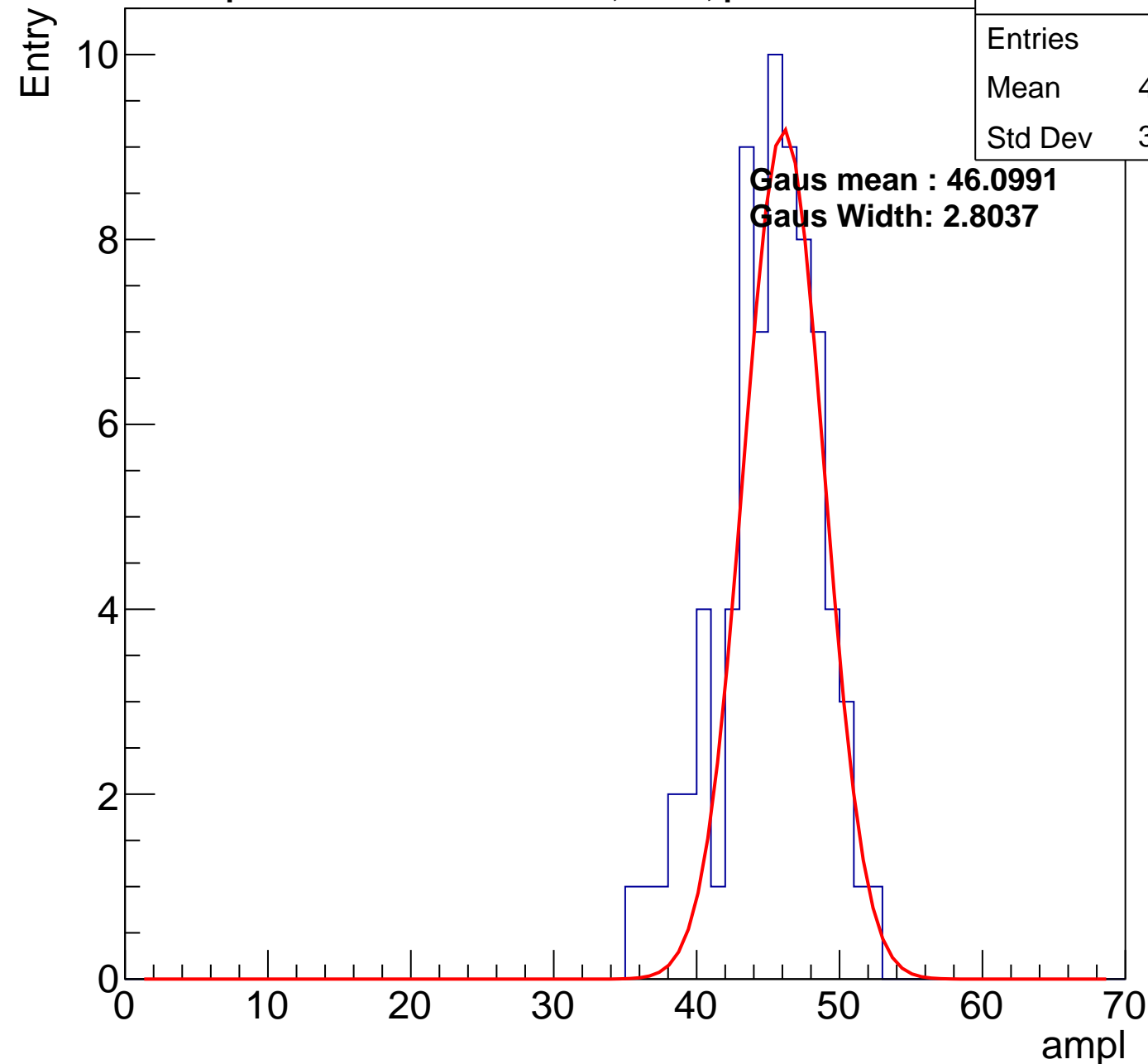
40

50

60

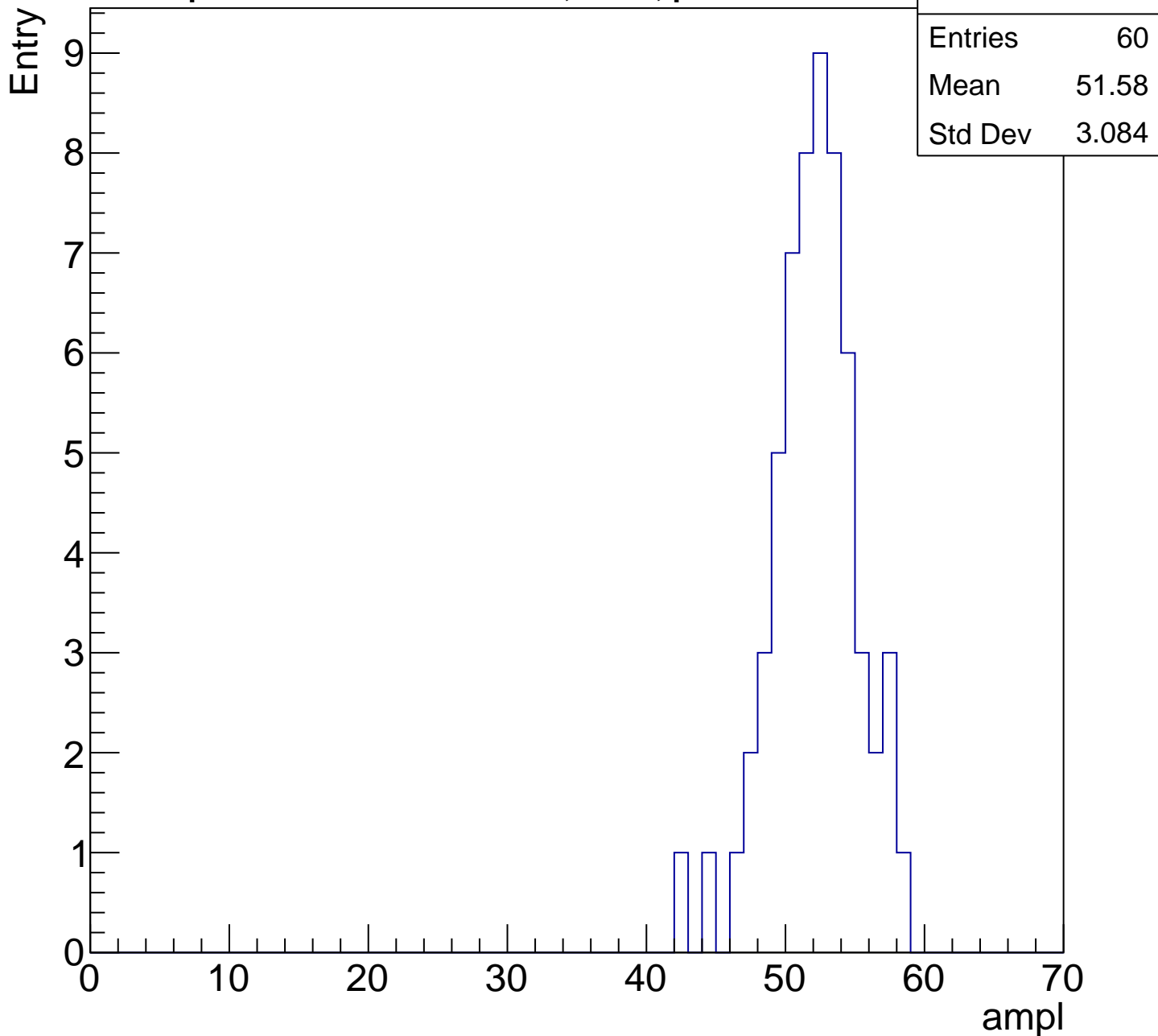
70

ampl



B0L002S, U7-ch83, adc3

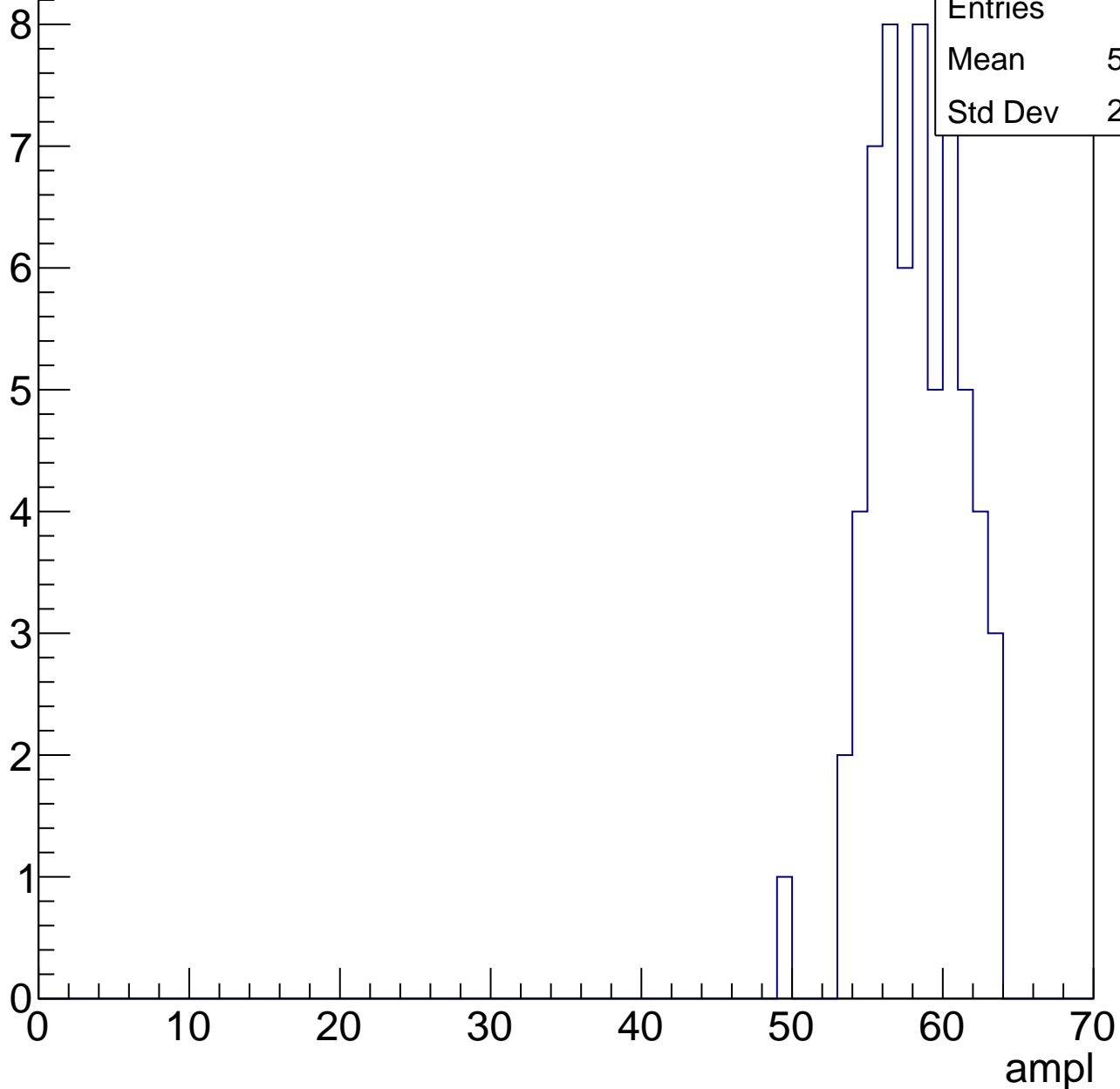
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

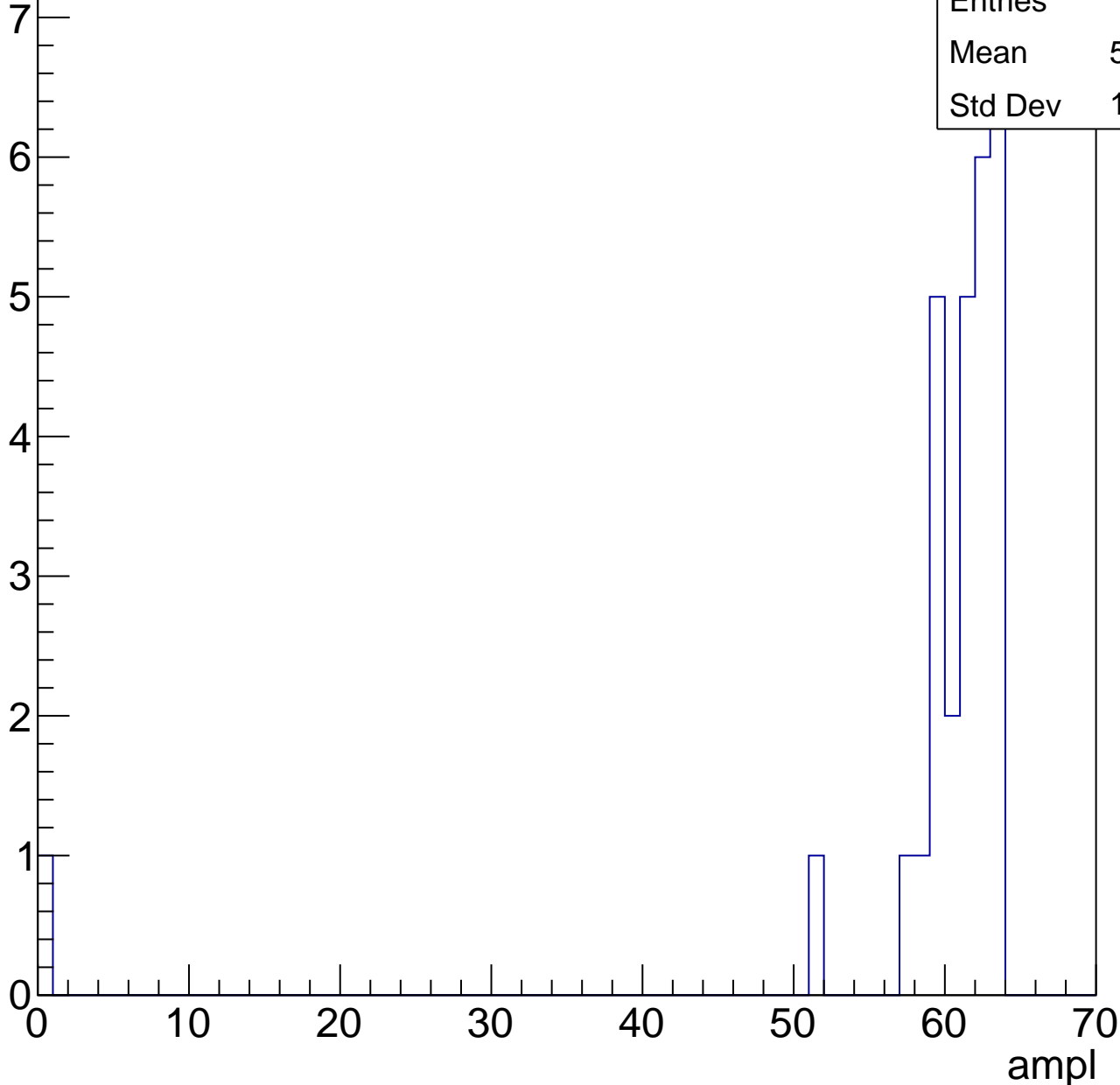


B0L002S, U7-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.59
Std Dev	11.35



B0L002S, U7-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch84, adc0

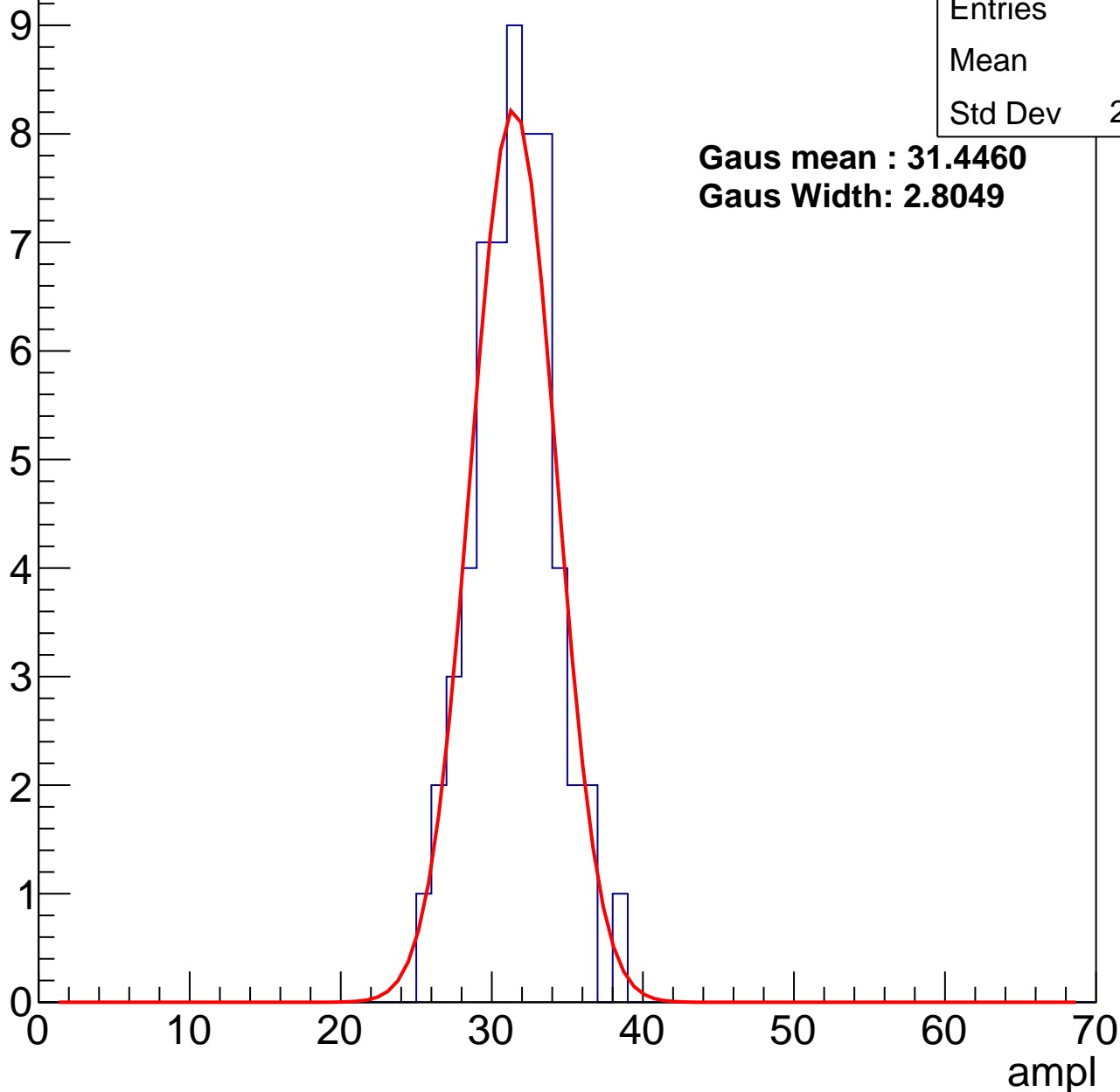
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31
Std Dev	2.665

Gaus mean : 31.4460

Gaus Width: 2.8049



B0L002S, U7-ch84, adc1

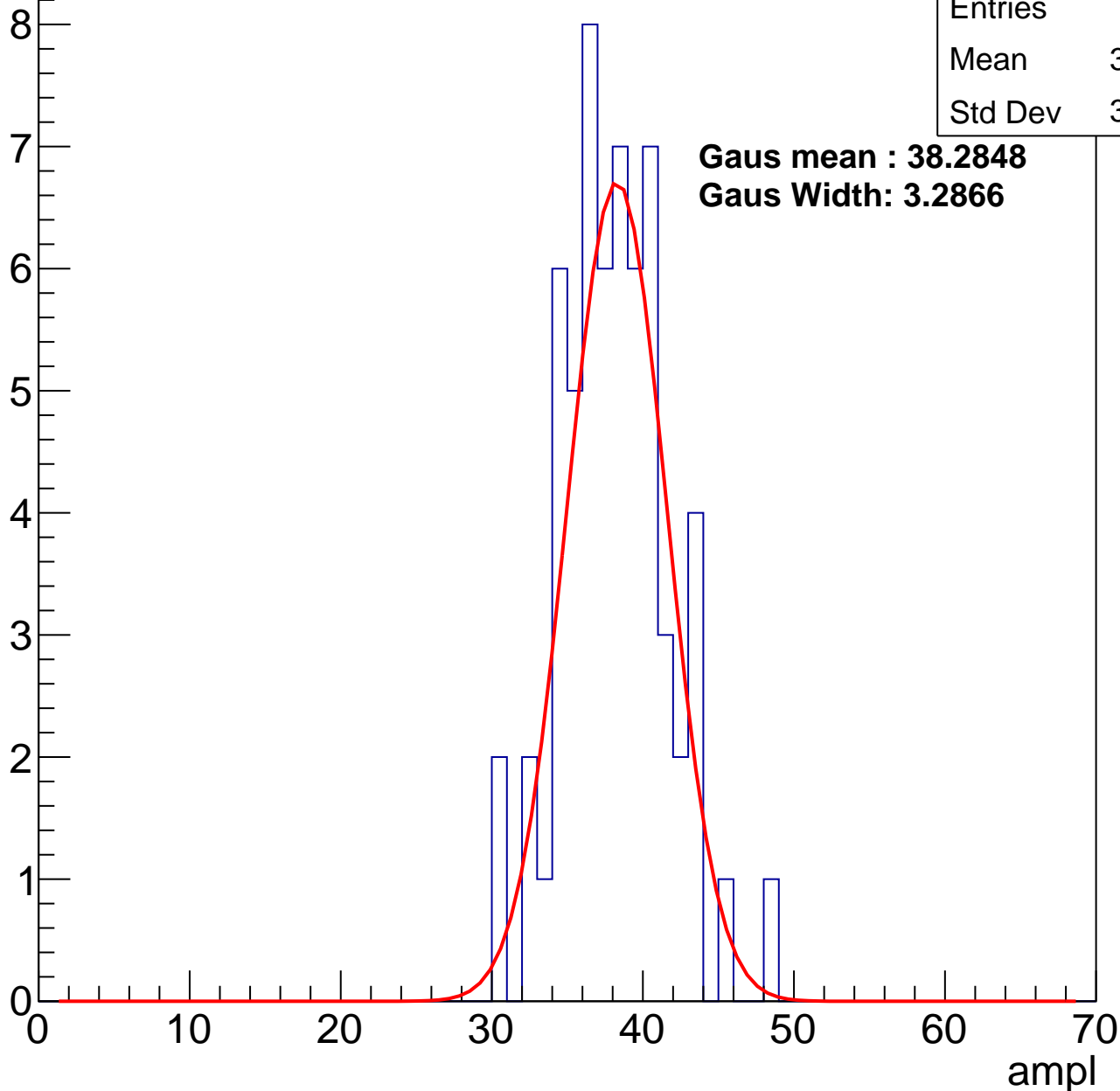
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.67
Std Dev	3.482

Gaus mean : 38.2848

Gaus Width: 3.2866



B0L002S, U7-ch84, adc2

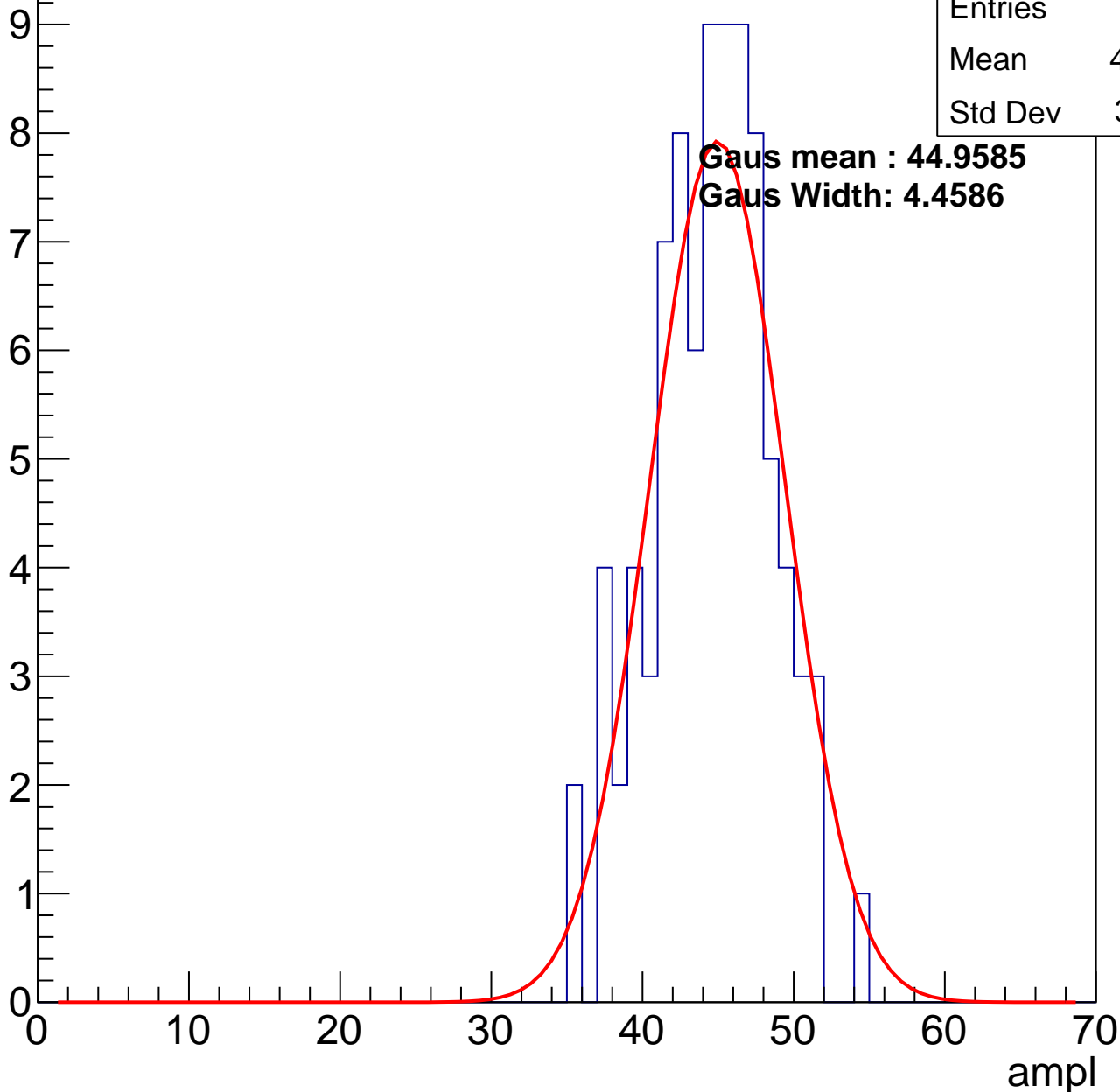
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	44.08
Std Dev	3.911

Gaus mean : 44.9585

Gaus Width: 4.4586

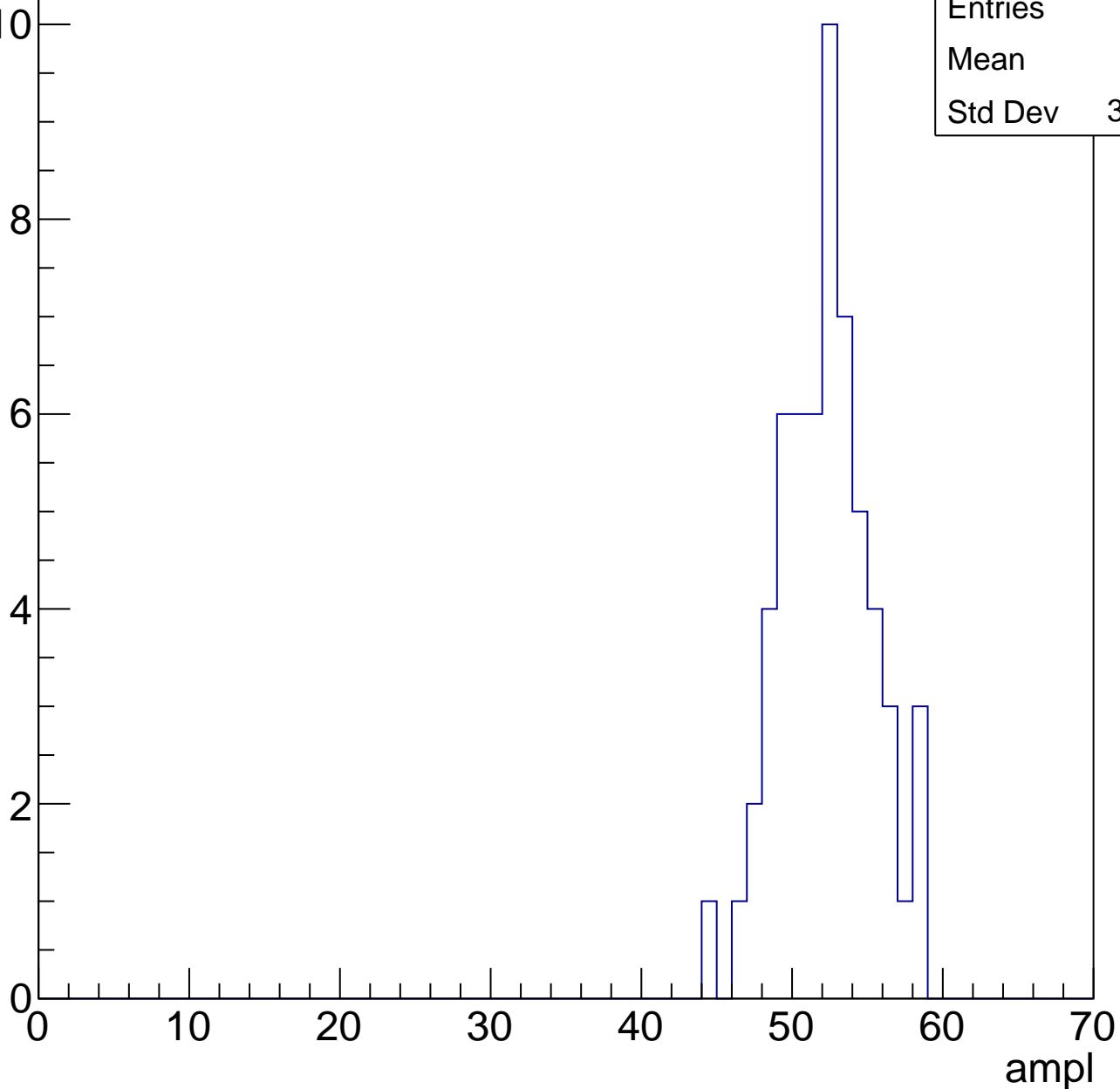


B0L002S, U7-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

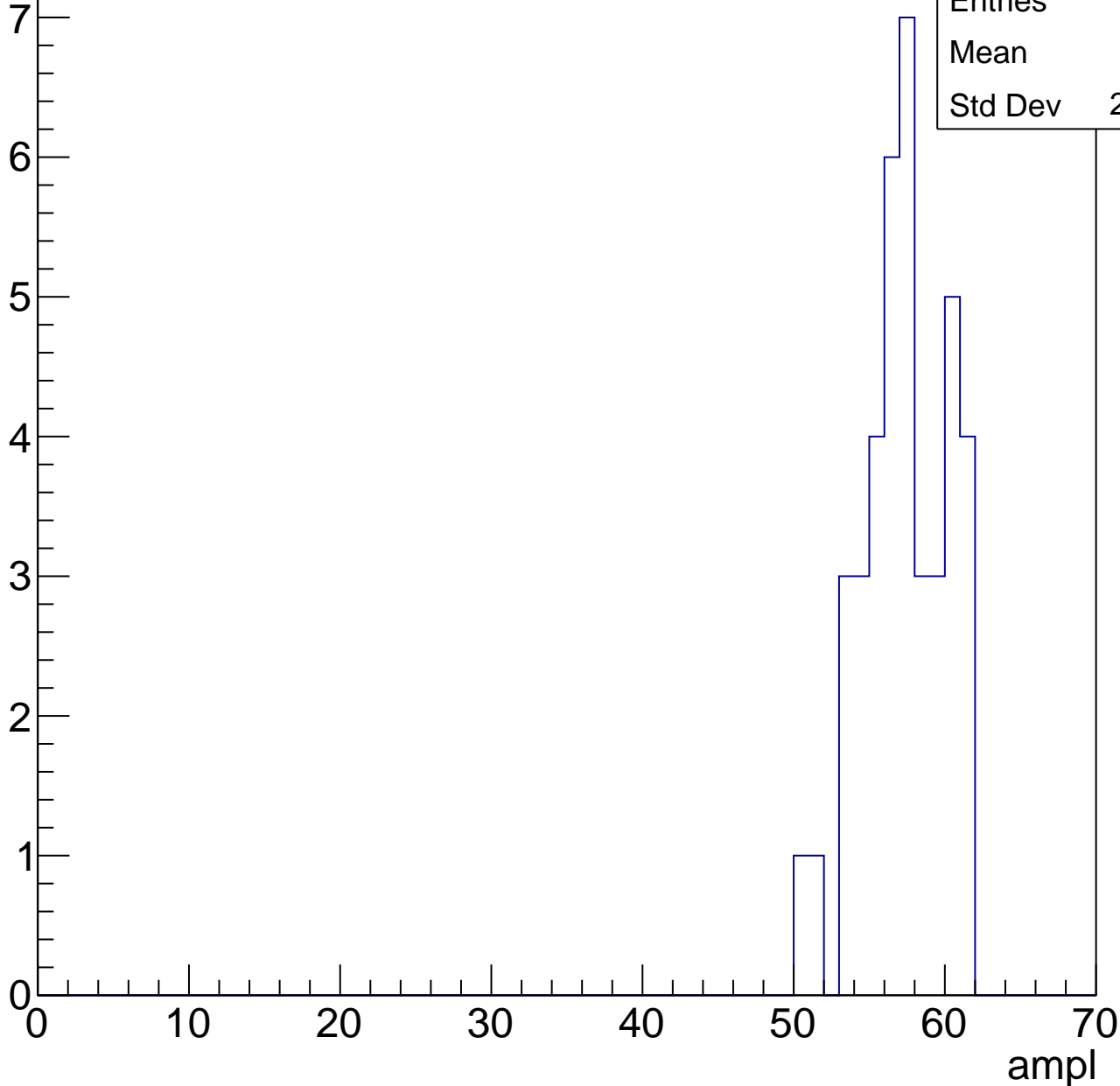
Entries	59
Mean	51.8
Std Dev	3.024



B0L002S, U7-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

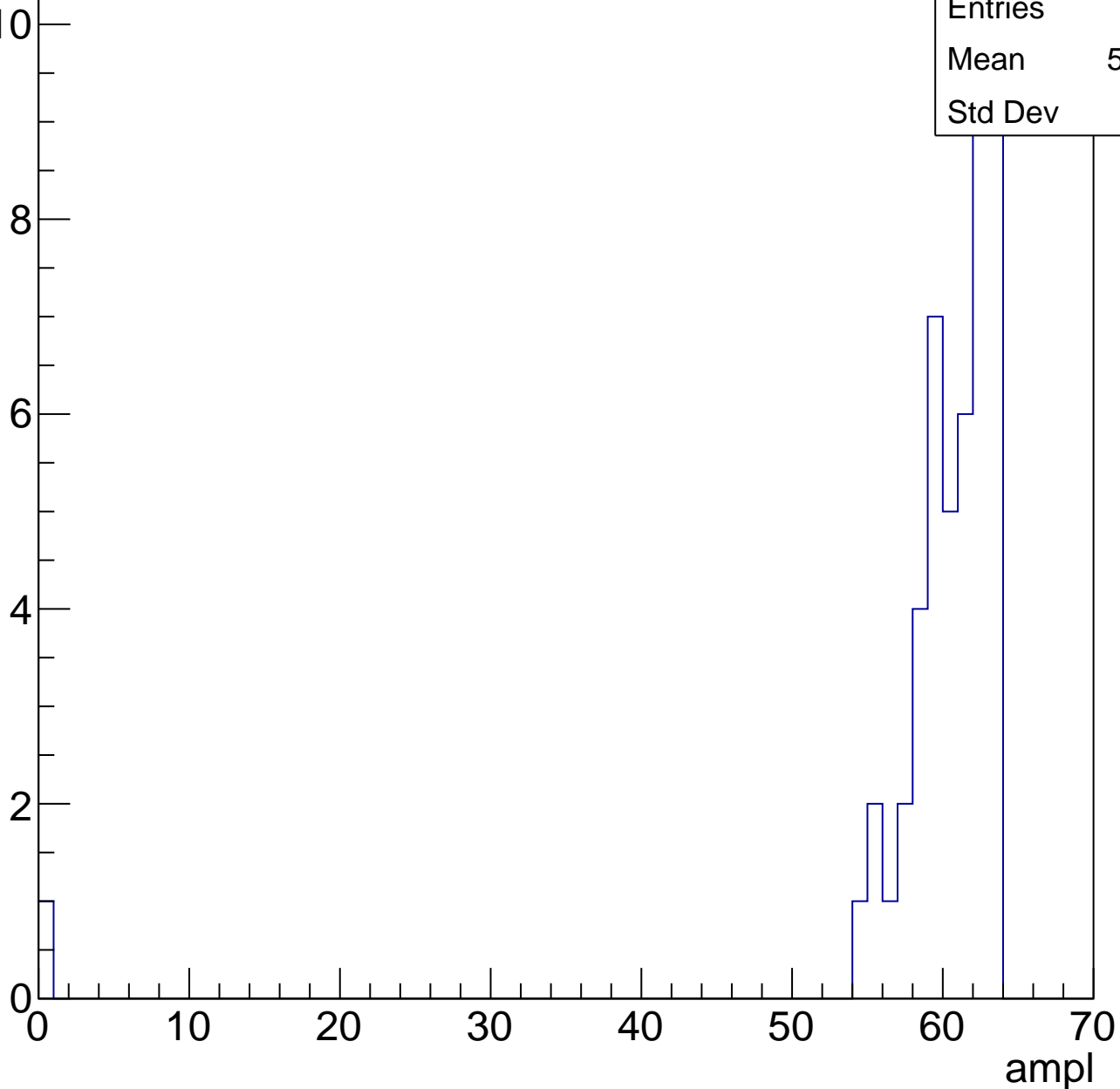


B0L002S, U7-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	59.08
Std Dev	8.85



B0L002S, U7-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch85, adc0

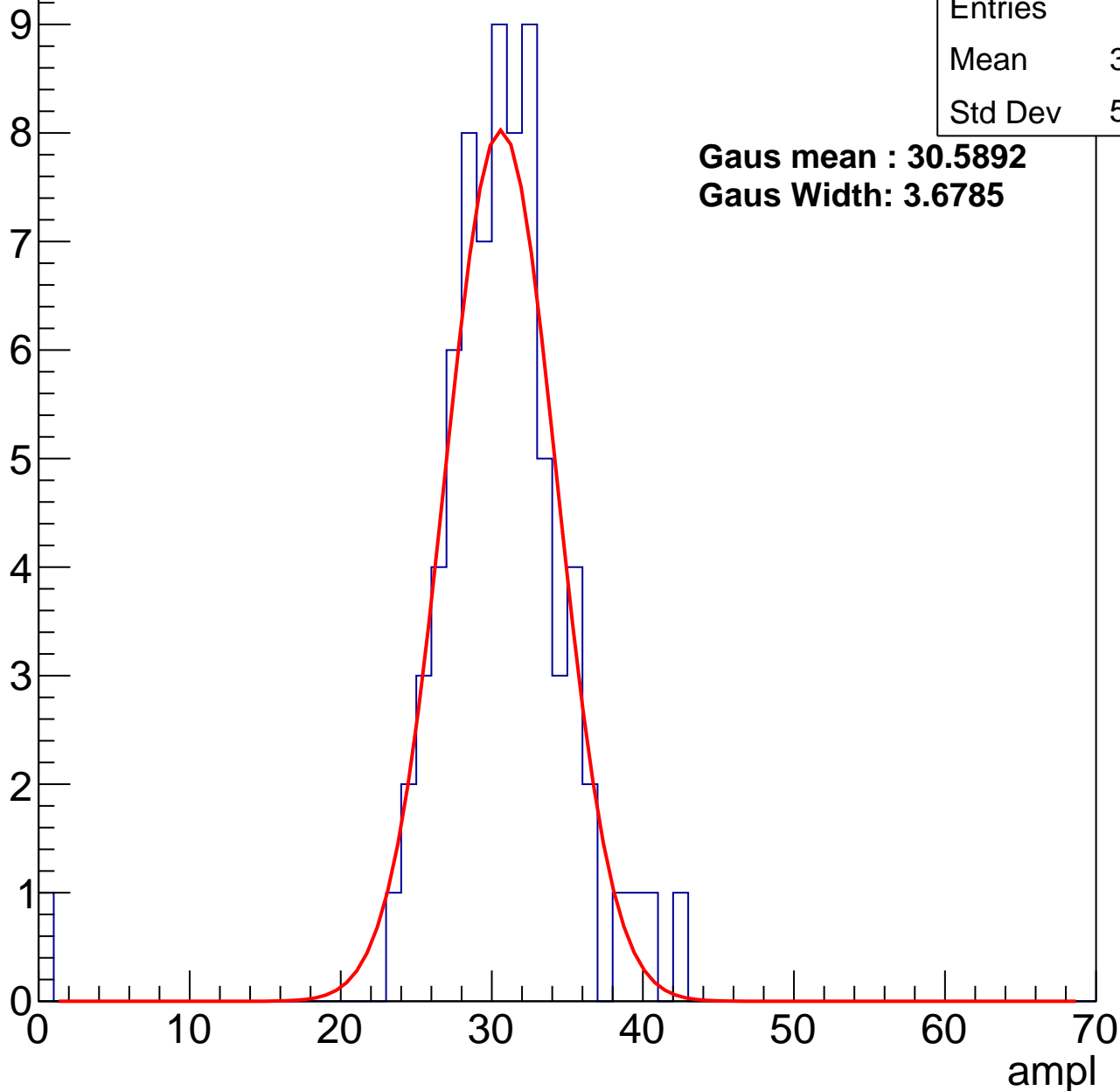
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.04
Std Dev	5.067

Gaus mean : 30.5892

Gaus Width: 3.6785



B0L002S, U7-ch85, adc1

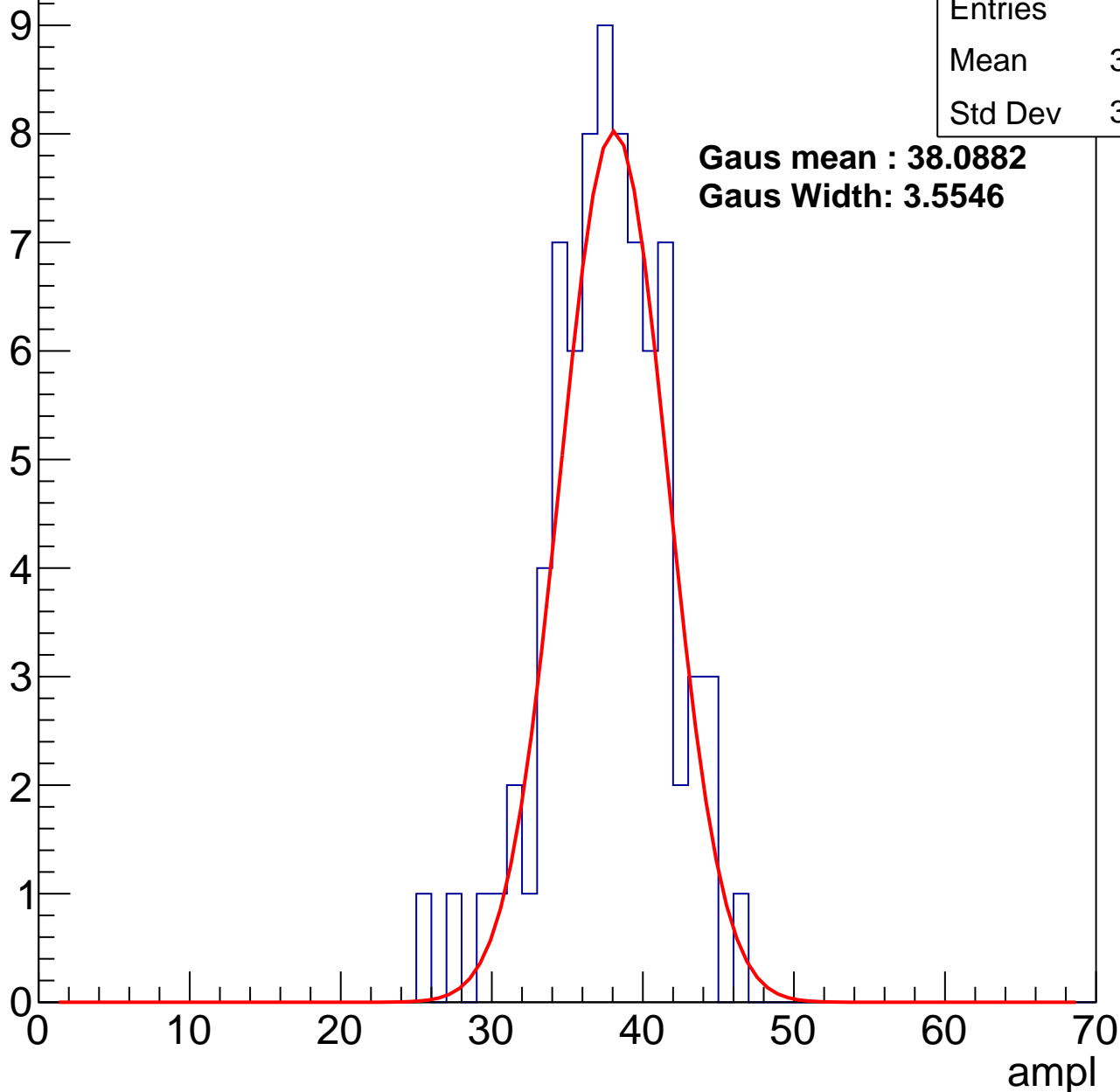
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	37.19
Std Dev	3.919

Gaus mean : 38.0882

Gaus Width: 3.5546



B0L002S, U7-ch85, adc2

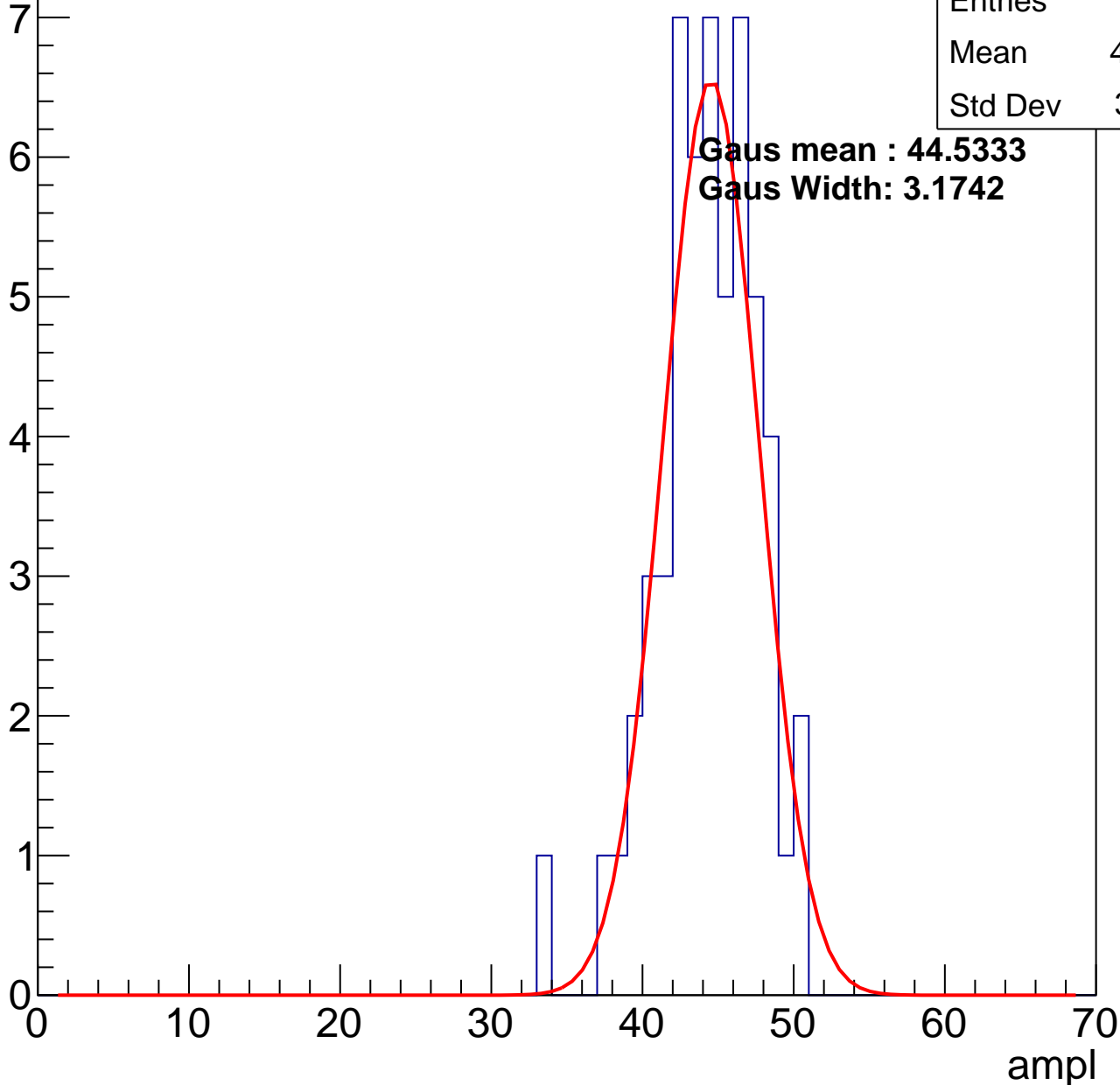
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	43.85
Std Dev	3.311

Gaus mean : 44.5333

Gaus Width: 3.1742

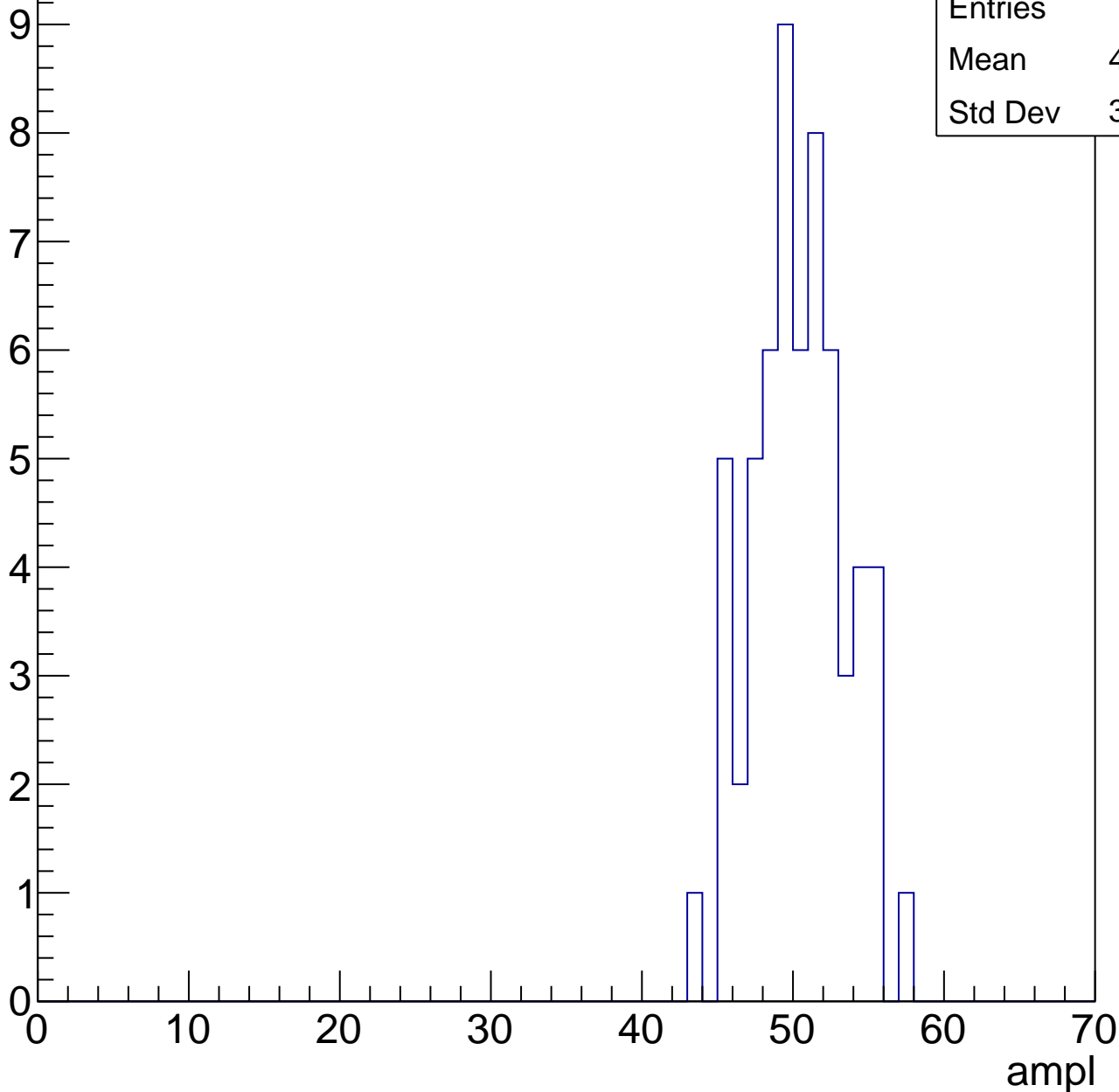


B0L002S, U7-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	49.93
Std Dev	3.043

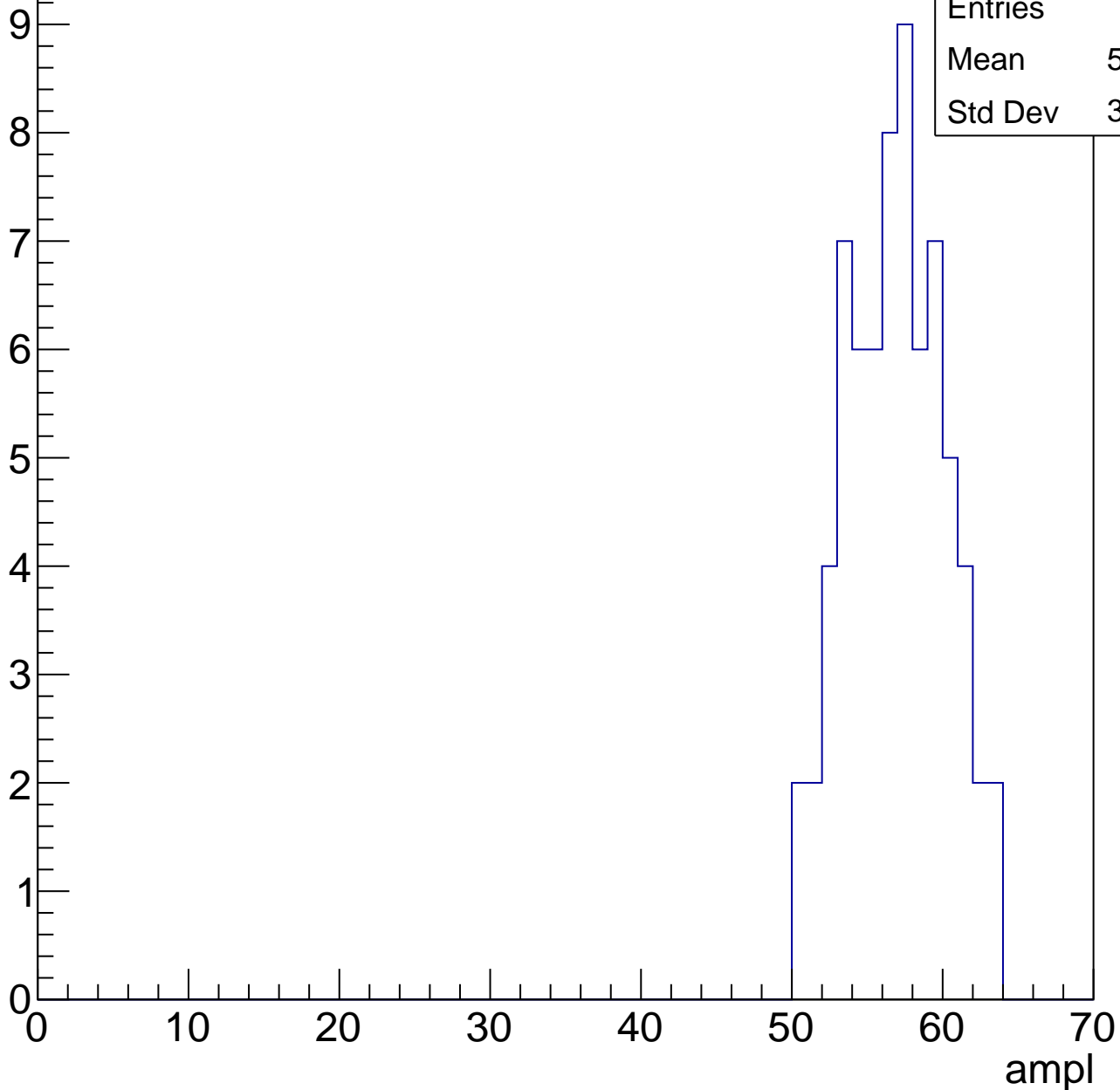


B0L002S, U7-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	56.44
Std Dev	3.188

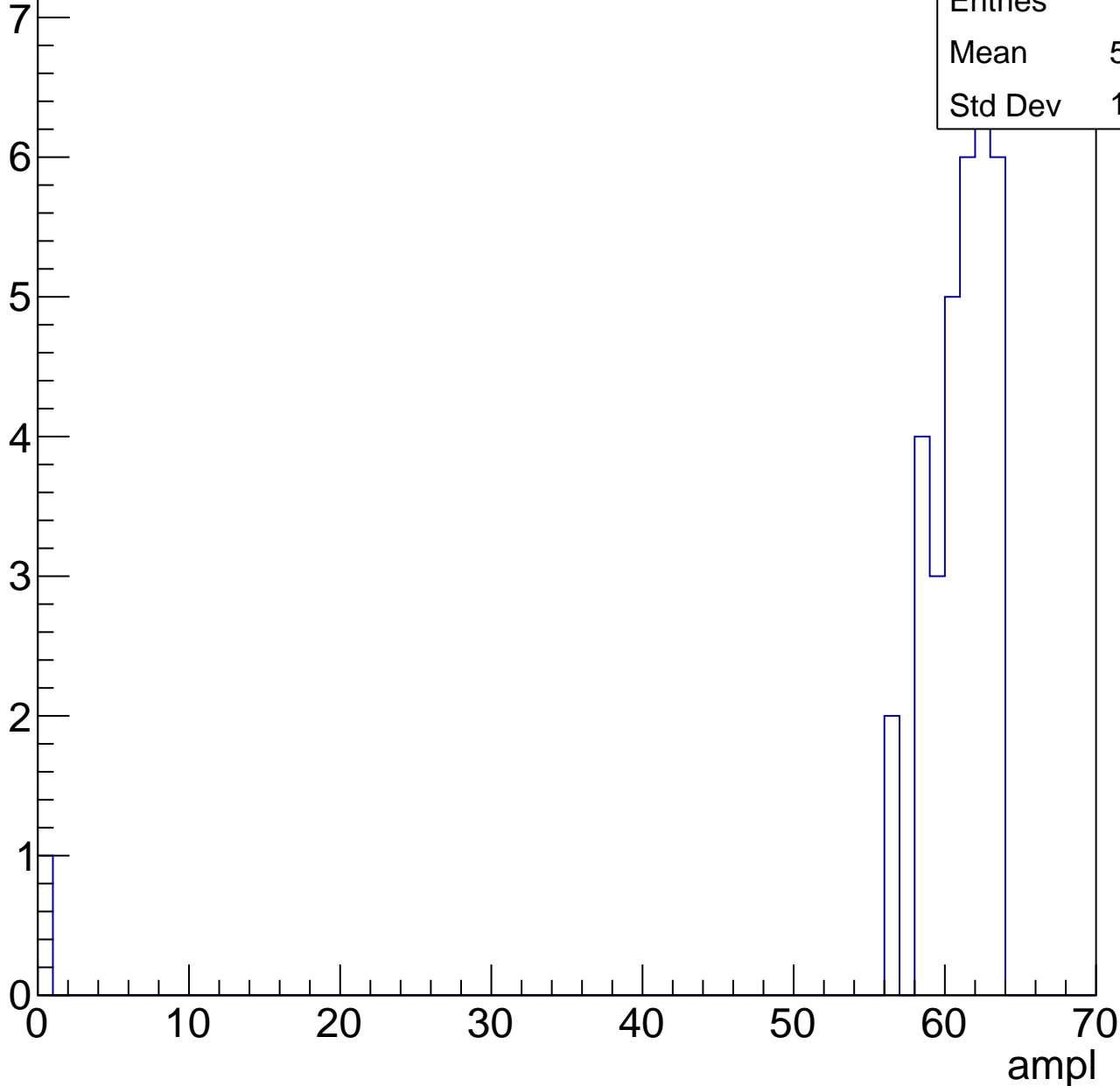


B0L002S, U7-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.79
Std Dev	10.42



B0L002S, U7-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

B0L002S, U7-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U7-ch86, adc0

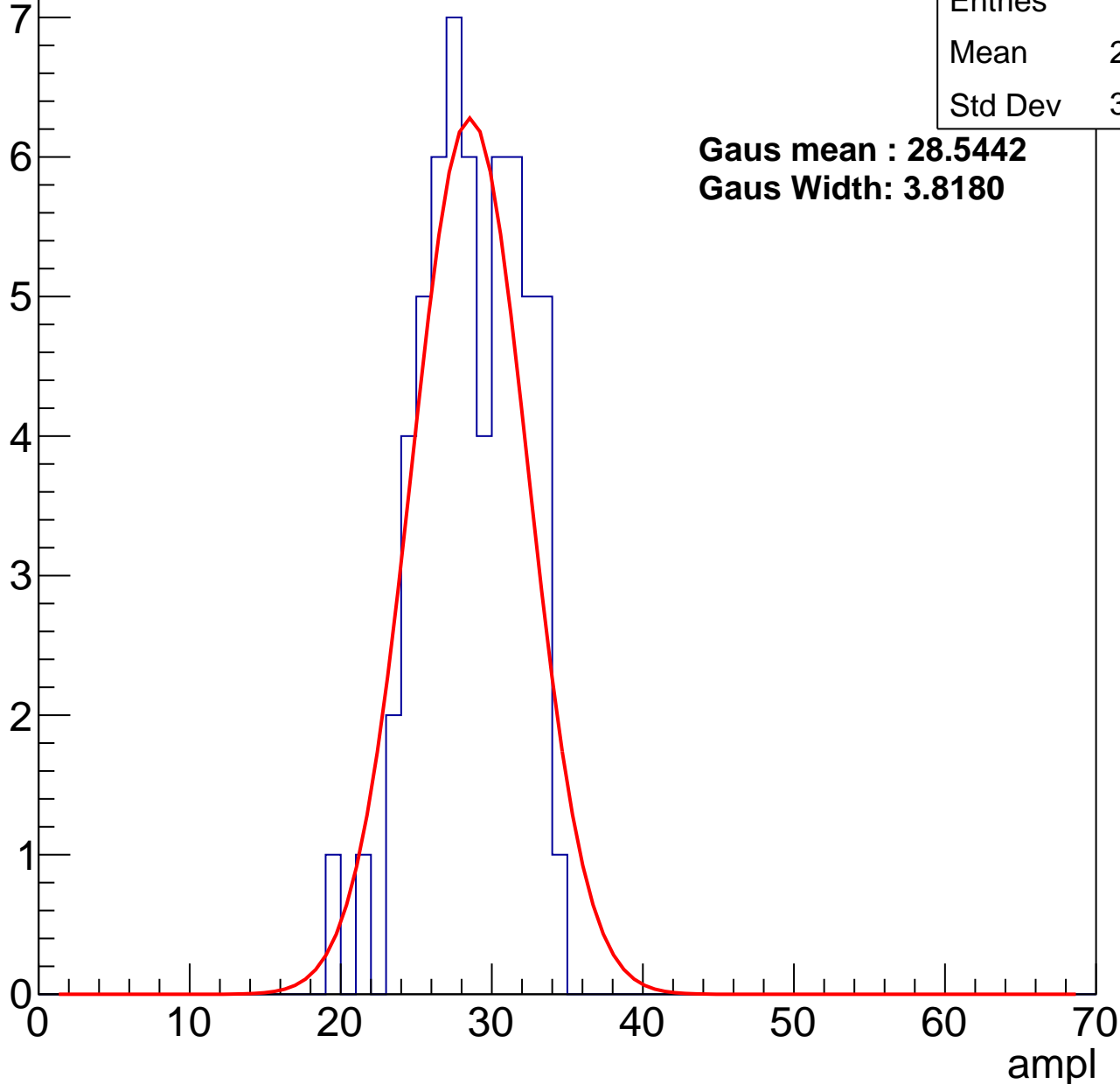
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	28.15
Std Dev	3.298

Gaus mean : 28.5442

Gaus Width: 3.8180



B0L002S, U7-ch86, adc1

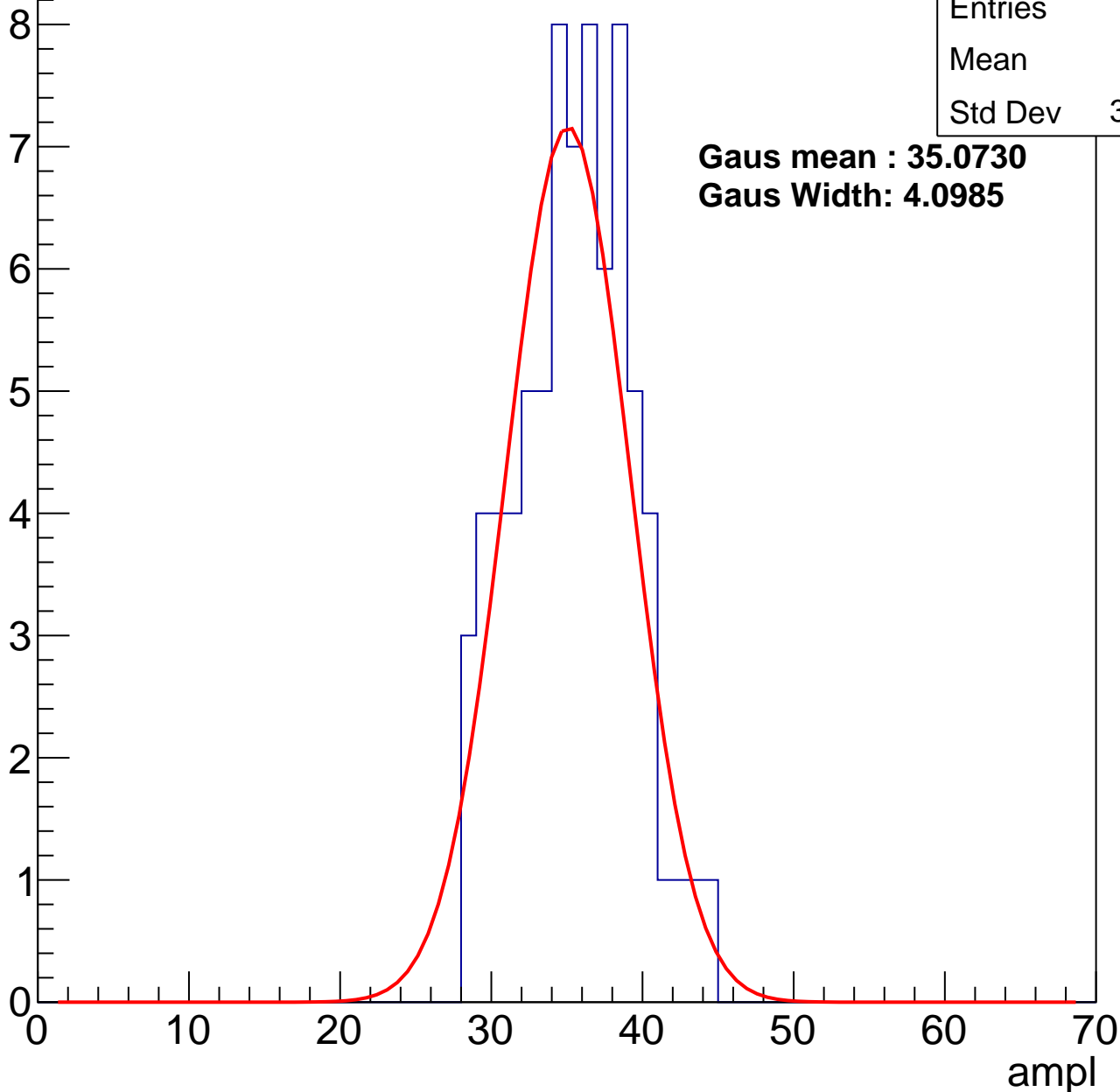
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	35
Std Dev	3.727

Gaus mean : 35.0730

Gaus Width: 4.0985



B0L002S, U7-ch86, adc2

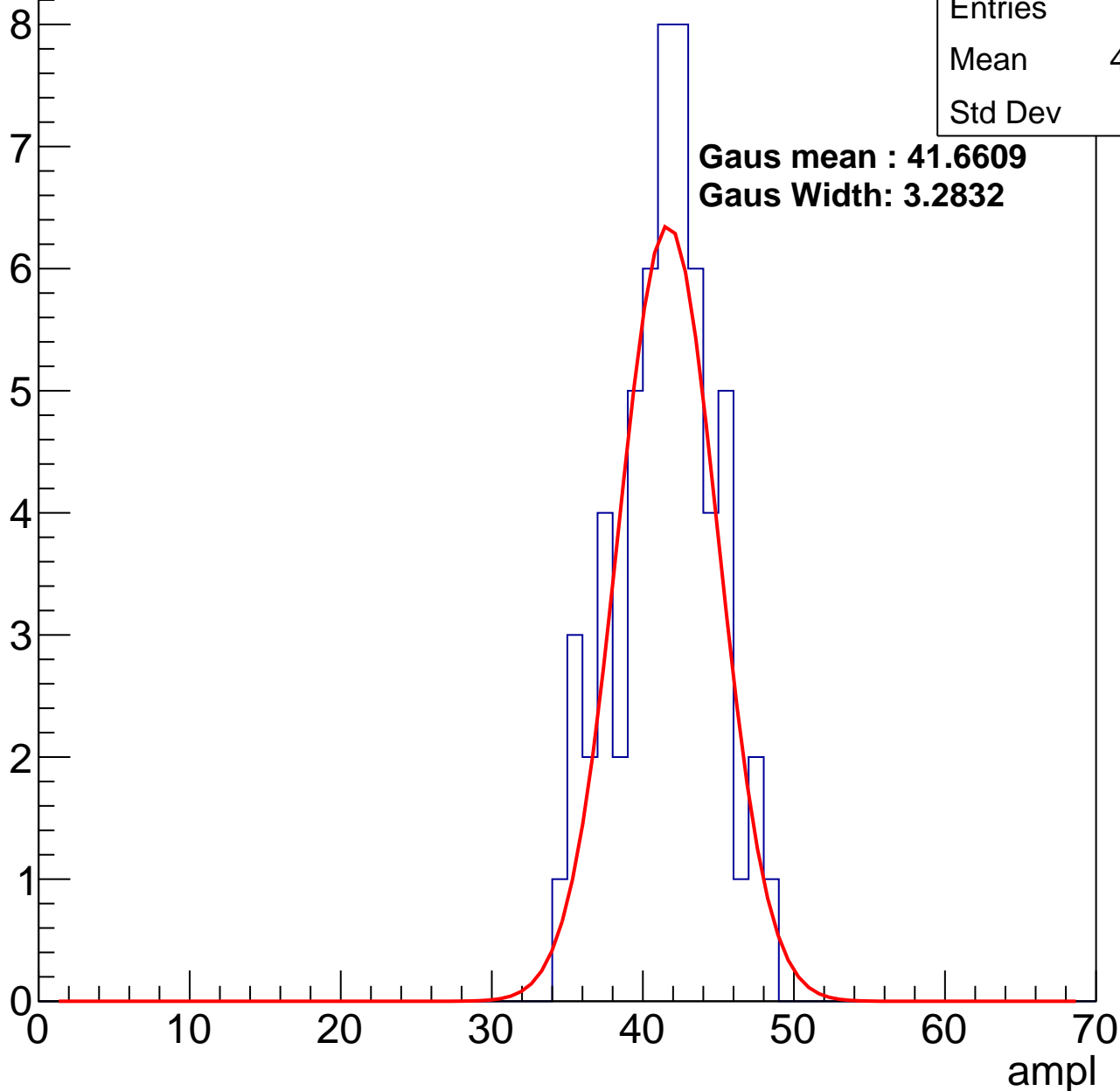
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	41.05
Std Dev	3.24

Gaus mean : 41.6609

Gaus Width: 3.2832

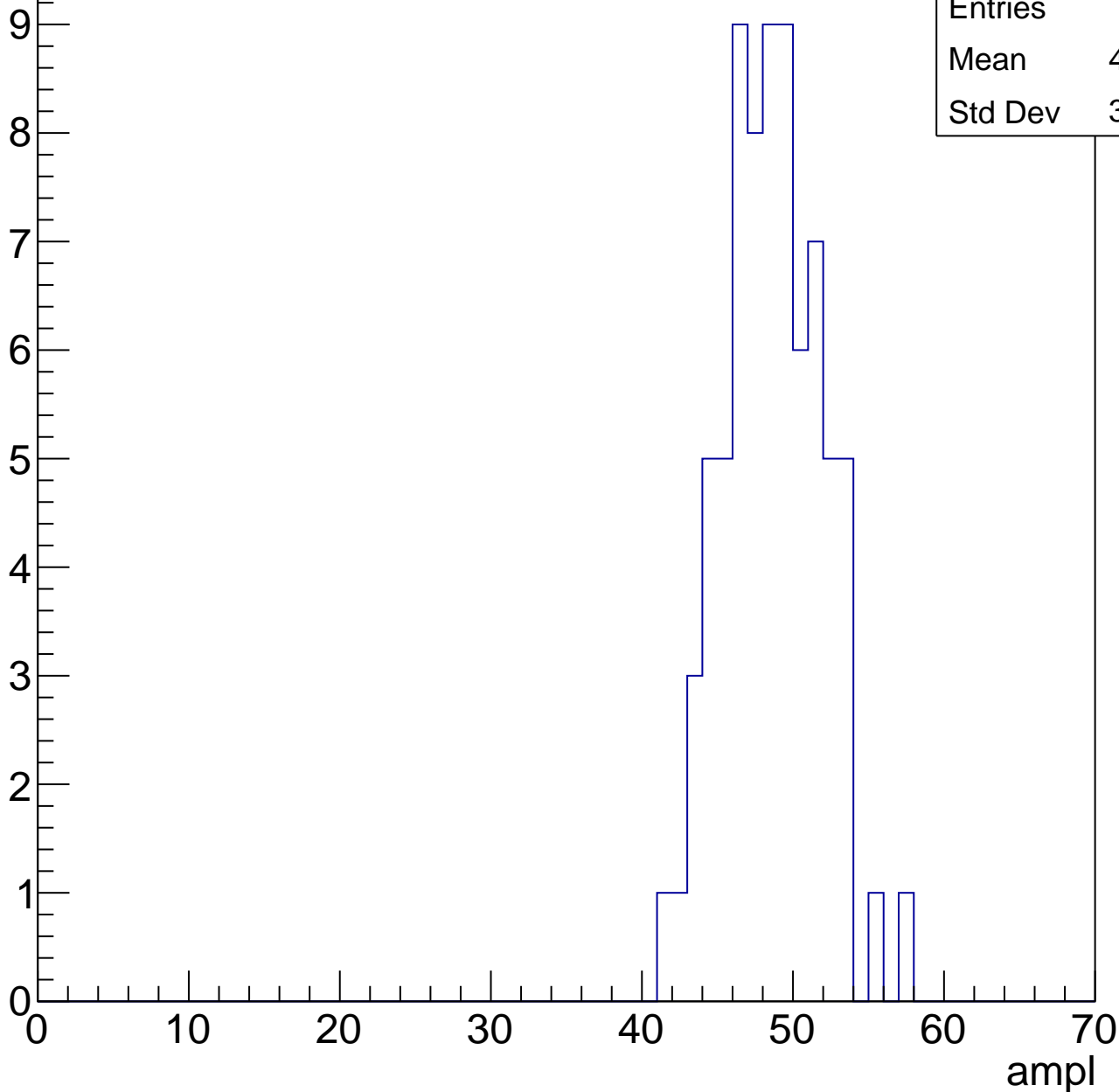


B0L002S, U7-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

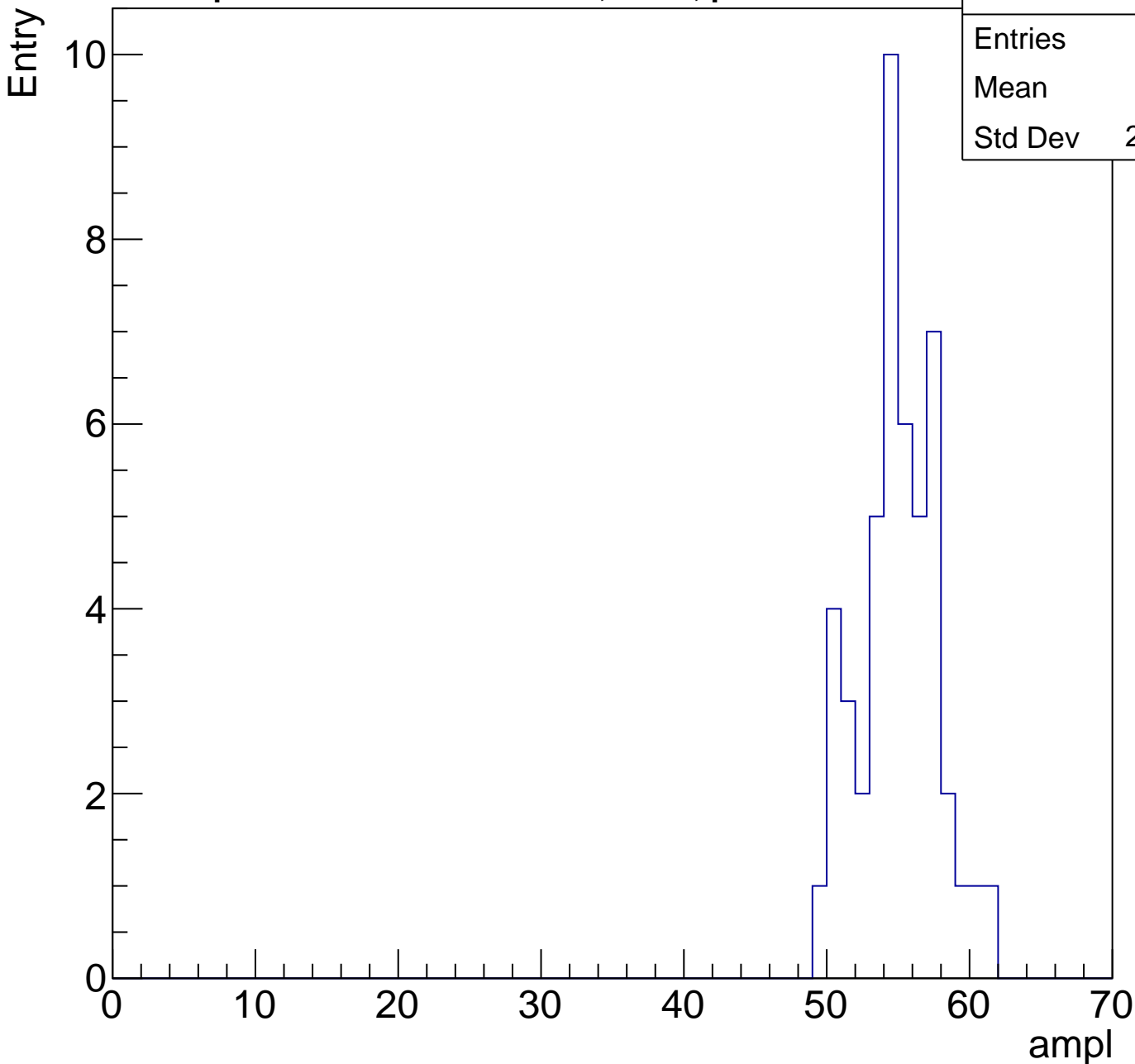
Entries	75
Mean	48.19
Std Dev	3.178



B0L002S, U7-ch86, adc4

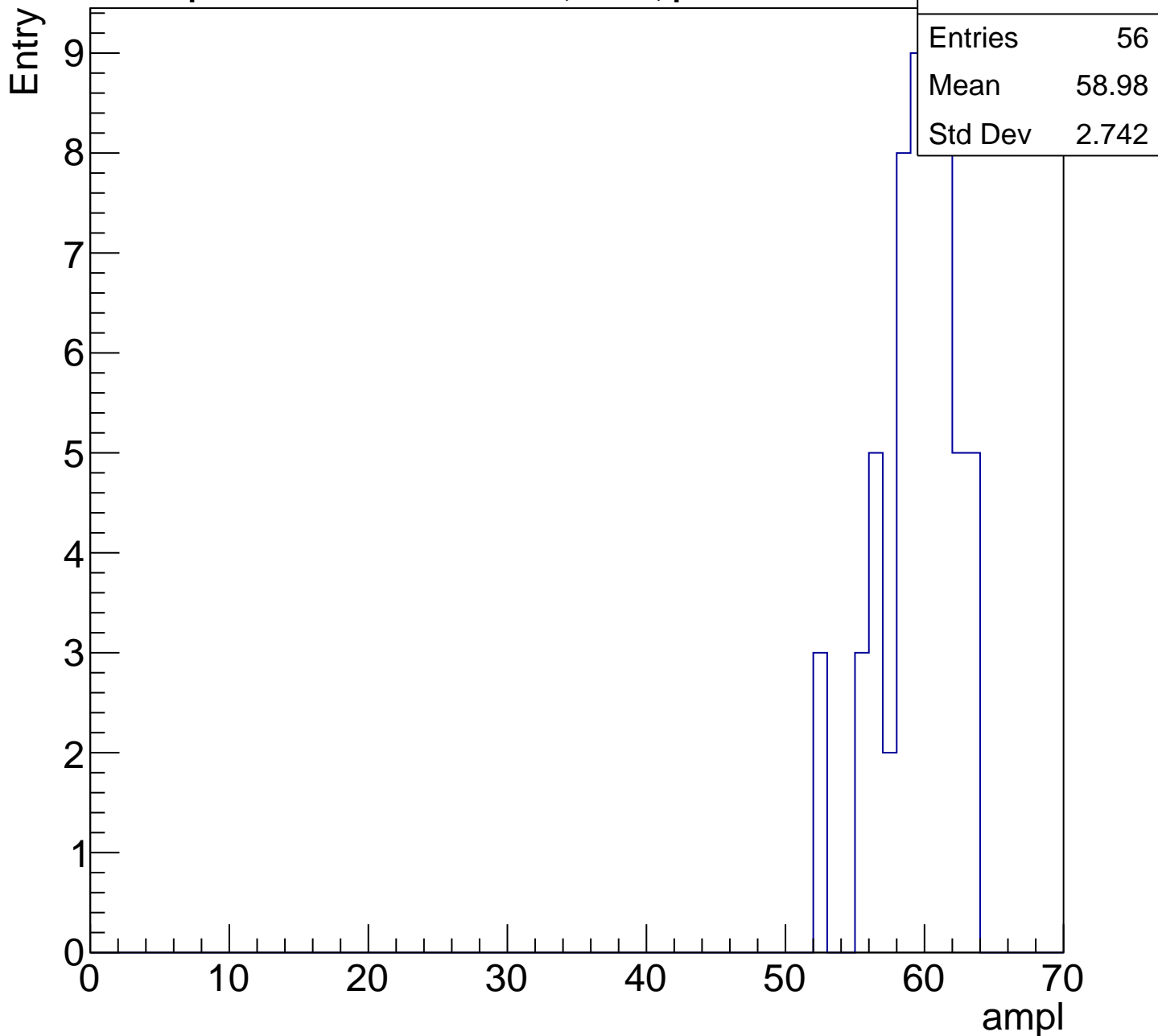
calib_packv5_042523_0143.root, FC#8, port C1

Entries	48
Mean	54.5
Std Dev	2.693



B0L002S, U7-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

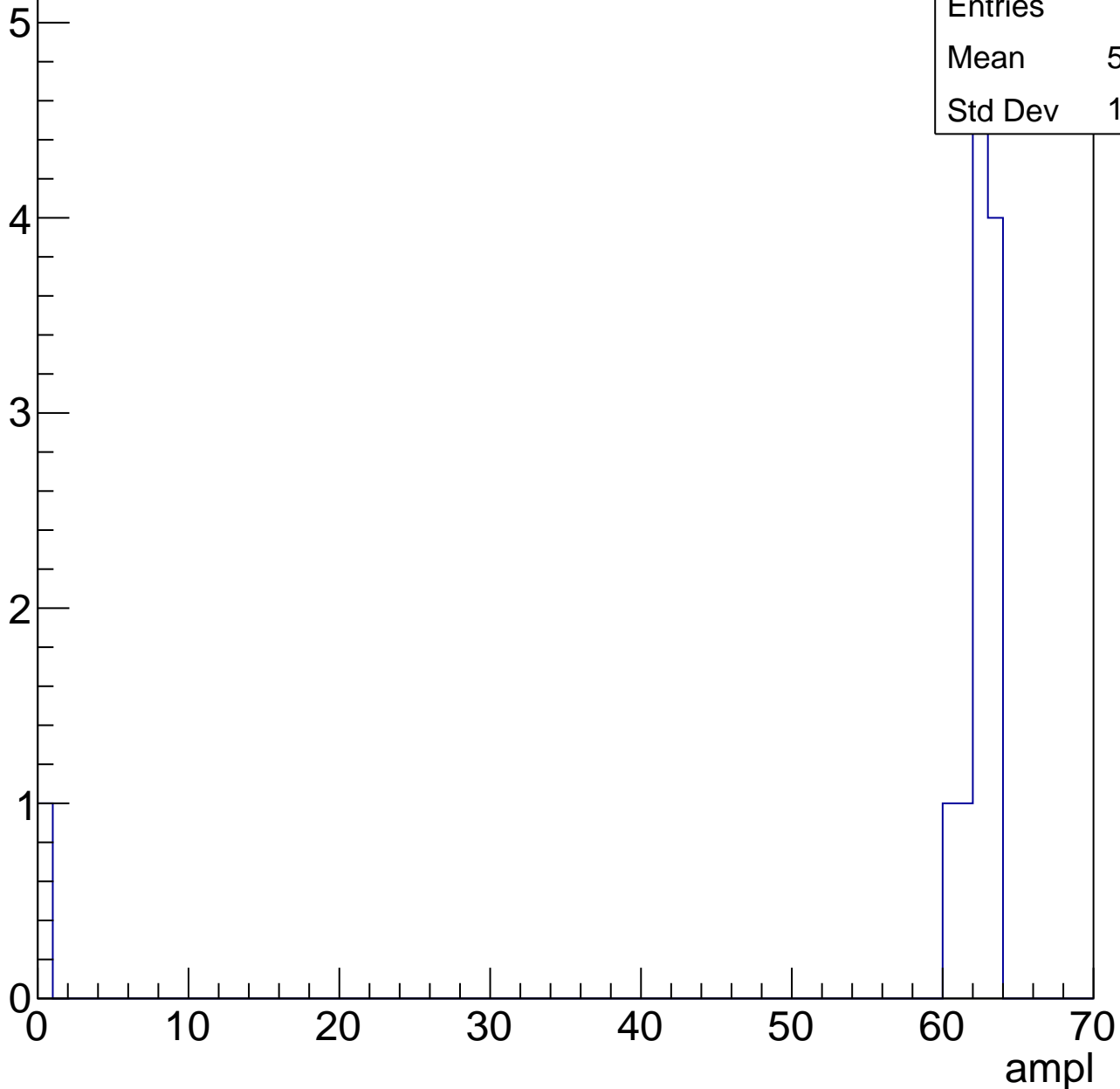


B0L002S, U7-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	56.92
Std Dev	17.18



B0L002S, U7-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch87, adc0

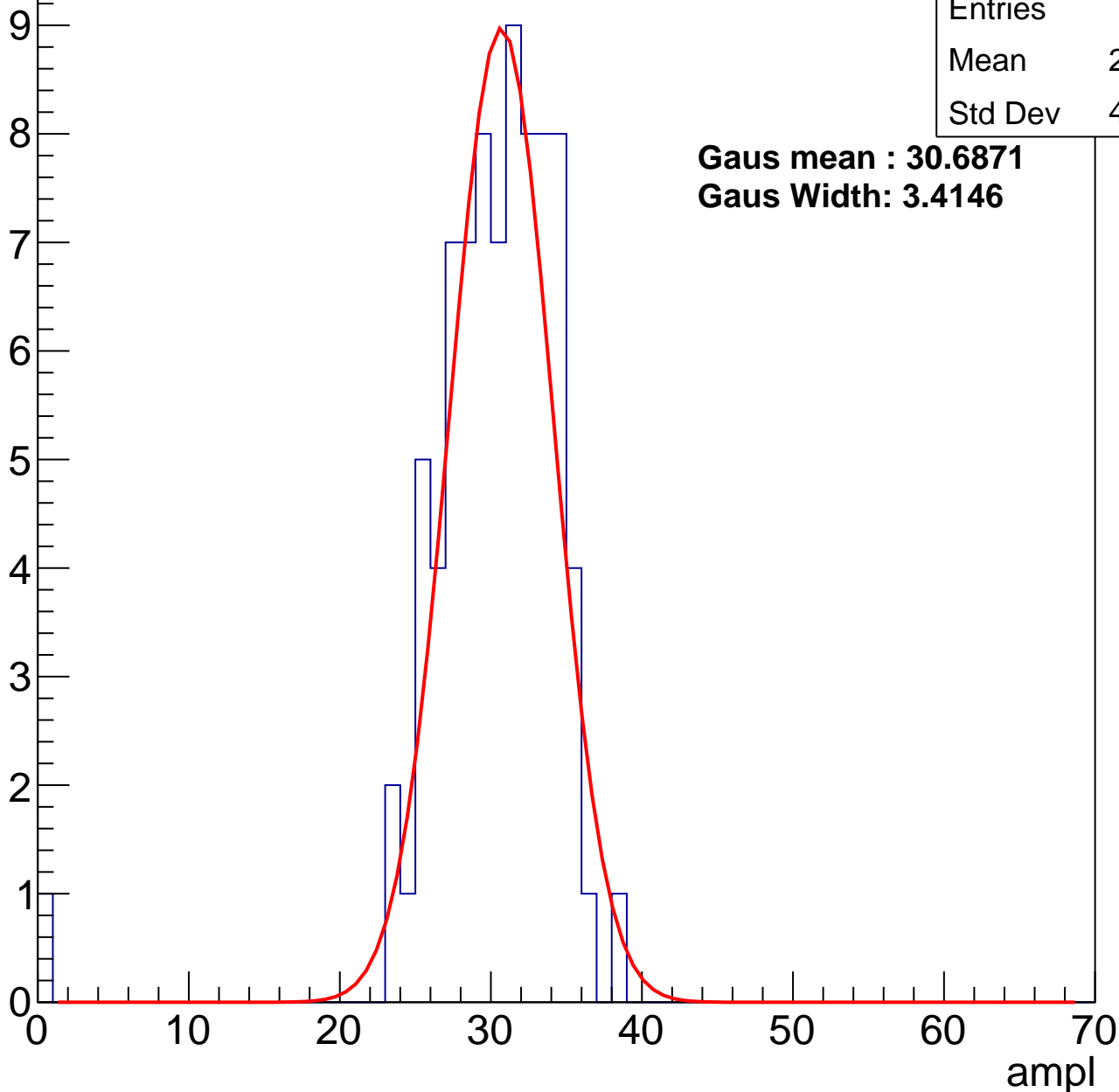
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.77
Std Dev	4.659

Gaus mean : 30.6871

Gaus Width: 3.4146



B0L002S, U7-ch87, adc1

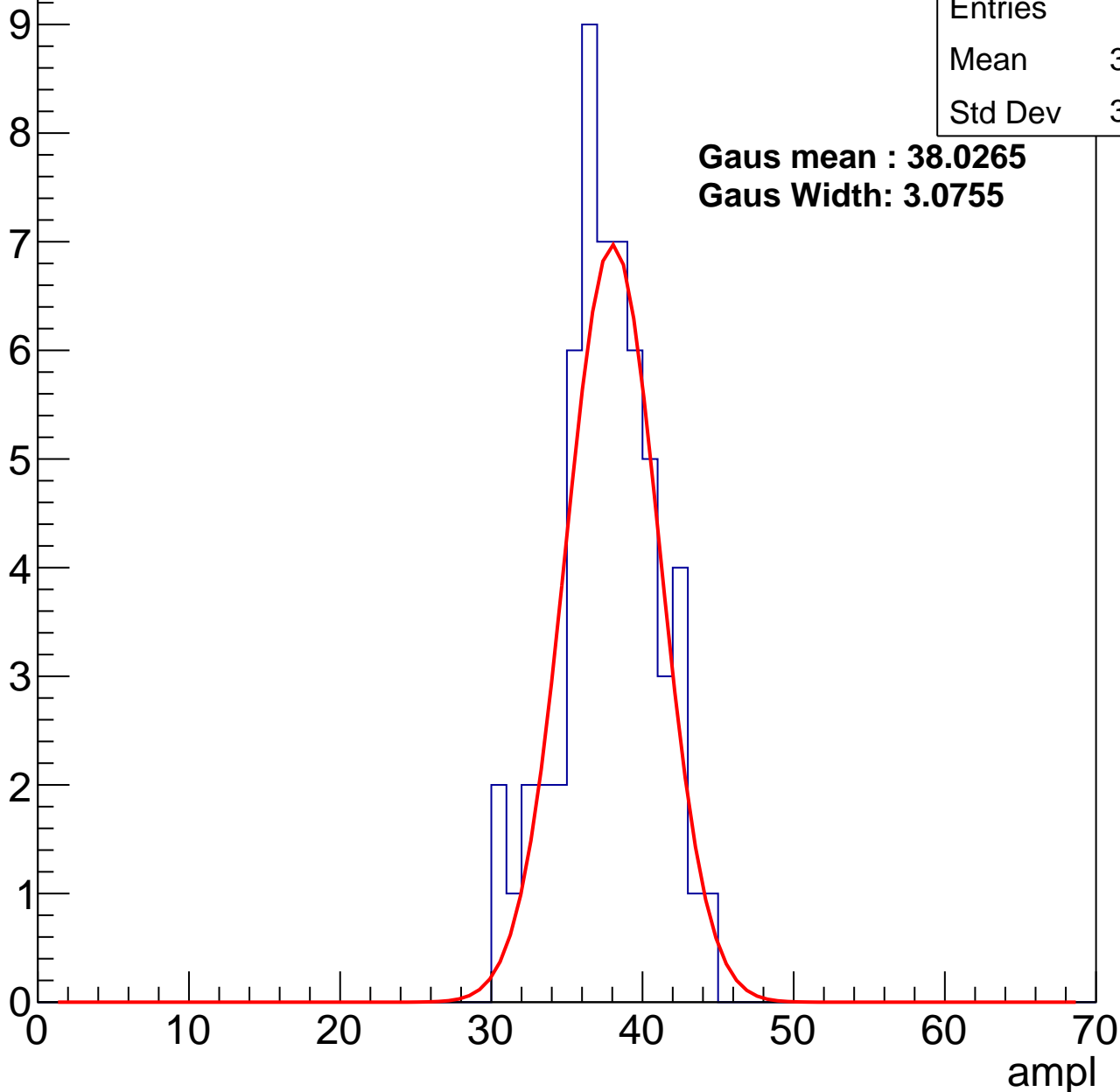
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.24
Std Dev	3.142

Gaus mean : 38.0265

Gaus Width: 3.0755



B0L002S, U7-ch87, adc2

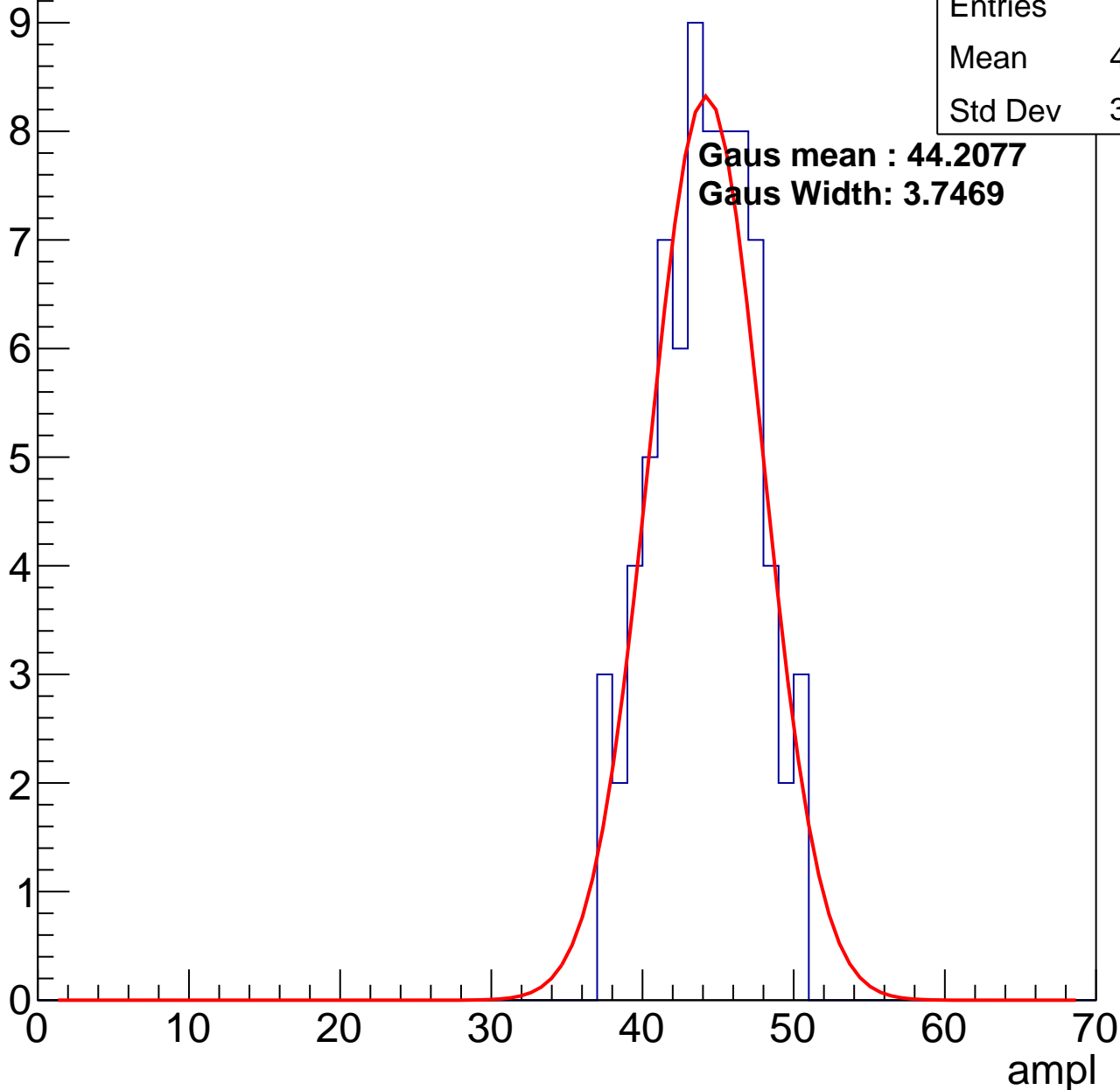
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	43.66
Std Dev	3.267

Gaus mean : 44.2077

Gaus Width: 3.7469

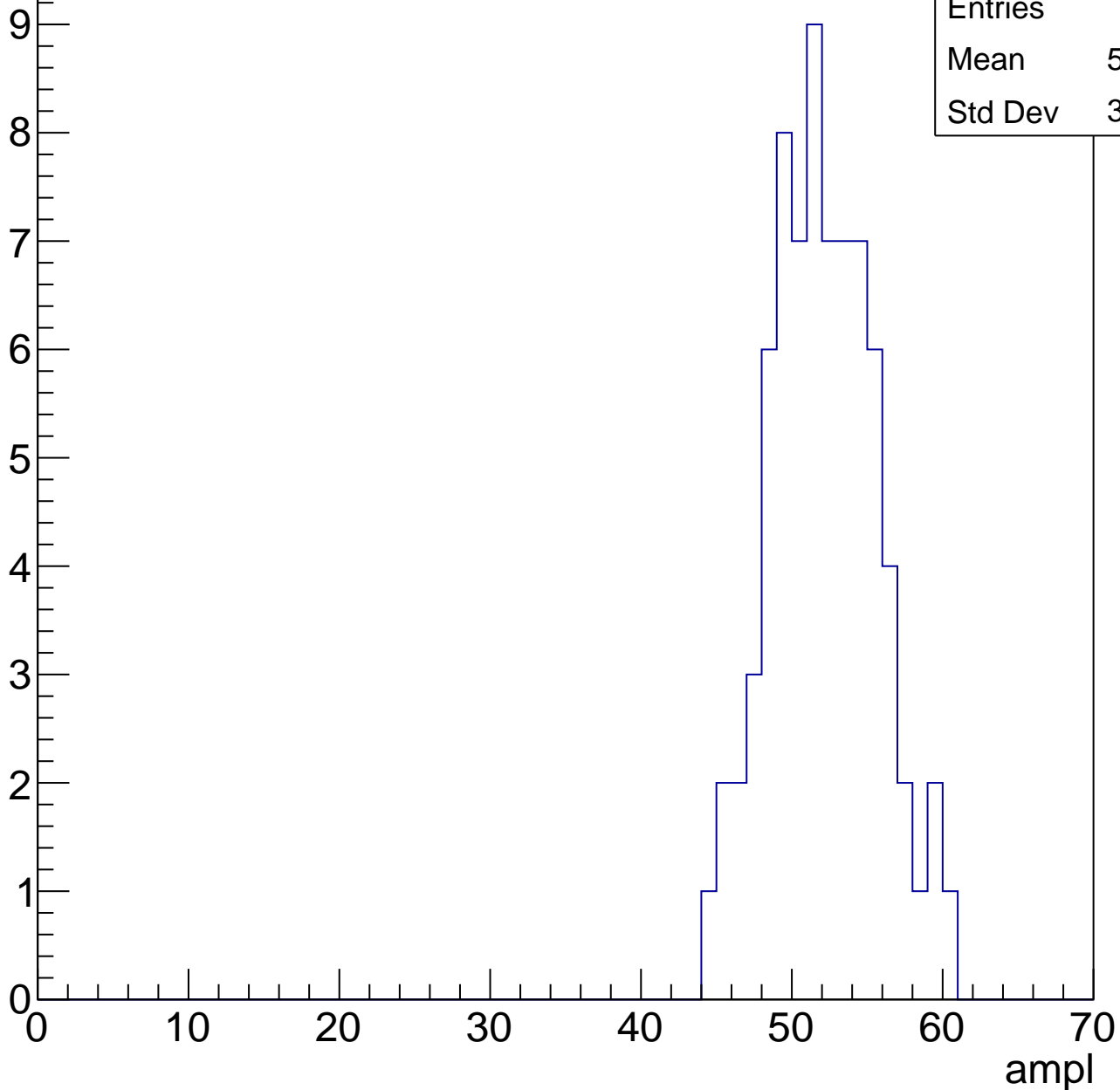


B0L002S, U7-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	51.64
Std Dev	3.474

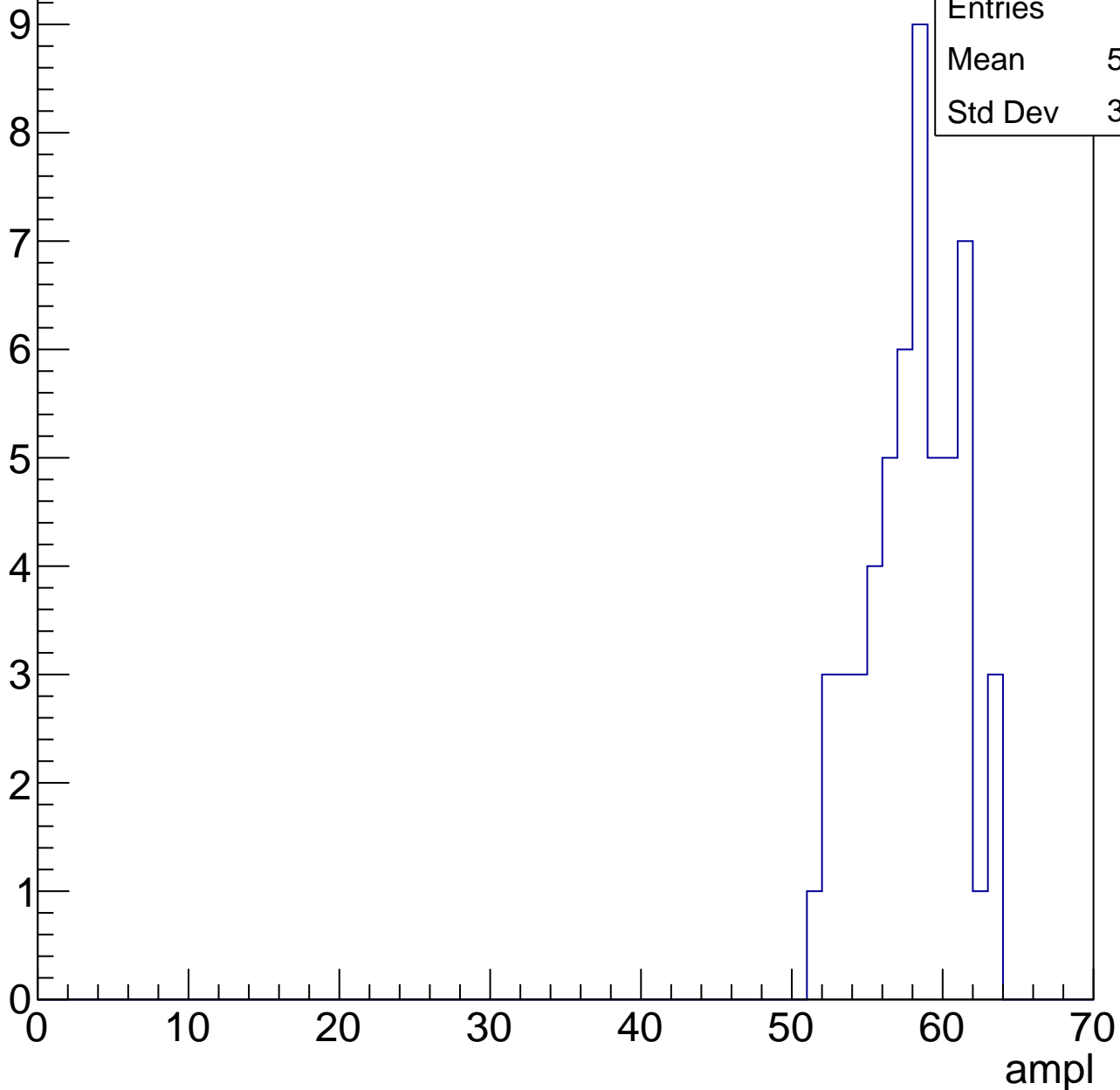


B0L002S, U7-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.55
Std Dev	3.044

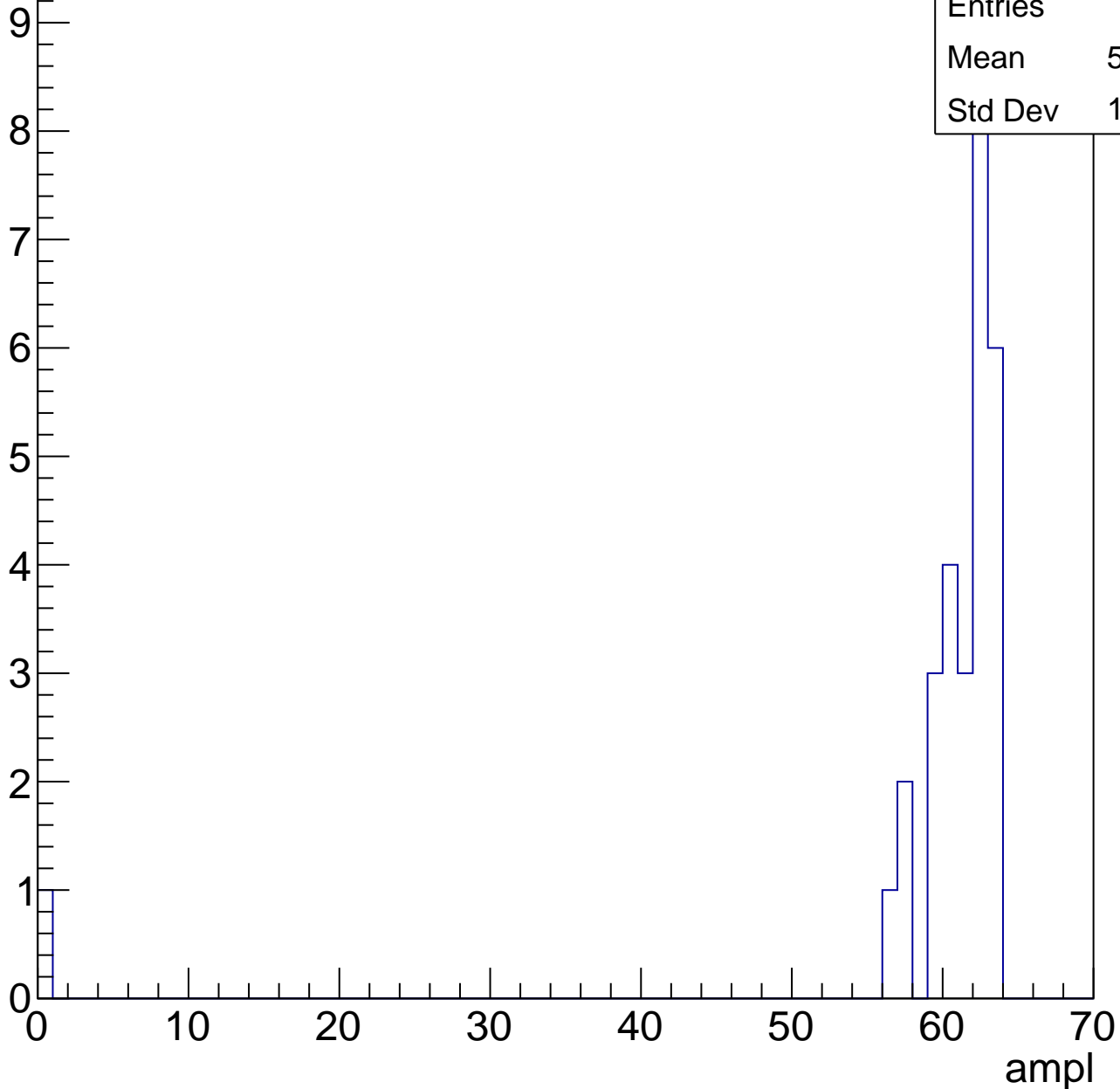


B0L002S, U7-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

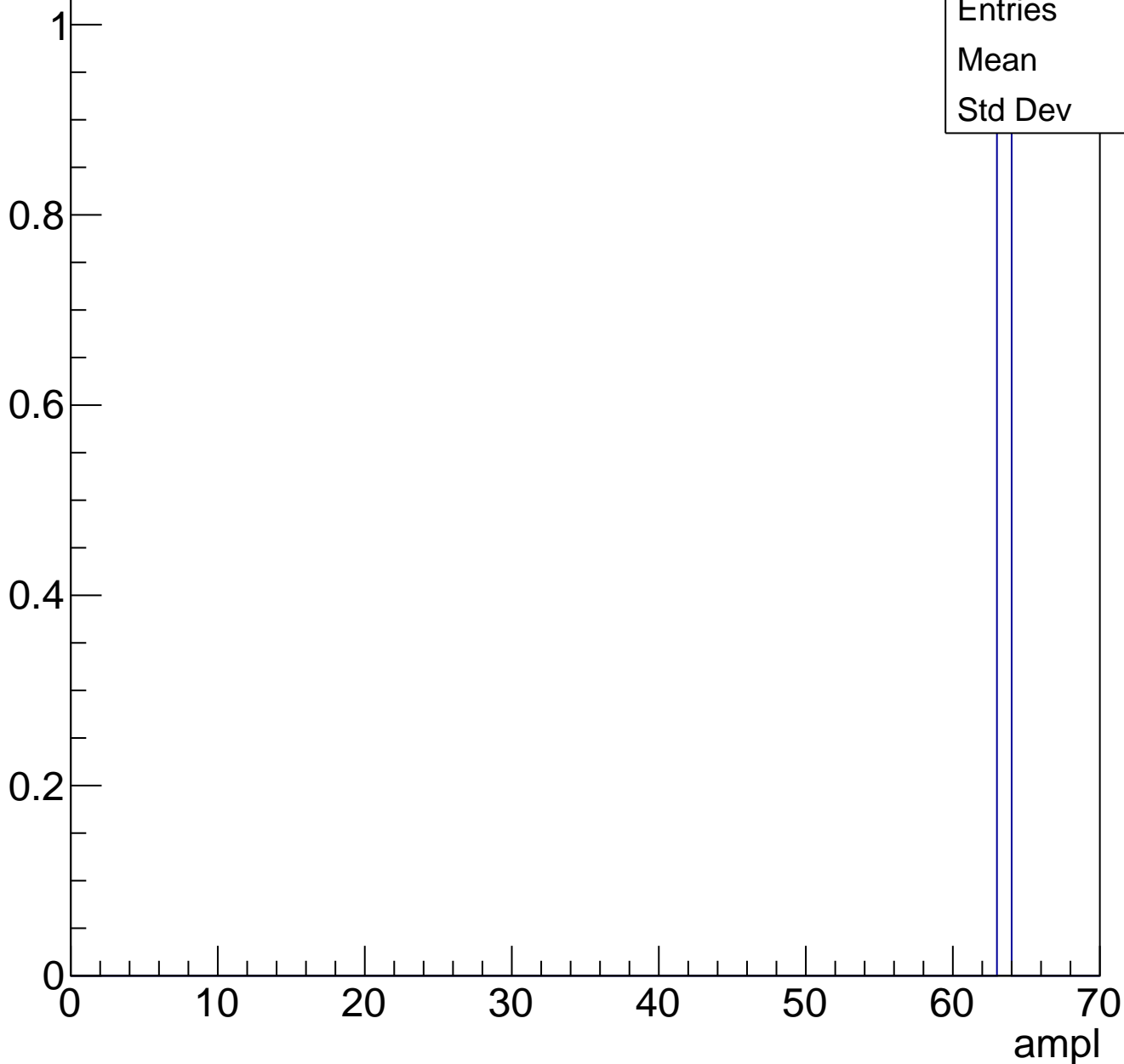
Entries	29
Mean	58.83
Std Dev	11.28



B0L002S, U7-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch88, adc0

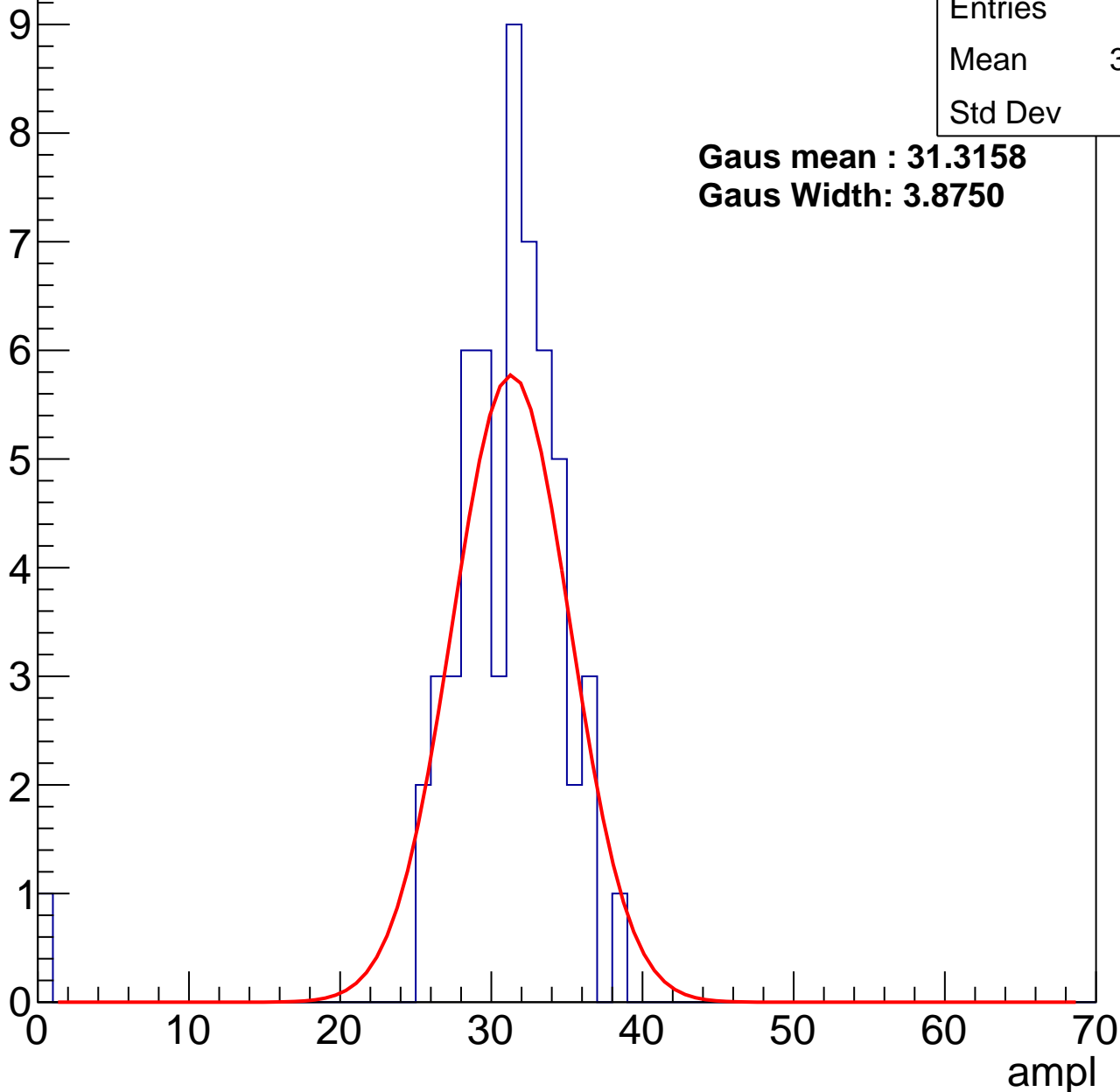
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	30.32
Std Dev	5.03

Gaus mean : 31.3158

Gaus Width: 3.8750



B0L002S, U7-ch88, adc1

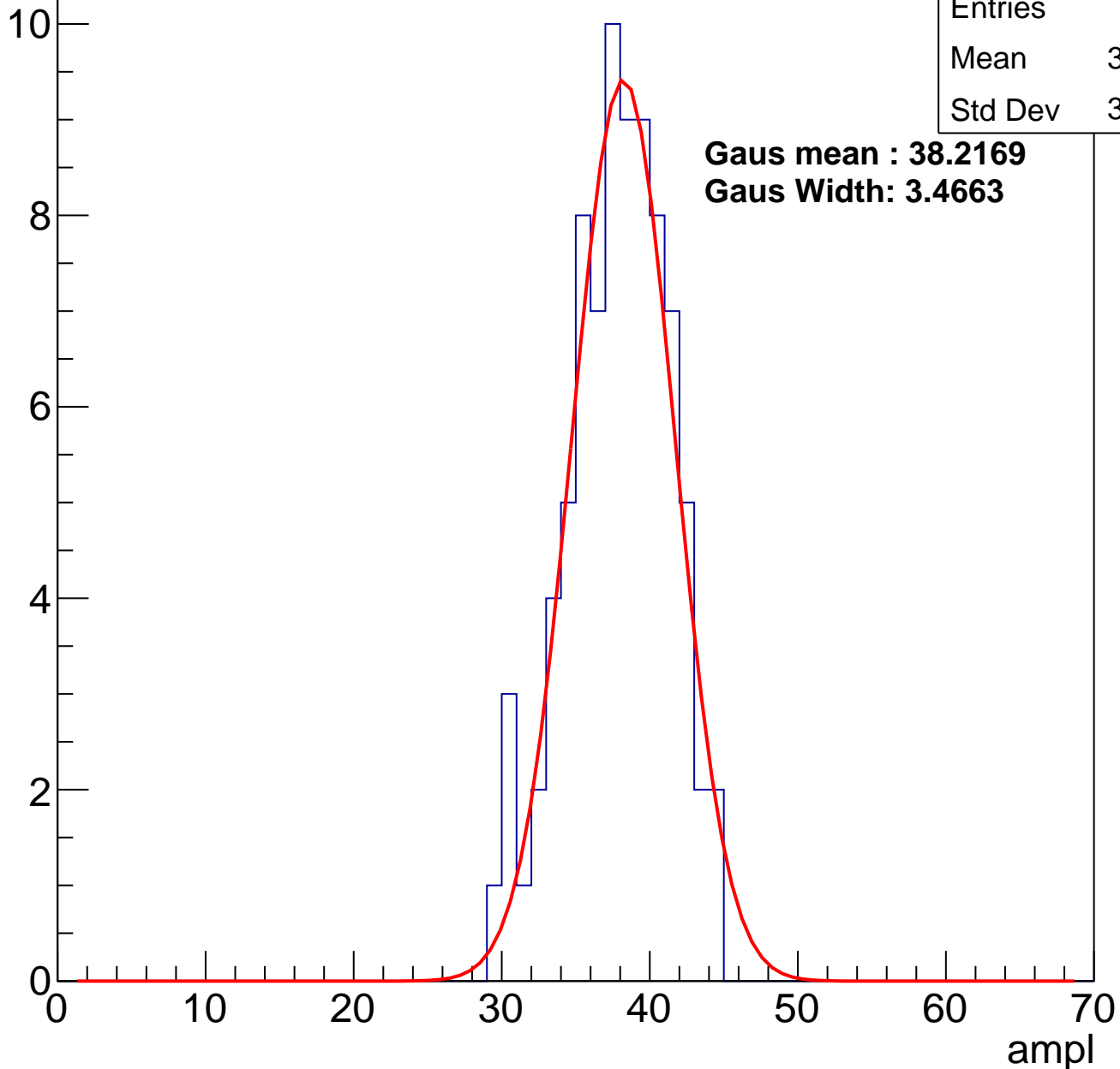
calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	37.37
Std Dev	3.396

Gaus mean : 38.2169

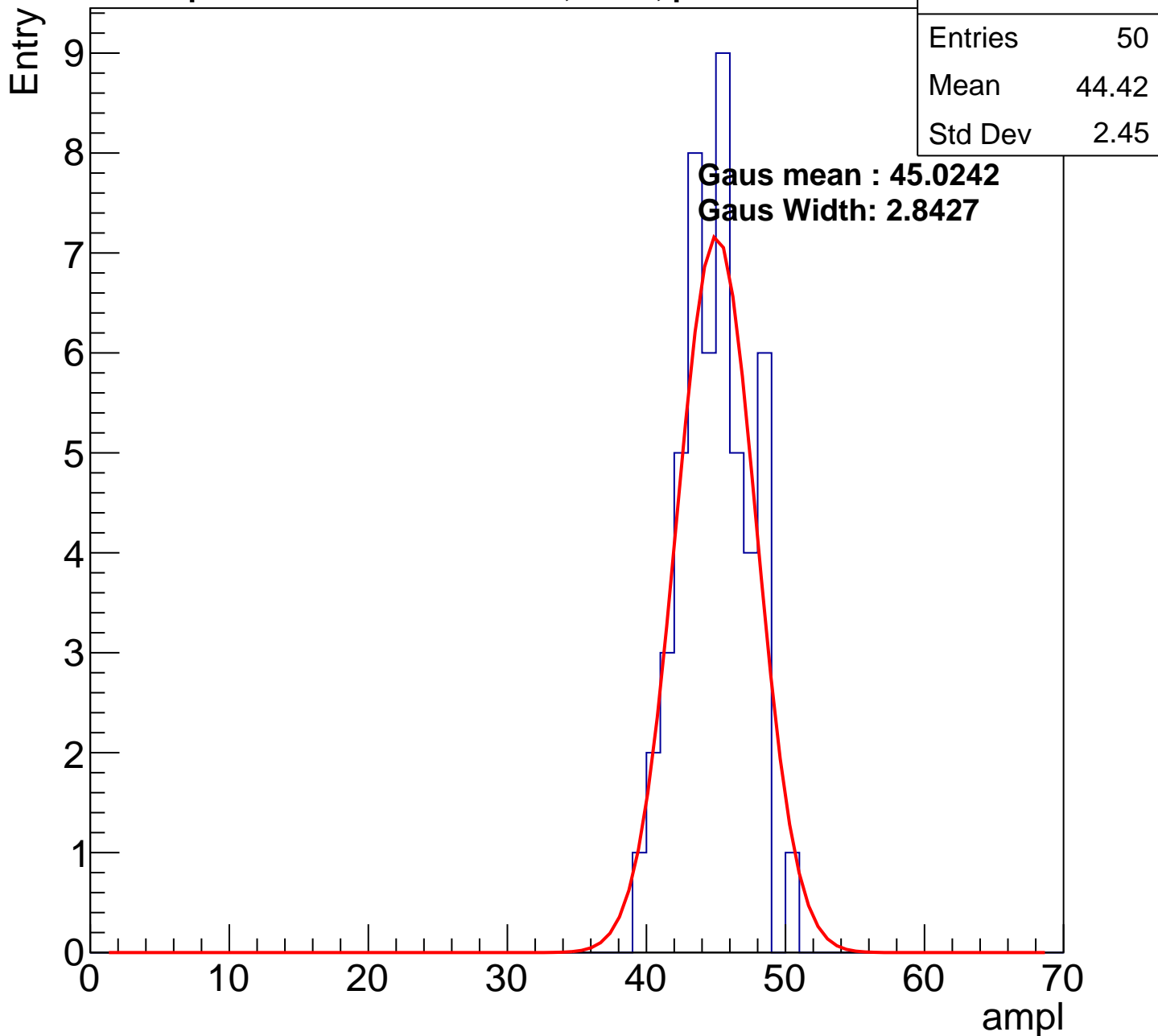
Gaus Width: 3.4663

Entry



B0L002S, U7-ch88, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U7-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

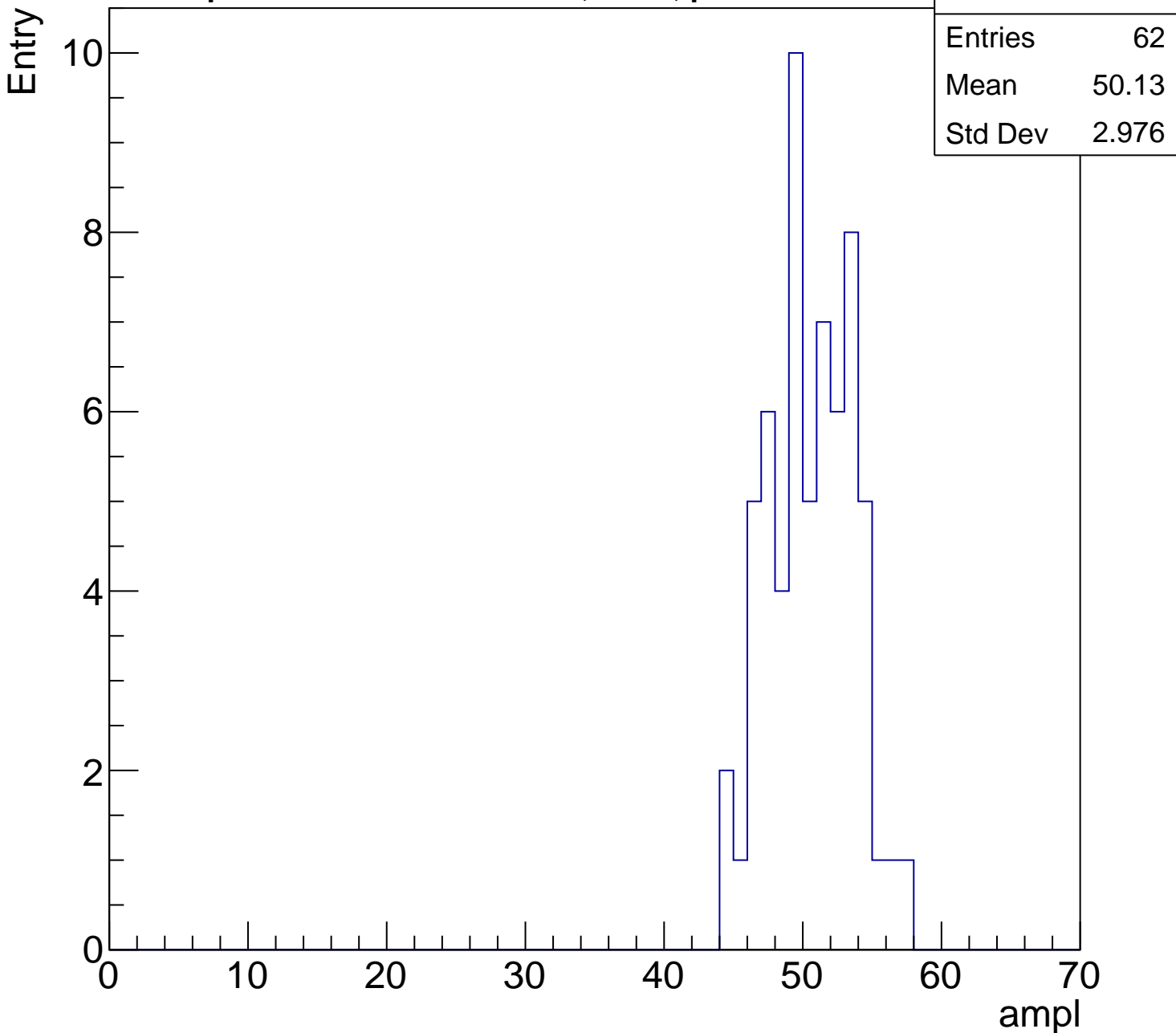
Entries	62
Mean	50.13
Std Dev	2.976

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U7-ch88, adc4

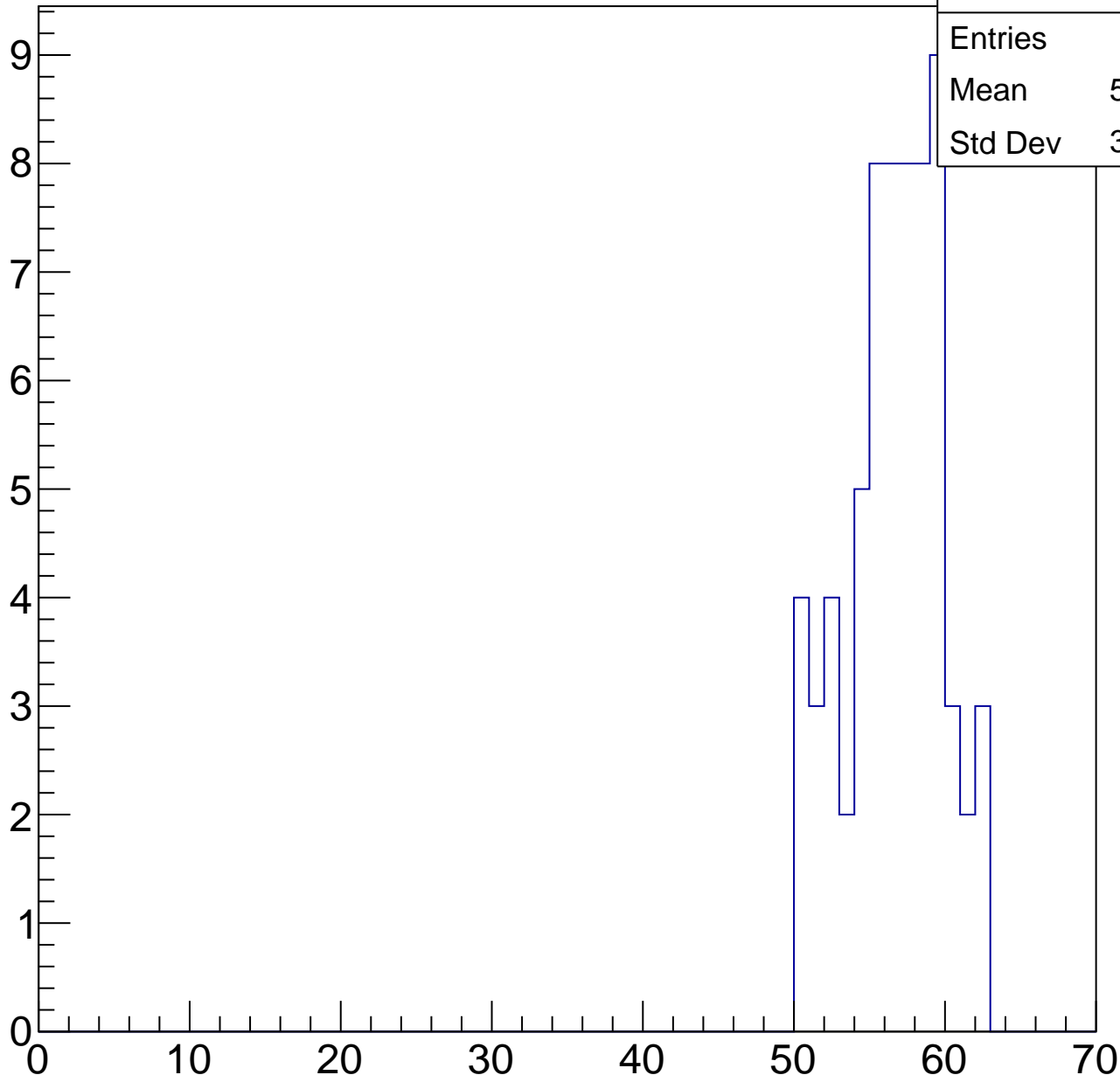
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	56.18
Std Dev	3.124

ampl



B0L002S, U7-ch88, adc5

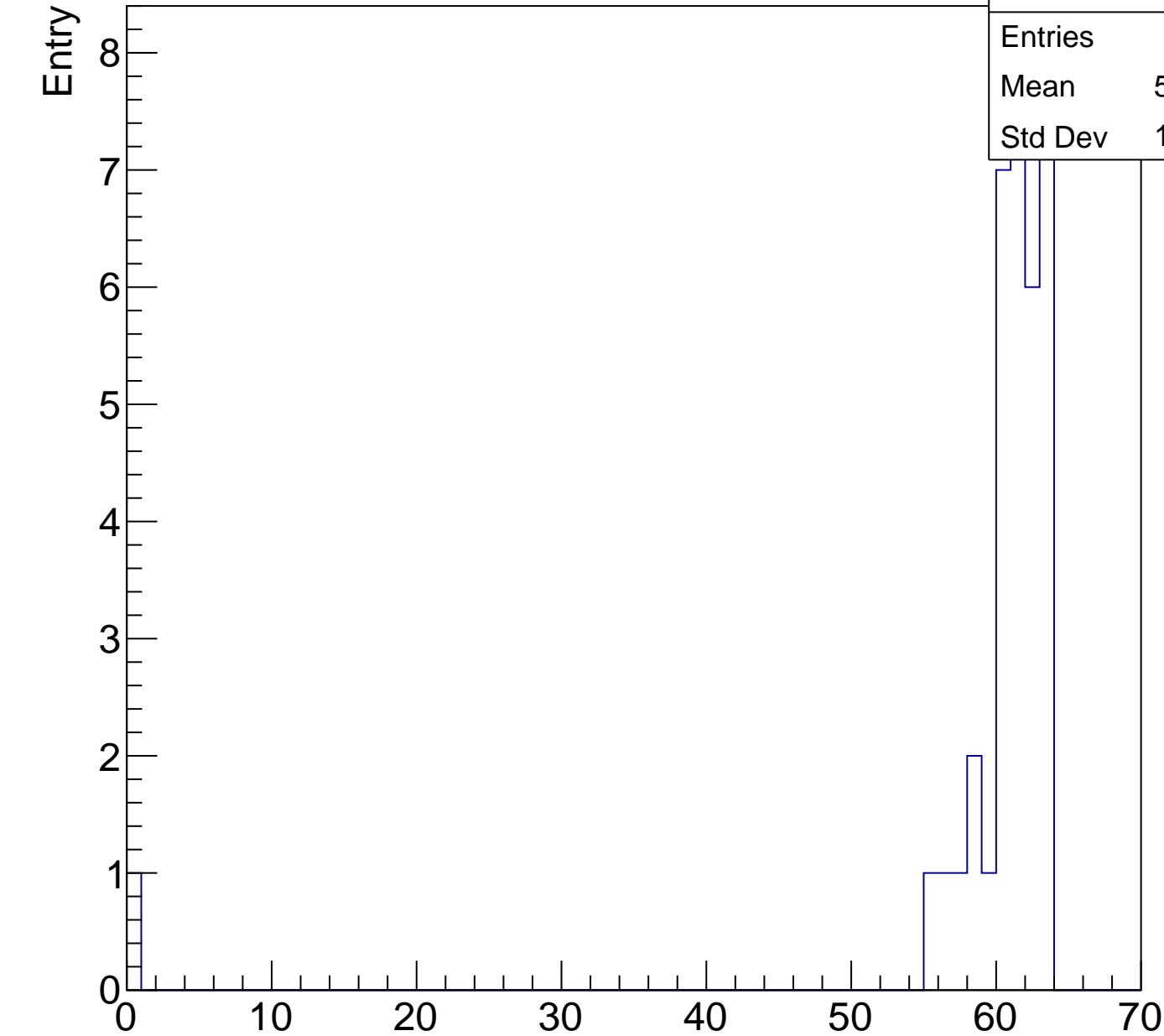
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	59.08
Std Dev	10.18

ampl



B0L002S, U7-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch89, adc0

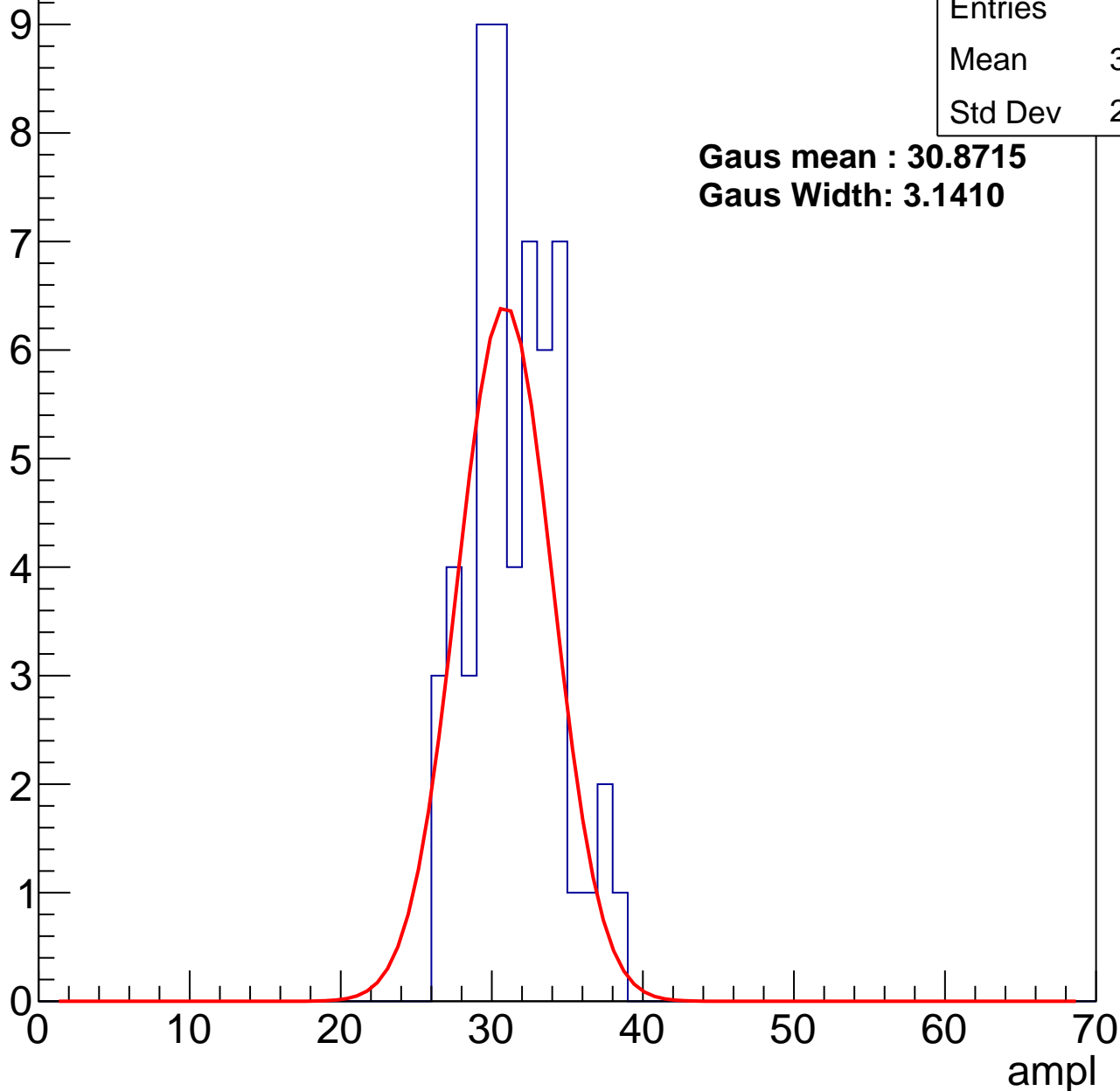
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	31.02
Std Dev	2.862

Gaus mean : 30.8715

Gaus Width: 3.1410



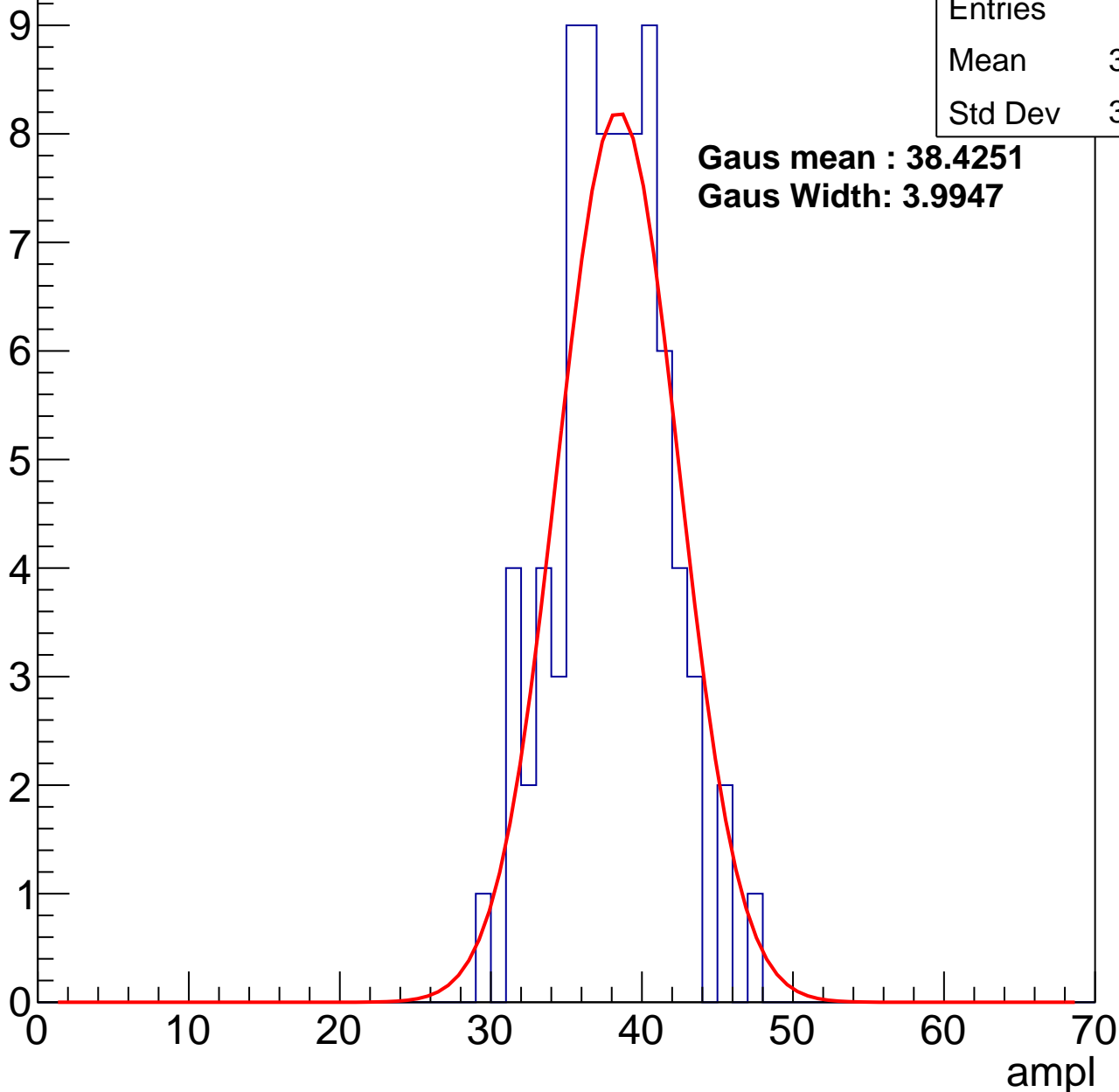
B0L002S, U7-ch89, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	37.56
Std Dev	3.552

Gaus mean : 38.4251
Gaus Width: 3.9947



B0L002S, U7-ch89, adc2

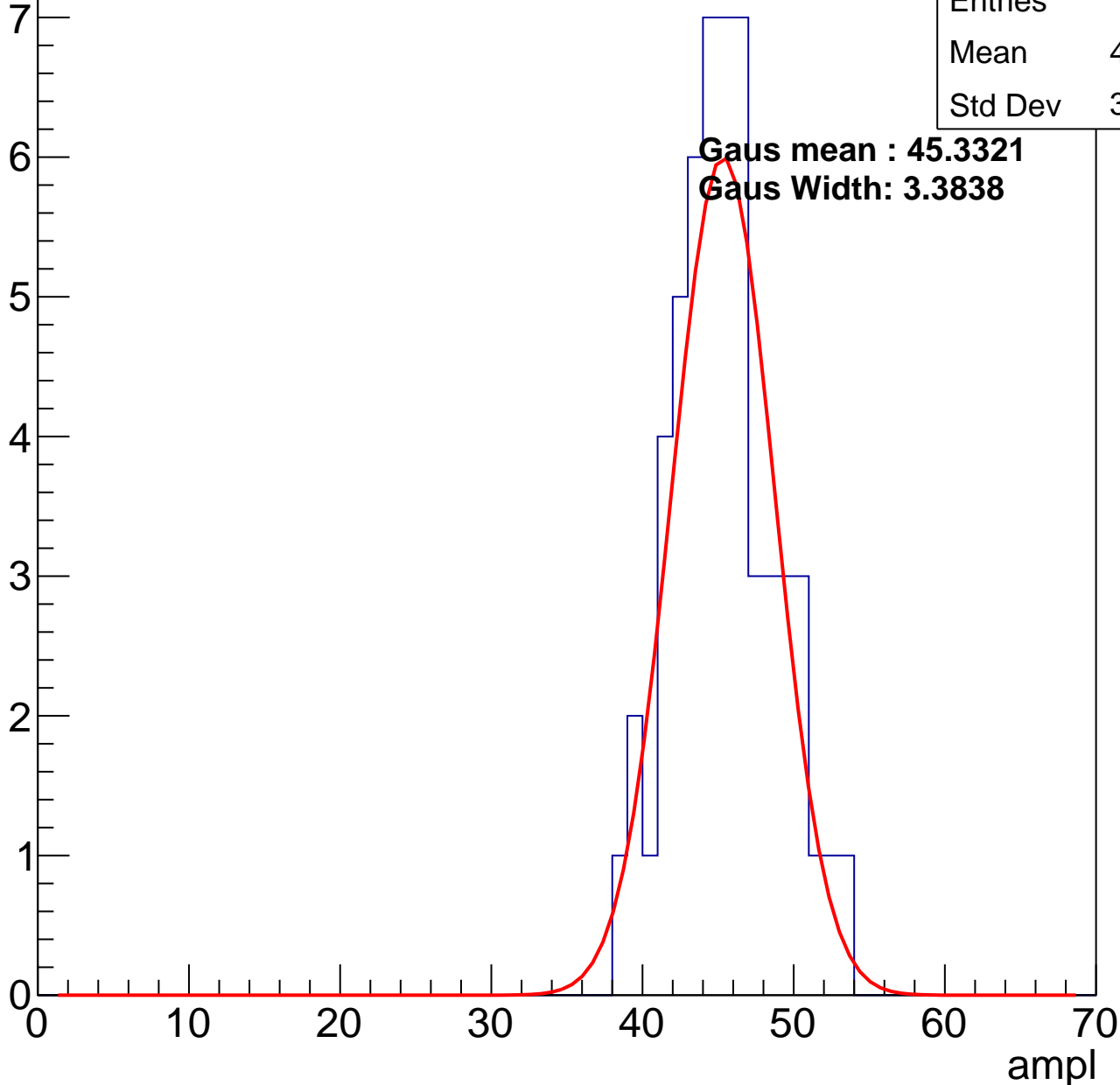
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.93
Std Dev	3.313

Gaus mean : 45.3321

Gaus Width: 3.3838

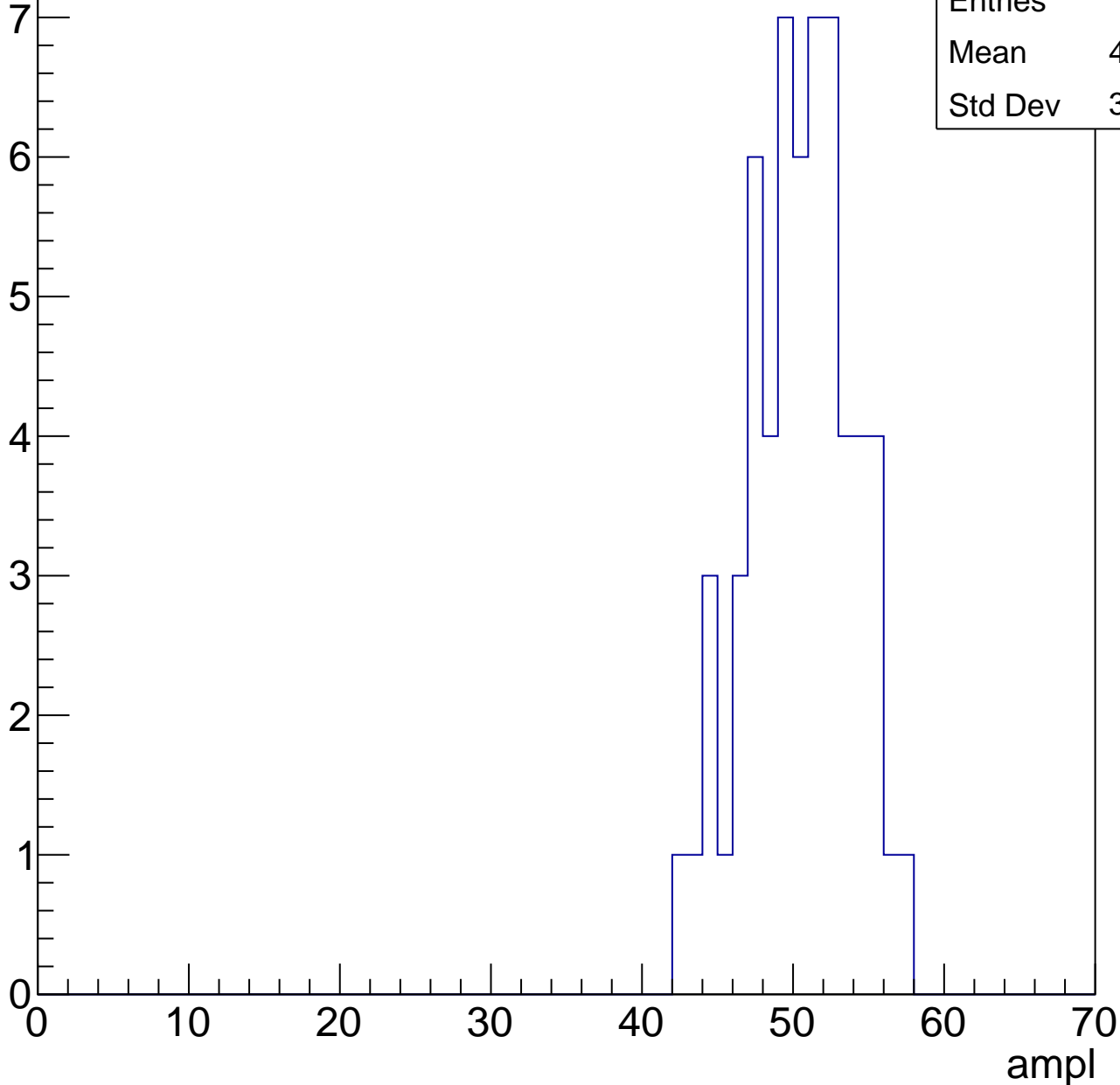


B0L002S, U7-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

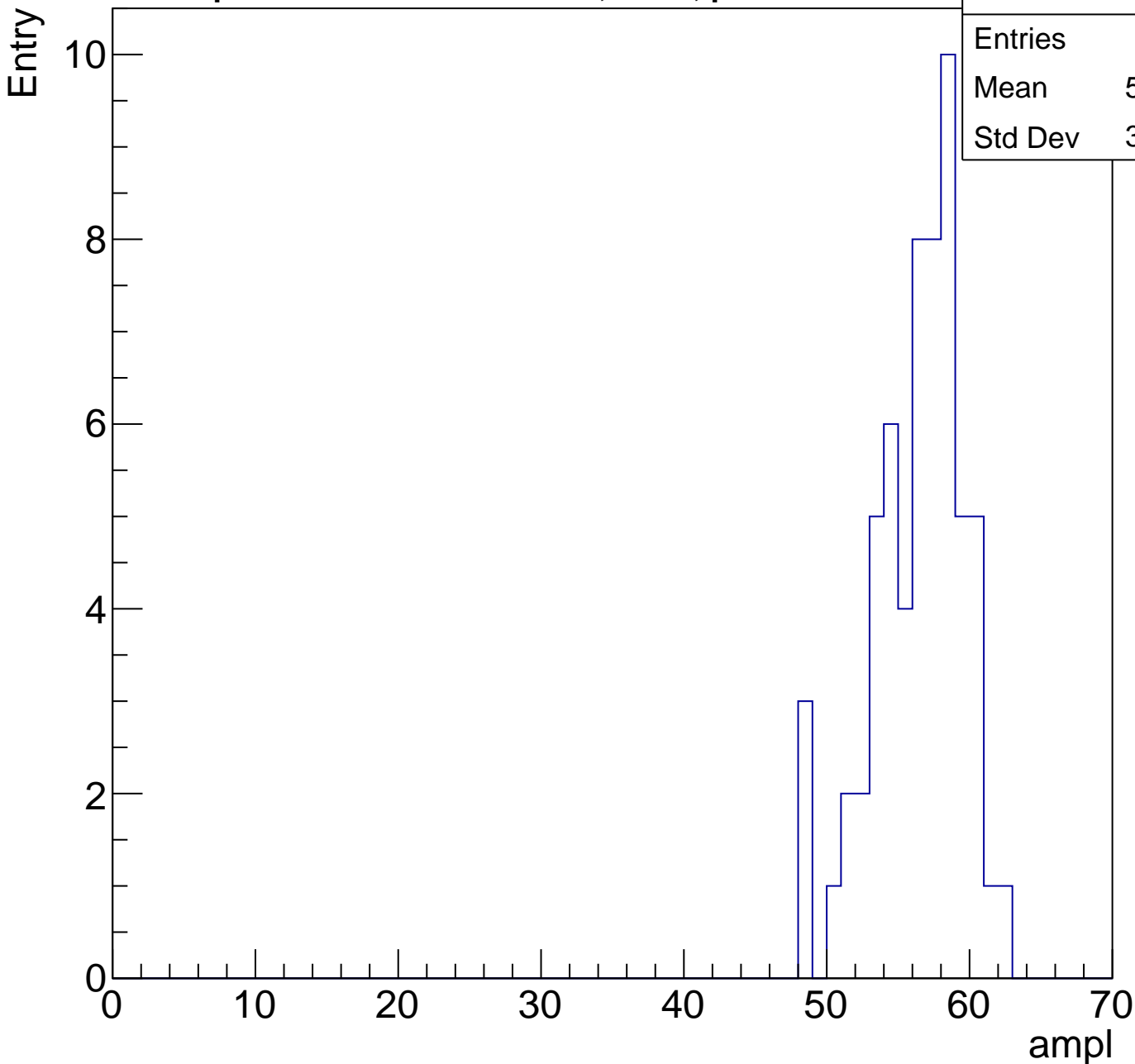
Entries	60
Mean	49.98
Std Dev	3.394



B0L002S, U7-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	55.92
Std Dev	3.179

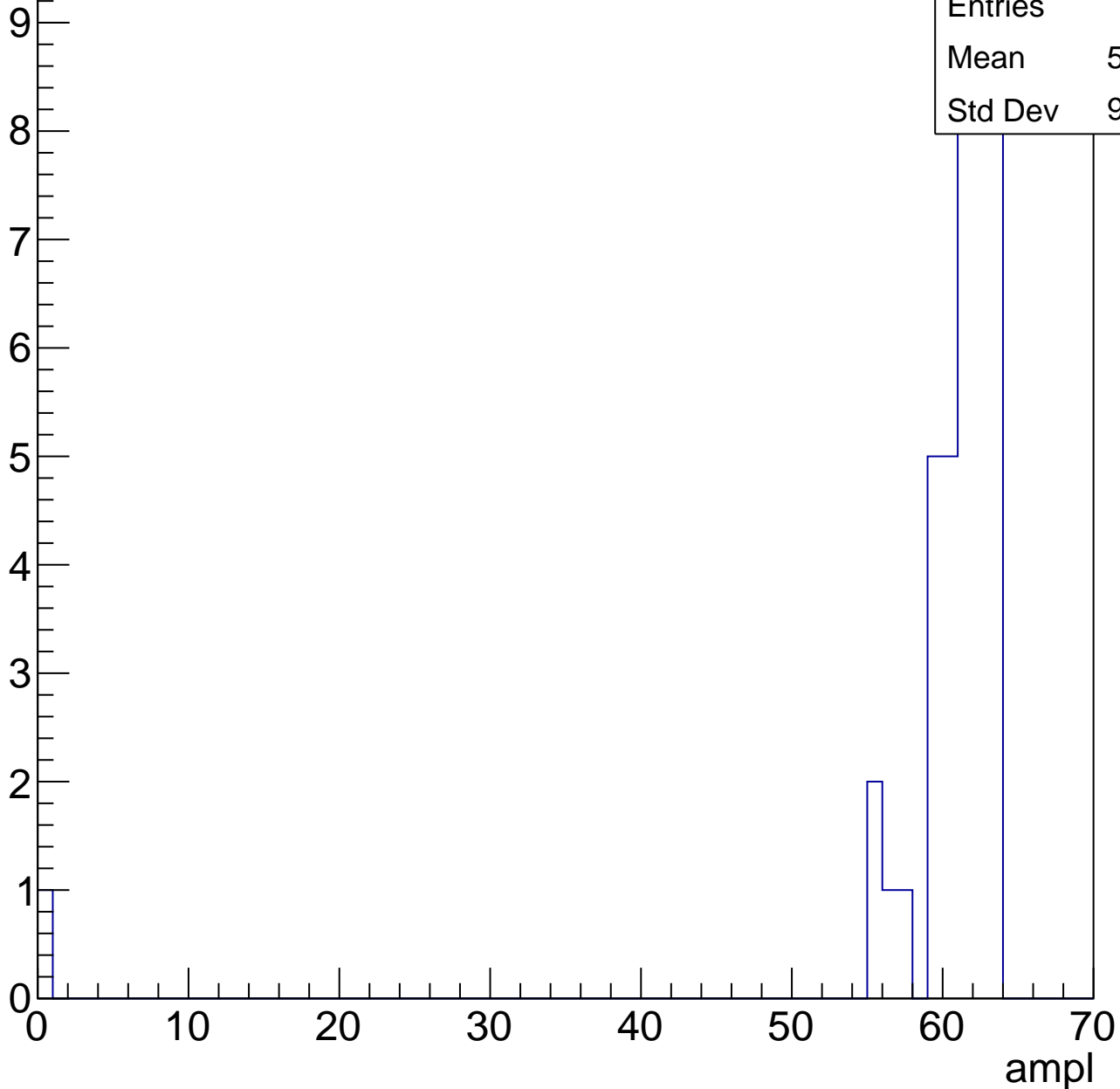


B0L002S, U7-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	59.27
Std Dev	9.602



B0L002S, U7-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

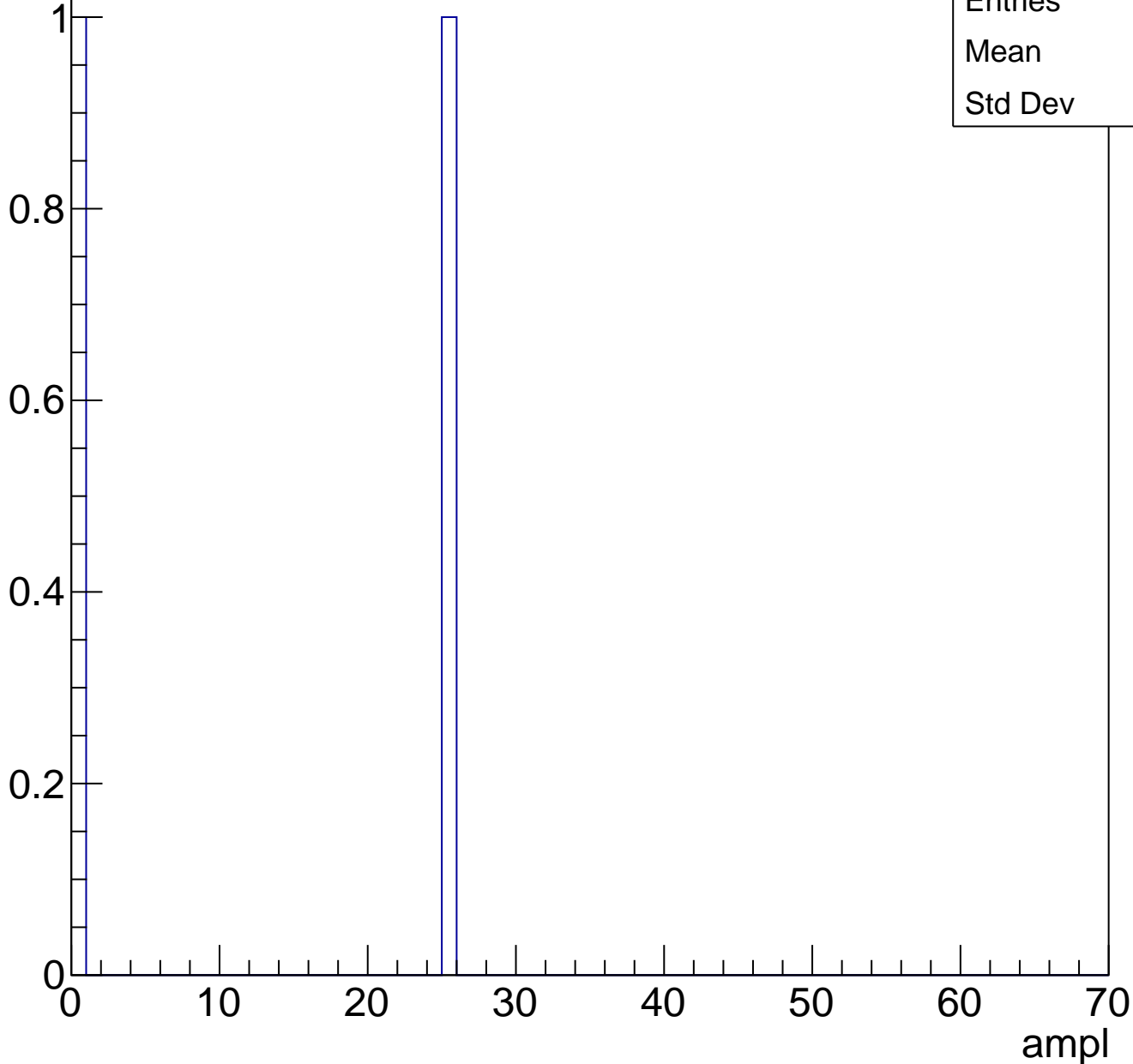
Entry



B0L002S, U7-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U7-ch90, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	29.09
Std Dev	5.125

Gaus mean : 30.3592

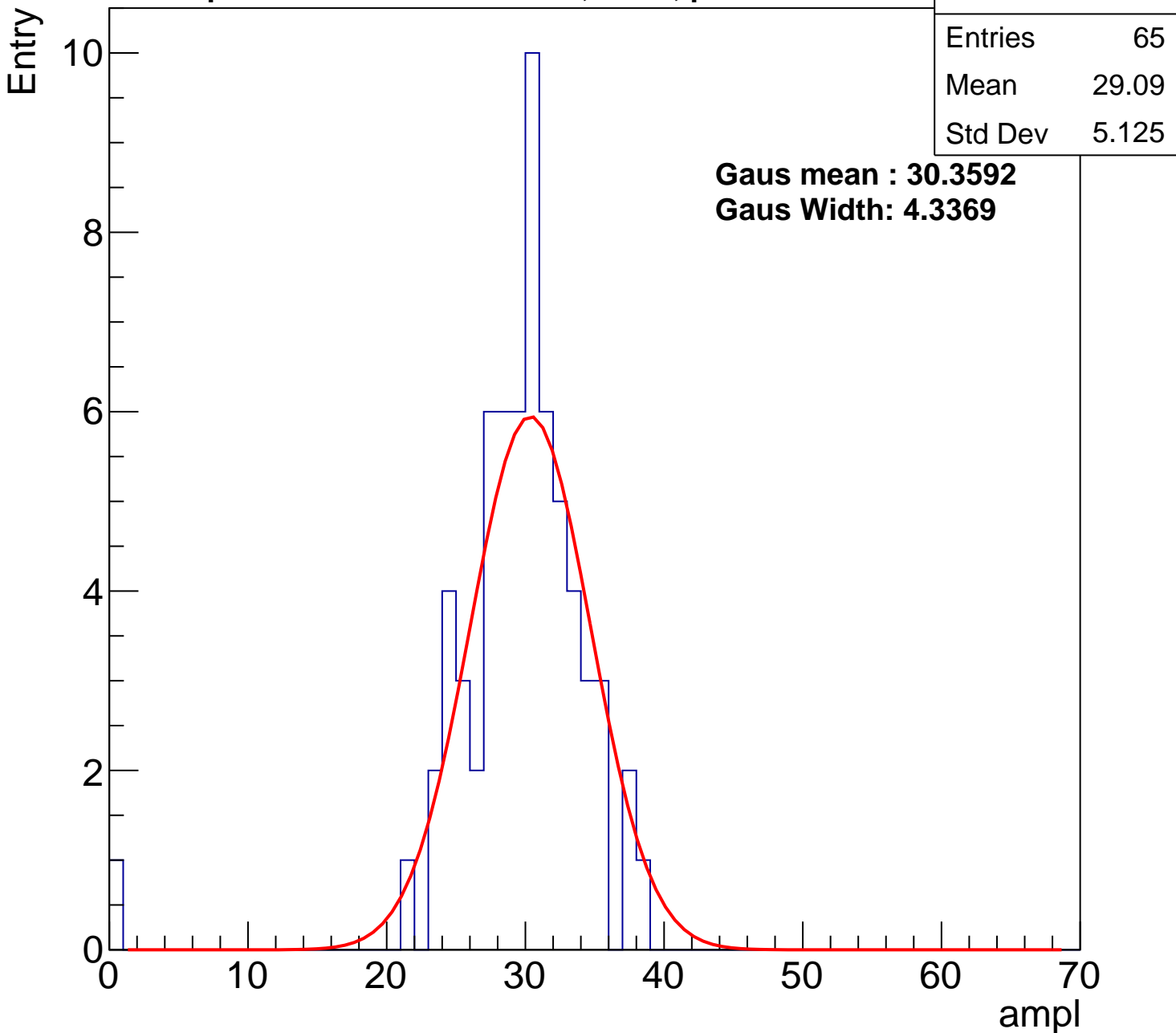
Gaus Width: 4.3369

Entry

10
8
6
4
2
0

ampl

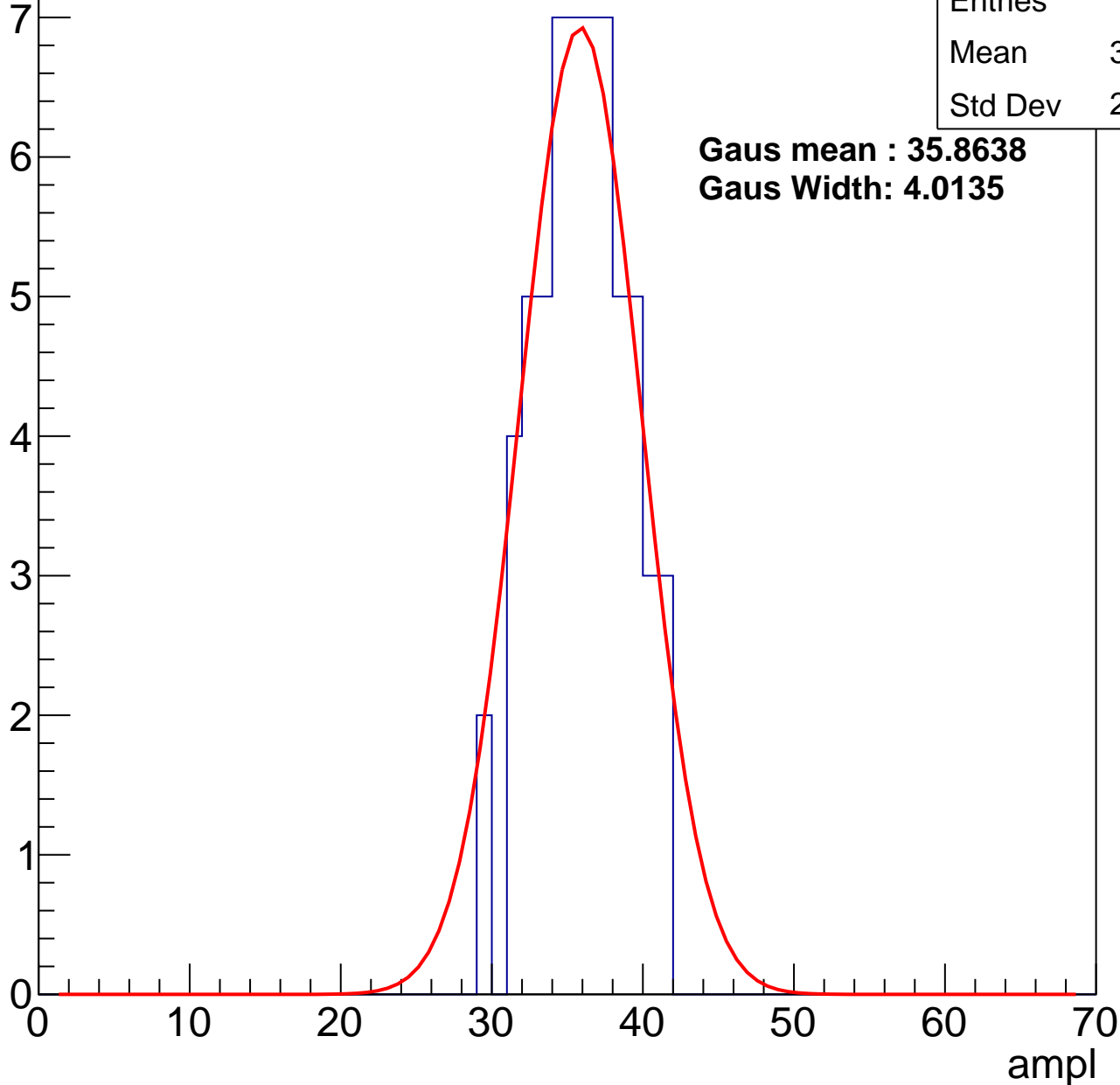
0 10 20 30 40 50 60 70



B0L002S, U7-ch90, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch90, adc2

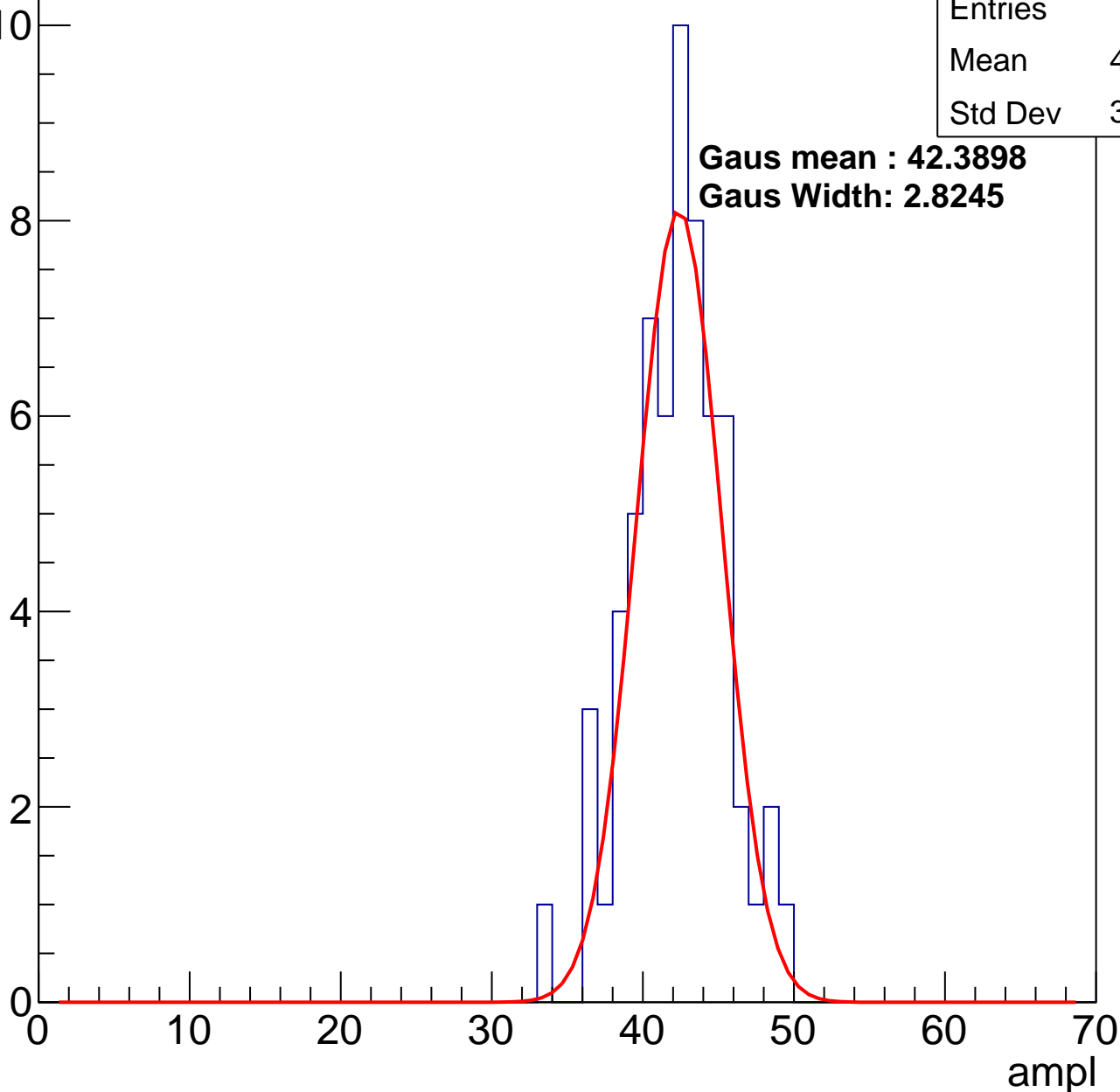
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.79
Std Dev	3.133

Gaus mean : 42.3898

Gaus Width: 2.8245

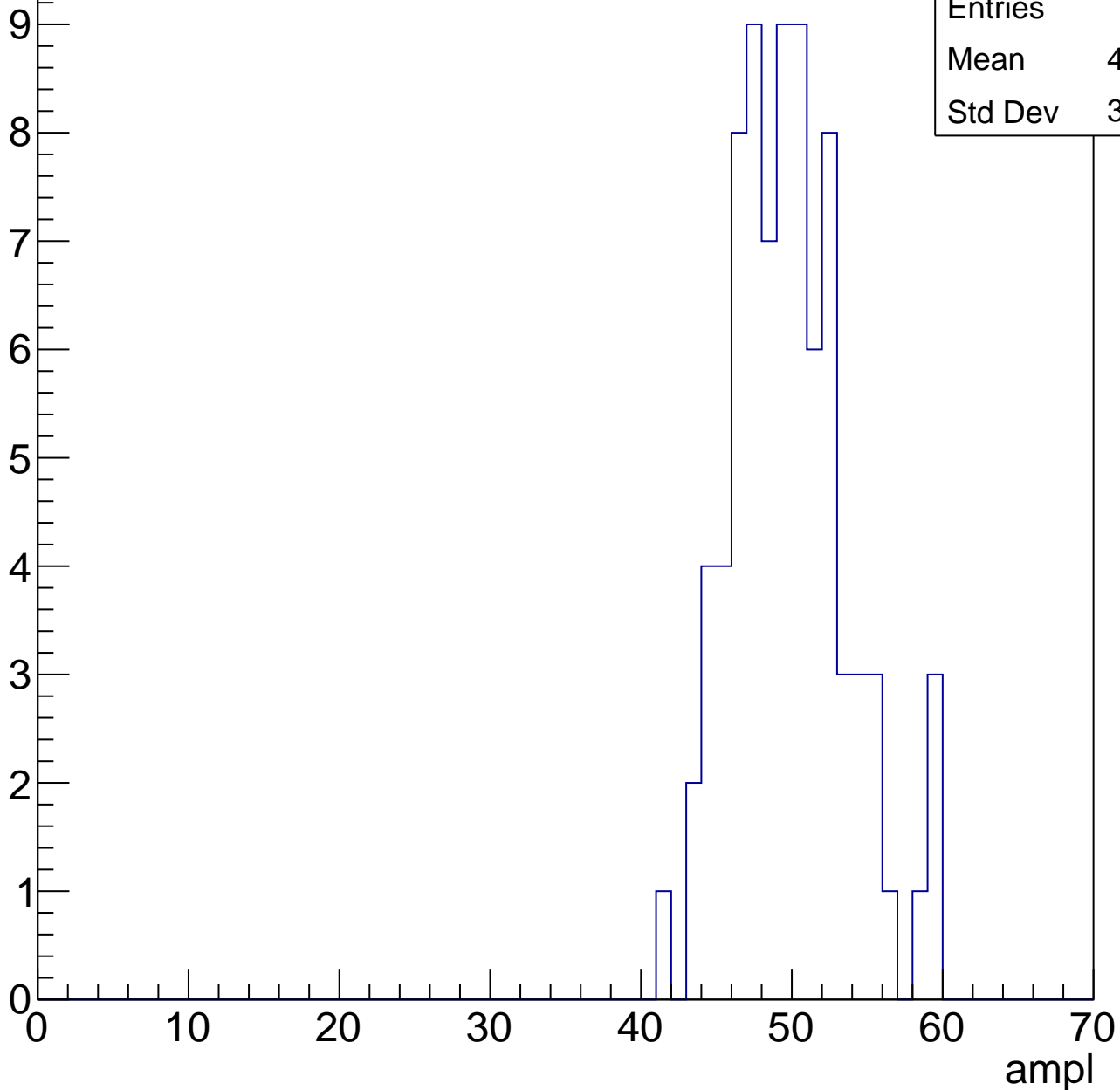


B0L002S, U7-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

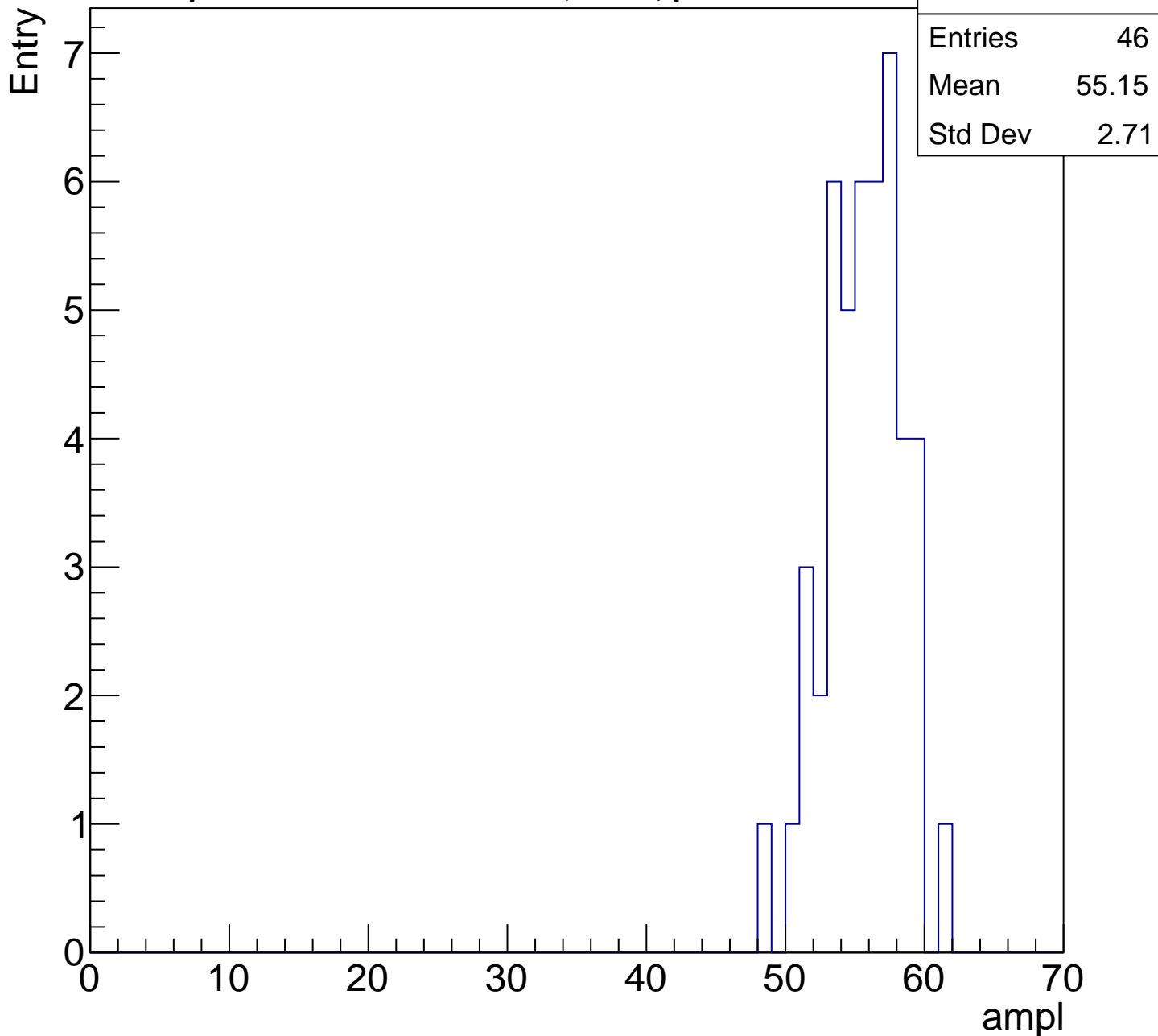
Entry

Entries	81
Mean	49.38
Std Dev	3.799



B0L002S, U7-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

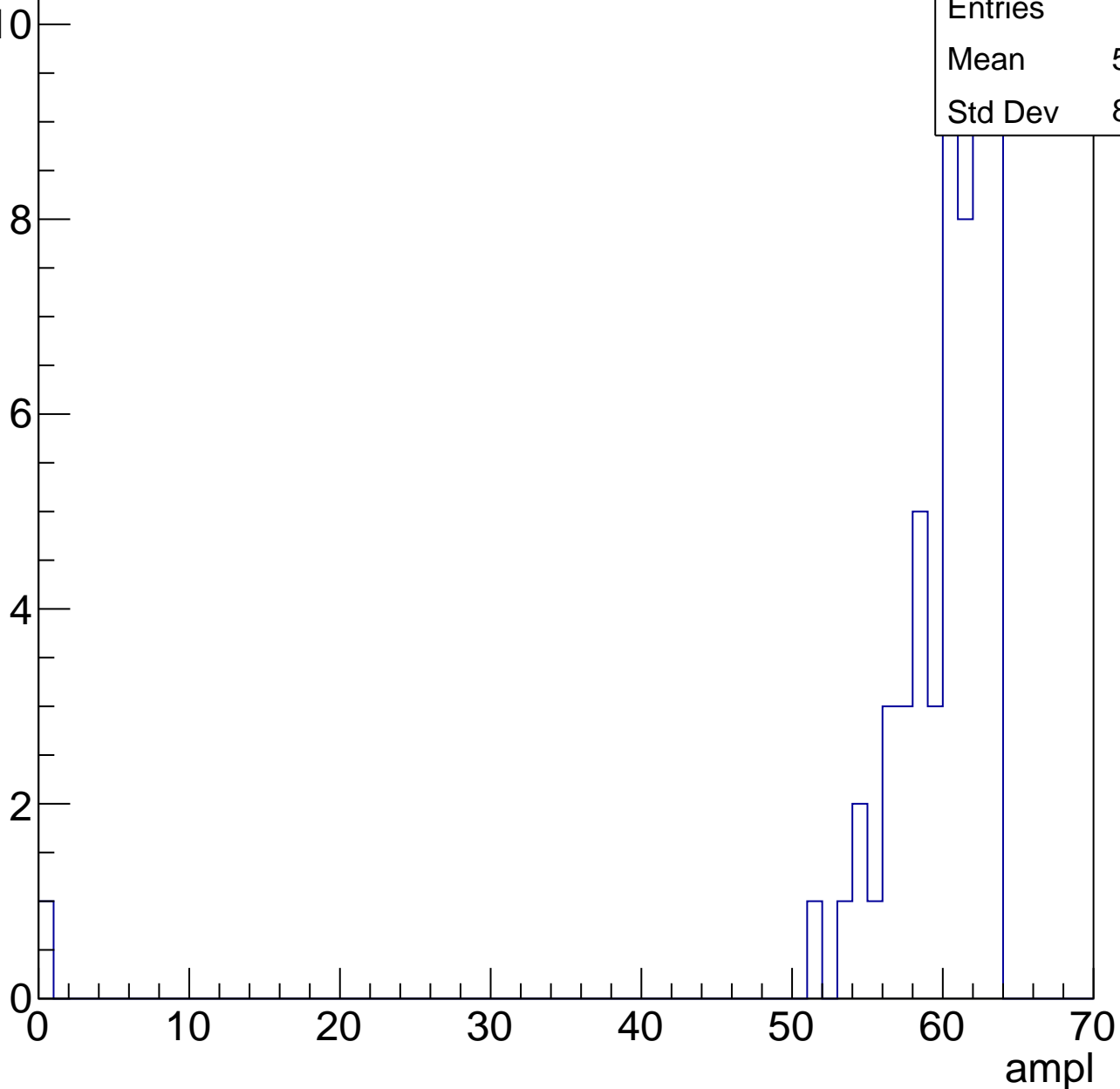


B0L002S, U7-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

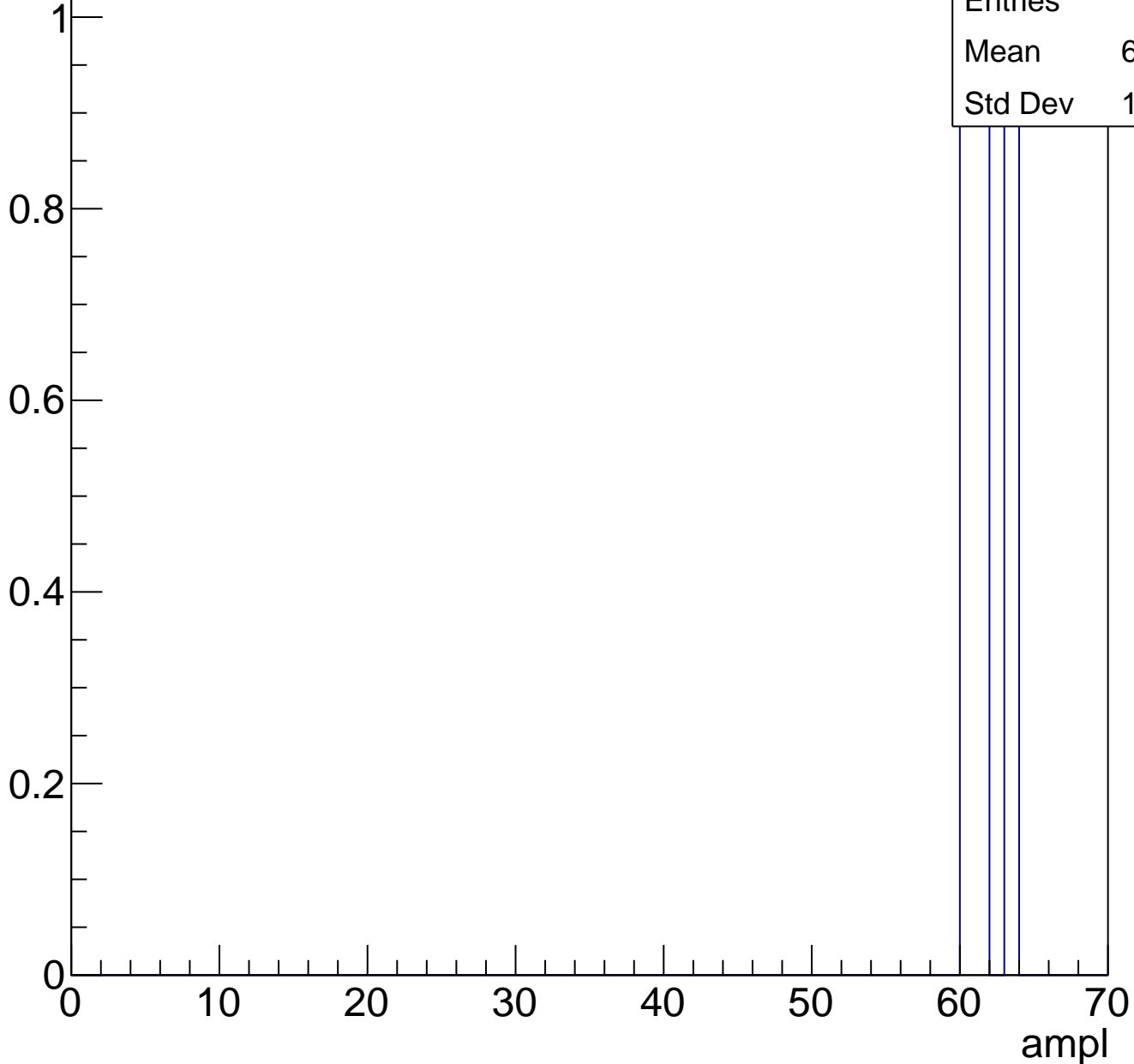
Entries	56
Mean	58.71
Std Dev	8.411



B0L002S, U7-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch91, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	31.77
Std Dev	3.265

Gaus mean : 32.6079

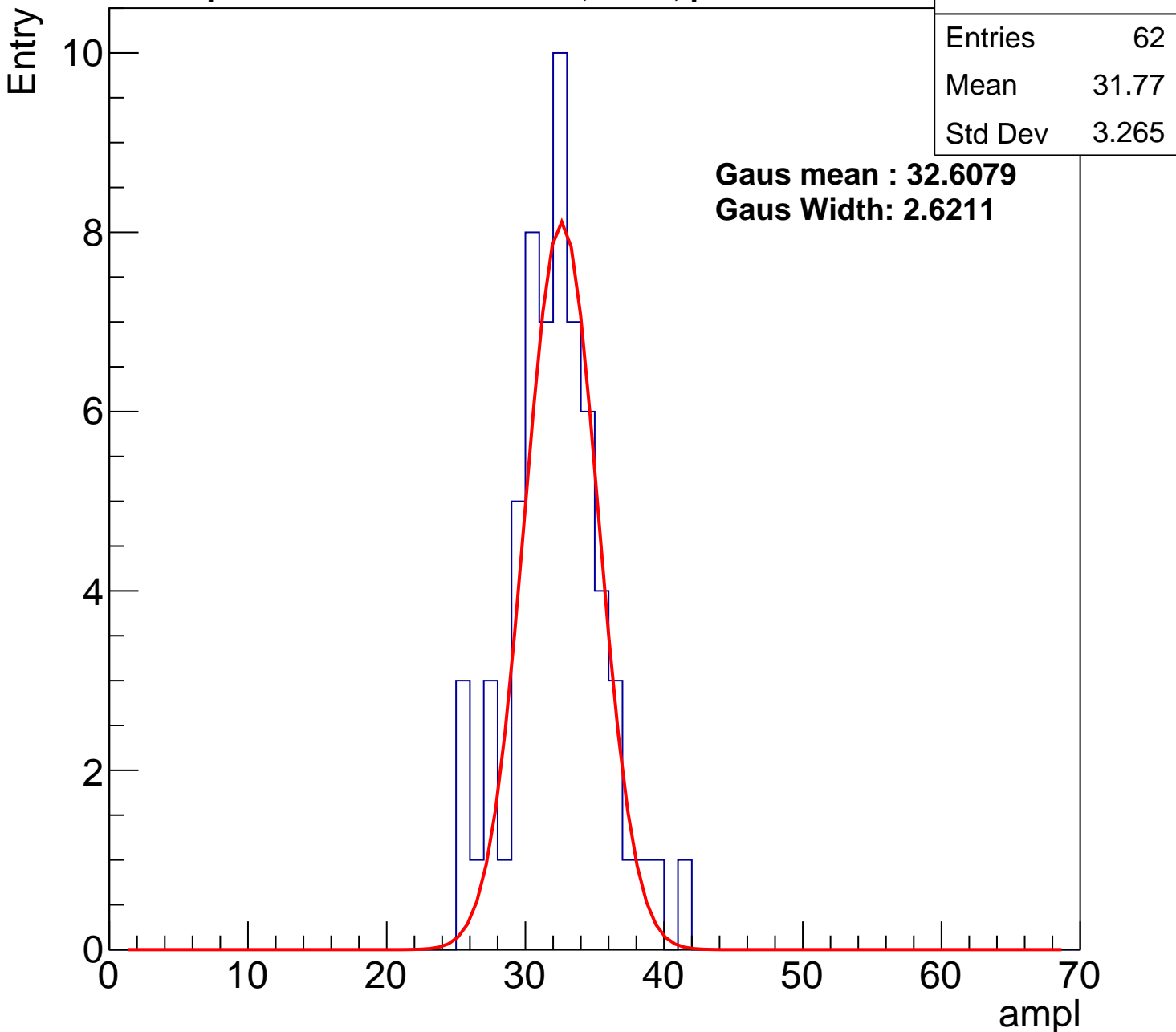
Gaus Width: 2.6211

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



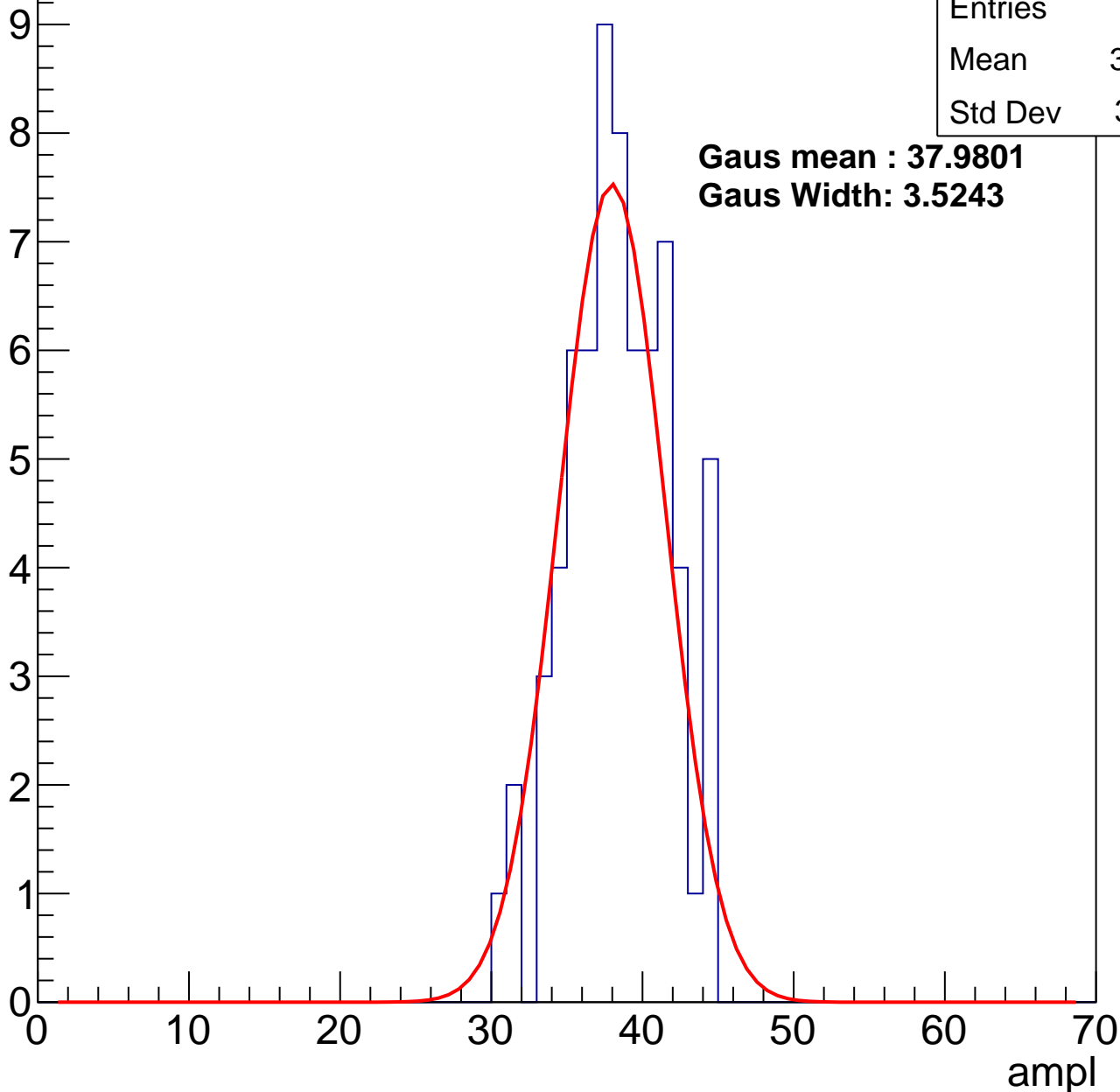
B0L002S, U7-ch91, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.97
Std Dev	3.321

Gaus mean : 37.9801
Gaus Width: 3.5243



B0L002S, U7-ch91, adc2

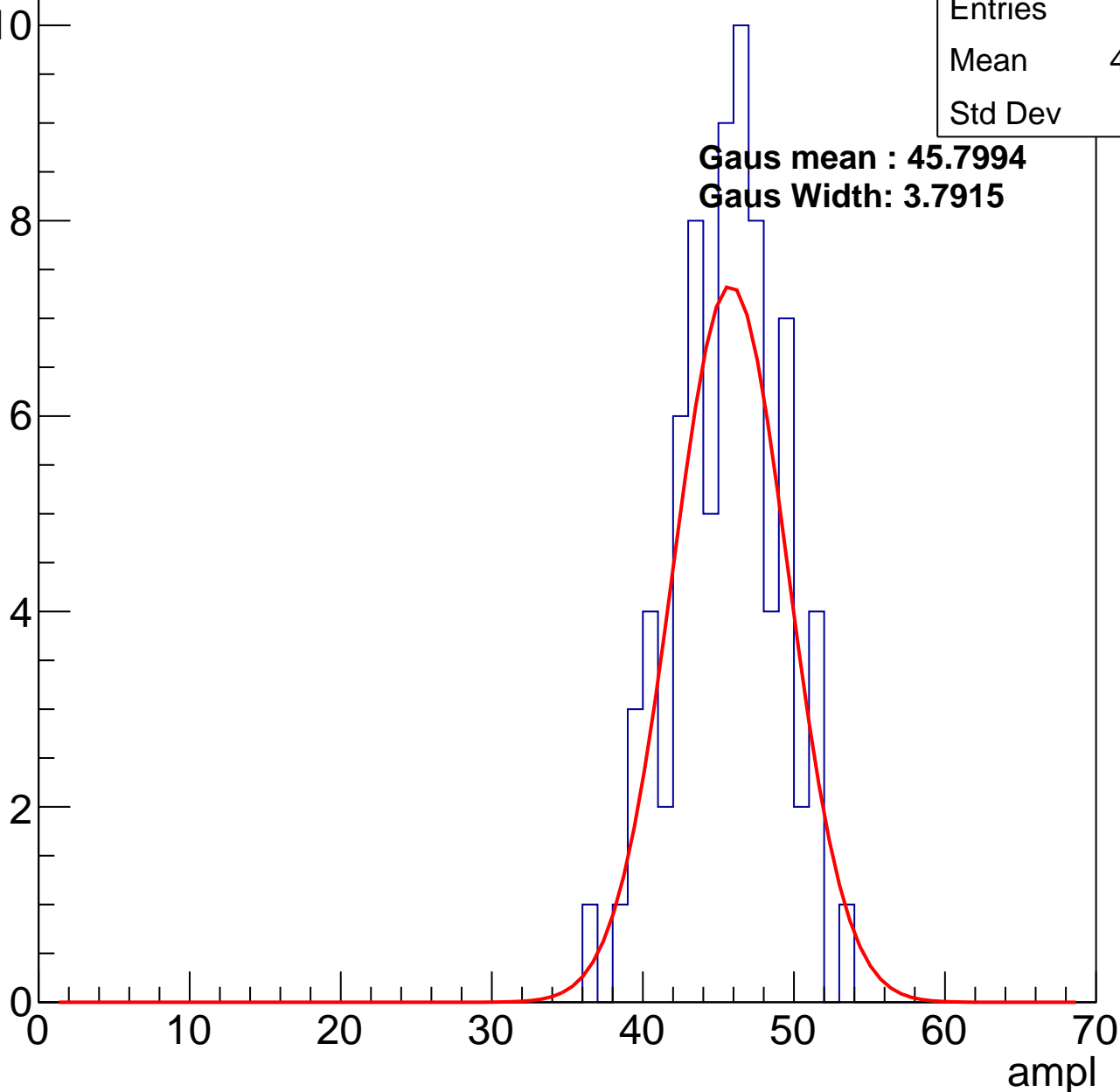
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	45.09
Std Dev	3.48

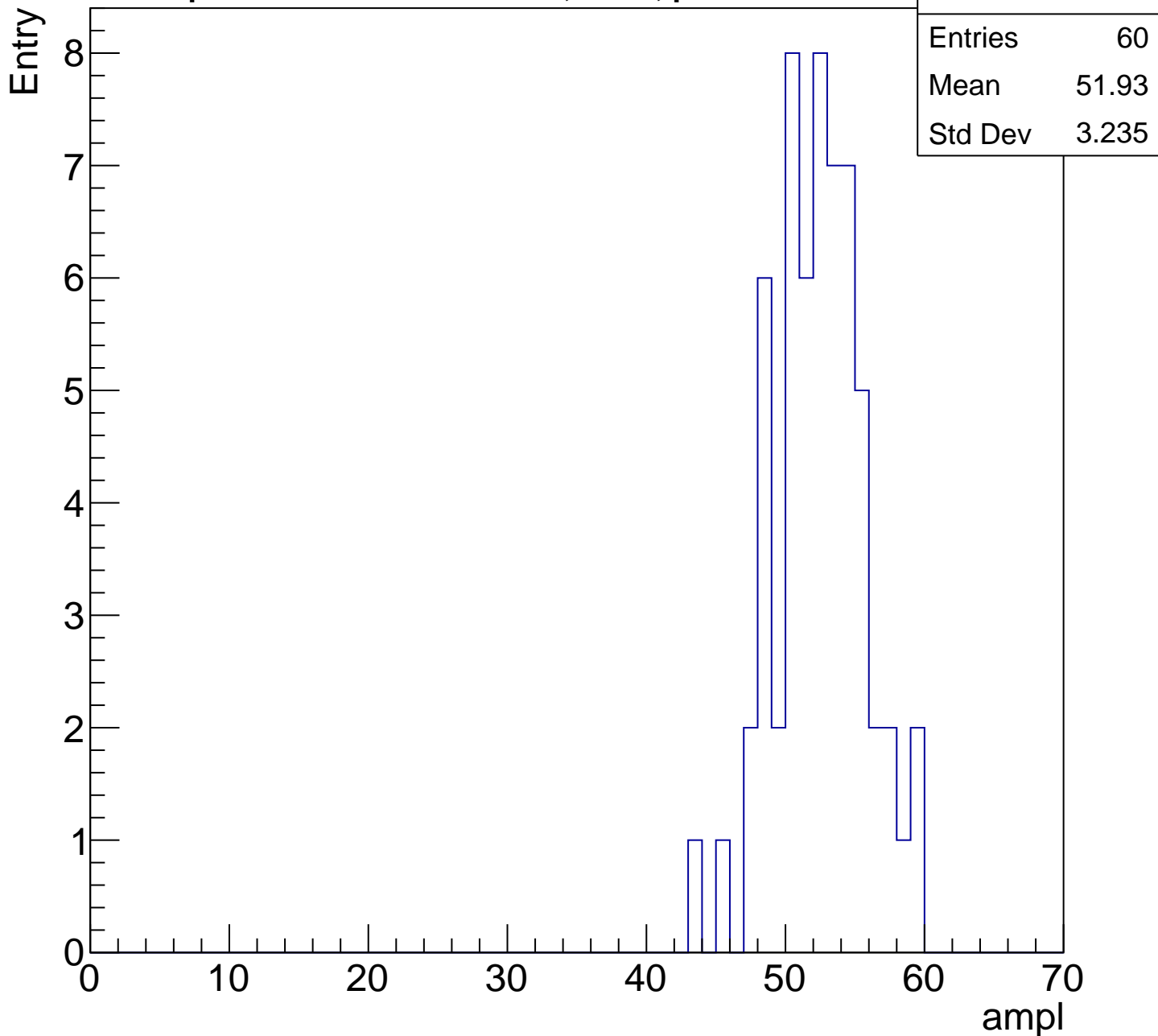
Gaus mean : 45.7994

Gaus Width: 3.7915



B0L002S, U7-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

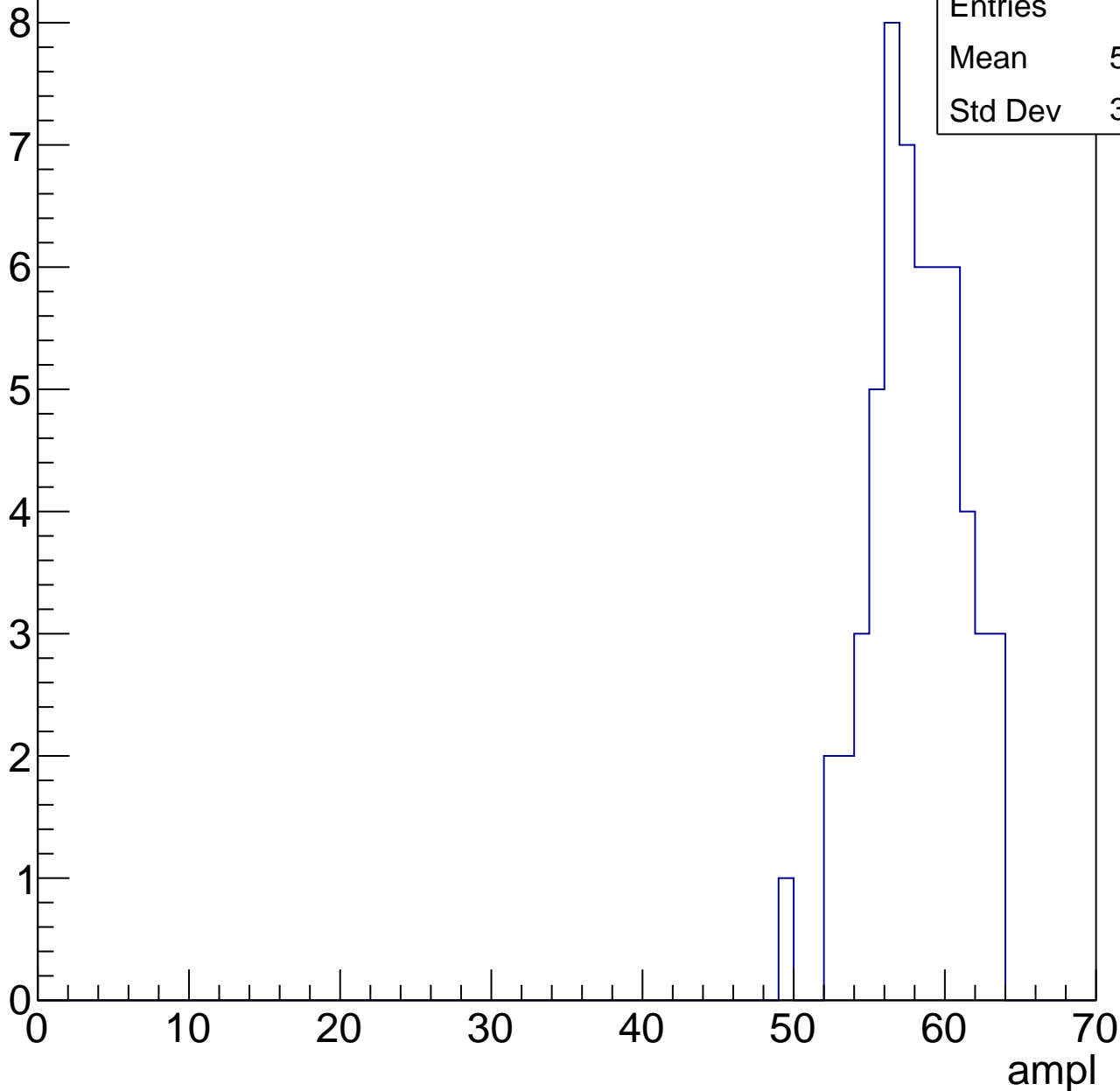


B0L002S, U7-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.57
Std Dev	3.029

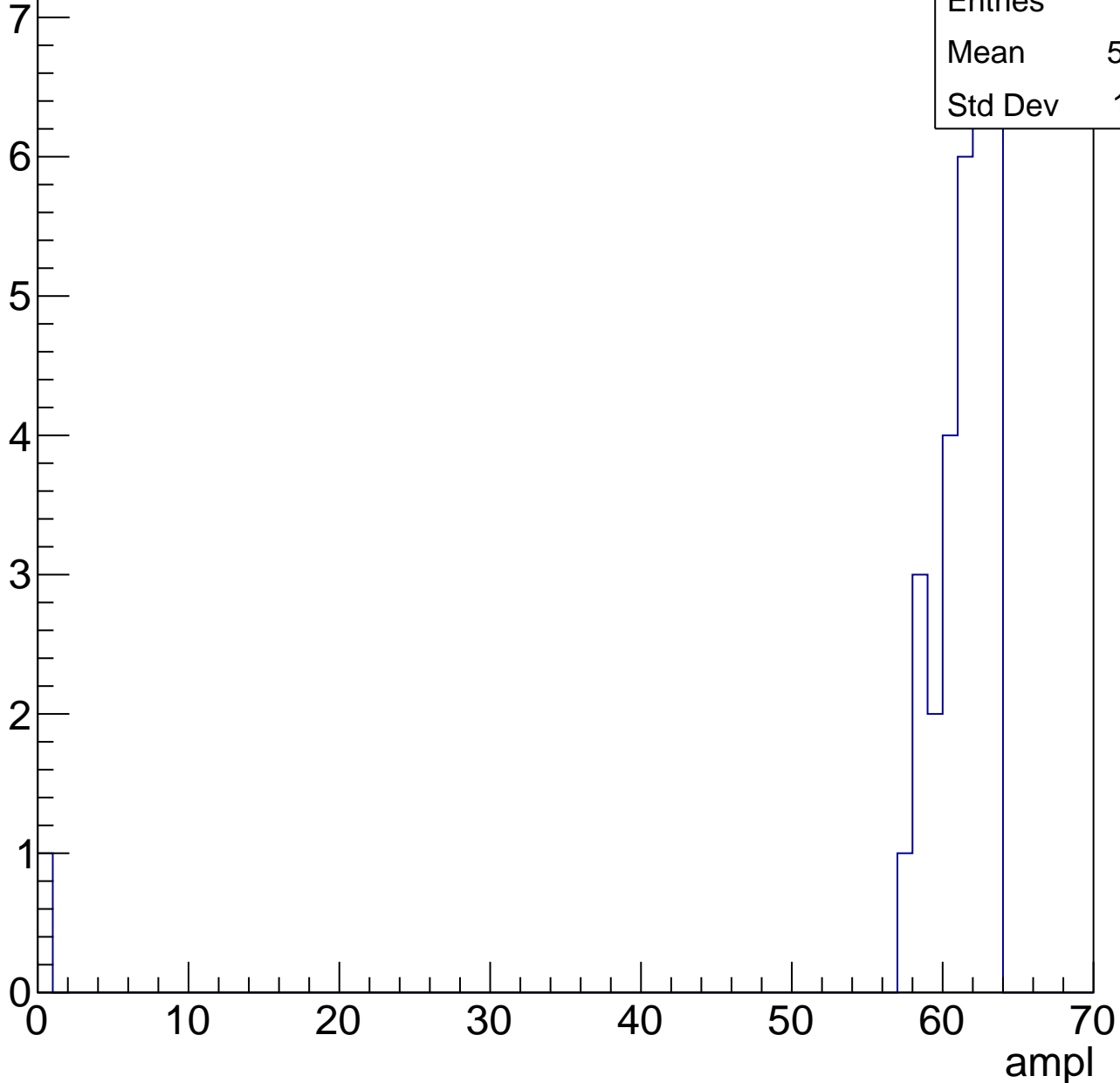


B0L002S, U7-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.03
Std Dev	10.91



B0L002S, U7-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch92, adc0

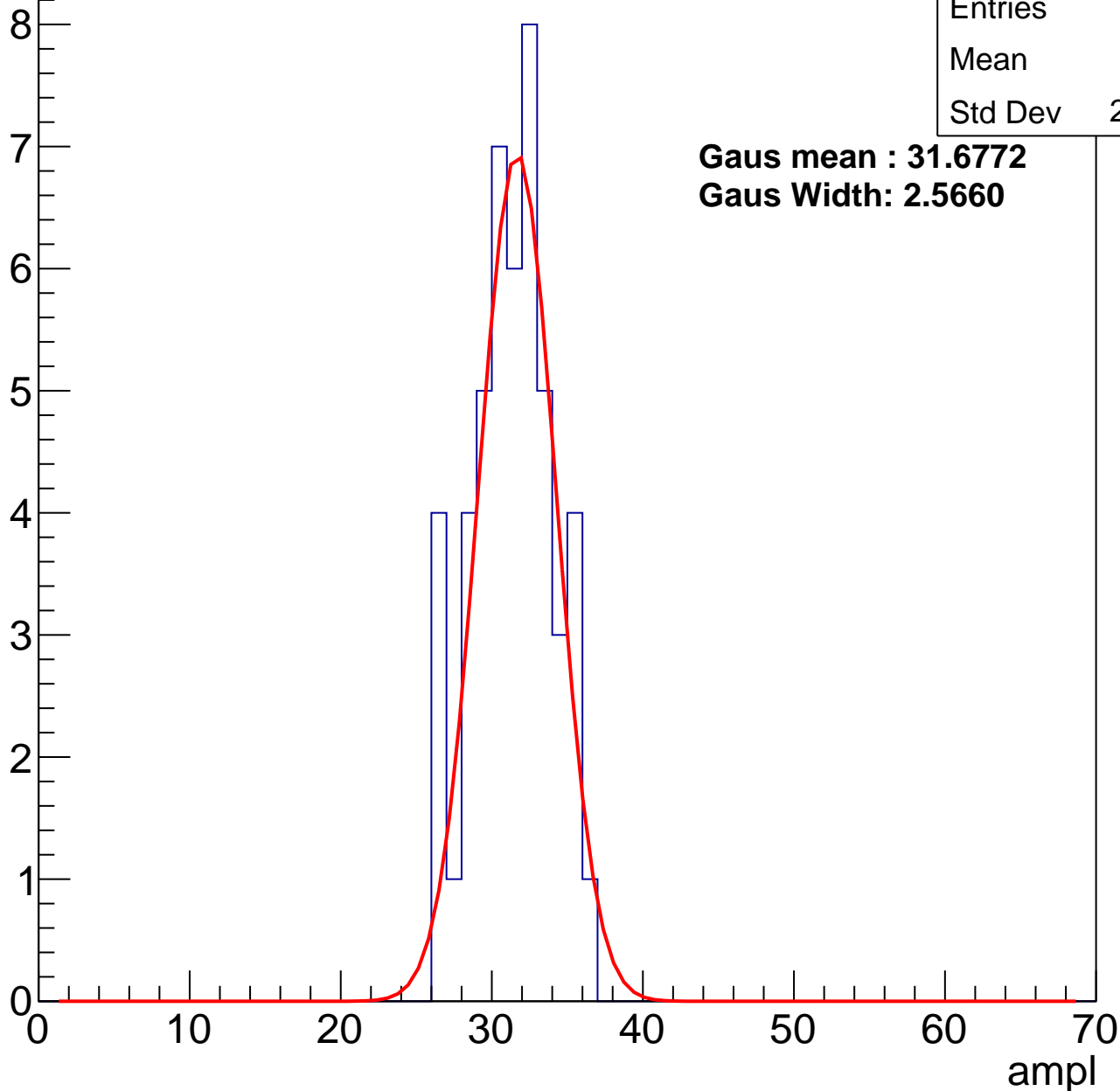
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	30.9
Std Dev	2.592

Gaus mean : 31.6772

Gaus Width: 2.5660



B0L002S, U7-ch92, adc1

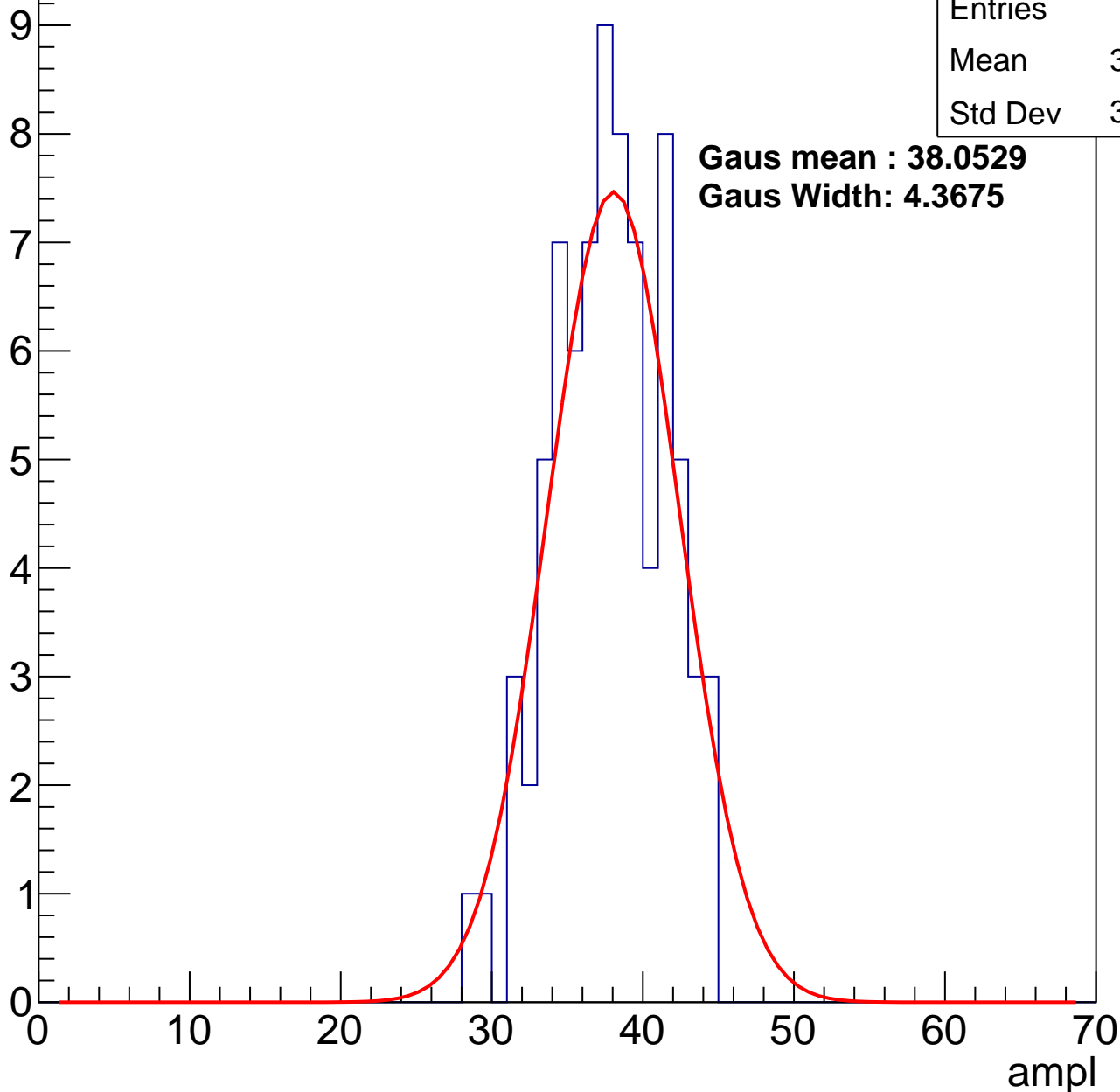
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.32
Std Dev	3.644

Gaus mean : 38.0529

Gaus Width: 4.3675



B0L002S, U7-ch92, adc2

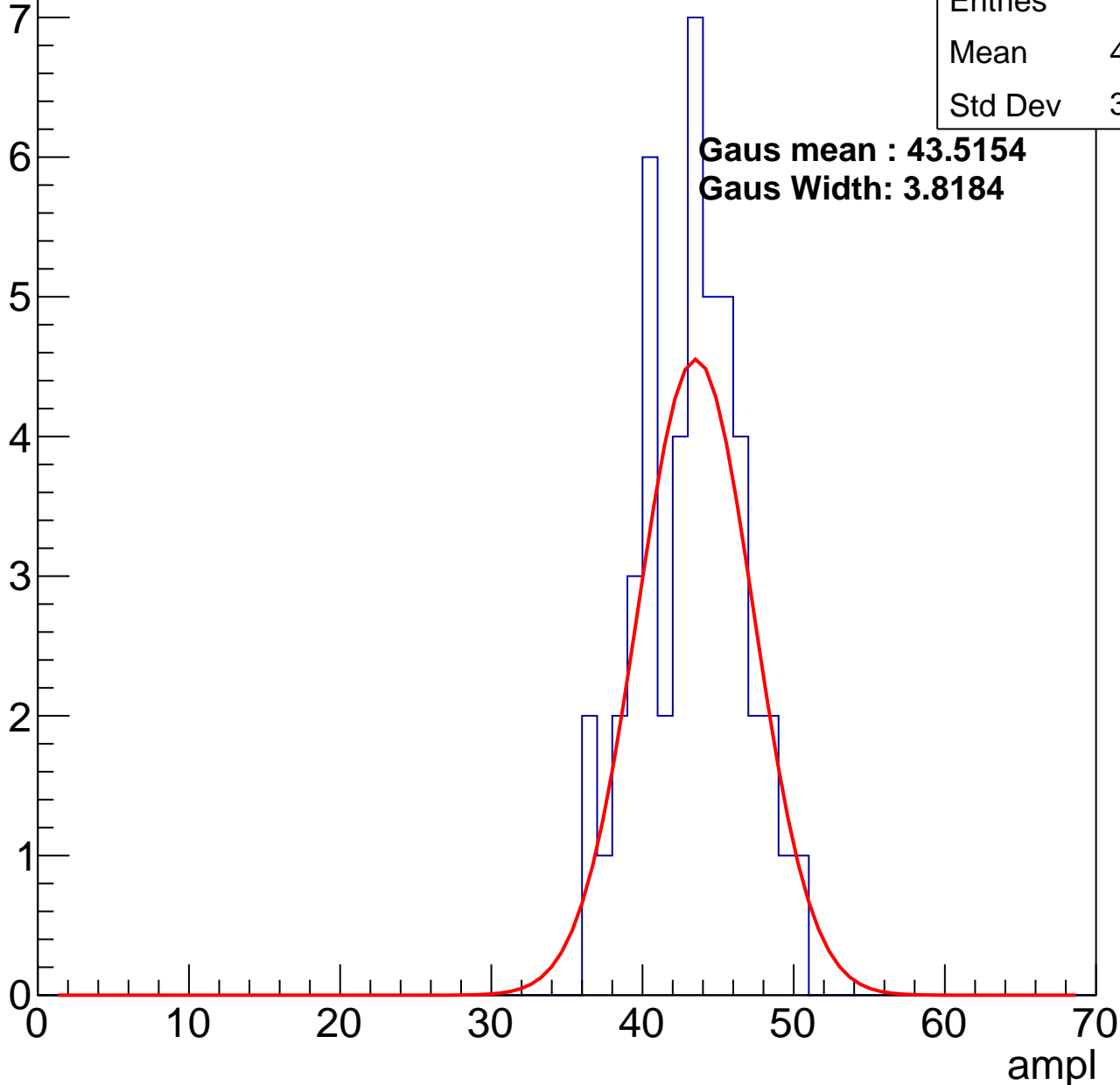
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	42.79
Std Dev	3.339

Gaus mean : 43.5154

Gaus Width: 3.8184

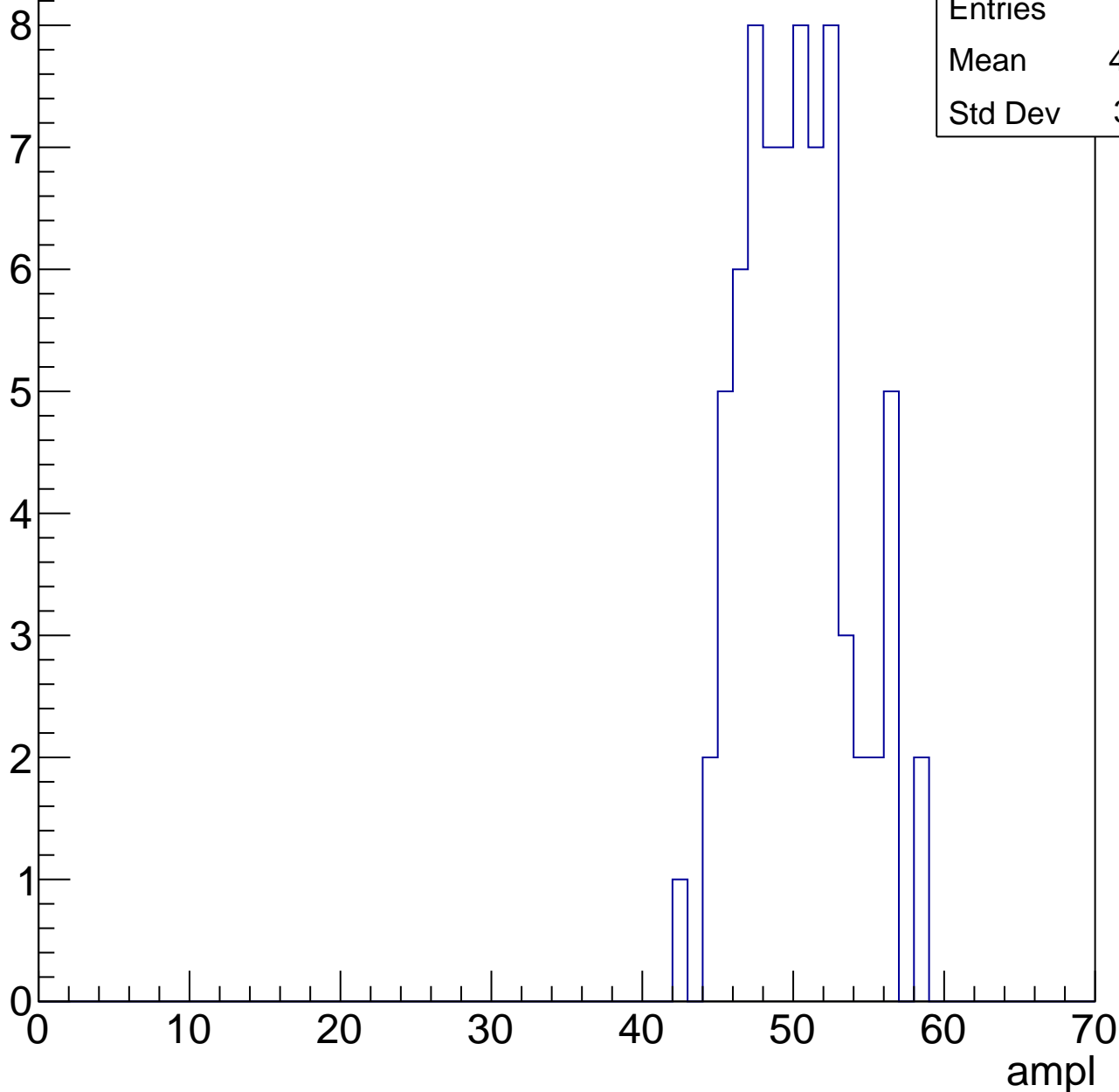


B0L002S, U7-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.75
Std Dev	3.541



B0L002S, U7-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

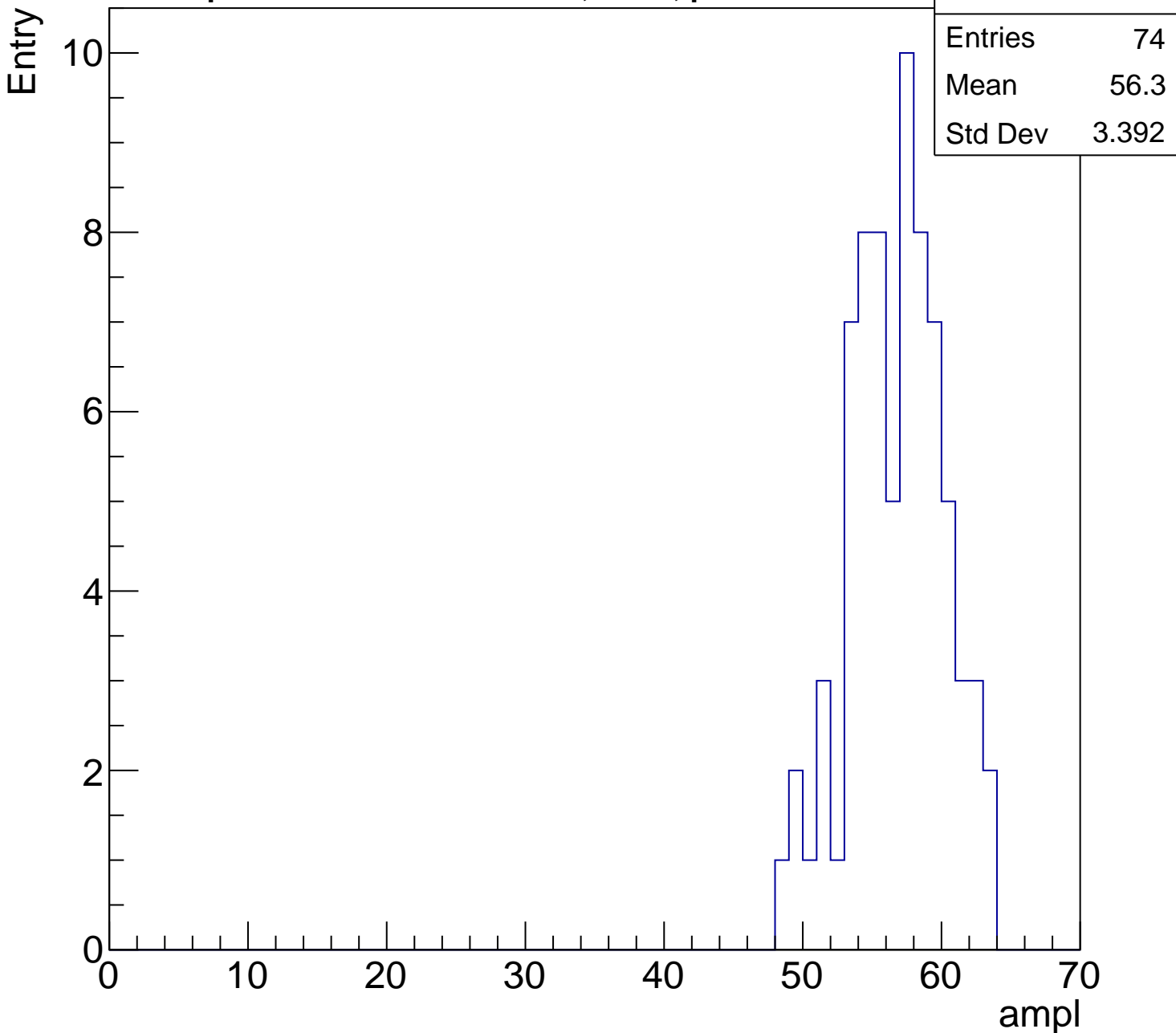
Entries	74
Mean	56.3
Std Dev	3.392

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

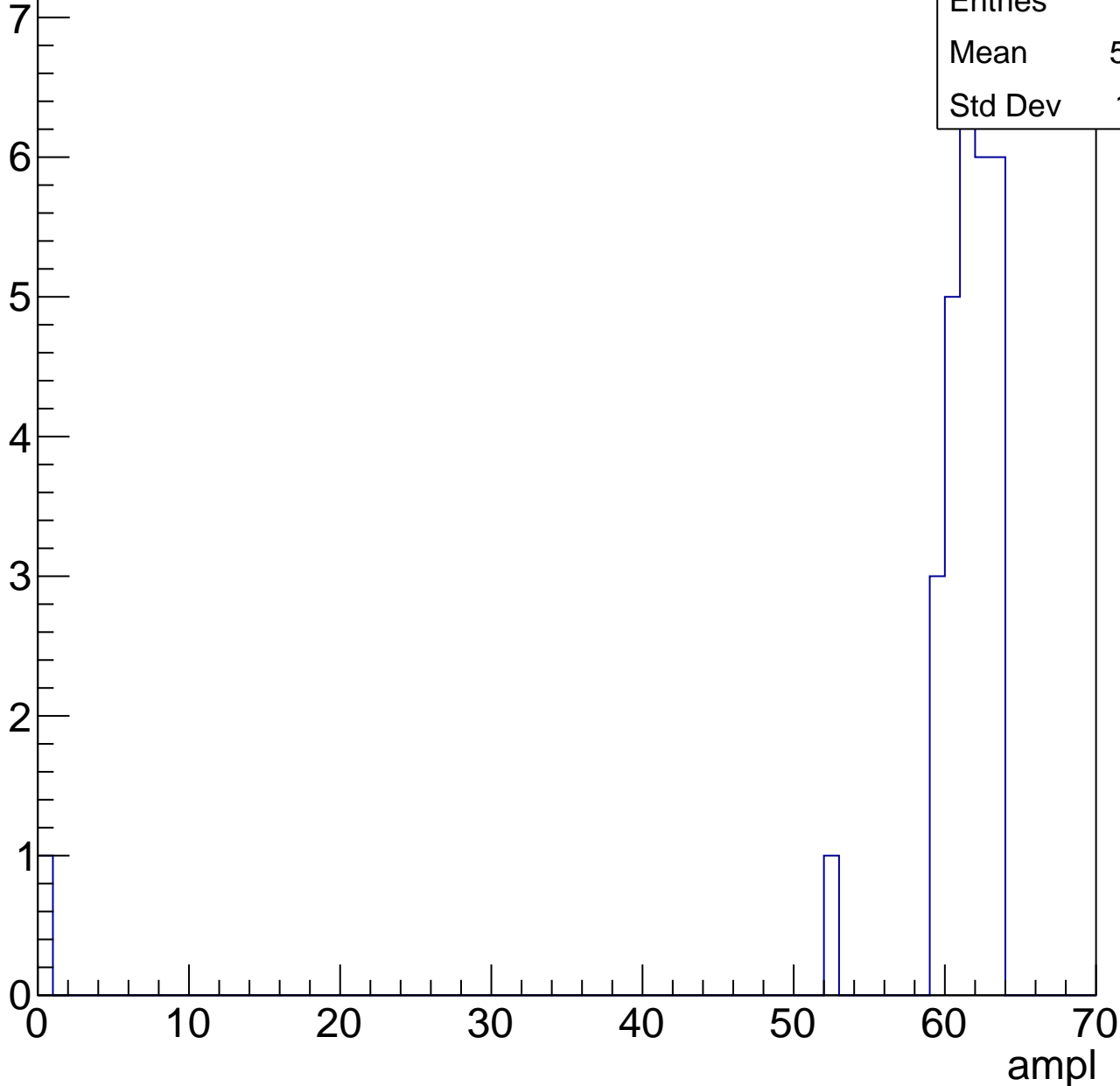


B0L002S, U7-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.83
Std Dev	11.31



B0L002S, U7-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

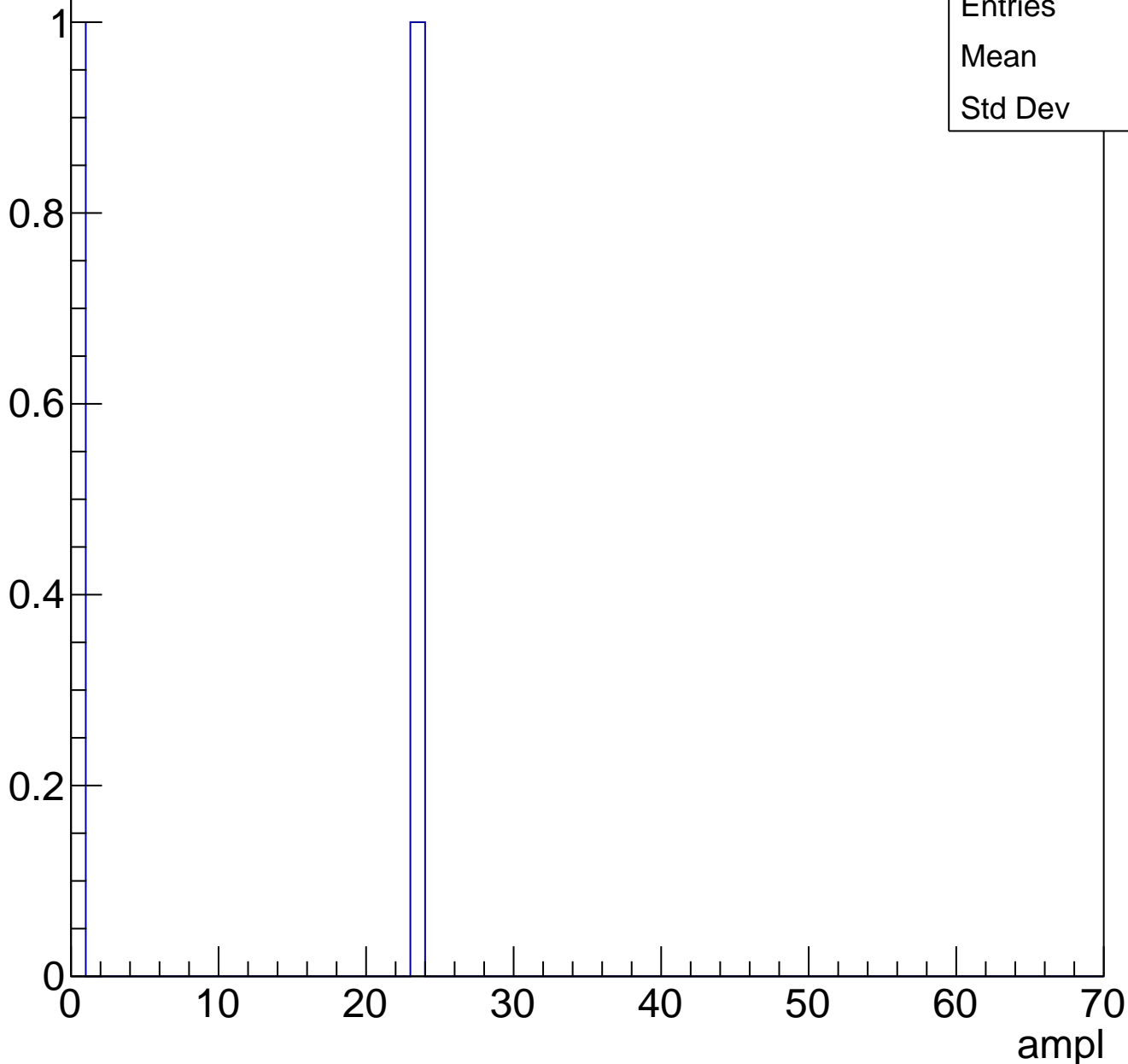


Entries	3
Mean	62.67
Std Dev	0.4714

B0L002S, U7-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch93, adc0

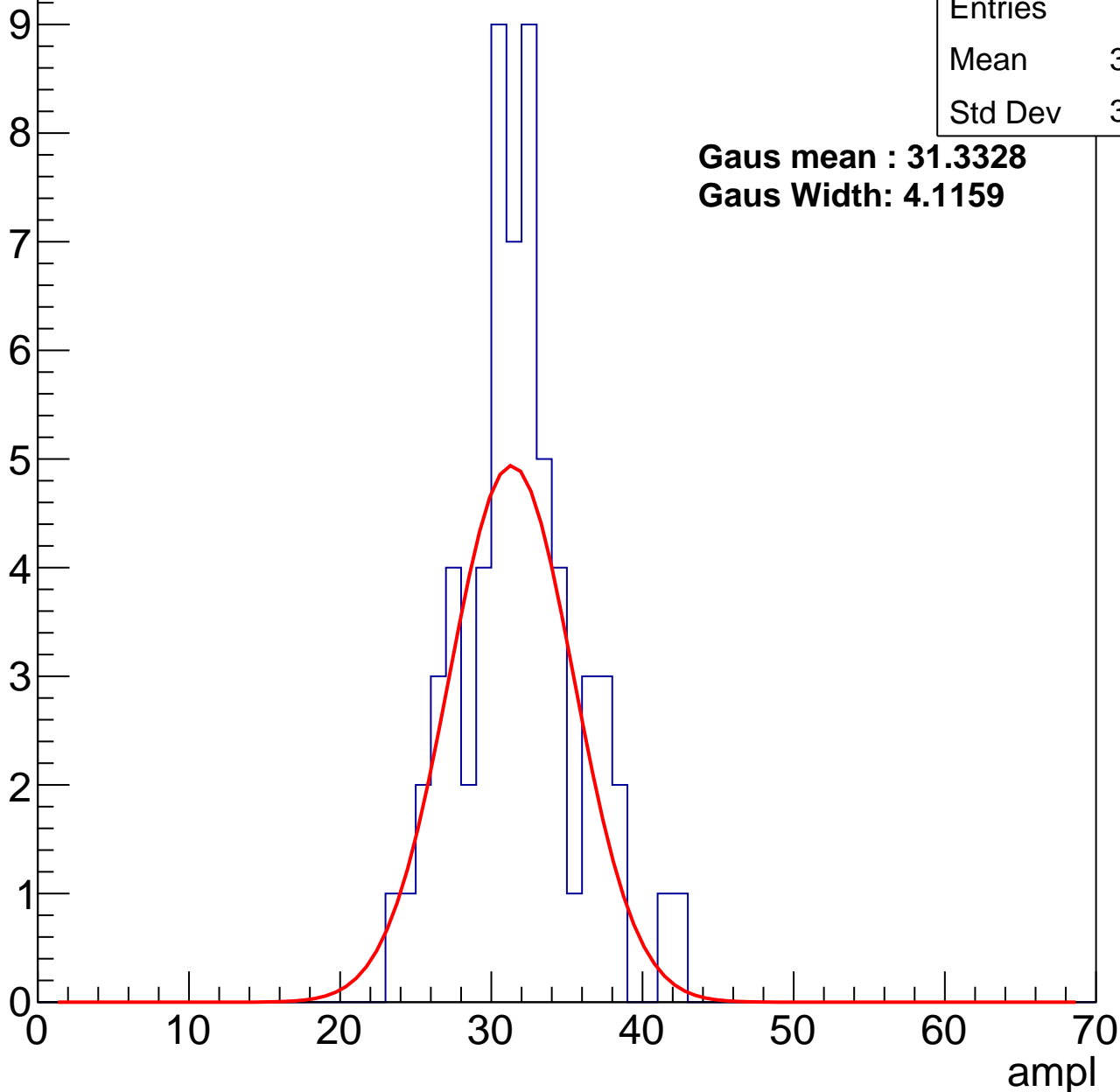
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.35
Std Dev	3.898

Gaus mean : 31.3328

Gaus Width: 4.1159



B0L002S, U7-ch93, adc1

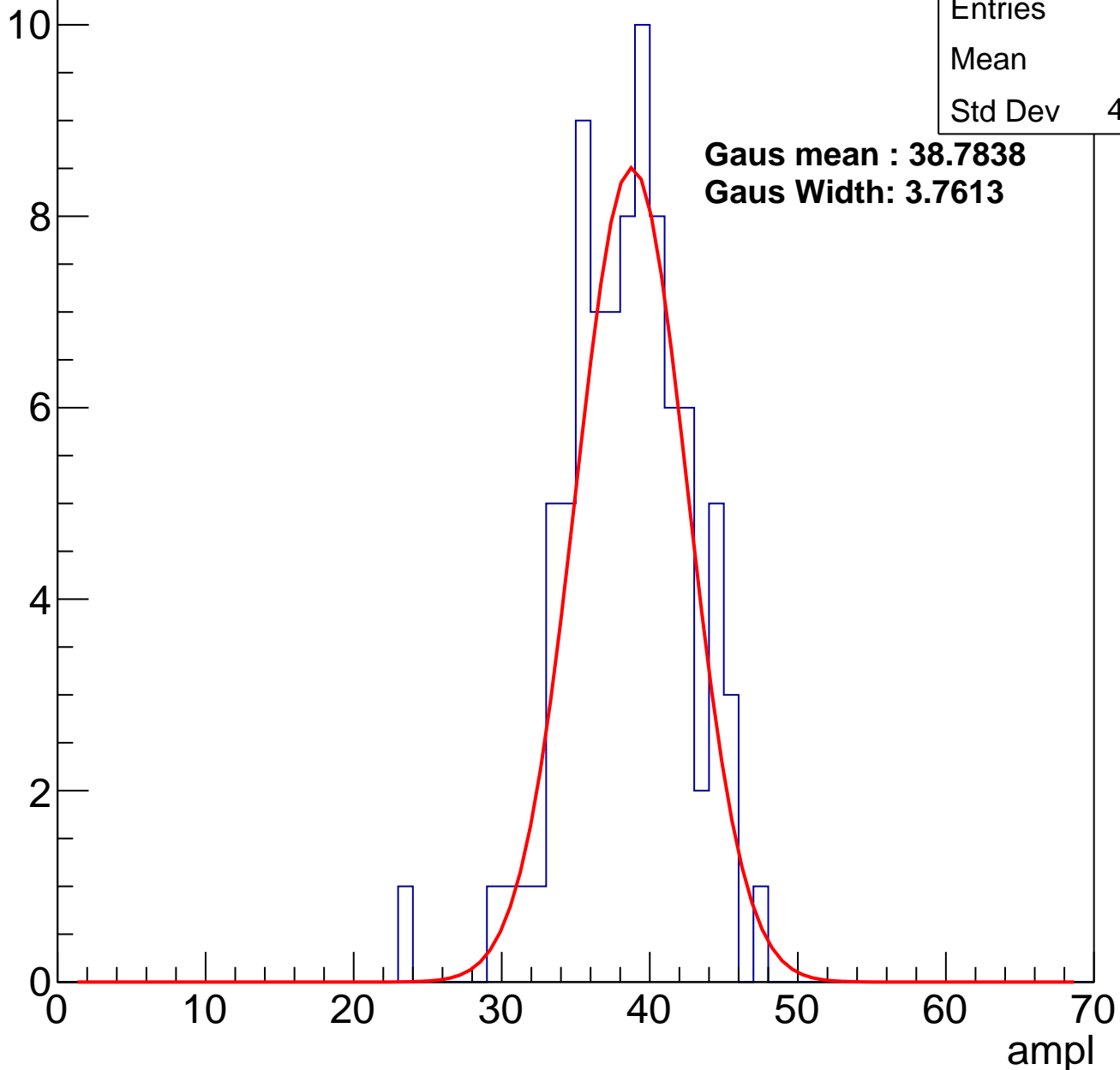
calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	38
Std Dev	4.049

Gaus mean : 38.7838

Gaus Width: 3.7613

Entry



B0L002S, U7-ch93, adc2

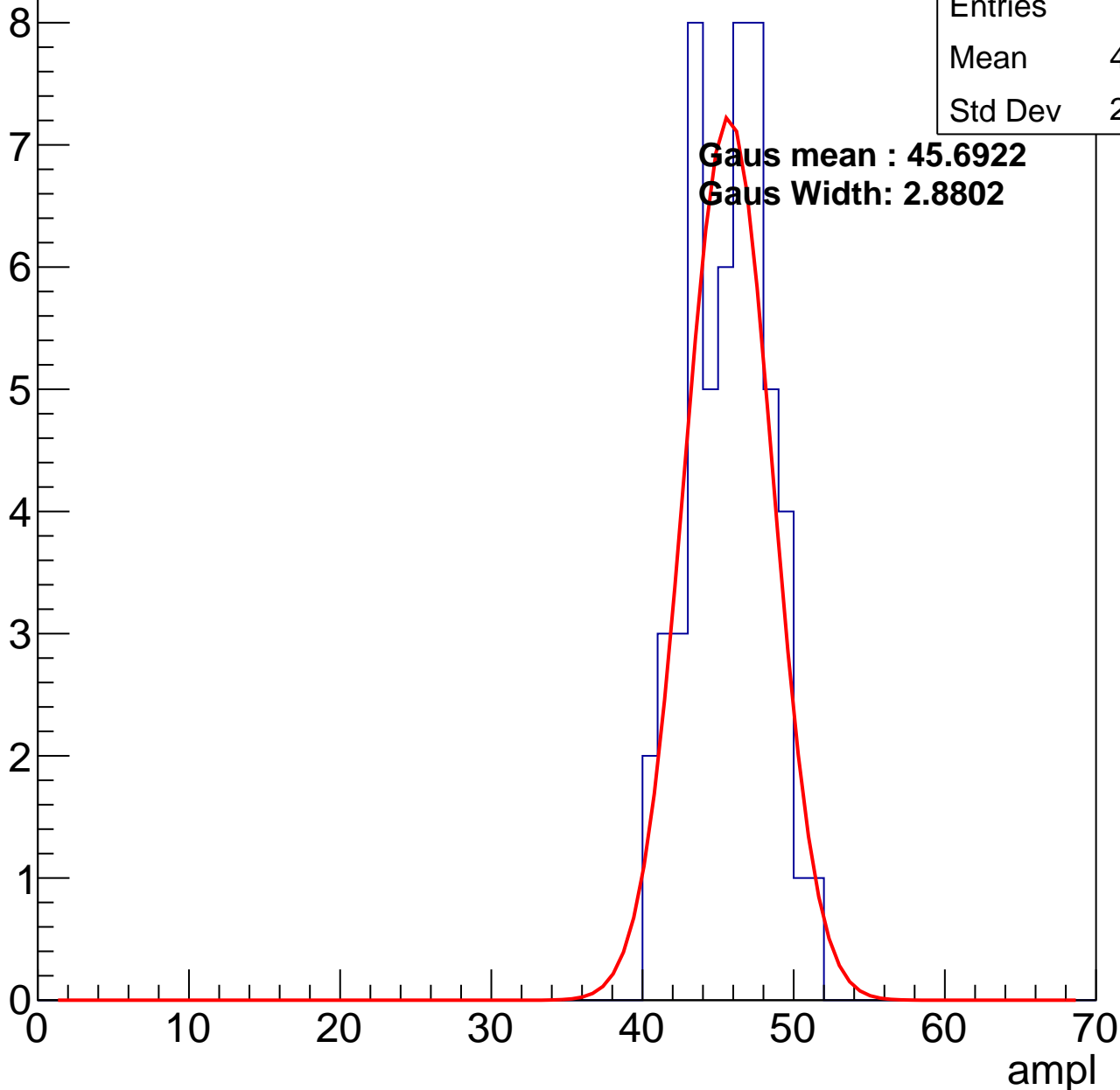
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	45.26
Std Dev	2.612

Gaus mean : 45.6922

Gaus Width: 2.8802

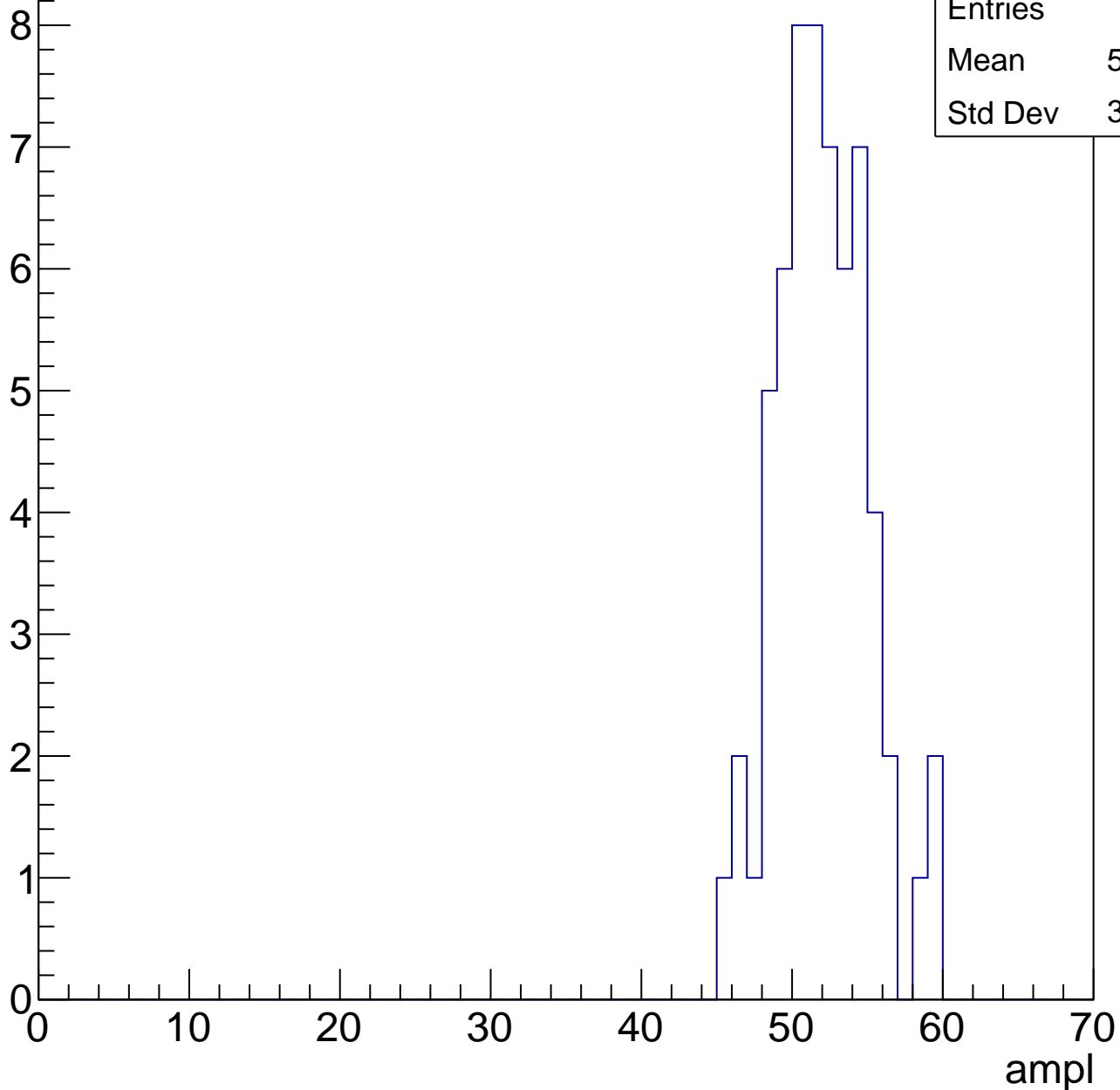


B0L002S, U7-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

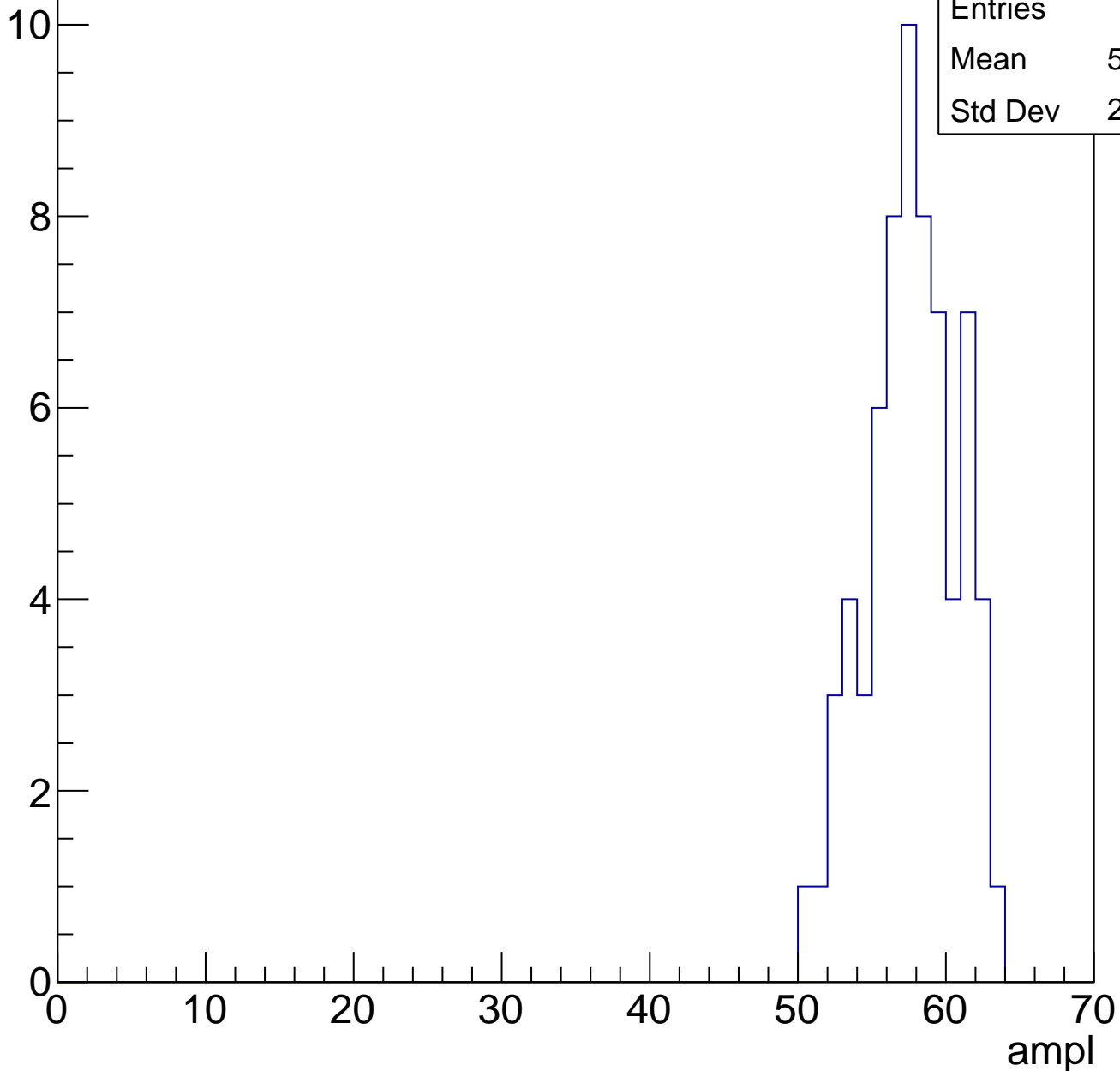
Entries	60
Mean	51.57
Std Dev	3.013



B0L002S, U7-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

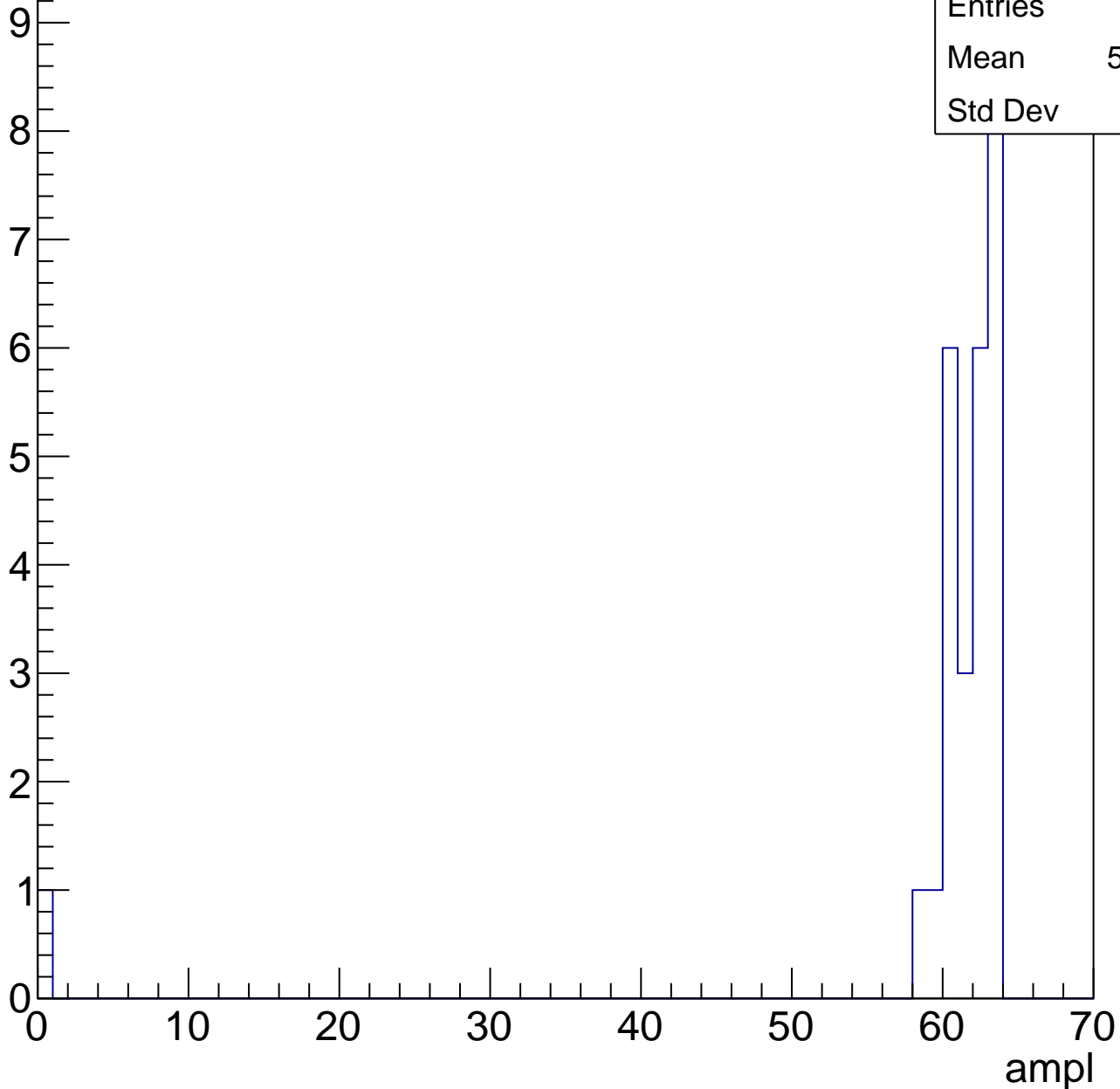
Entry



B0L002S, U7-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch94, adc0

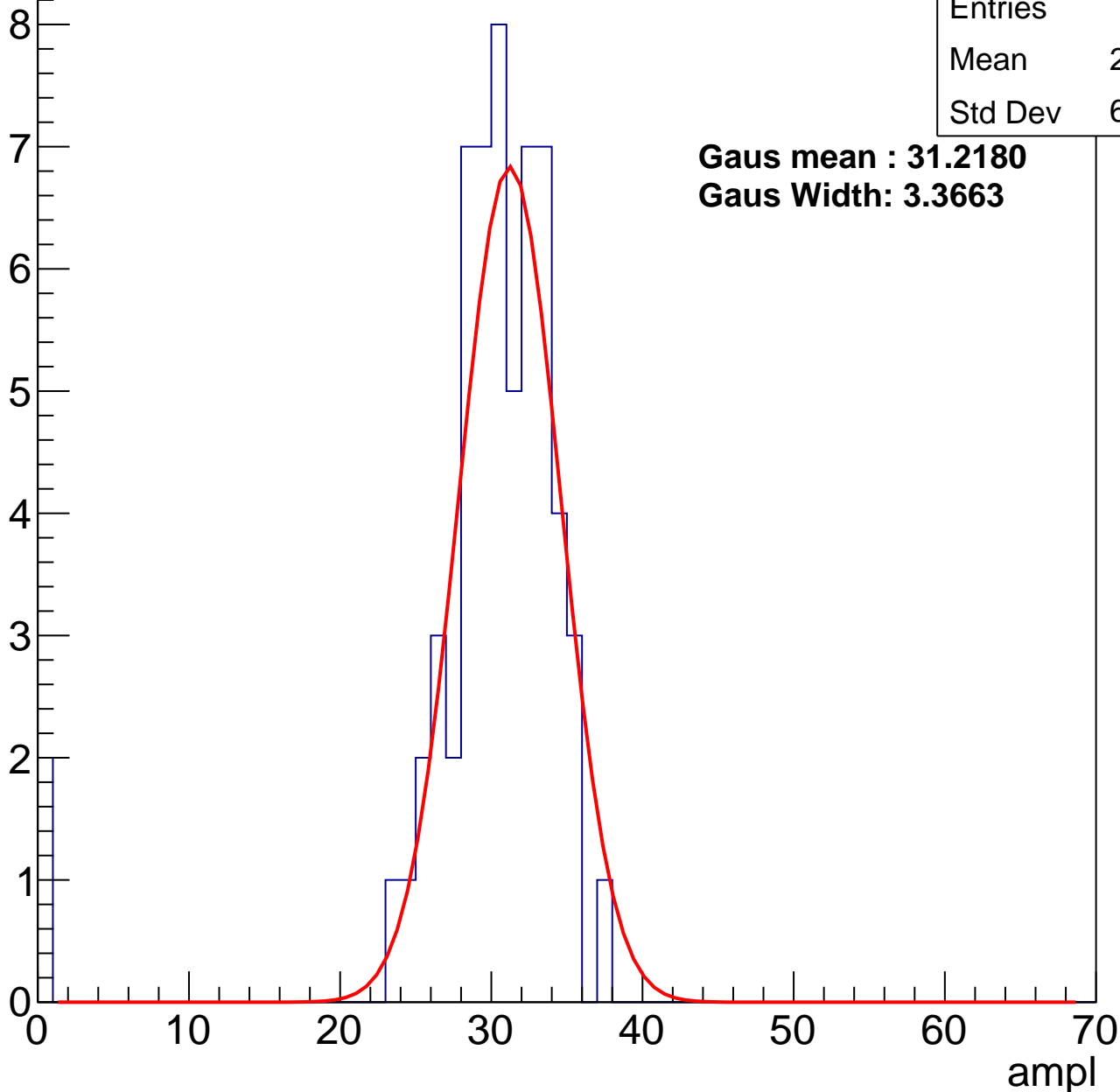
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.27
Std Dev	6.175

Gaus mean : 31.2180

Gaus Width: 3.3663



B0L002S, U7-ch94, adc1

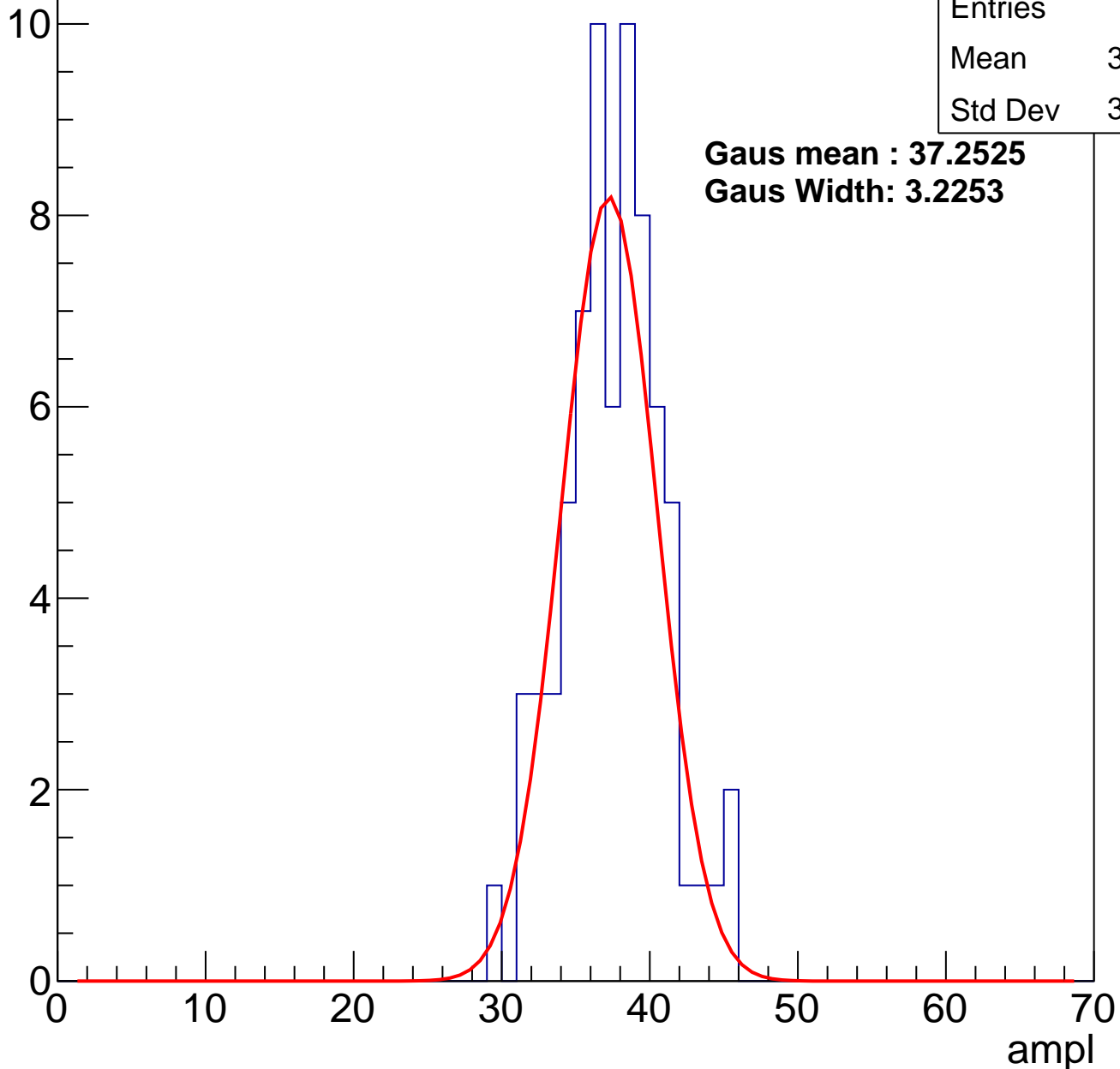
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	37.08
Std Dev	3.316

Gaus mean : 37.2525

Gaus Width: 3.2253

Entry



B0L002S, U7-ch94, adc2

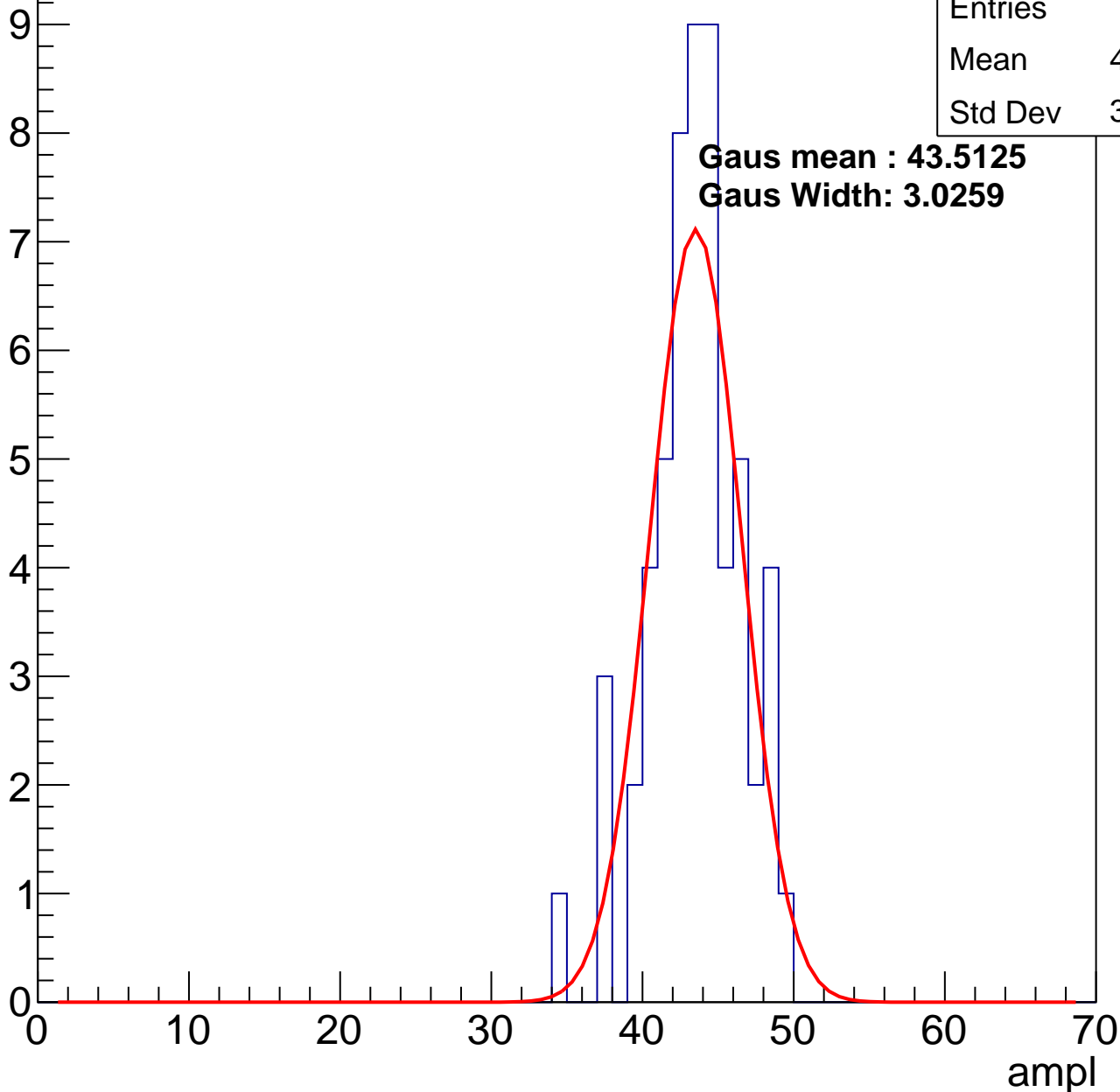
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.02
Std Dev	3.029

Gaus mean : 43.5125

Gaus Width: 3.0259

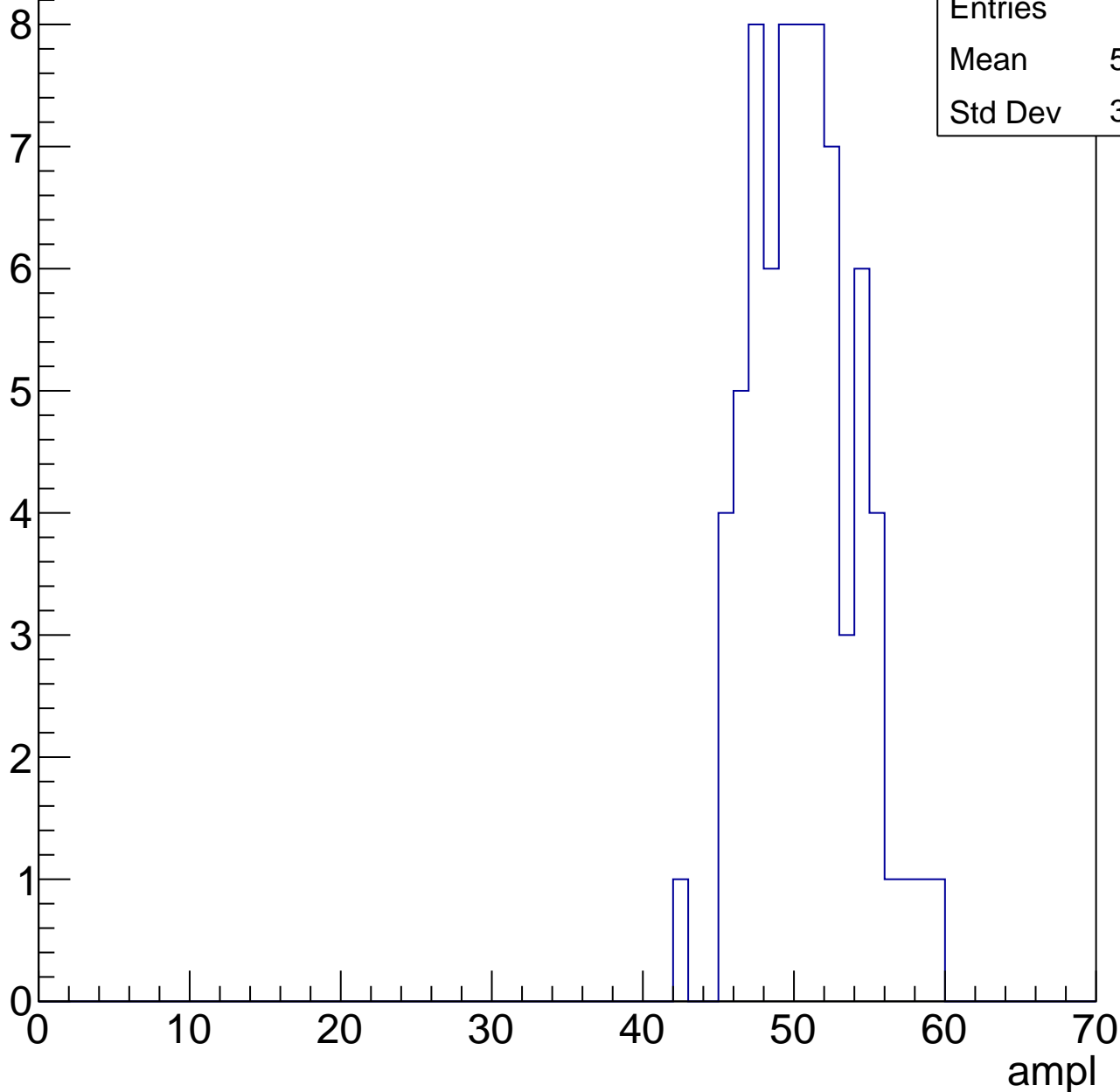


B0L002S, U7-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	50.18
Std Dev	3.405

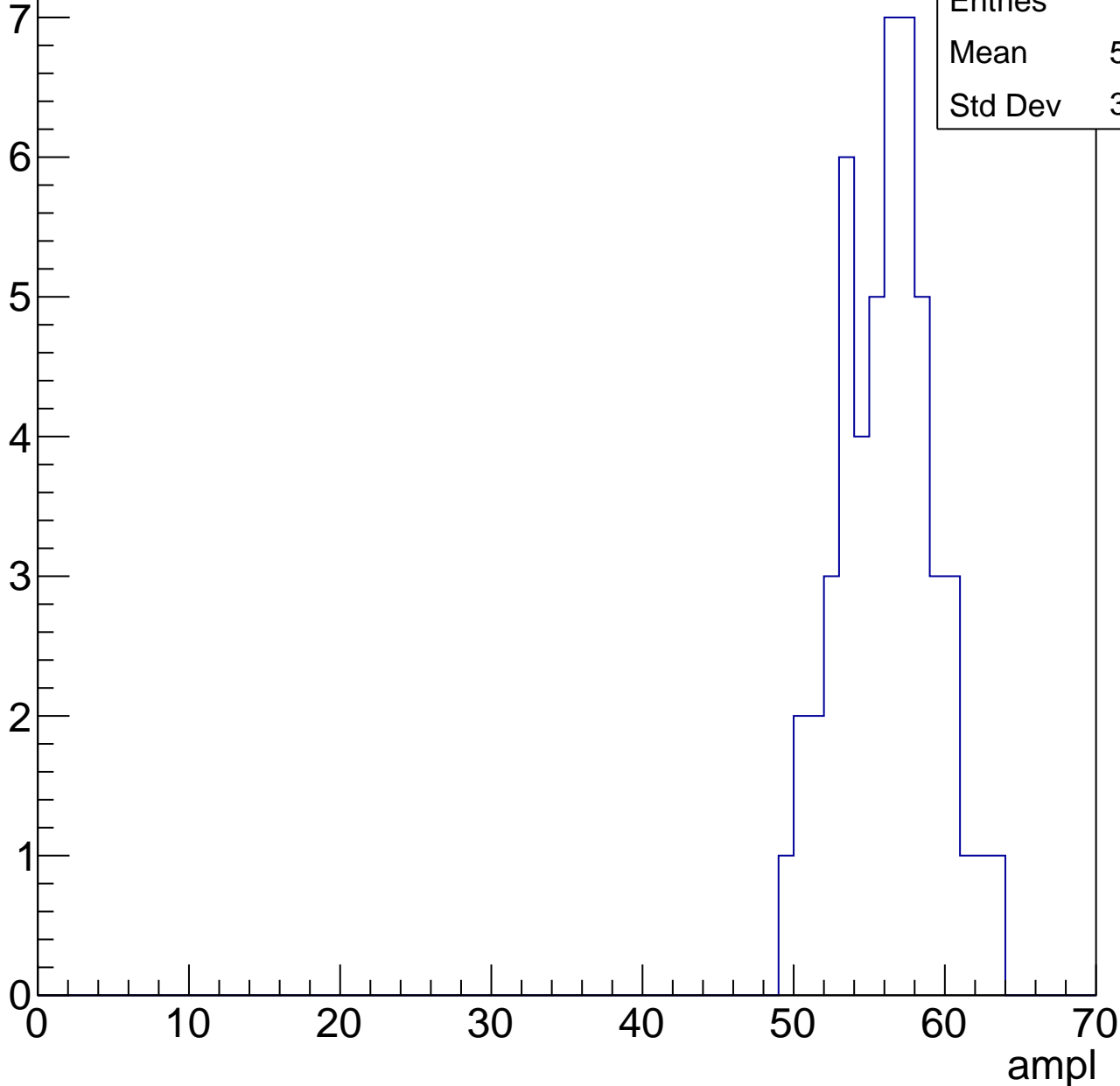


B0L002S, U7-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.69
Std Dev	3.134

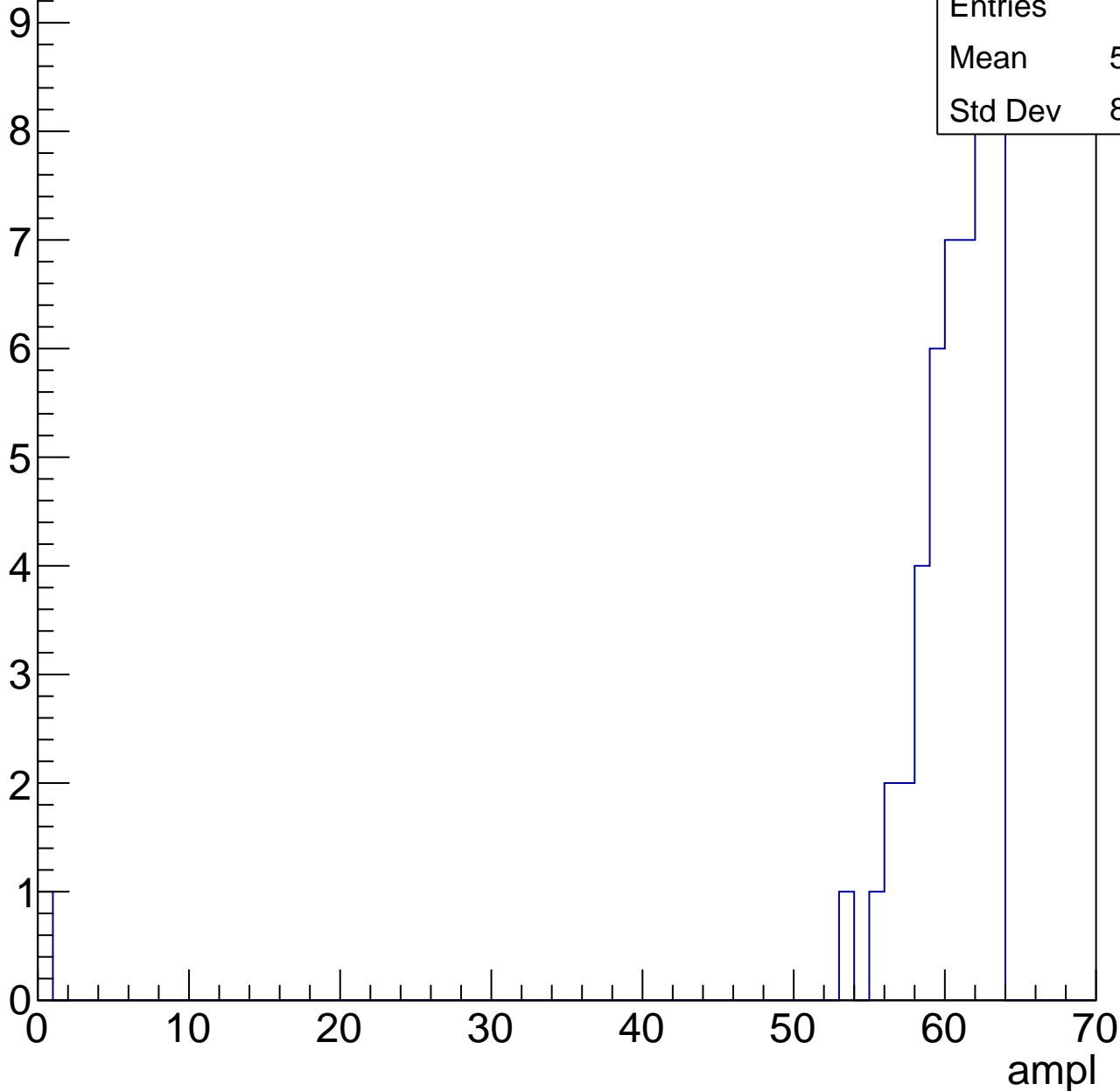


B0L002S, U7-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

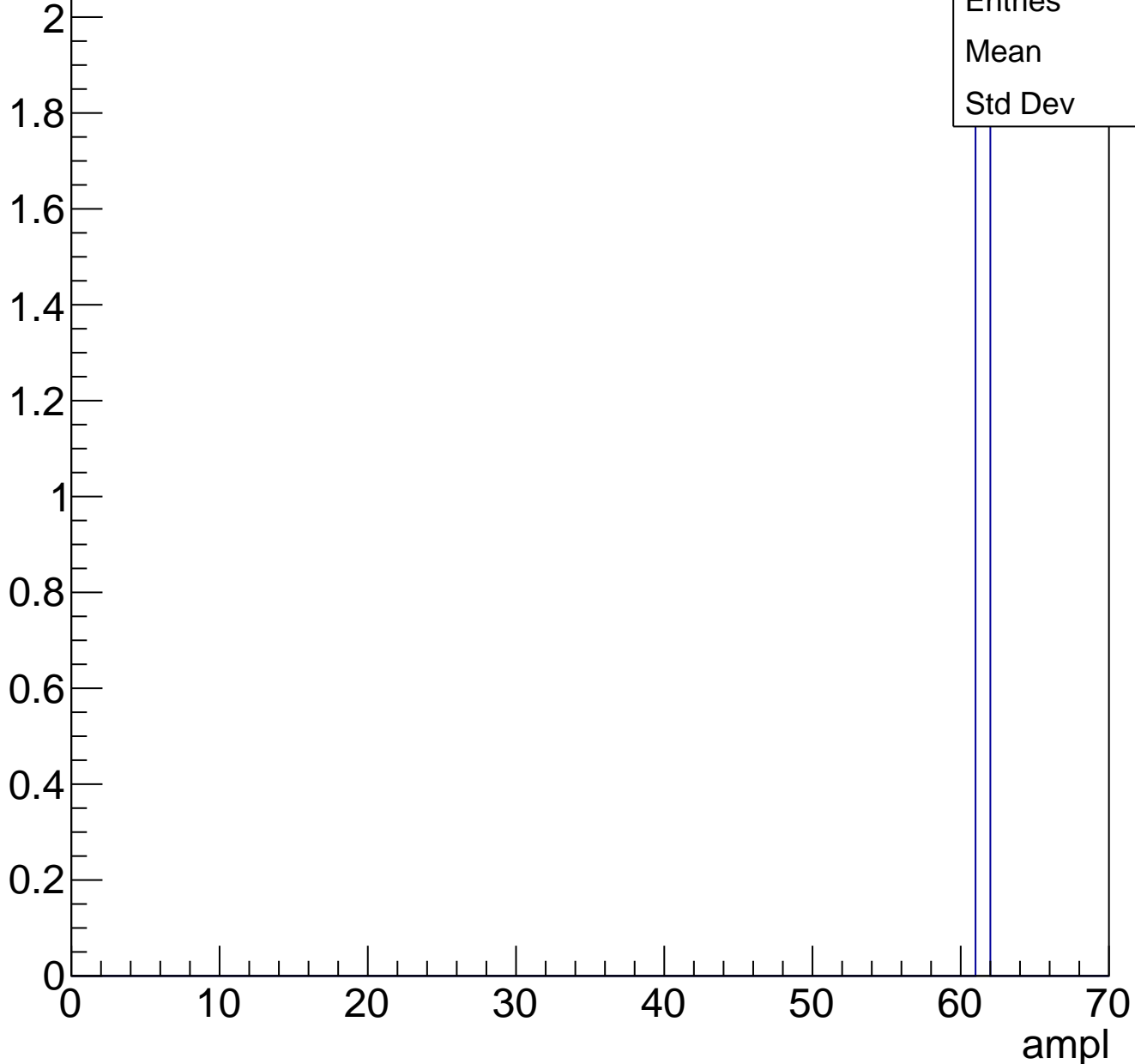
Entries	49
Mean	59.02
Std Dev	8.833



B0L002S, U7-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch95, adc0

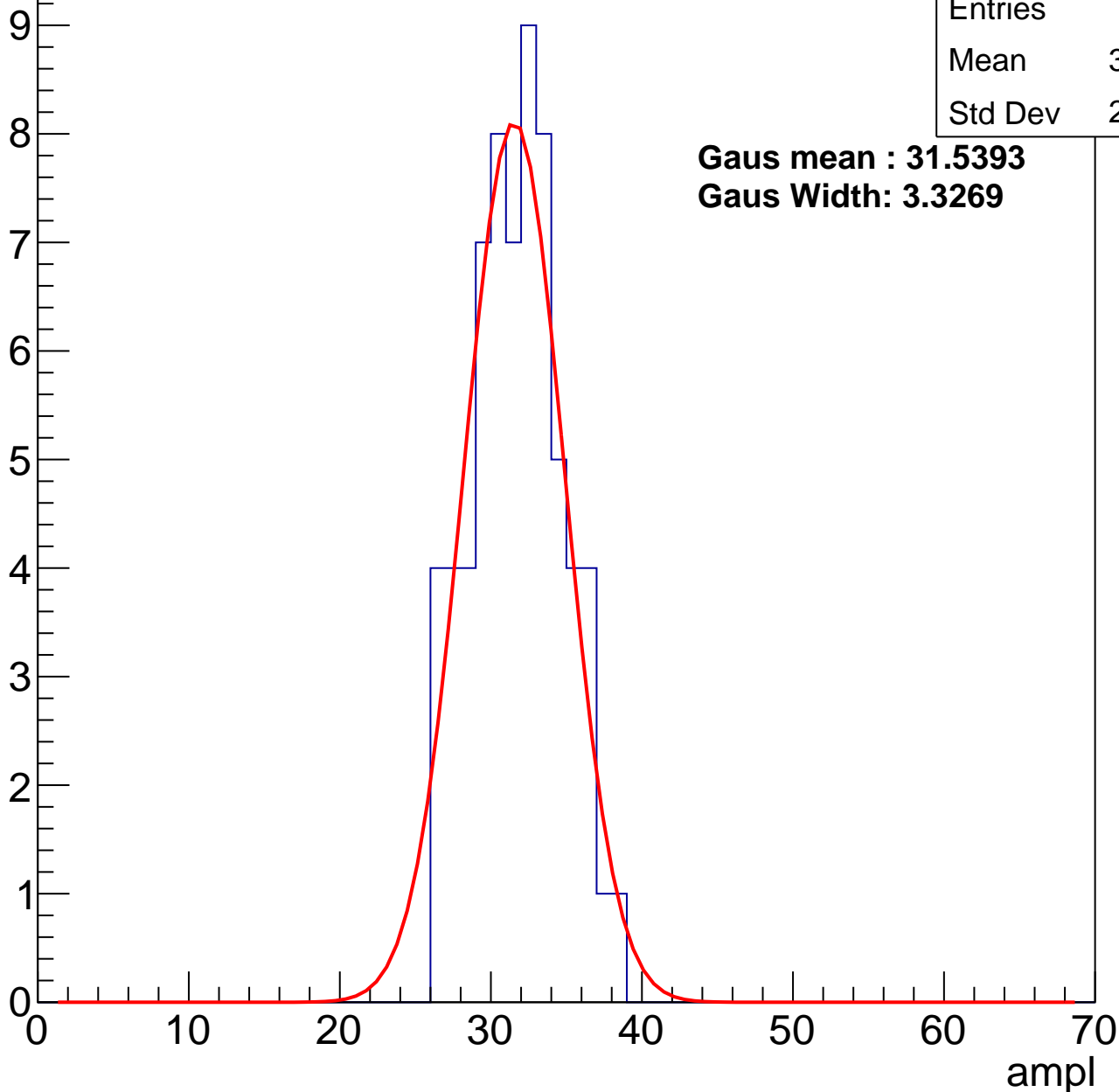
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.29
Std Dev	2.927

Gaus mean : 31.5393

Gaus Width: 3.3269



B0L002S, U7-ch95, adc1

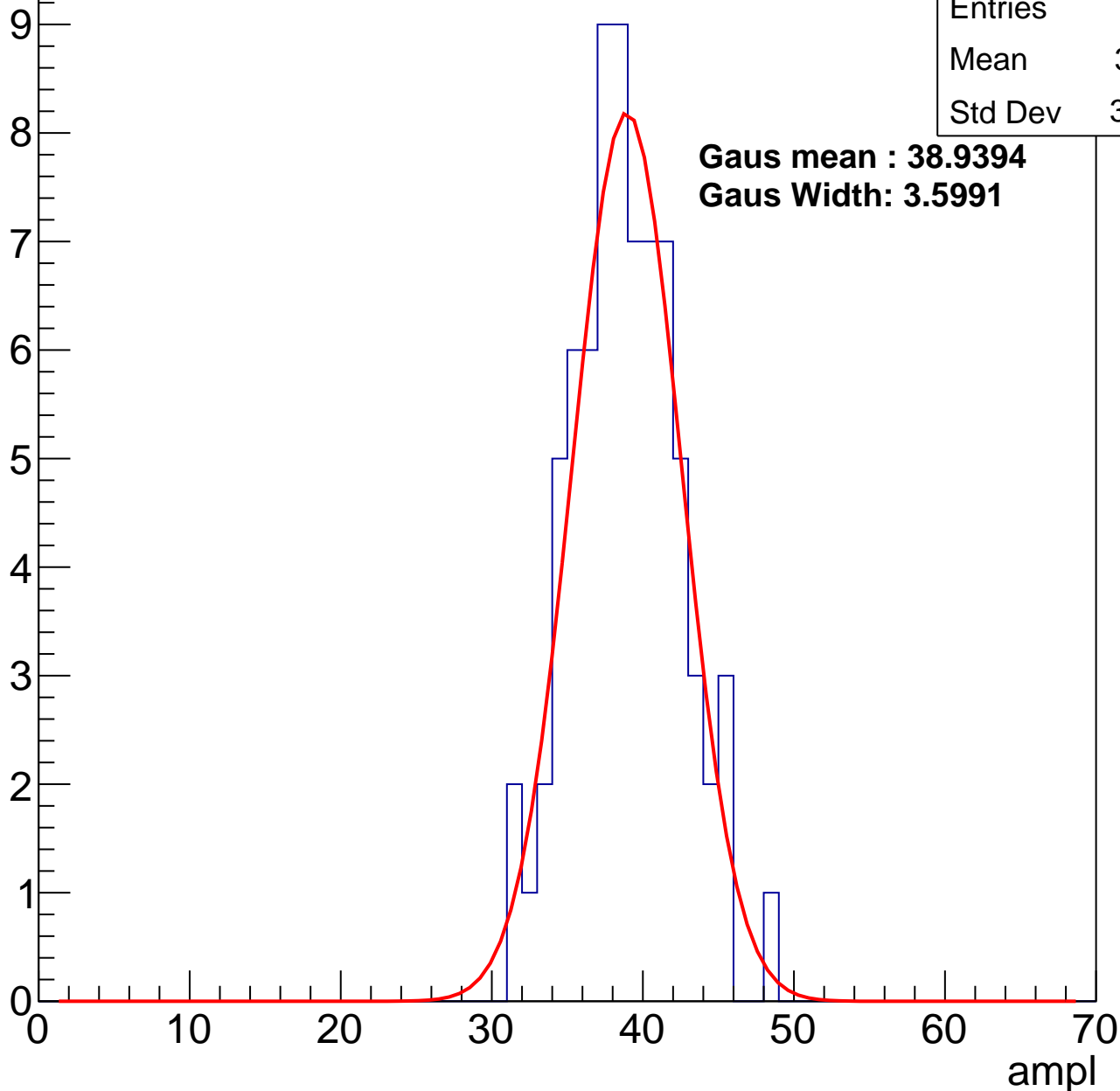
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.41
Std Dev	3.484

Gaus mean : 38.9394

Gaus Width: 3.5991



B0L002S, U7-ch95, adc2

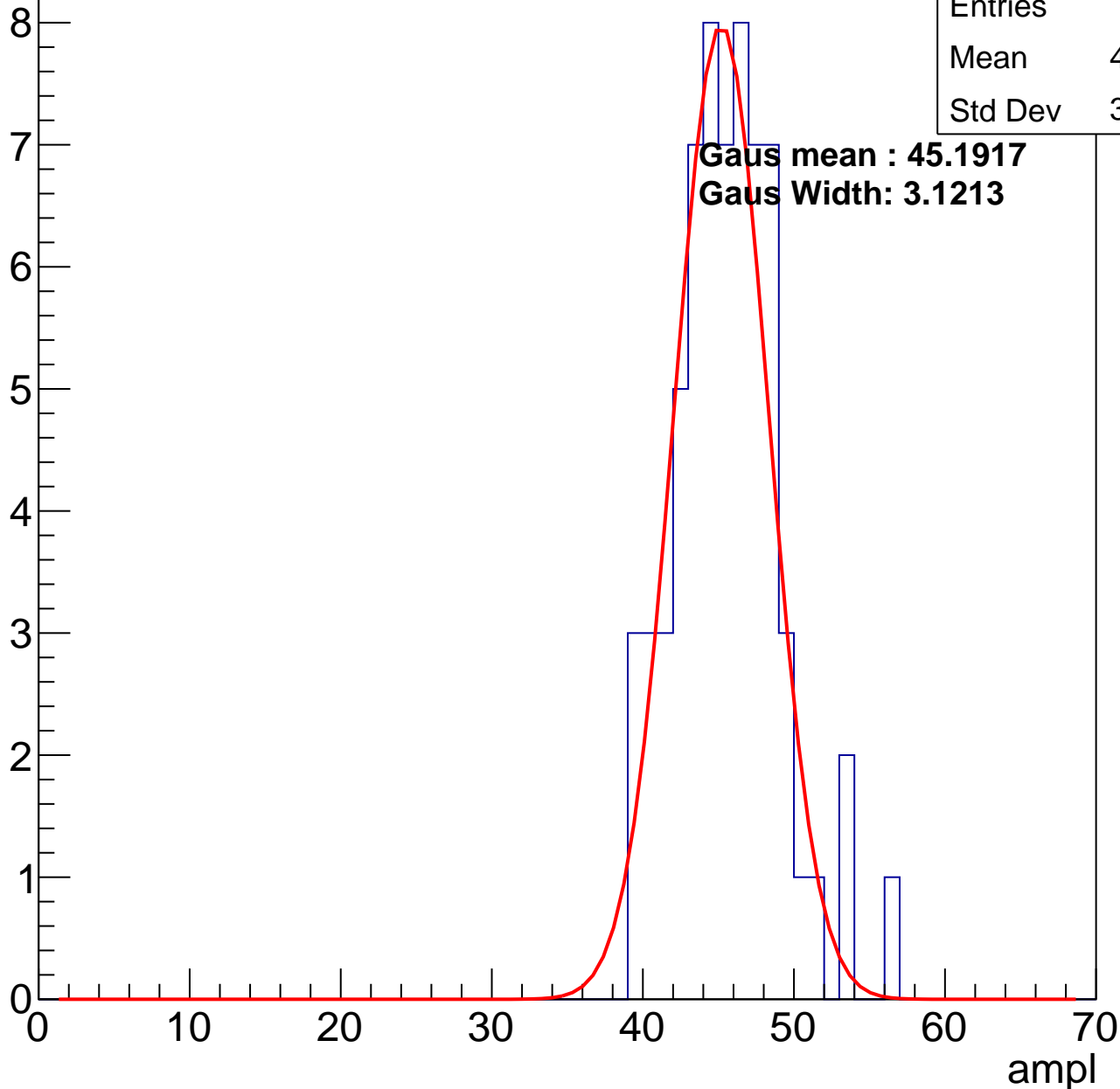
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.17
Std Dev	3.409

Gaus mean : 45.1917

Gaus Width: 3.1213



B0L002S, U7-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

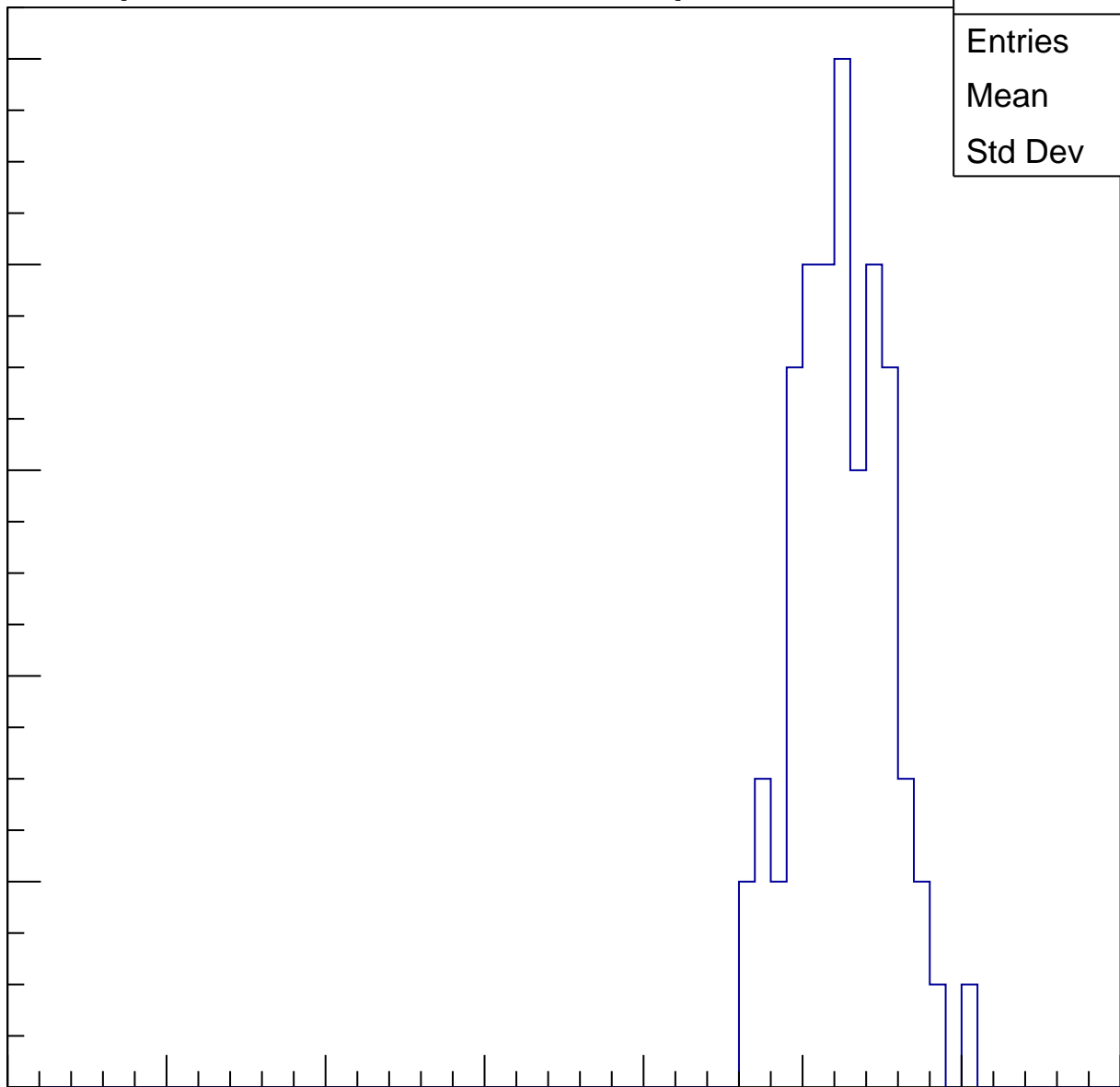
Entries	68
Mean	51.99
Std Dev	2.923

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

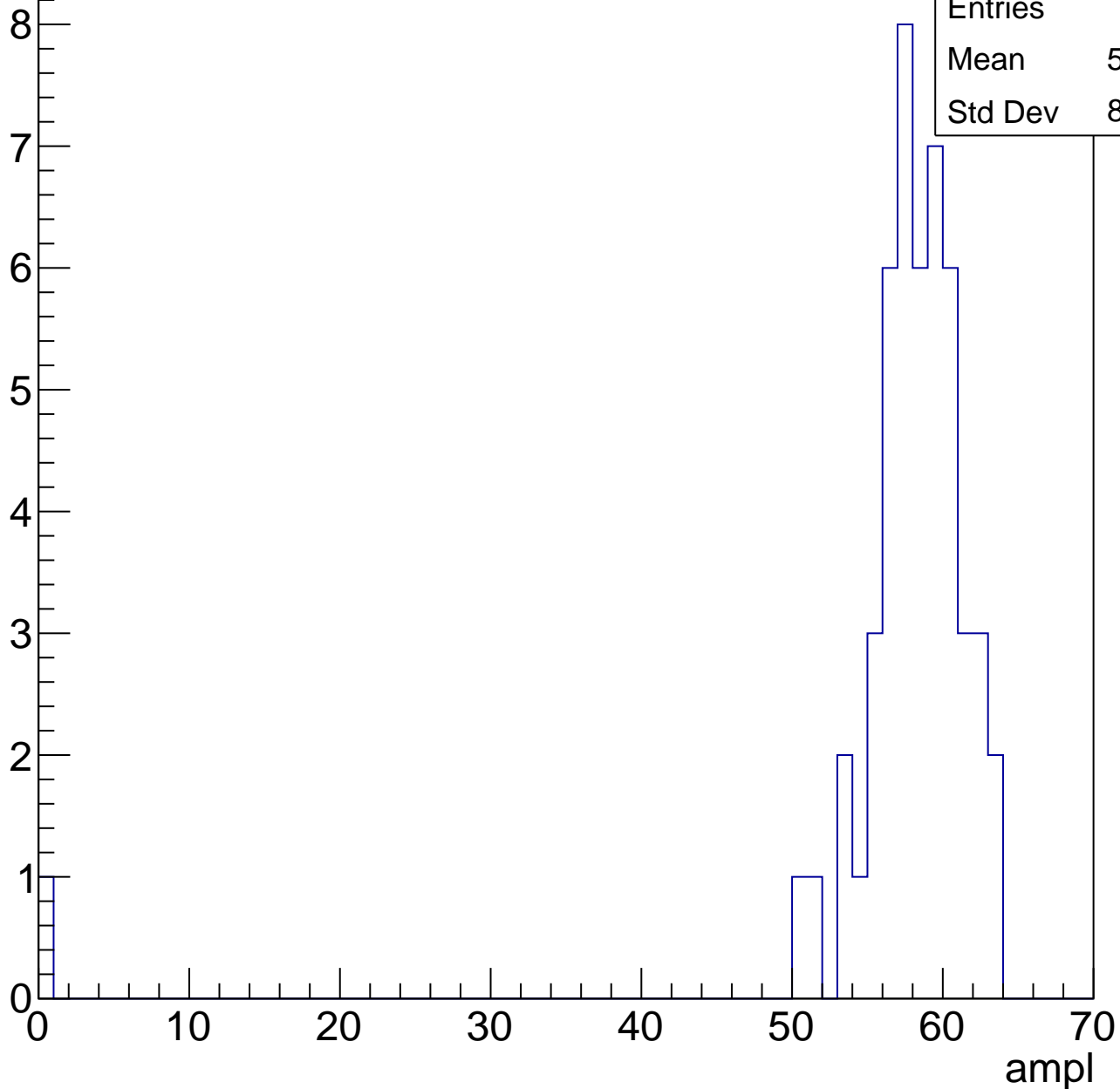
ampl



B0L002S, U7-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

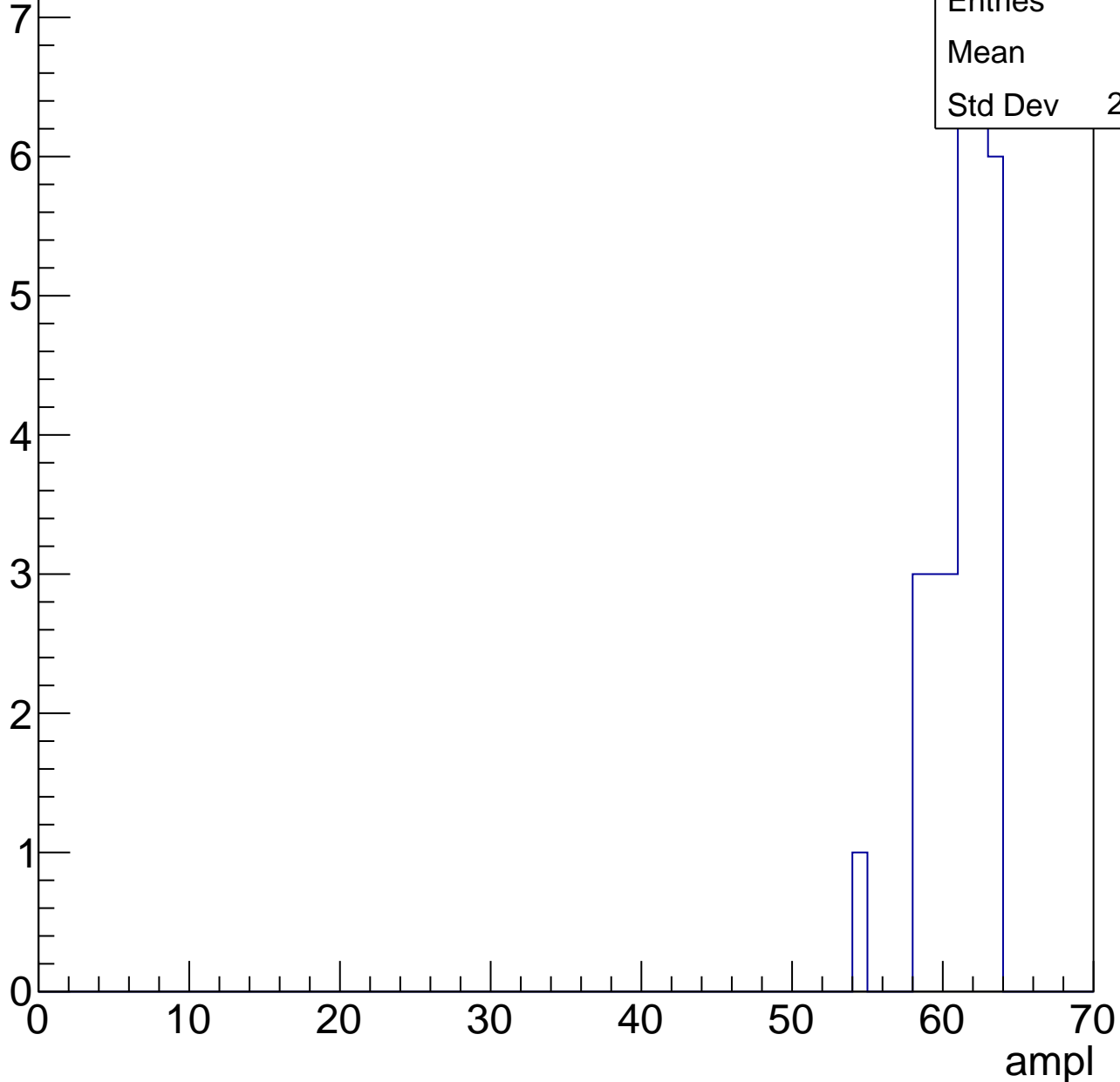
Entry



B0L002S, U7-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch96, adc0

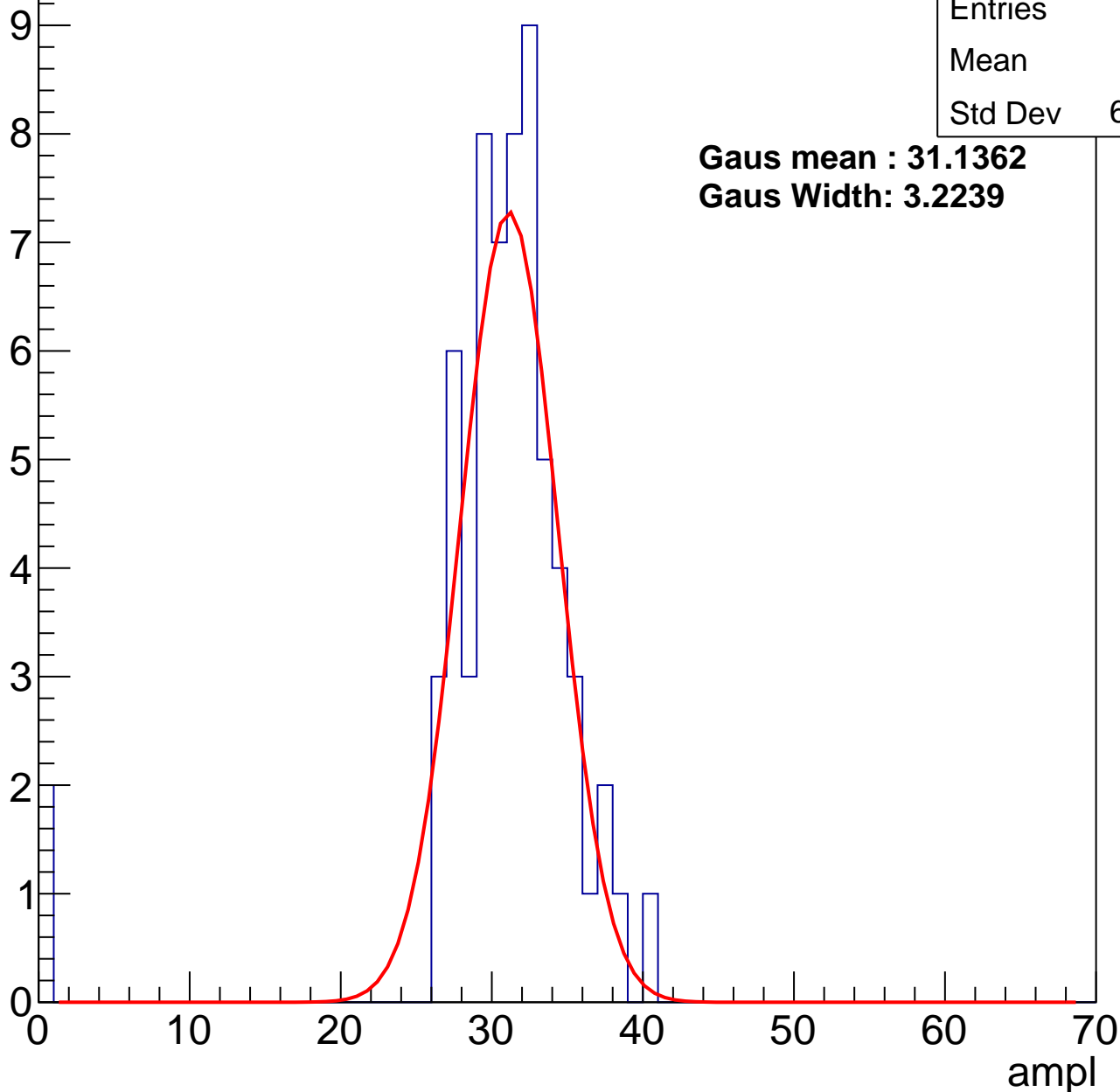
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.1
Std Dev	6.233

Gaus mean : 31.1362

Gaus Width: 3.2239



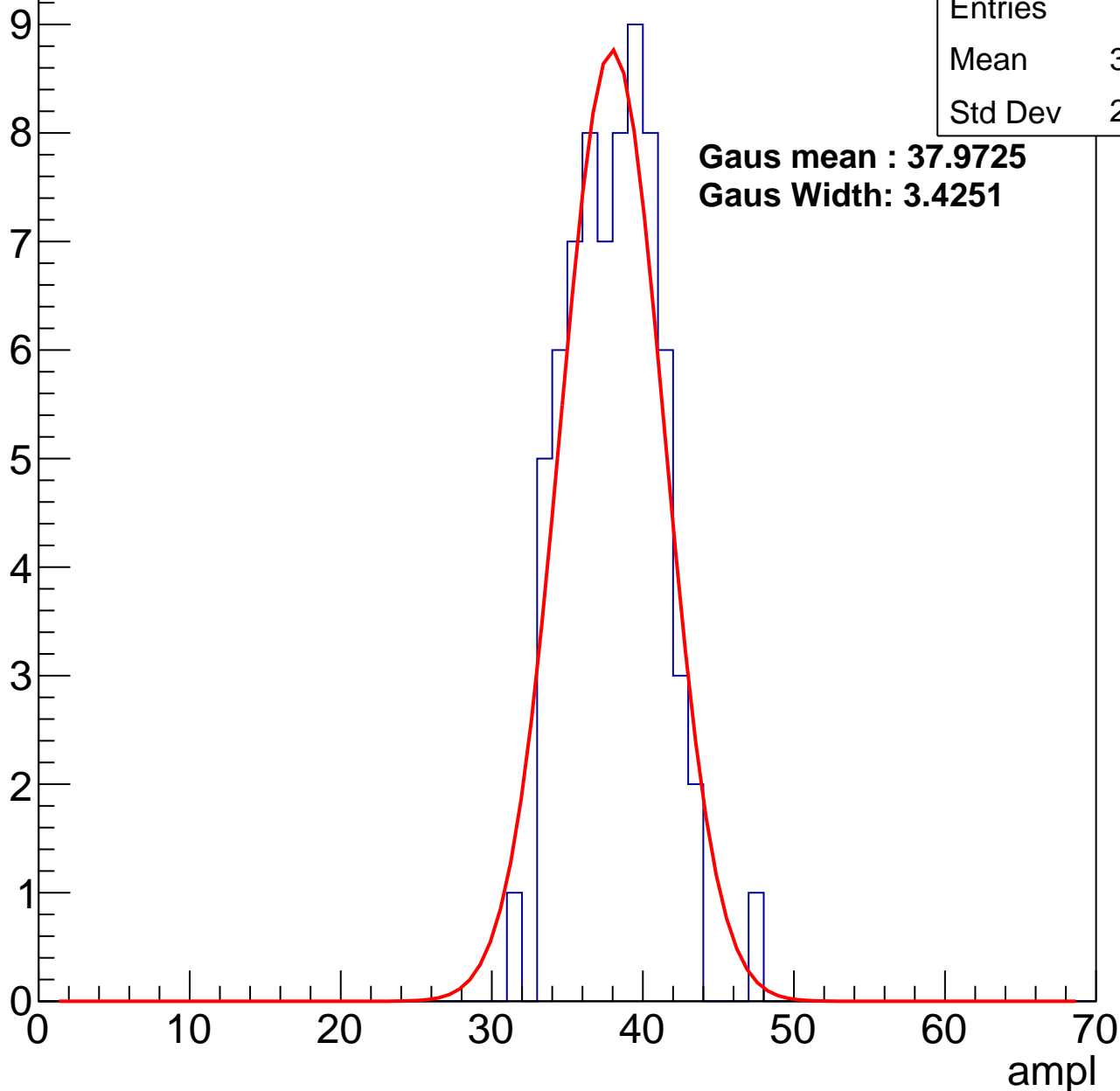
B0L002S, U7-ch96, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.63
Std Dev	2.994

Gaus mean : 37.9725
Gaus Width: 3.4251



B0L002S, U7-ch96, adc2

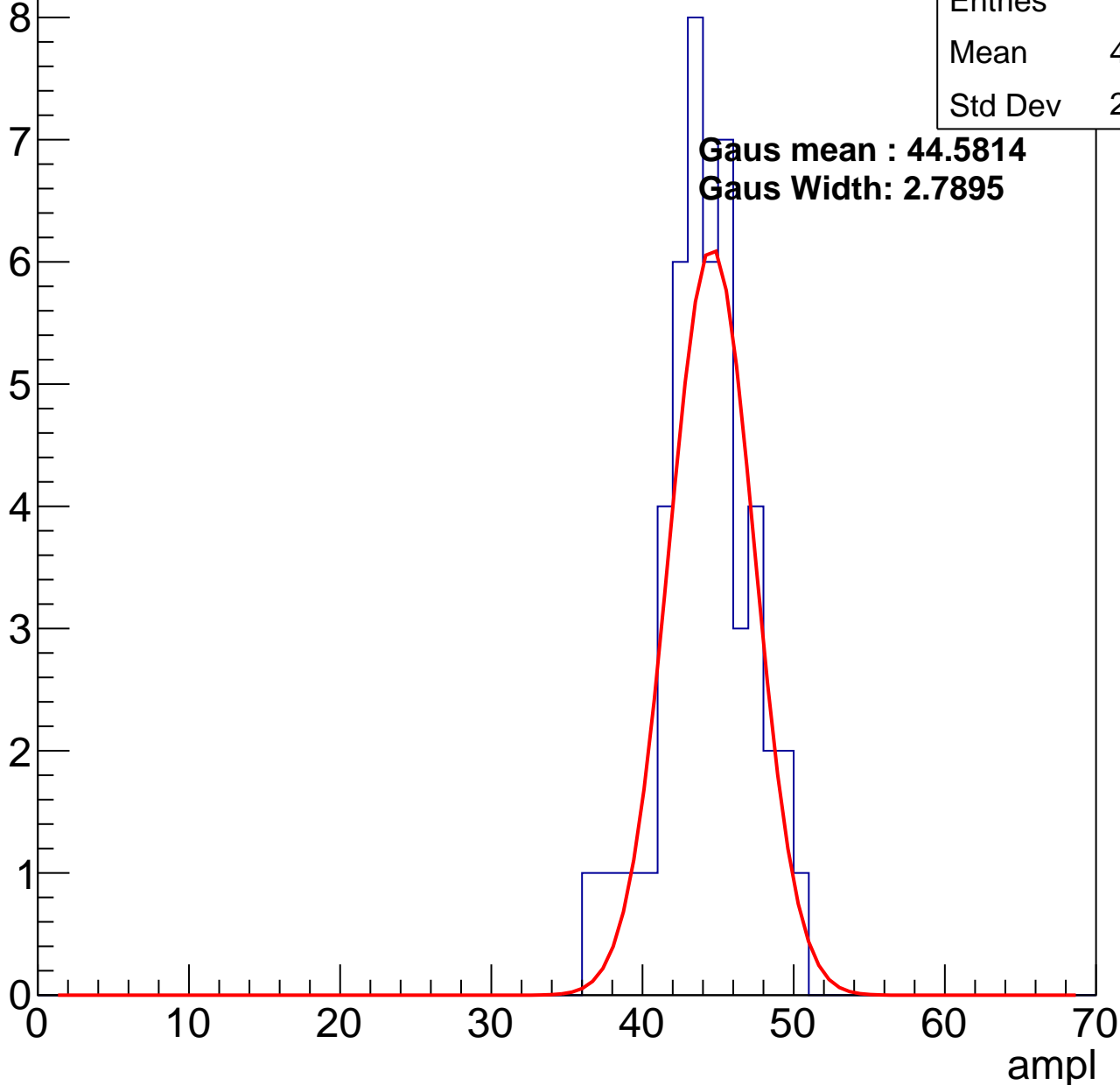
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.73
Std Dev	2.984

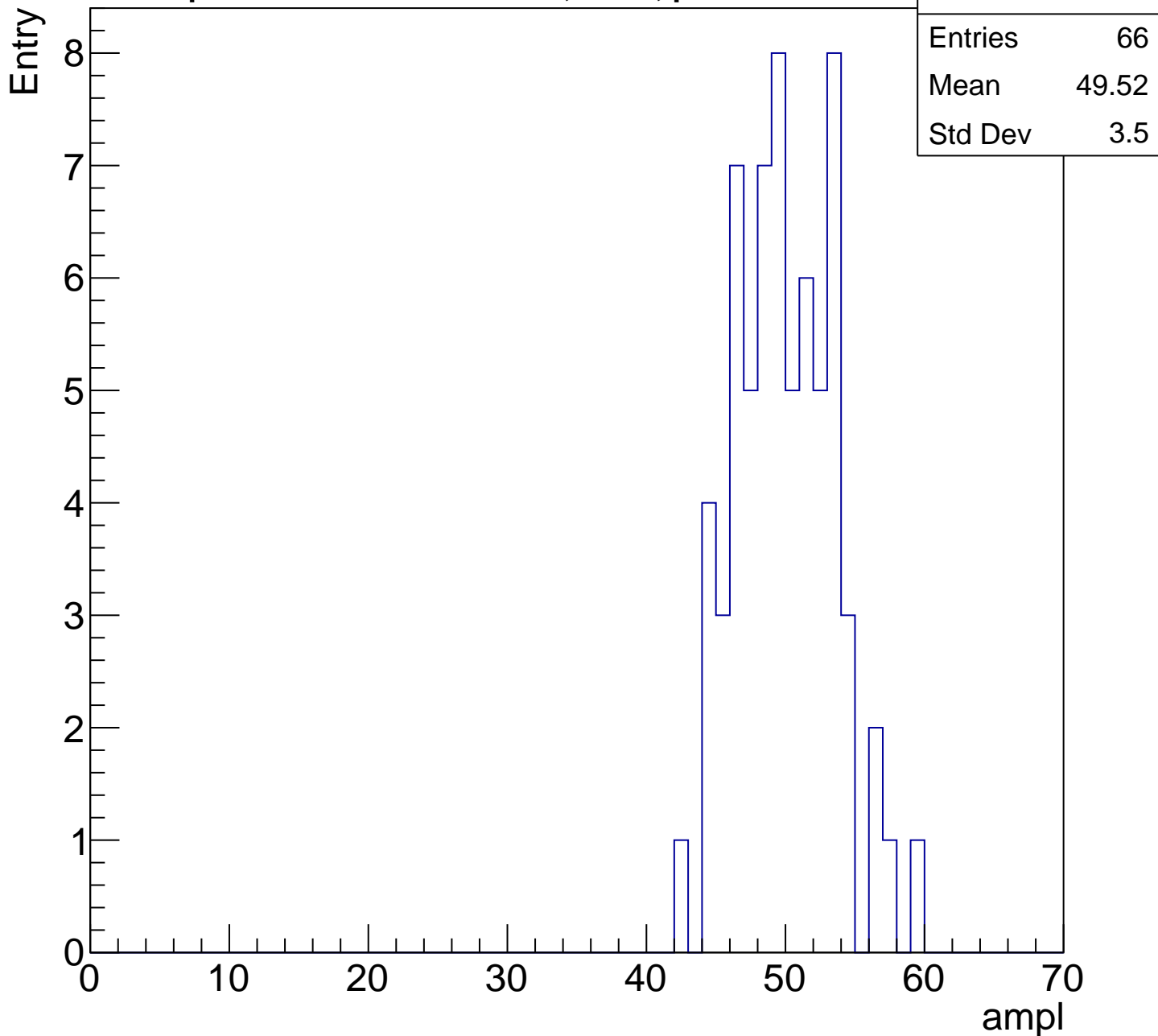
Gaus mean : 44.5814

Gaus Width: 2.7895



B0L002S, U7-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

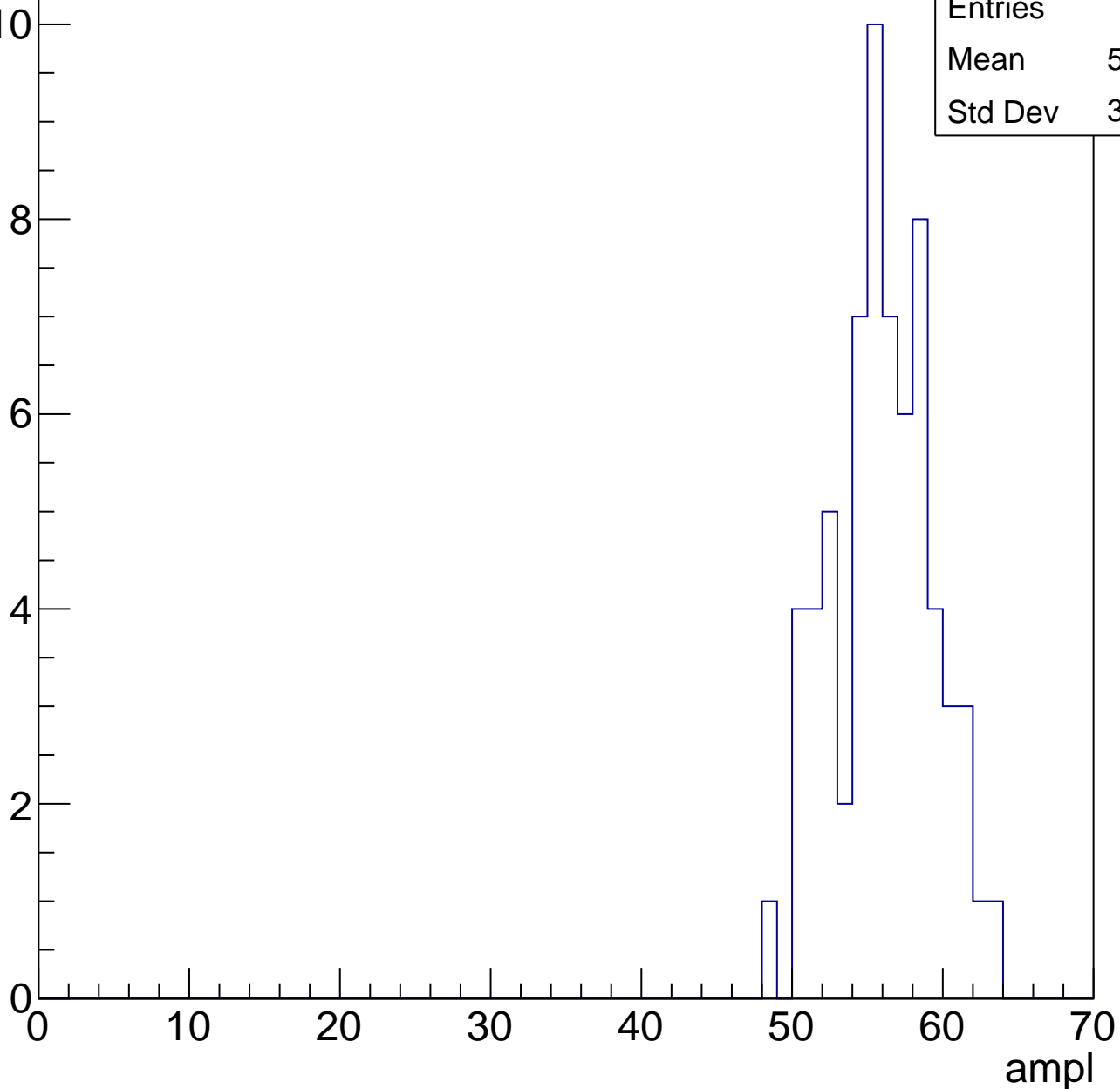


B0L002S, U7-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.58
Std Dev	3.289

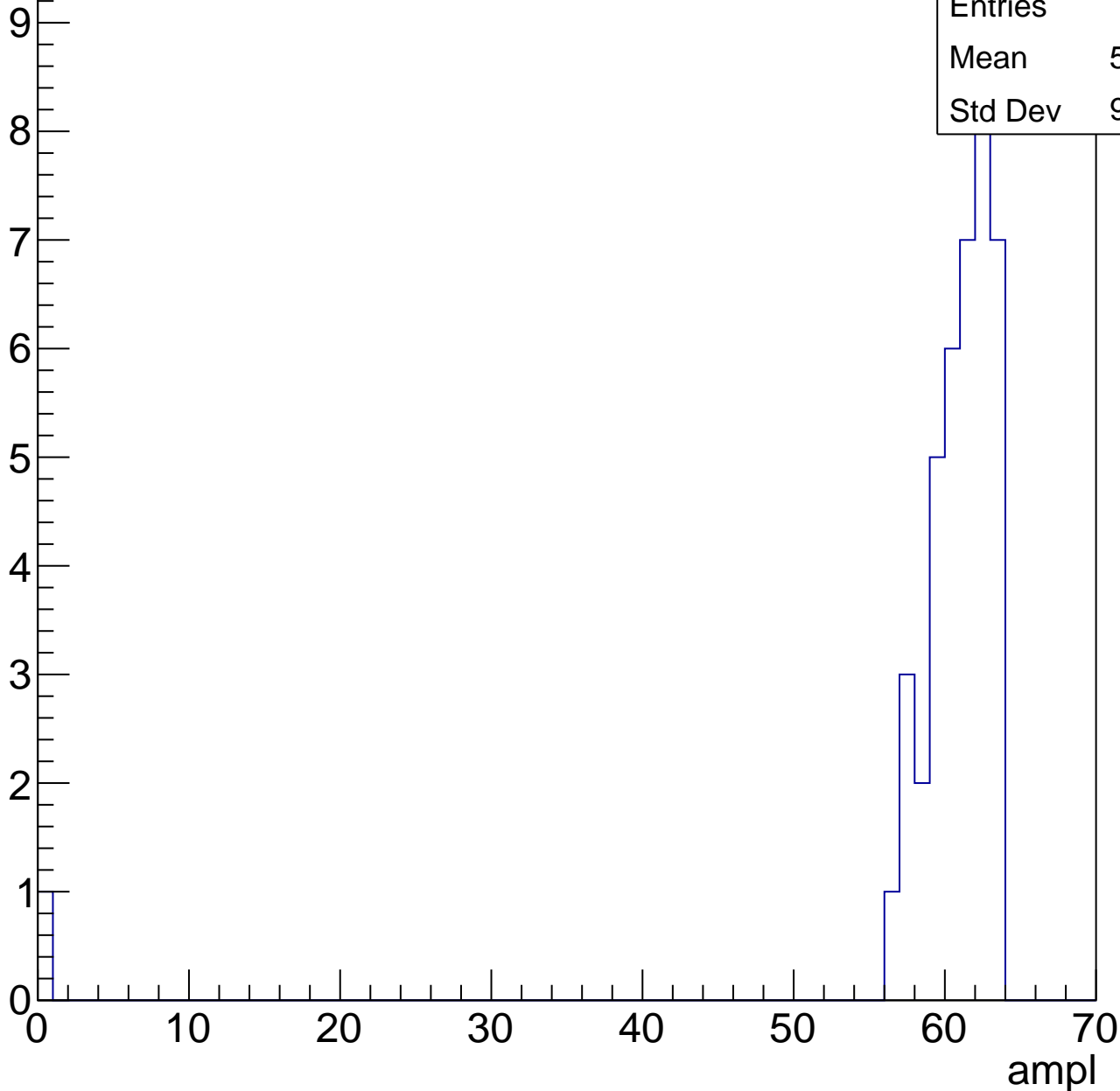


B0L002S, U7-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

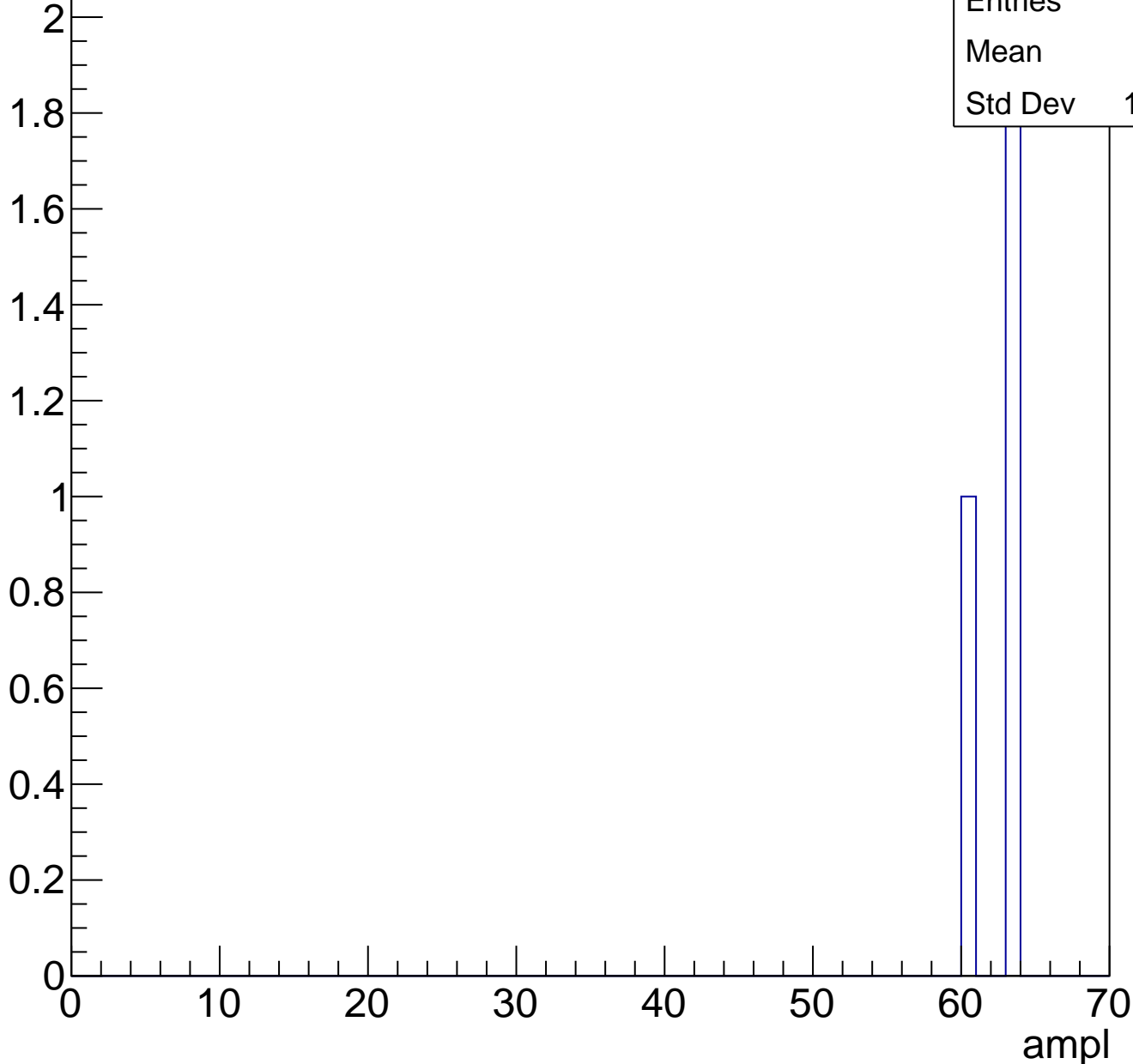
Entries	41
Mean	59.12
Std Dev	9.539



B0L002S, U7-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch97, adc0

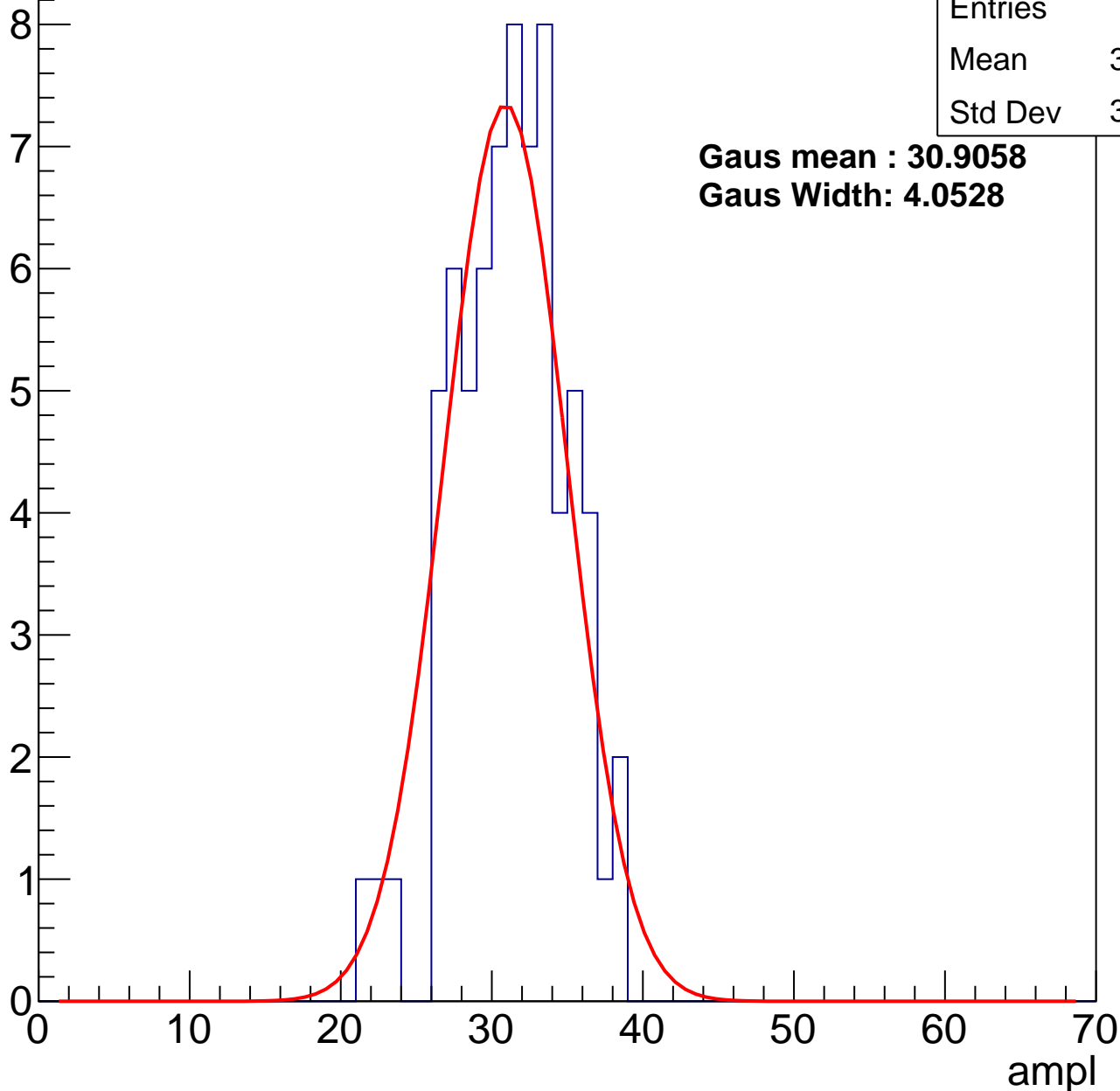
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.79
Std Dev	3.615

Gaus mean : 30.9058

Gaus Width: 4.0528



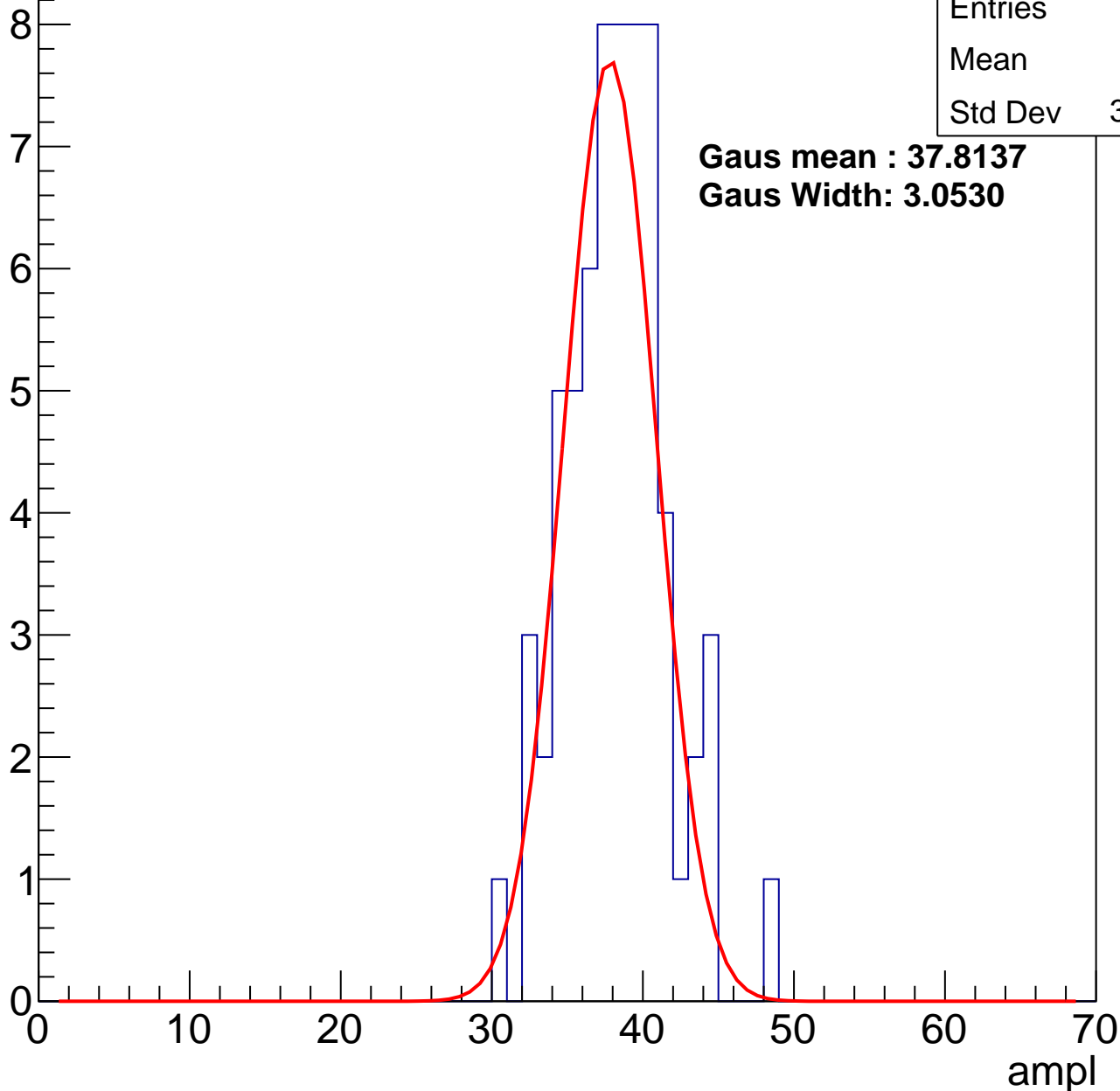
B0L002S, U7-ch97, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.8
Std Dev	3.343

Gaus mean : 37.8137
Gaus Width: 3.0530



B0L002S, U7-ch97, adc2

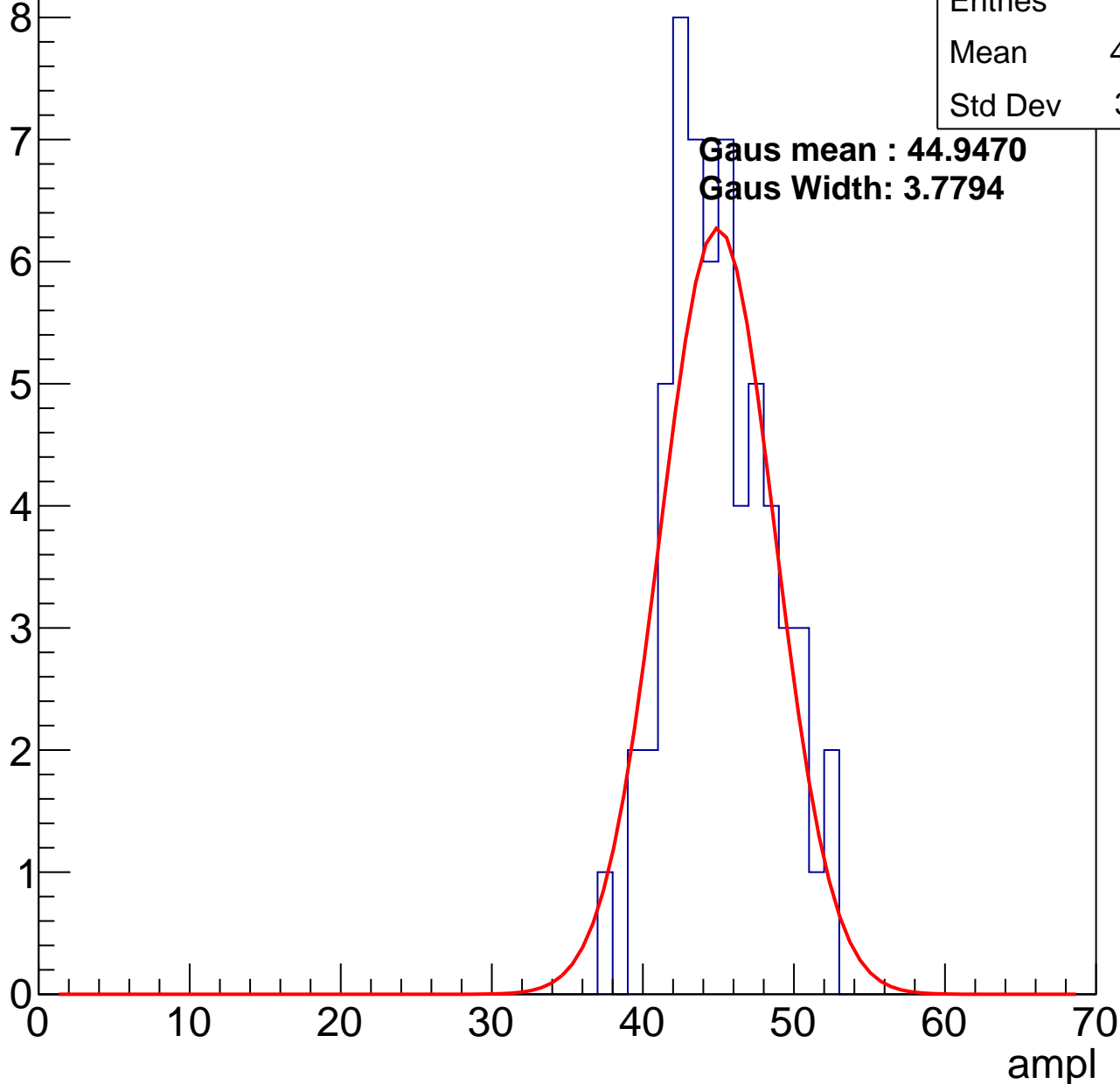
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.65
Std Dev	3.371

Gaus mean : 44.9470

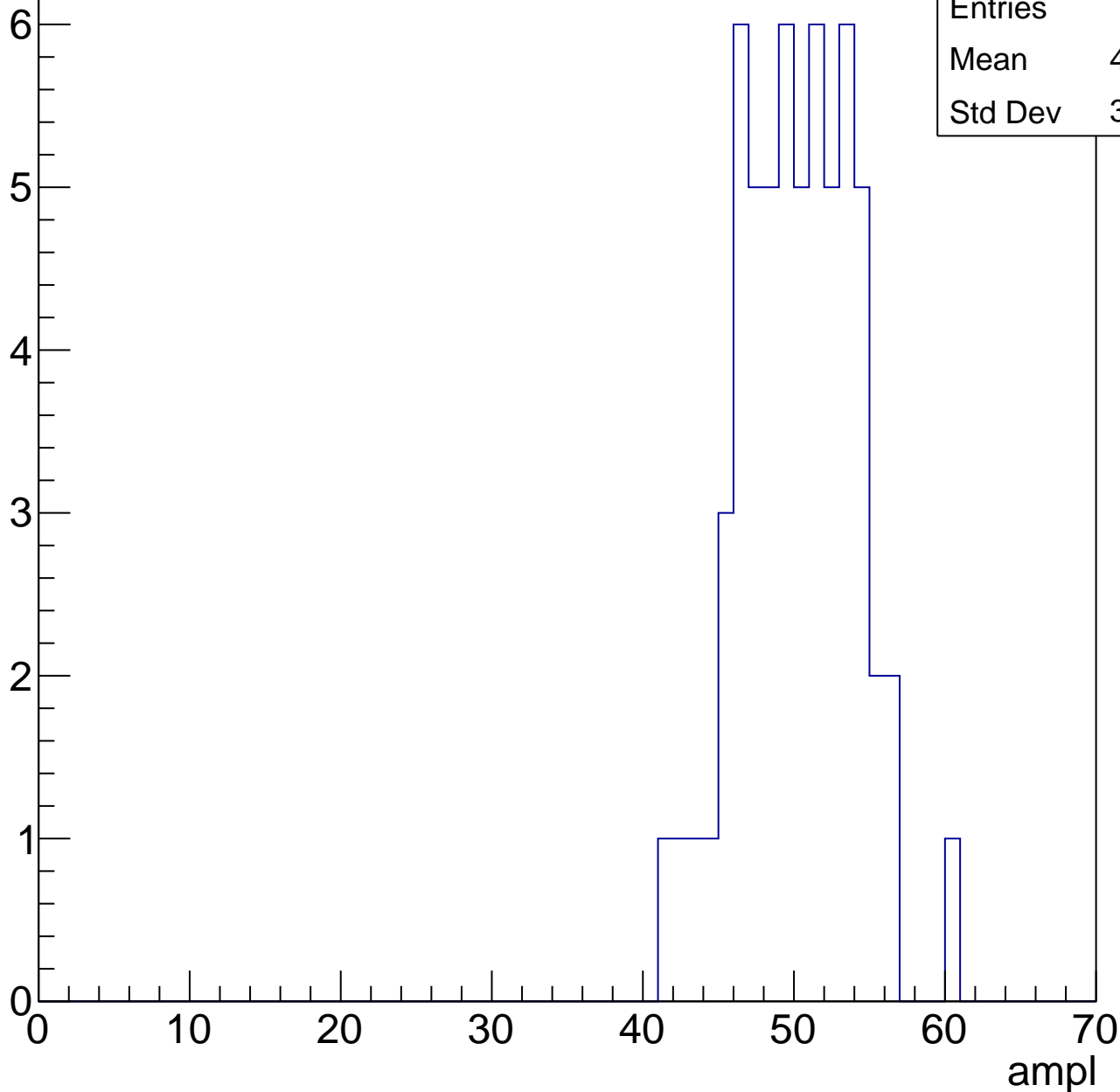
Gaus Width: 3.7794



B0L002S, U7-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

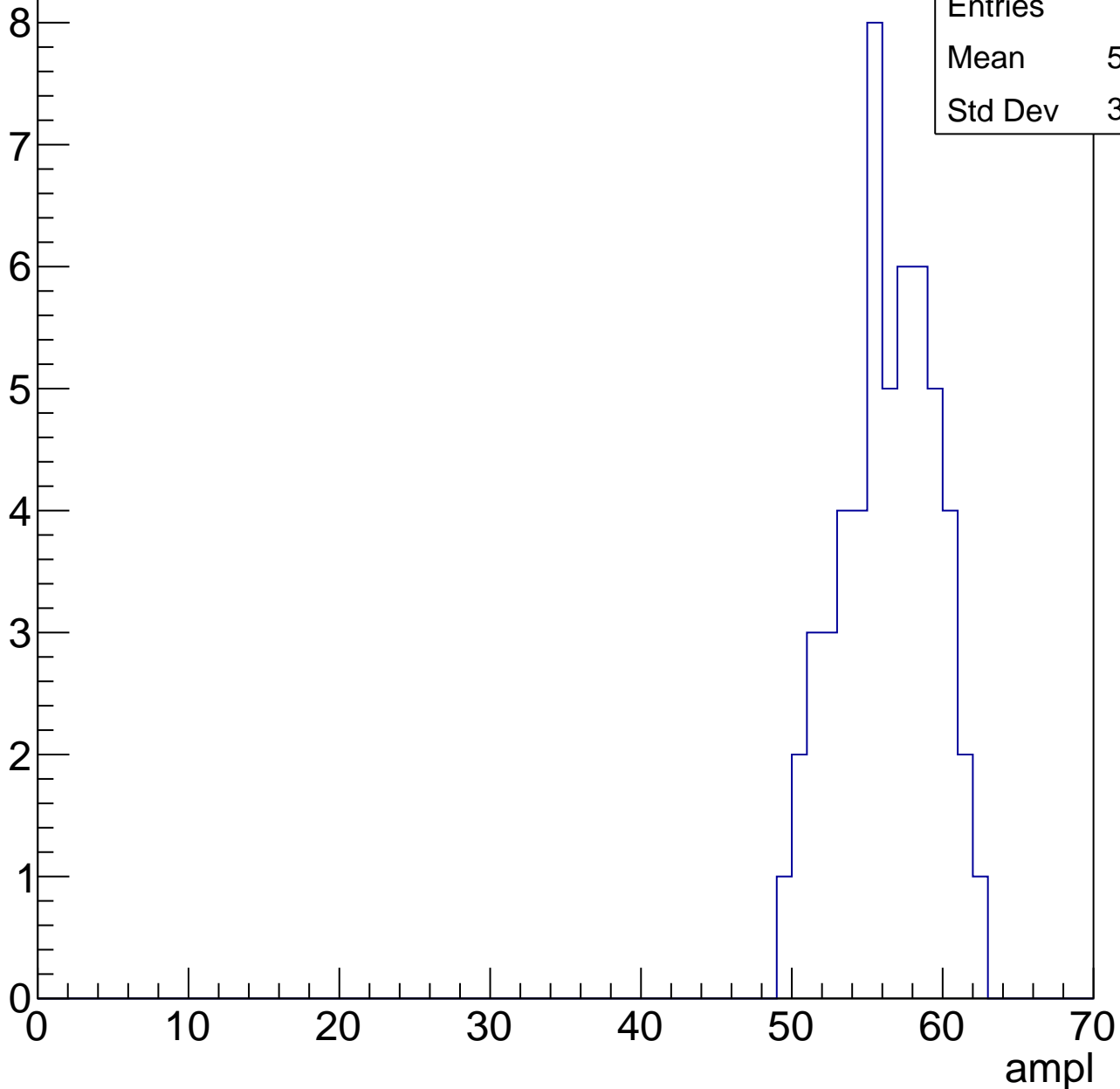


B0L002S, U7-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	55.83
Std Dev	3.125

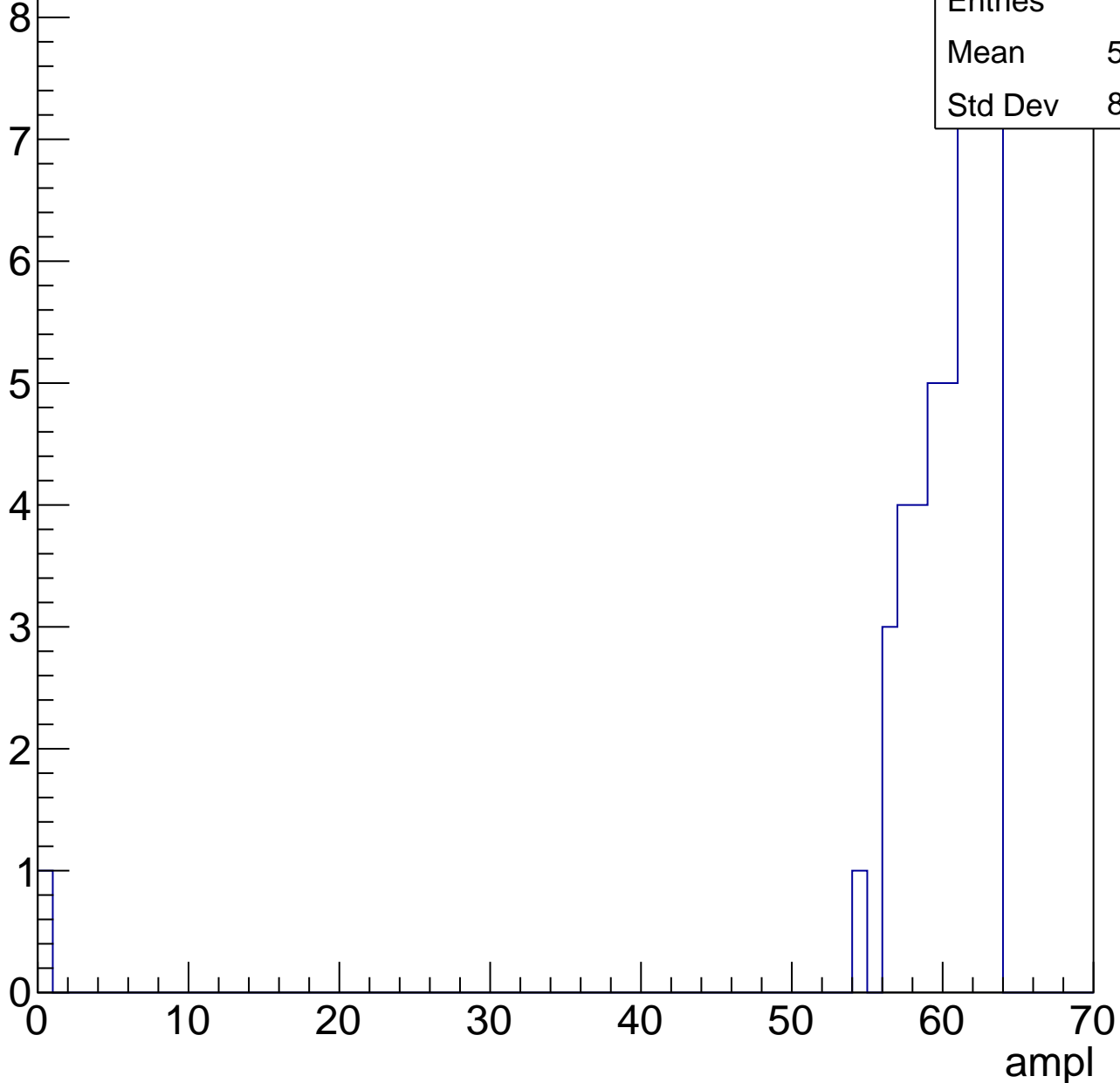


B0L002S, U7-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.83
Std Dev	8.978



B0L002S, U7-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

B0L002S, U7-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch98, adc0

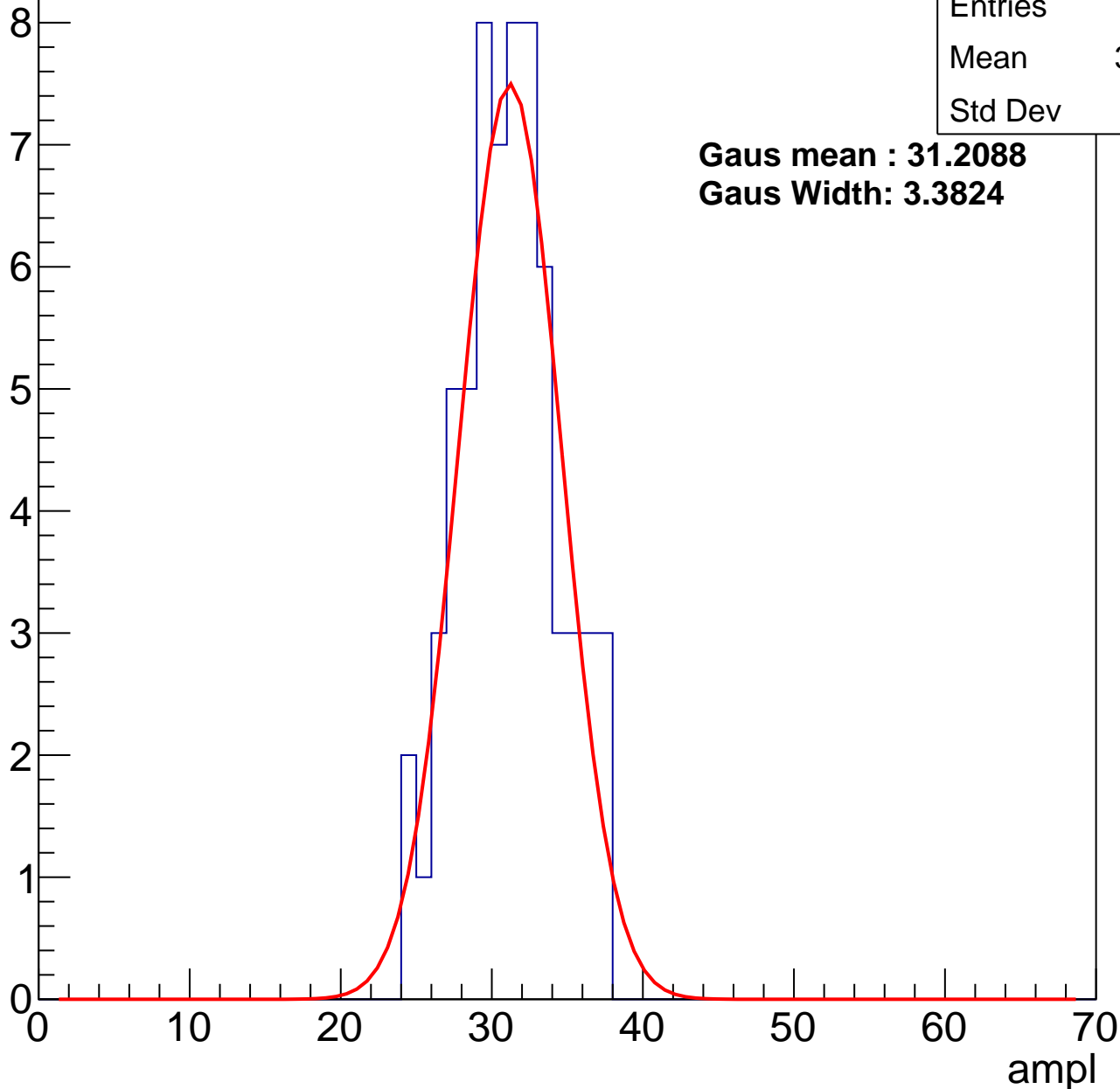
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.71
Std Dev	3.18

Gaus mean : 31.2088

Gaus Width: 3.3824



B0L002S, U7-ch98, adc1

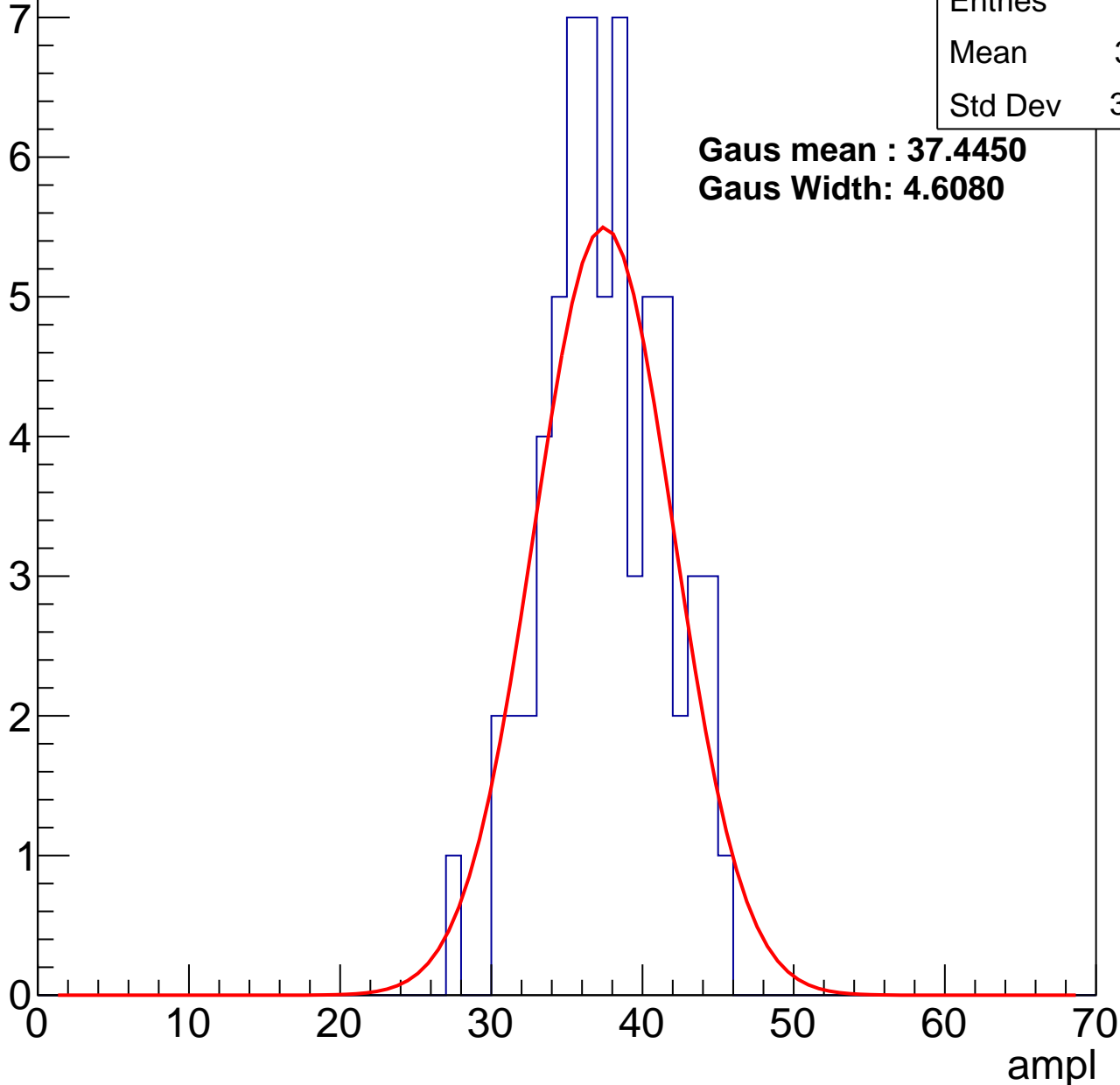
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.11
Std Dev	3.906

Gaus mean : 37.4450

Gaus Width: 4.6080



B0L002S, U7-ch98, adc2

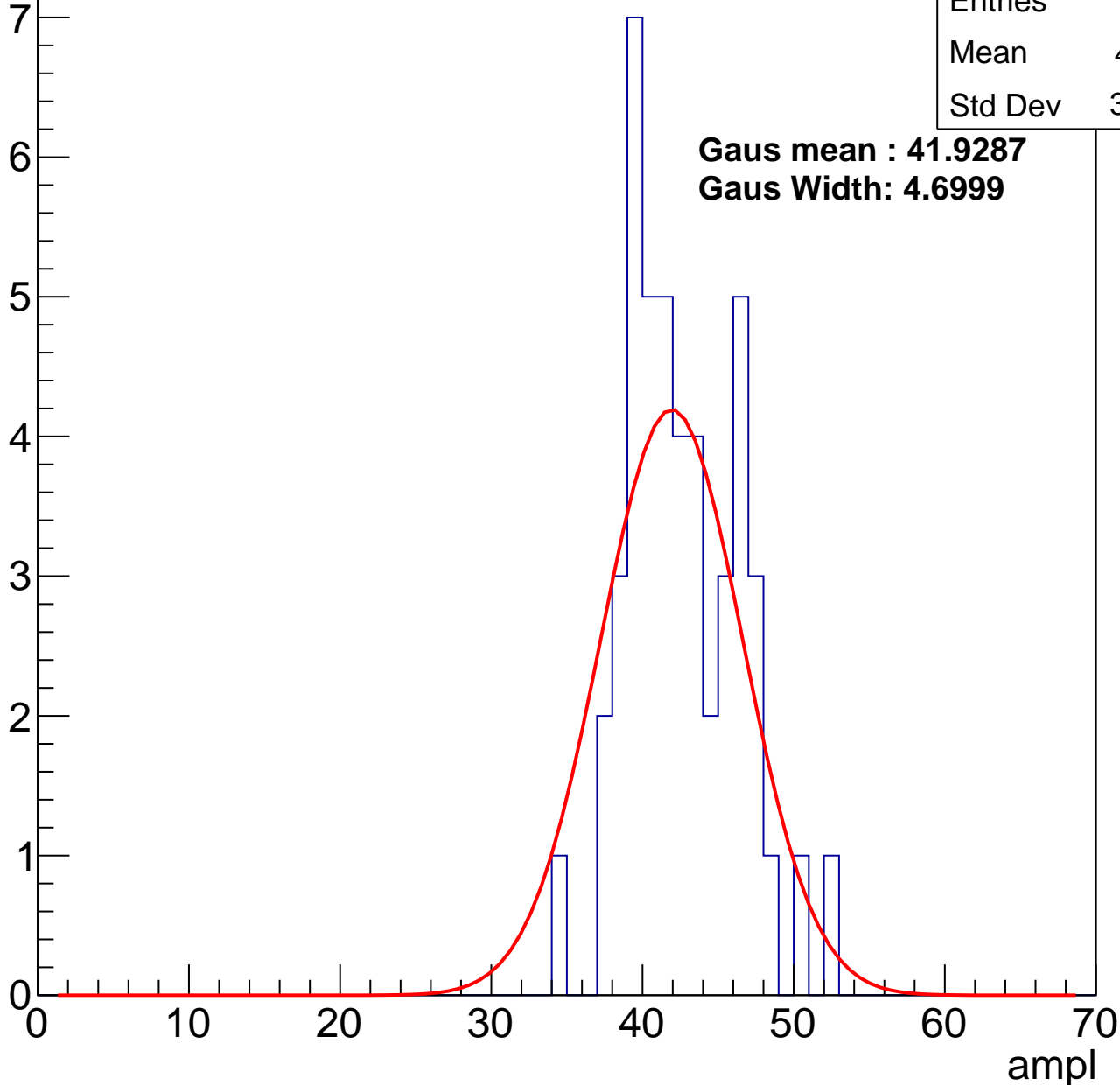
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	42.21
Std Dev	3.696

Gaus mean : 41.9287

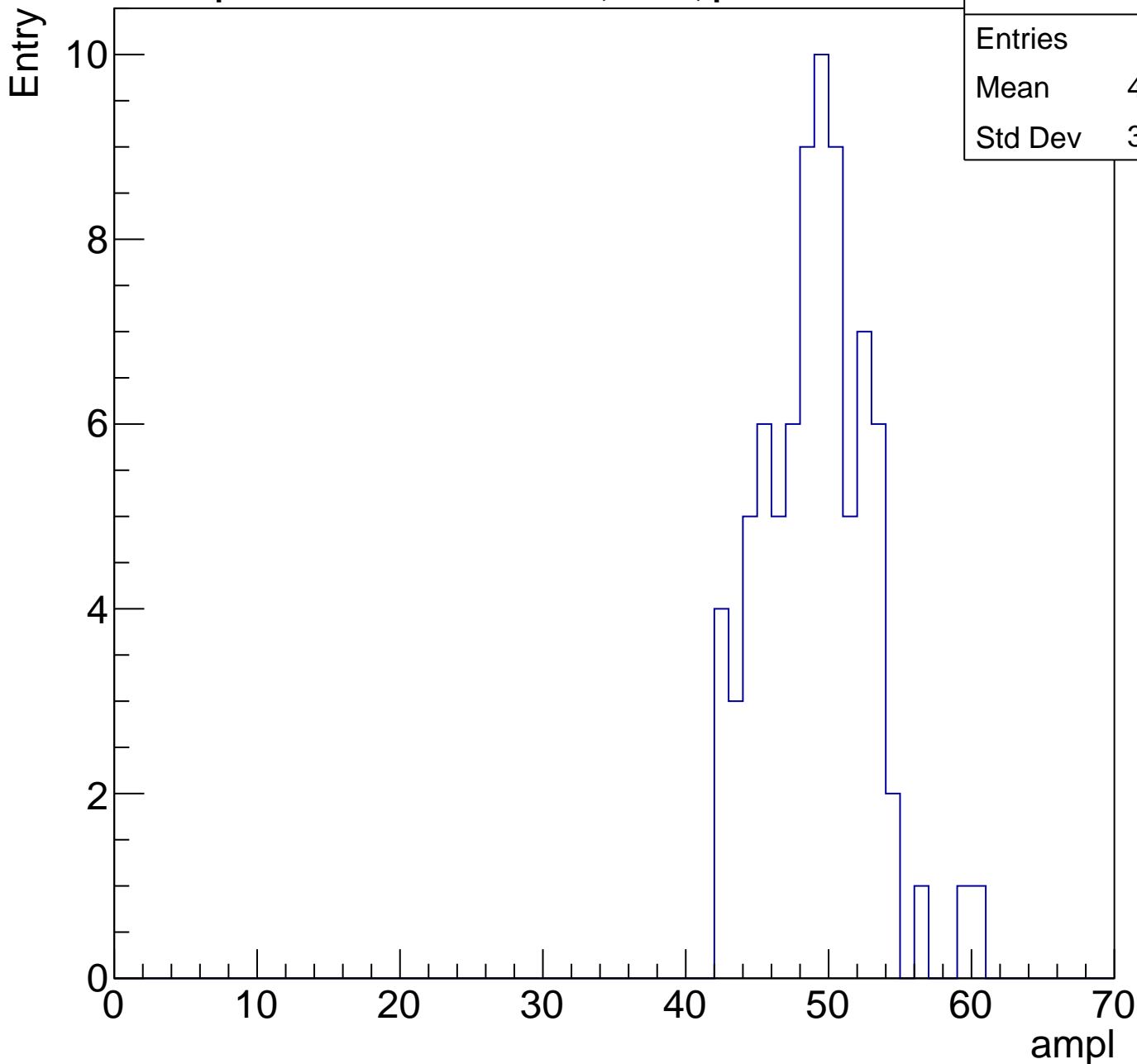
Gaus Width: 4.6999



B0L002S, U7-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	48.64
Std Dev	3.709

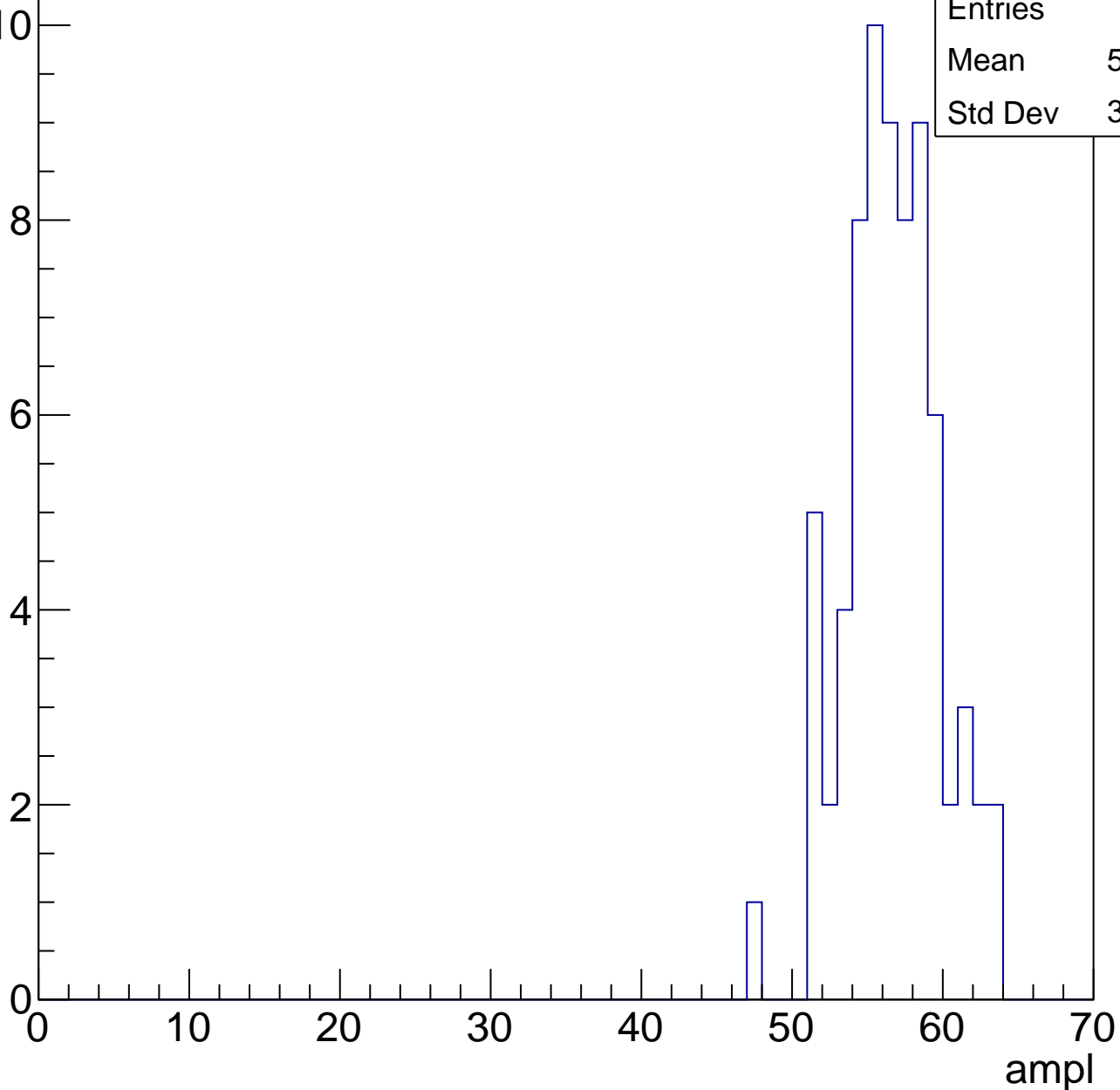


B0L002S, U7-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	56.18
Std Dev	3.114



B0L002S, U7-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	29
Mean	60.66
Std Dev	1.646

ampl

10

20

30

40

50

60

70

B0L002S, U7-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch99, adc0

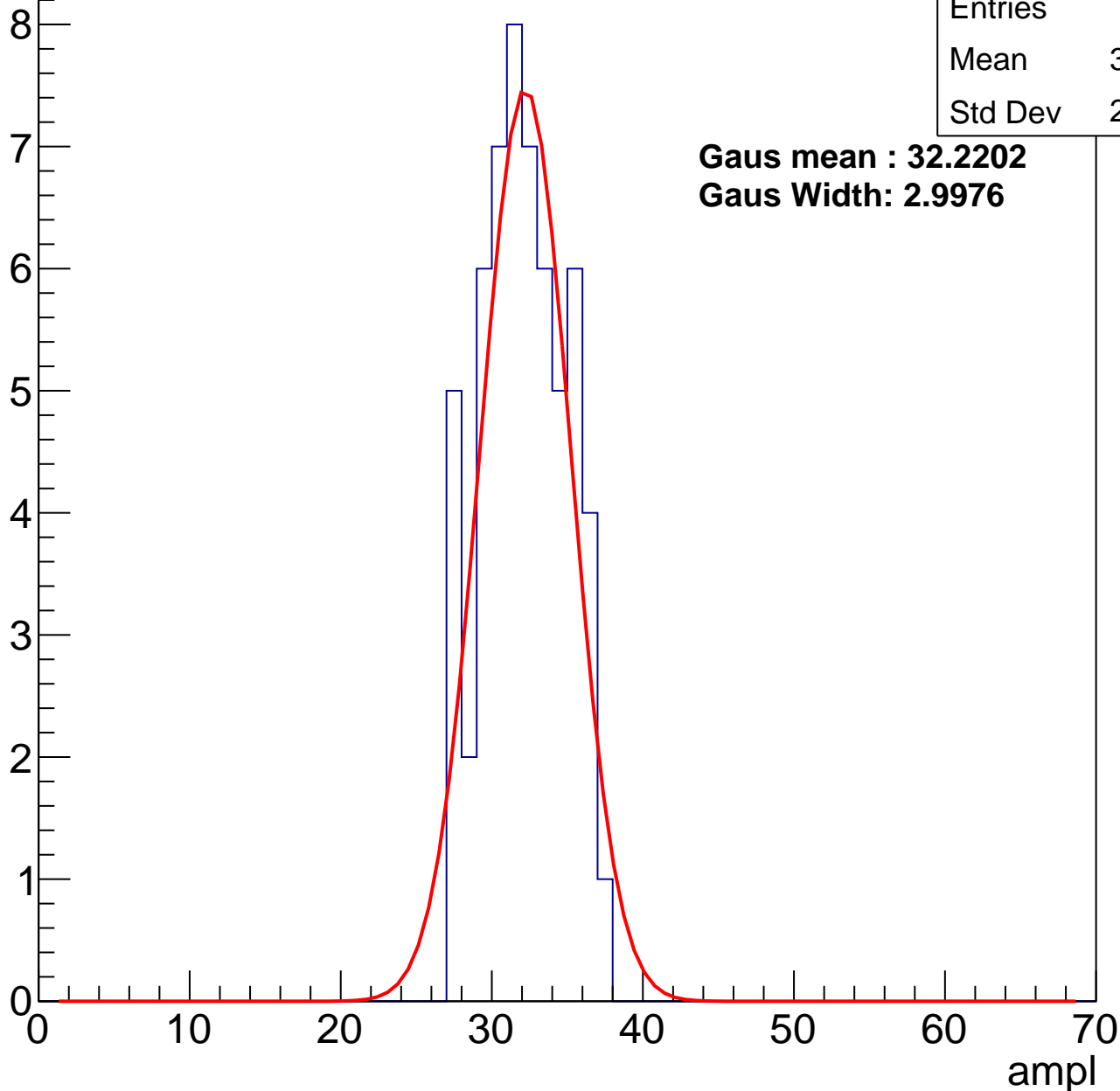
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	31.68
Std Dev	2.683

Gaus mean : 32.2202

Gaus Width: 2.9976



B0L002S, U7-ch99, adc1

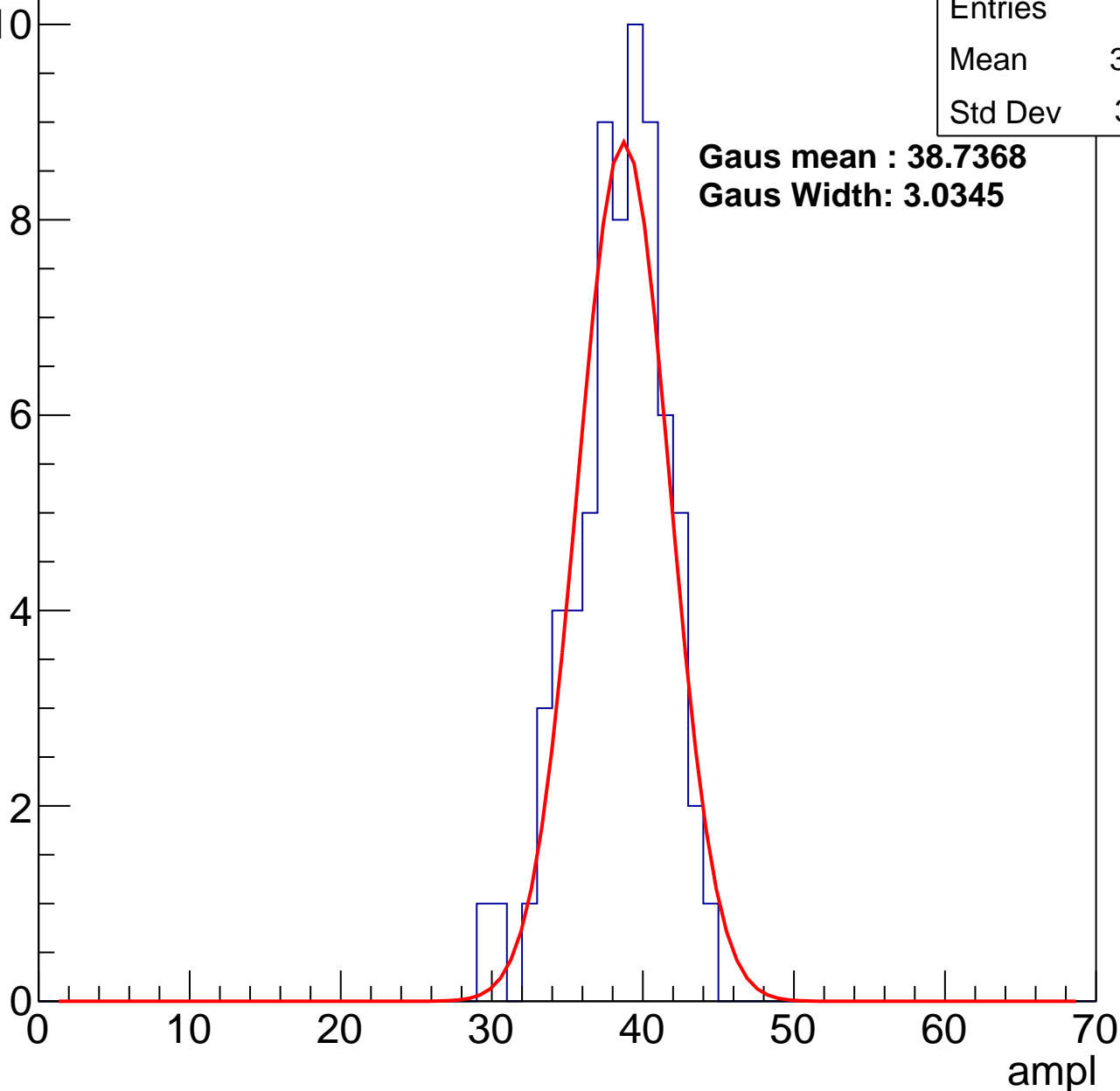
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.96
Std Dev	3.071

Gaus mean : 38.7368

Gaus Width: 3.0345



B0L002S, U7-ch99, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	44.92
Std Dev	3.025

Gaus mean : 45.5826

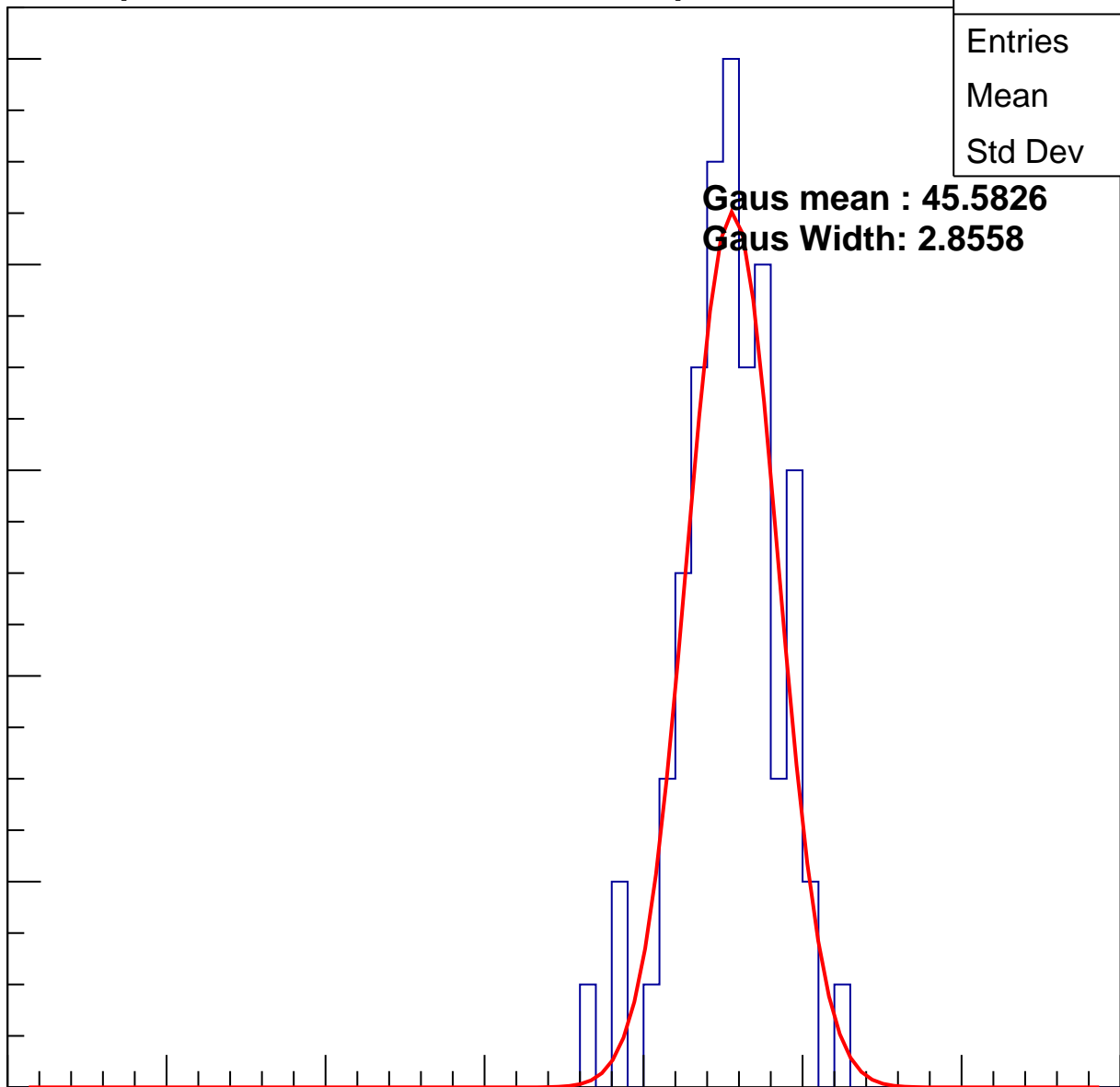
Gaus Width: 2.8558

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

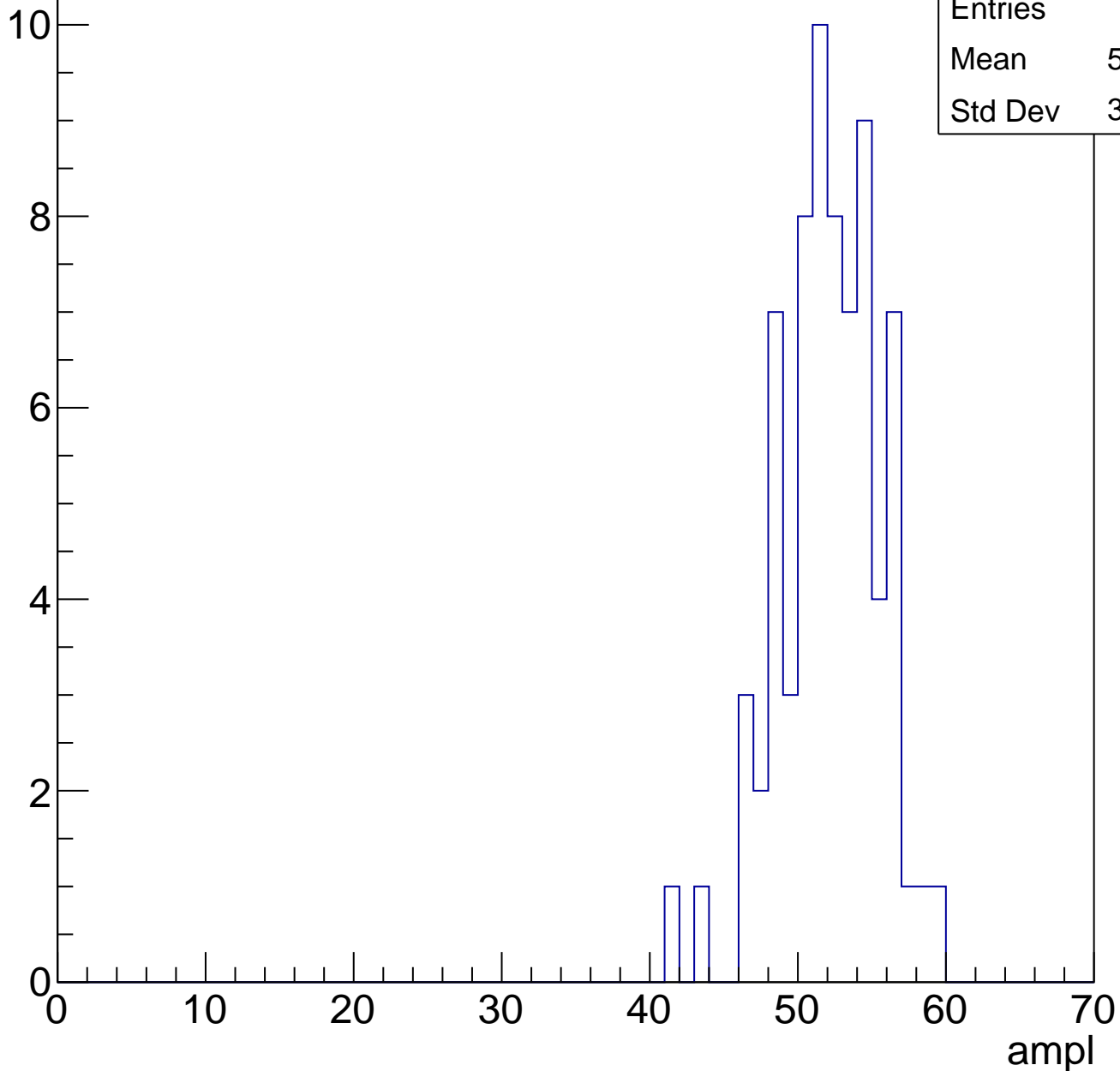


B0L002S, U7-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	51.62
Std Dev	3.374

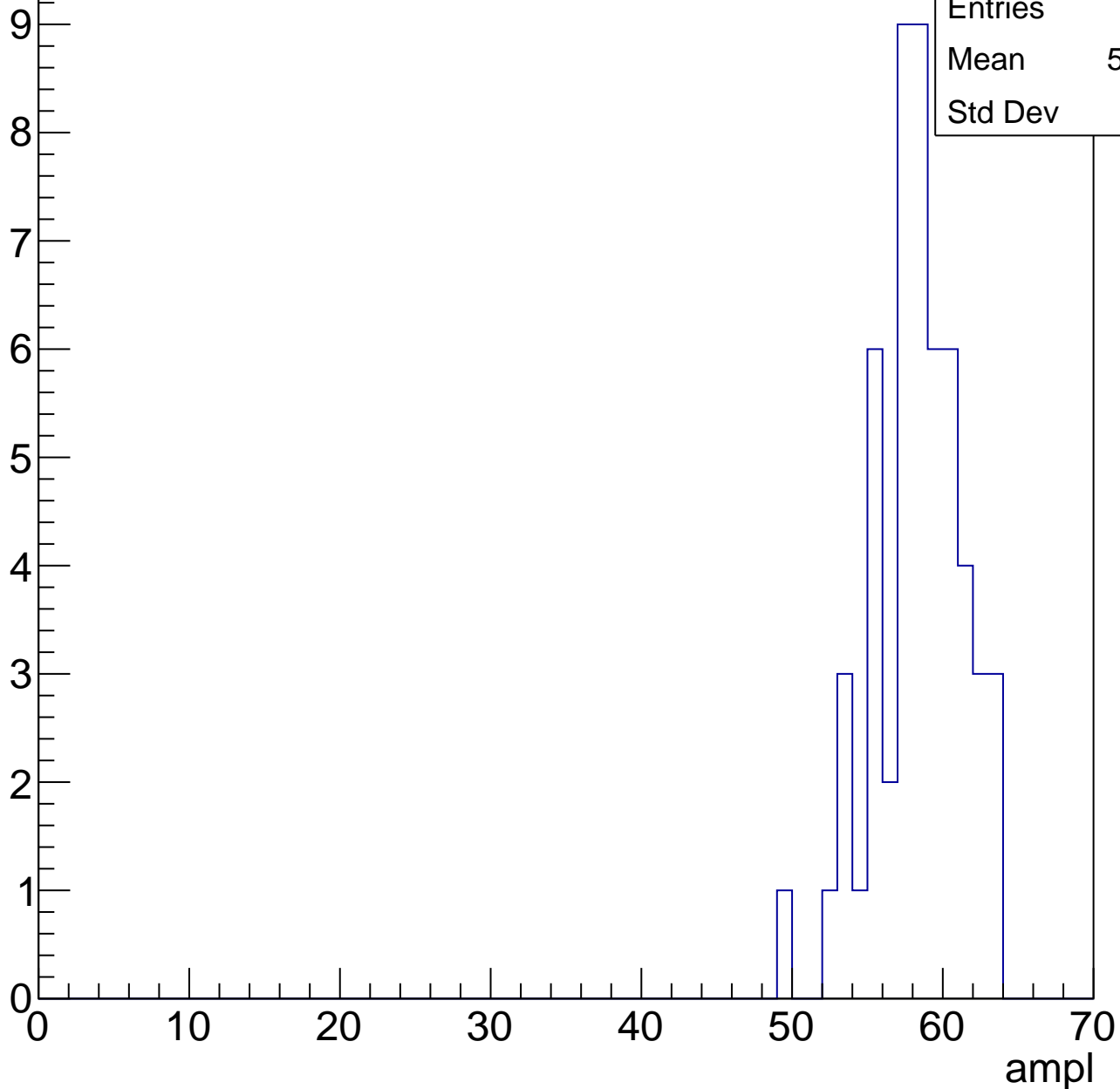
Entry



B0L002S, U7-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

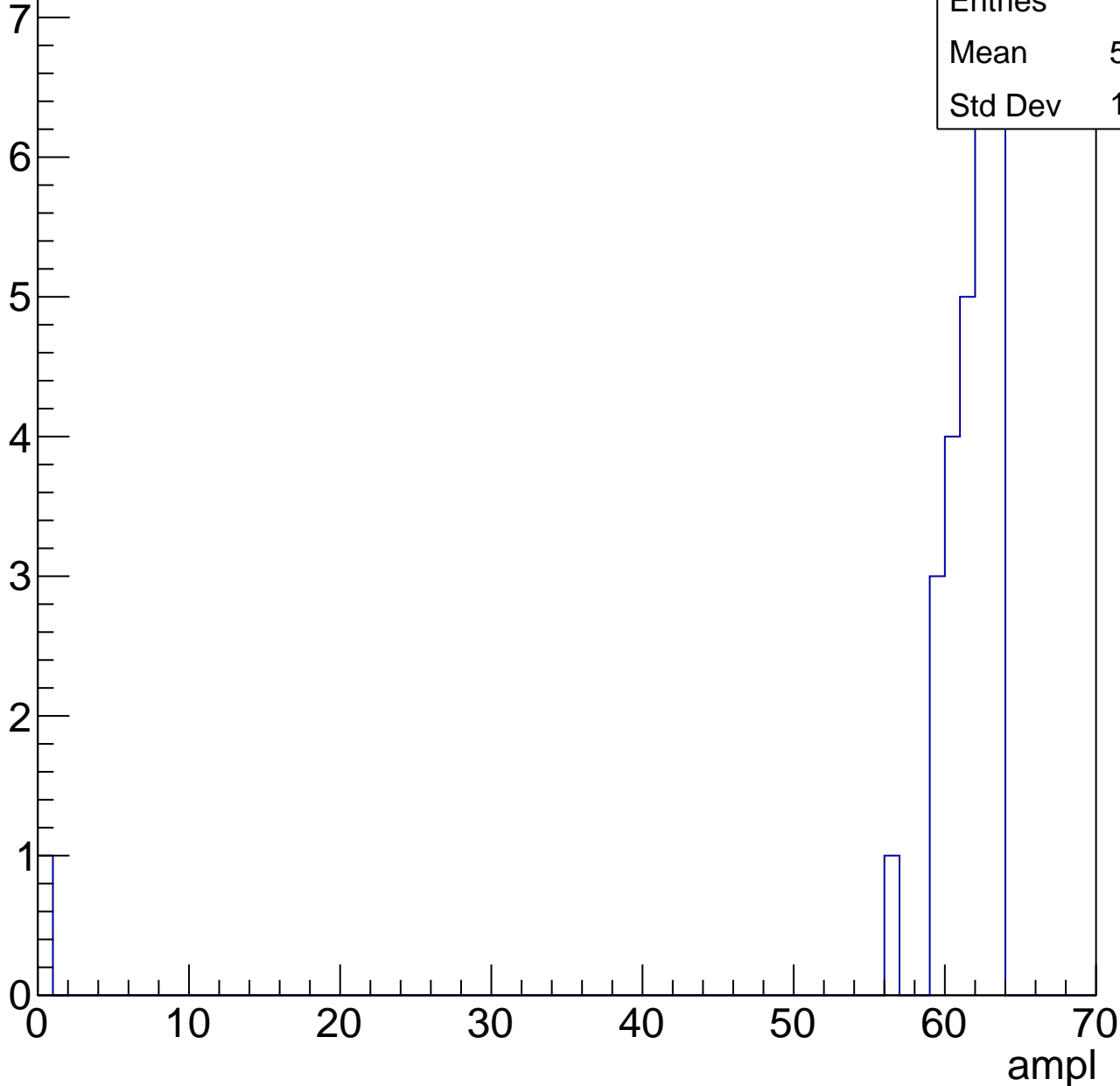


B0L002S, U7-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59.04
Std Dev	11.48



B0L002S, U7-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L002S, U7-ch100, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	28.73
Std Dev	6.034

Gaus mean : 30.2683

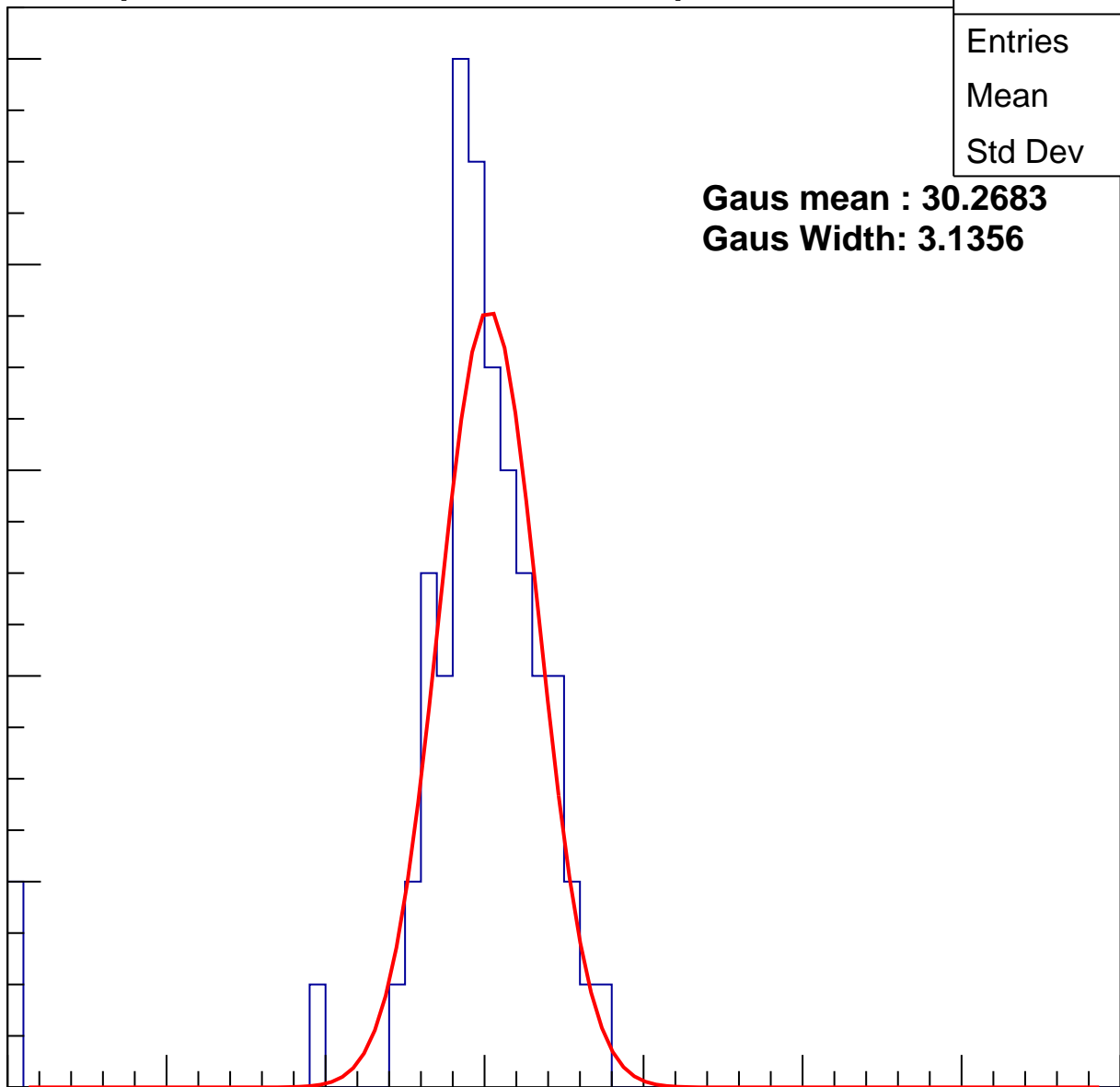
Gaus Width: 3.1356

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



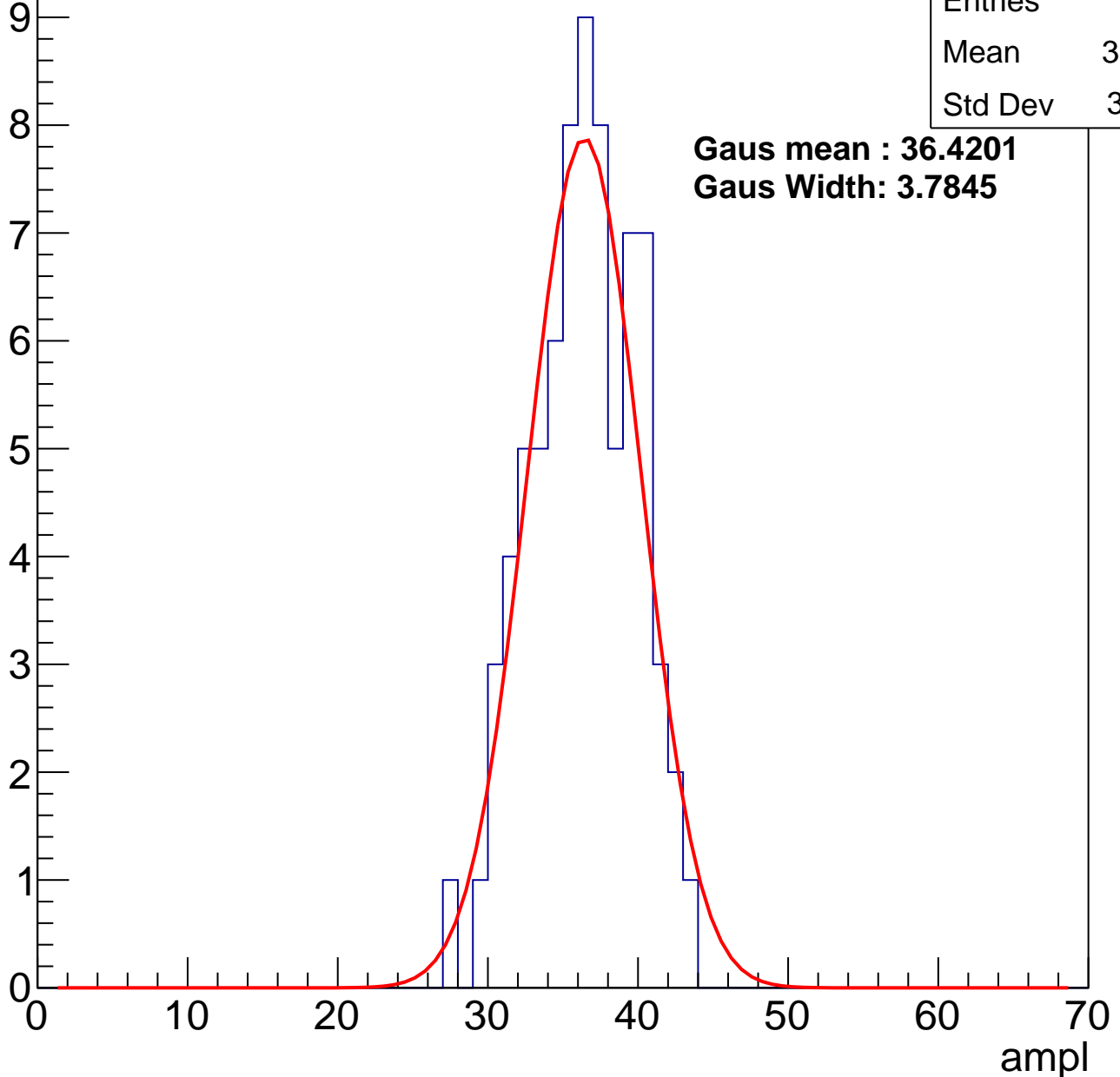
B0L002S, U7-ch100, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	35.89
Std Dev	3.451

Gaus mean : 36.4201
Gaus Width: 3.7845



B0L002S, U7-ch100, adc2

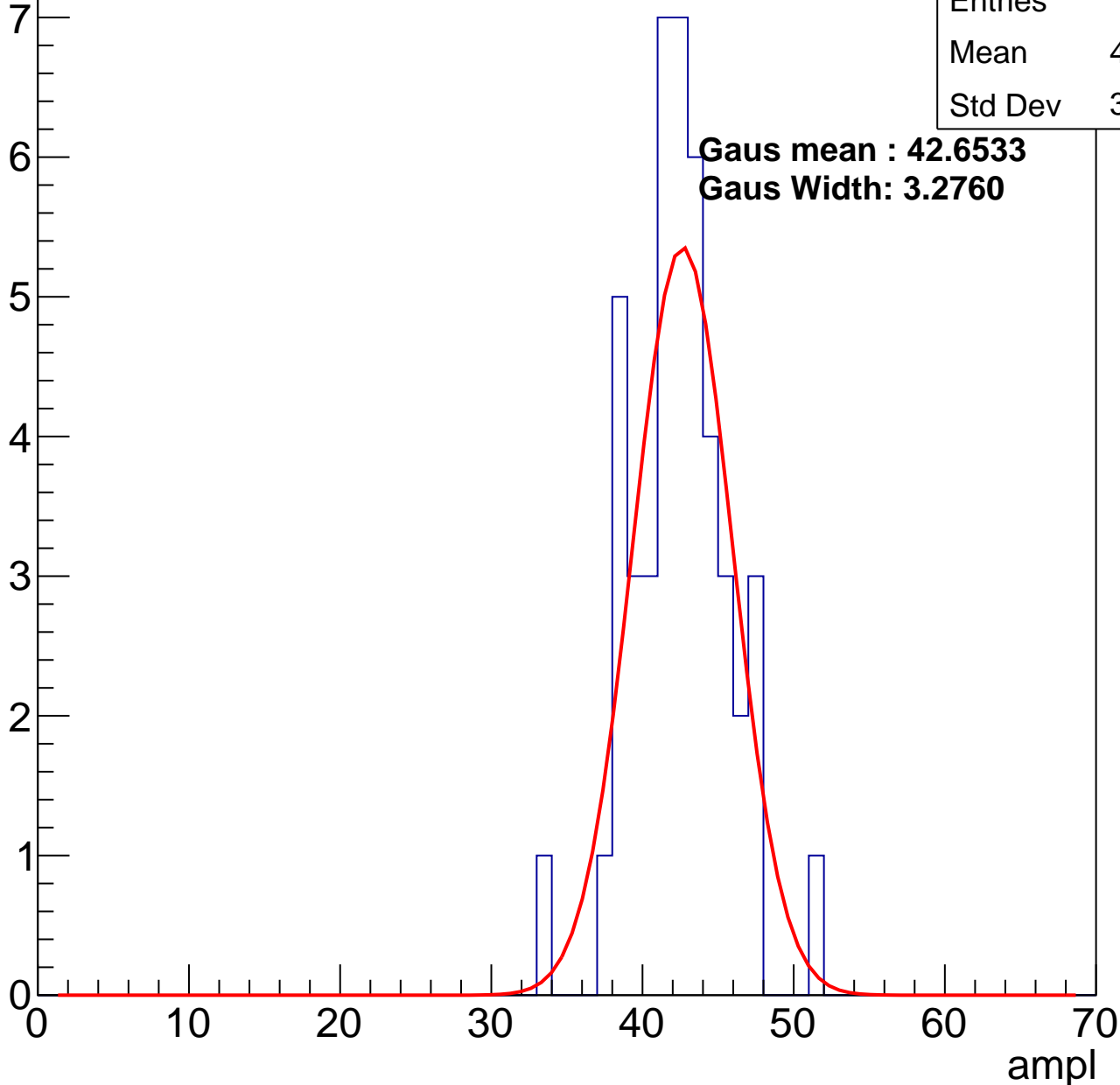
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	41.98
Std Dev	3.193

Gaus mean : 42.6533

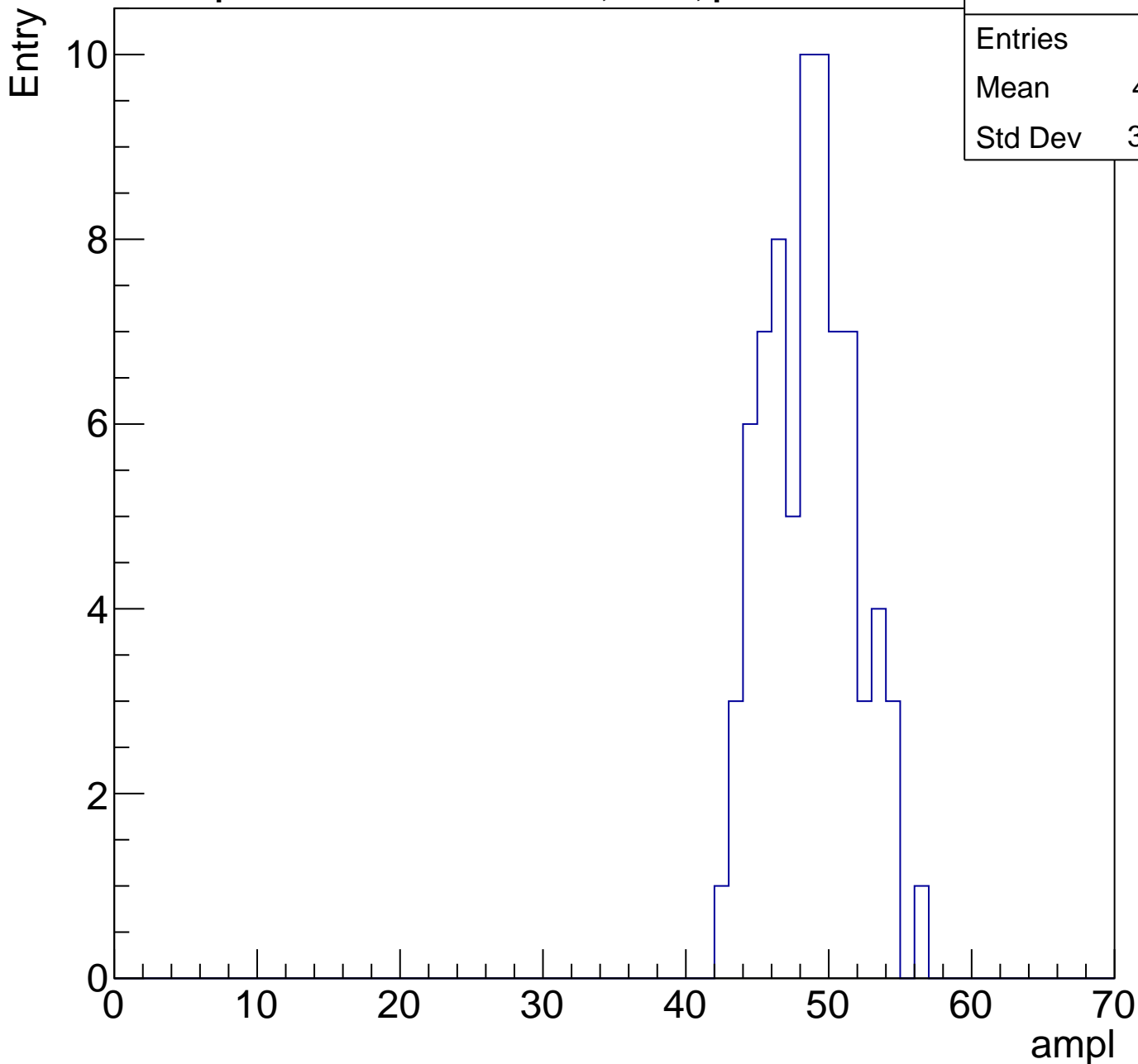
Gaus Width: 3.2760



B0L002S, U7-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	48.21
Std Dev	3.108

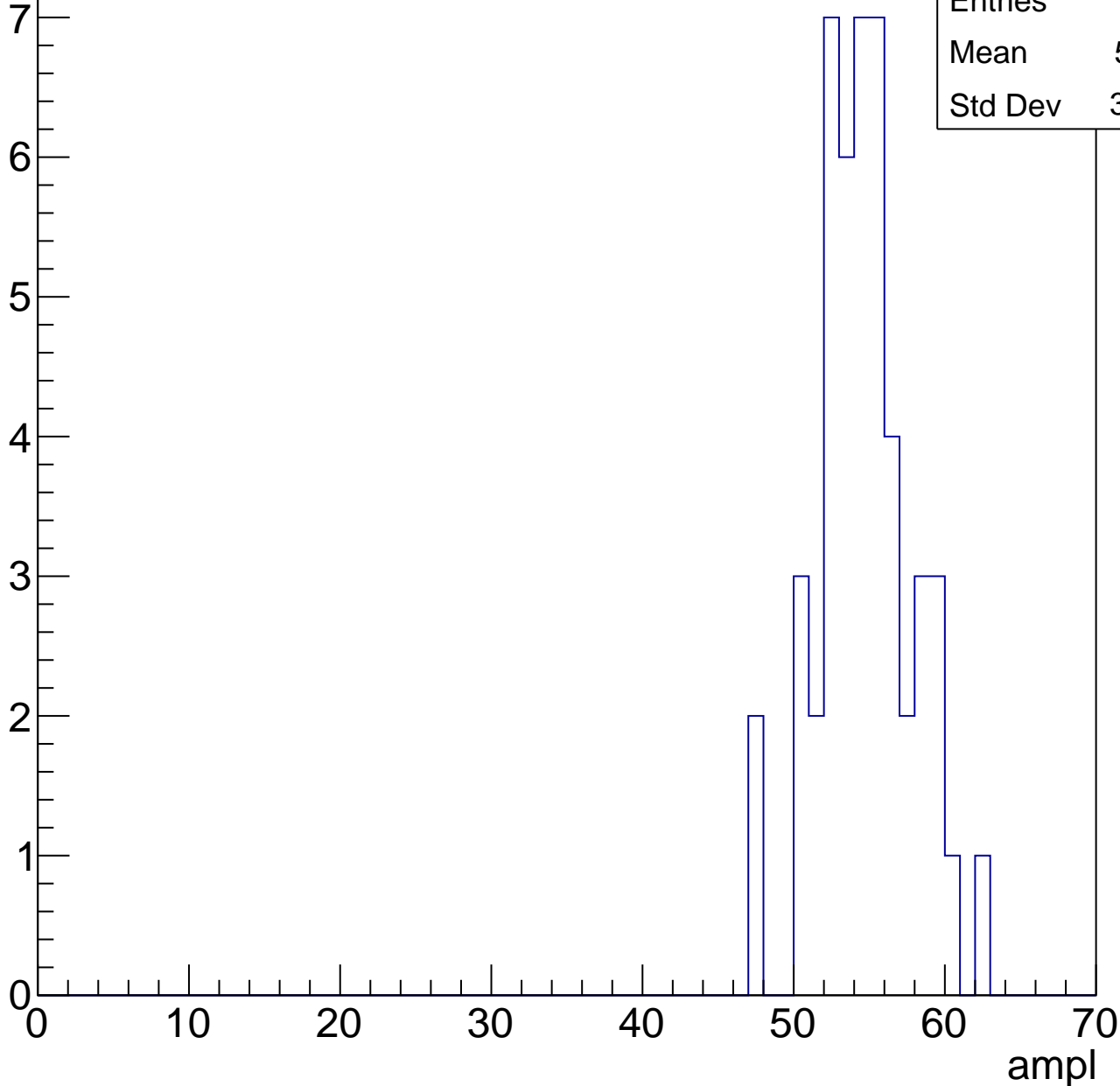


B0L002S, U7-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	54.21
Std Dev	3.095

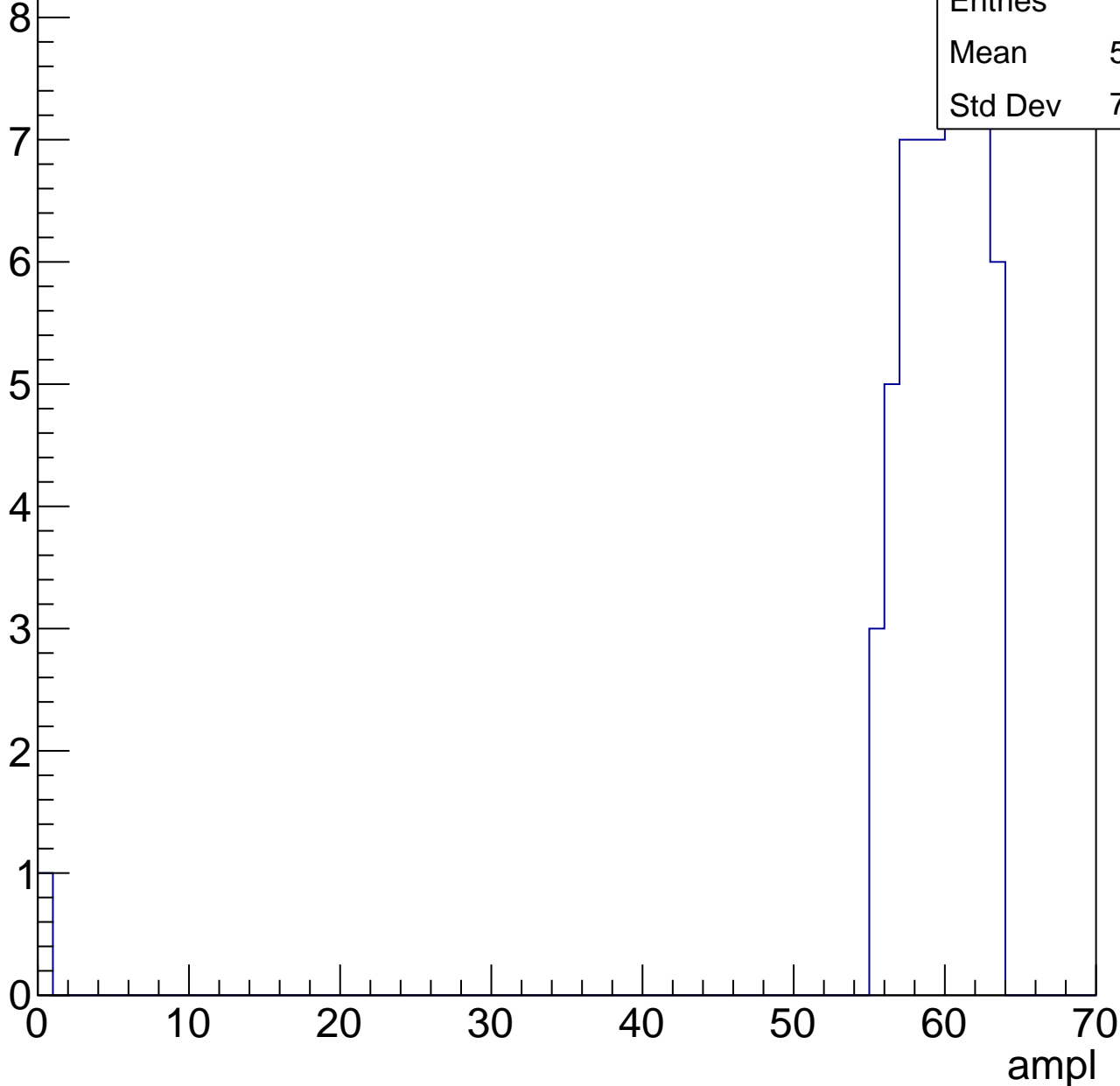


B0L002S, U7-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

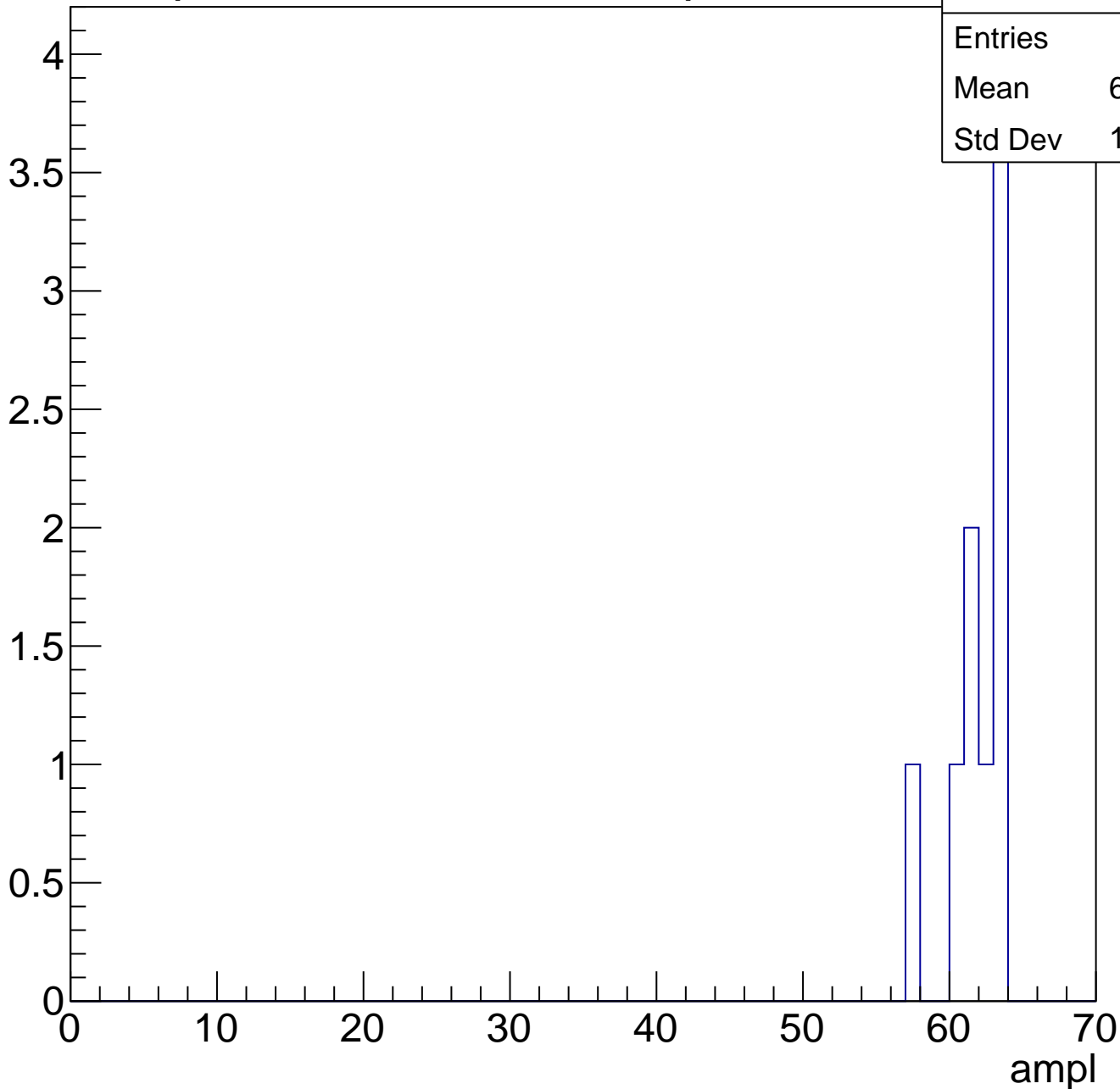
Entries	60
Mean	58.42
Std Dev	7.955



B0L002S, U7-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch101, adc0

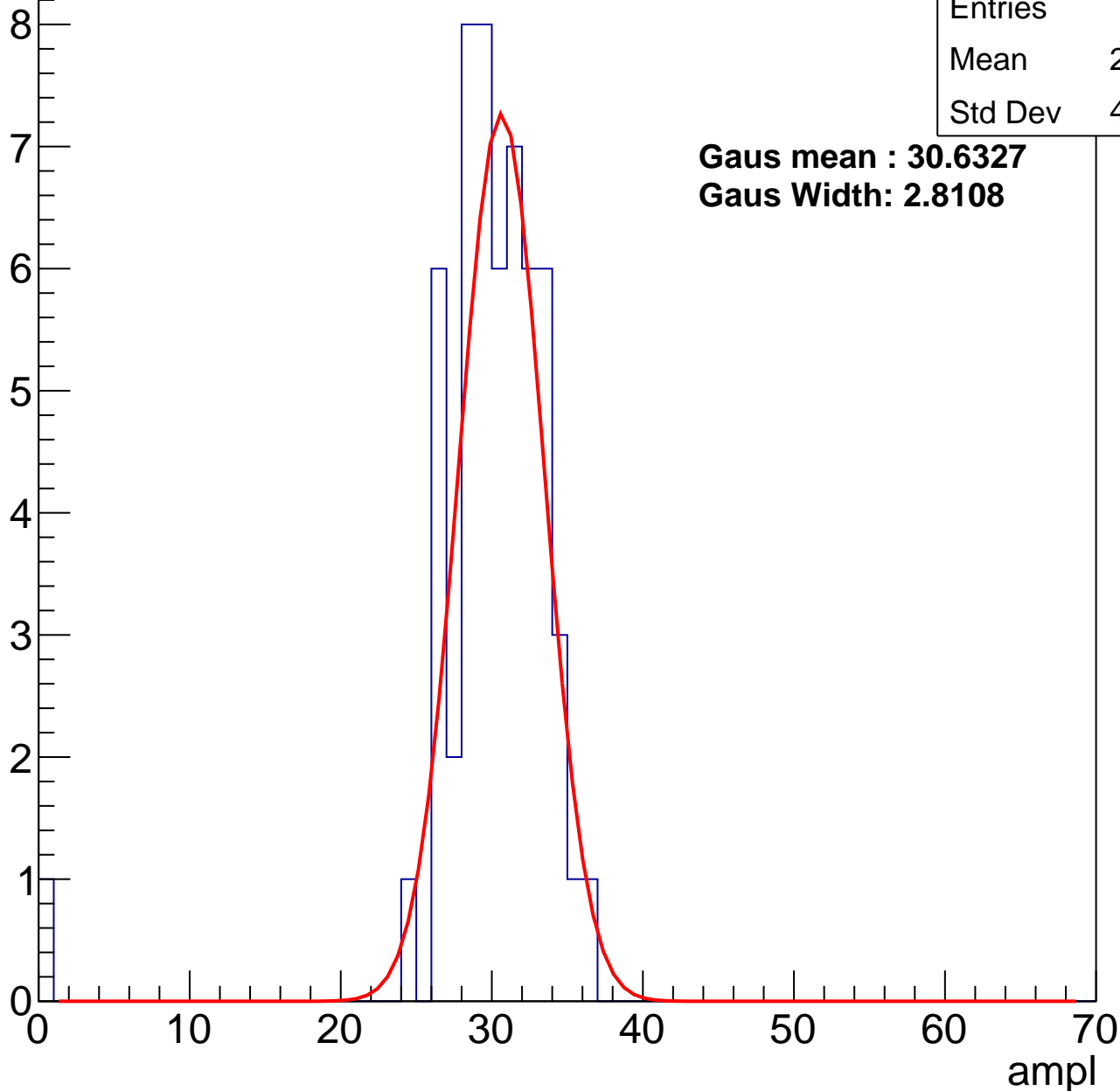
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	29.46
Std Dev	4.758

Gaus mean : 30.6327

Gaus Width: 2.8108

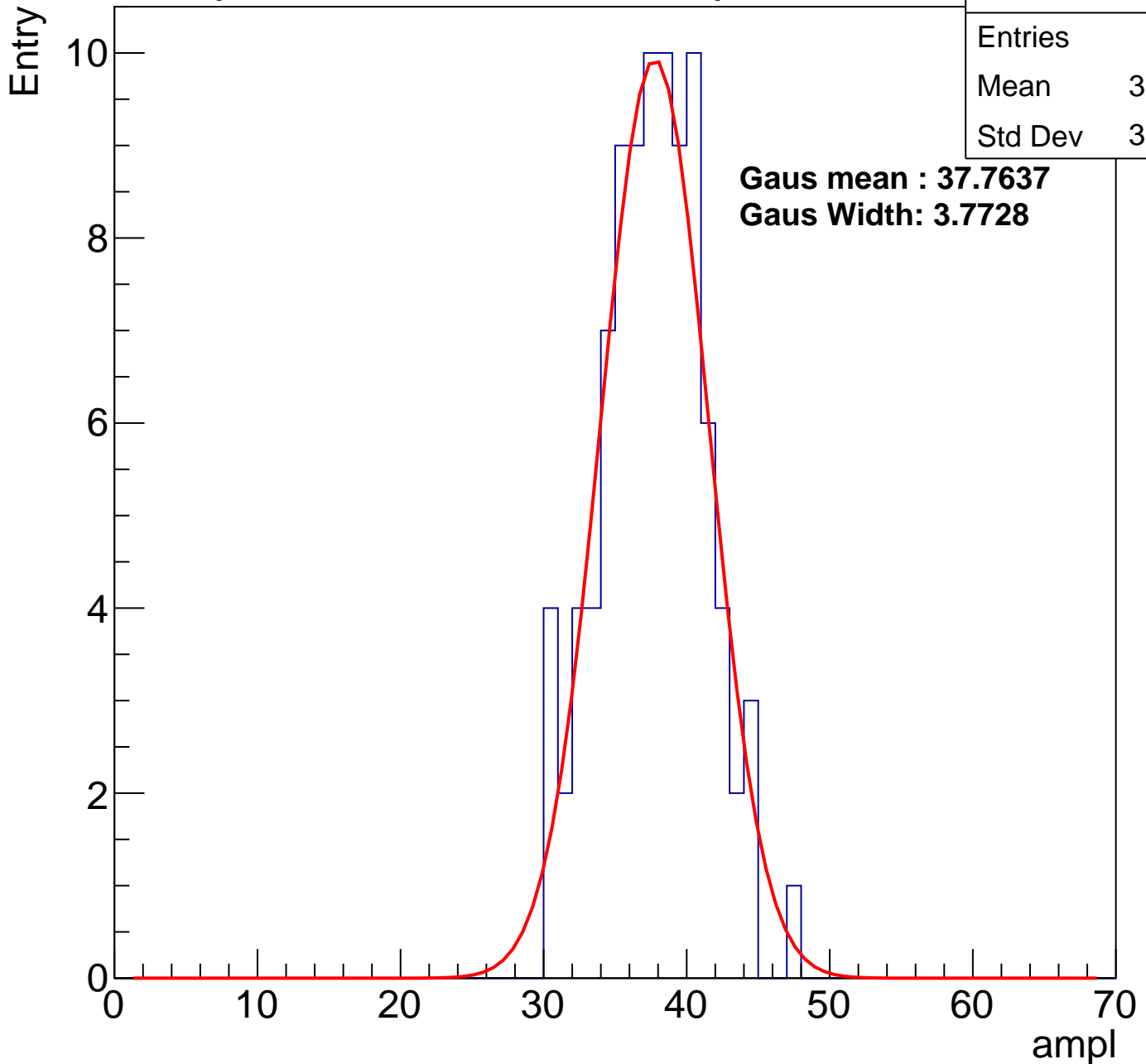


B0L002S, U7-ch101, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	94
Mean	37.22
Std Dev	3.553

Gaus mean : 37.7637
Gaus Width: 3.7728



B0L002S, U7-ch101, adc2

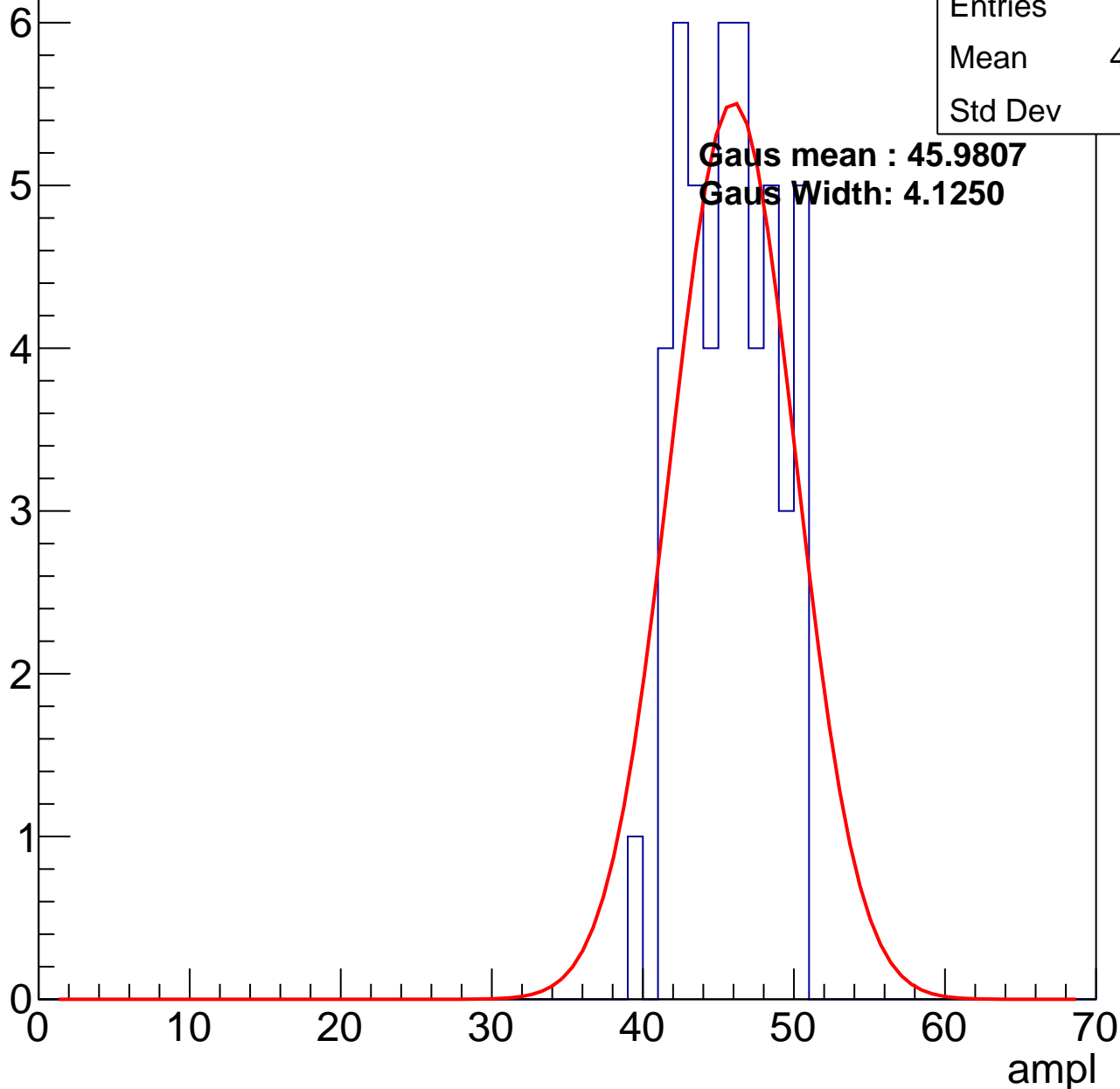
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	45.24
Std Dev	2.91

Gaus mean : 45.9807

Gaus Width: 4.1250

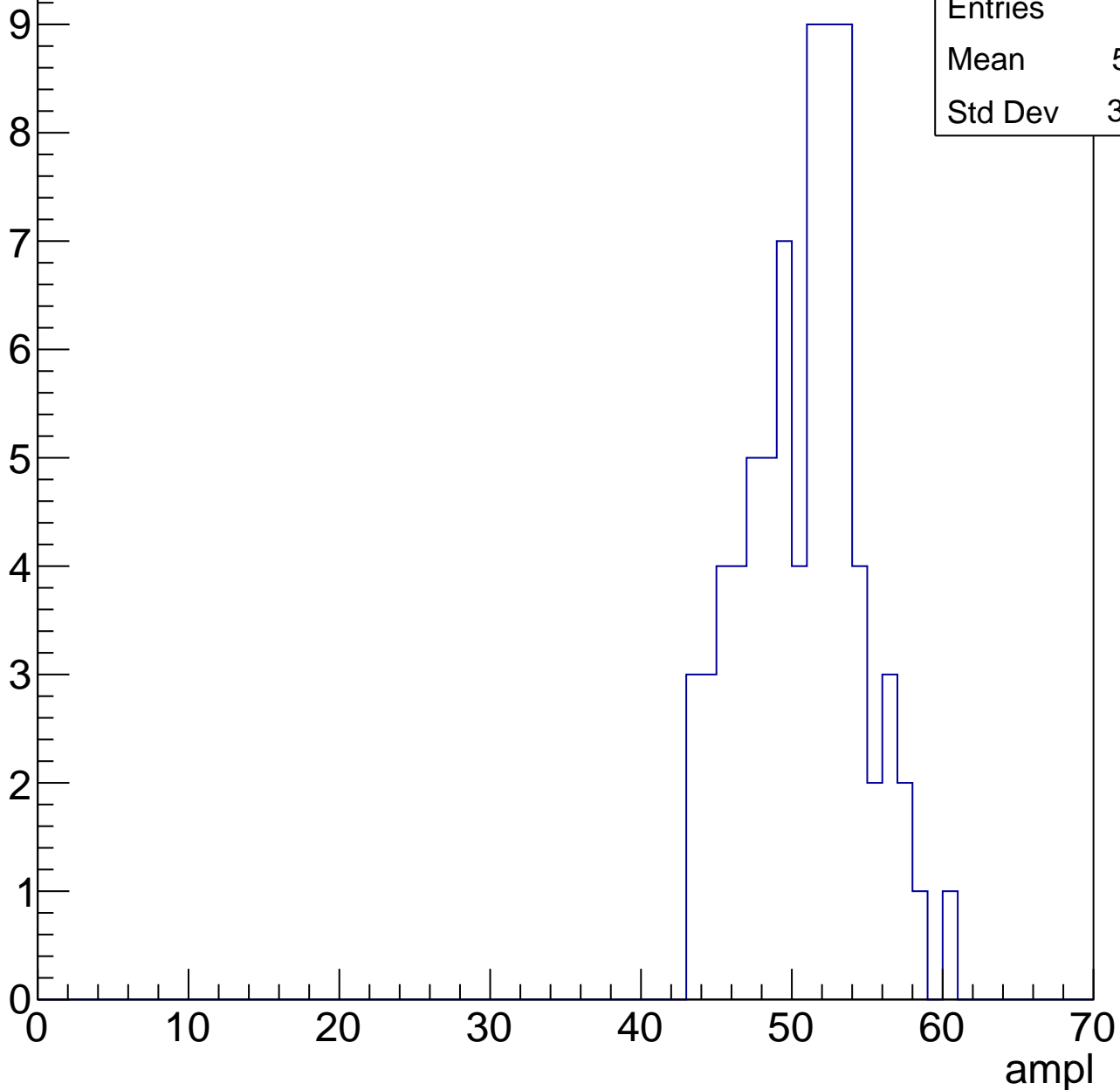


B0L002S, U7-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	50.31
Std Dev	3.819

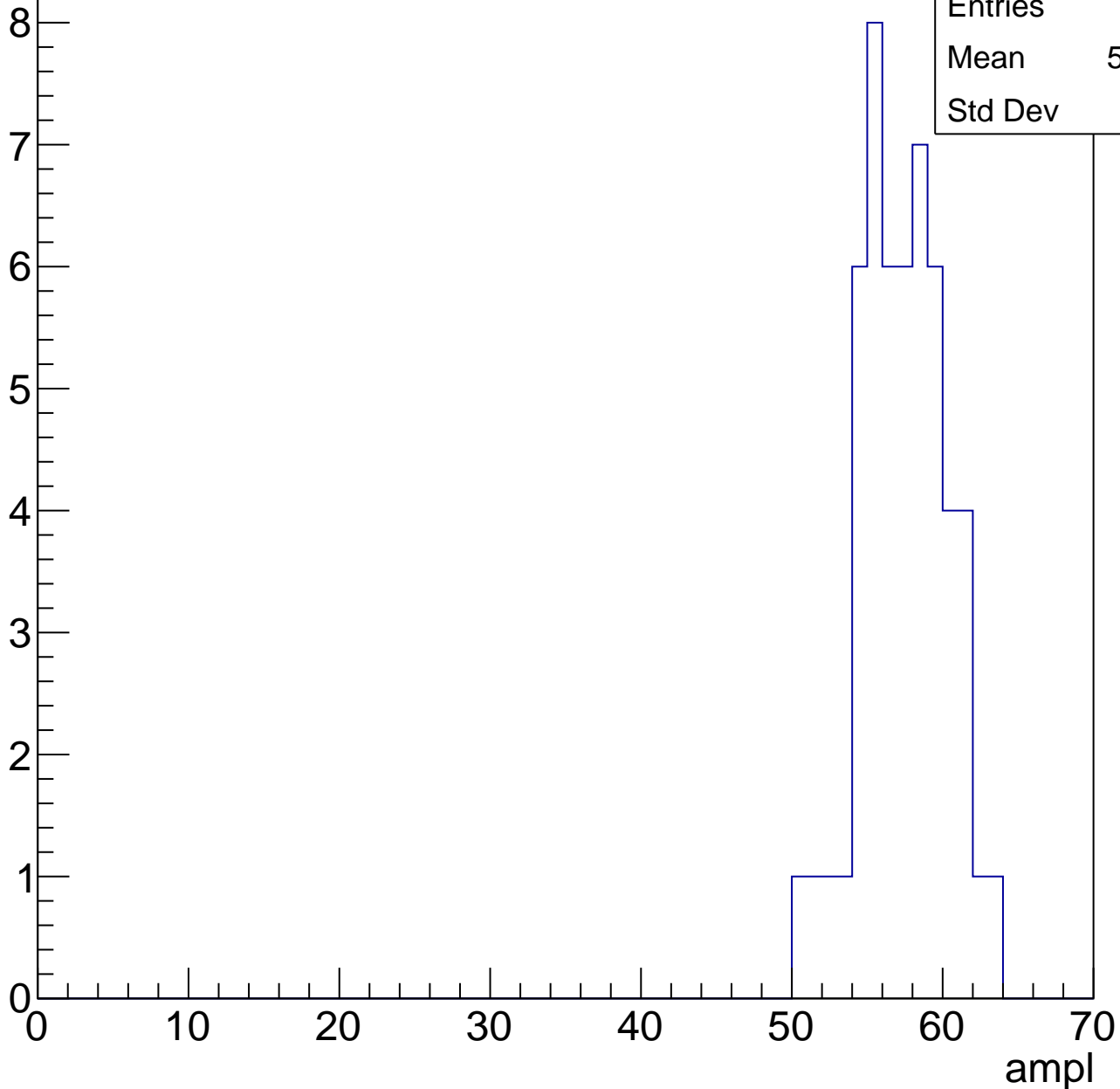


B0L002S, U7-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.92
Std Dev	2.78

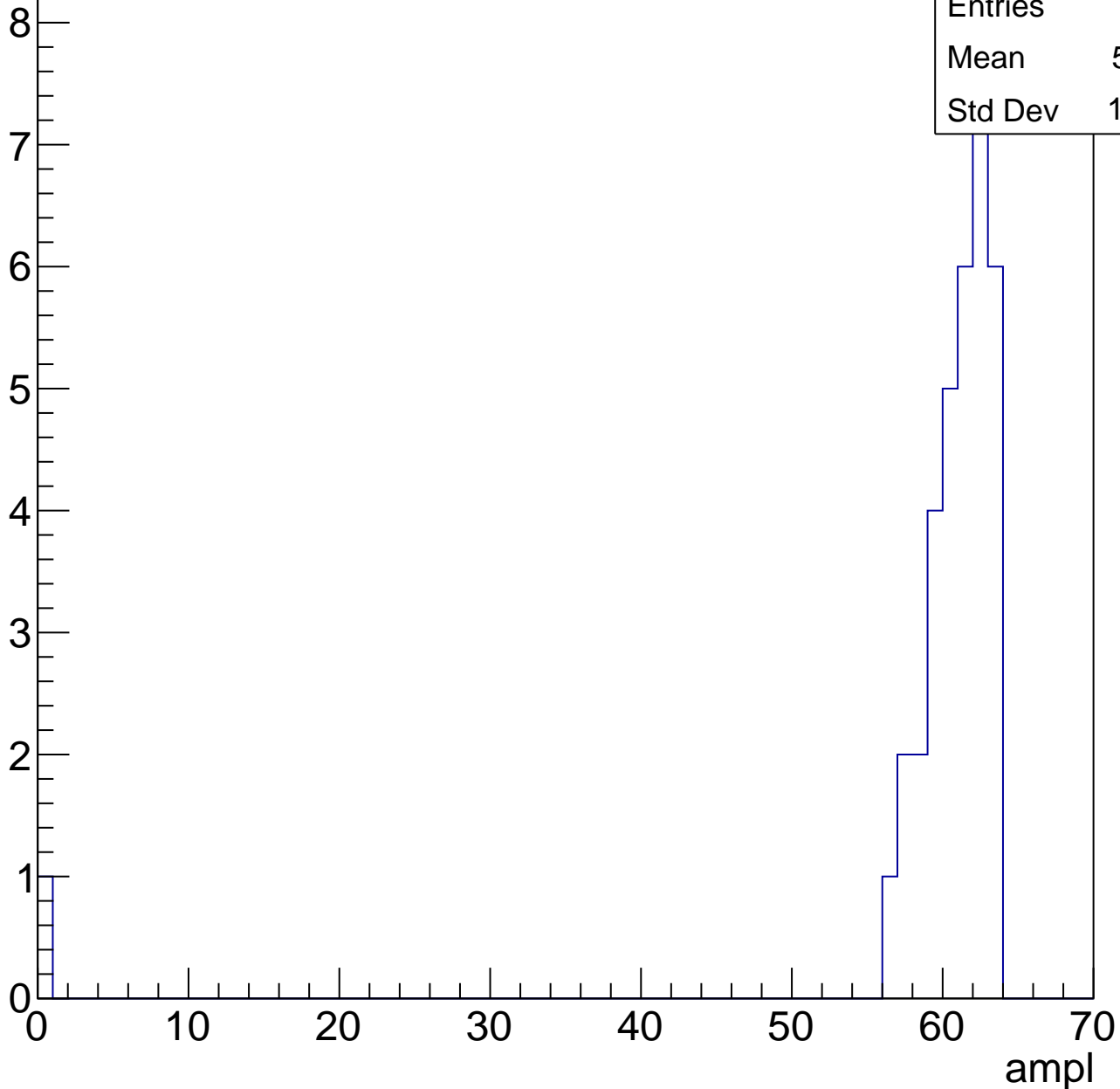


B0L002S, U7-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.91
Std Dev	10.28



B0L002S, U7-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B0L002S, U7-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12
Std Dev	12

B0L002S, U7-ch102, adc0

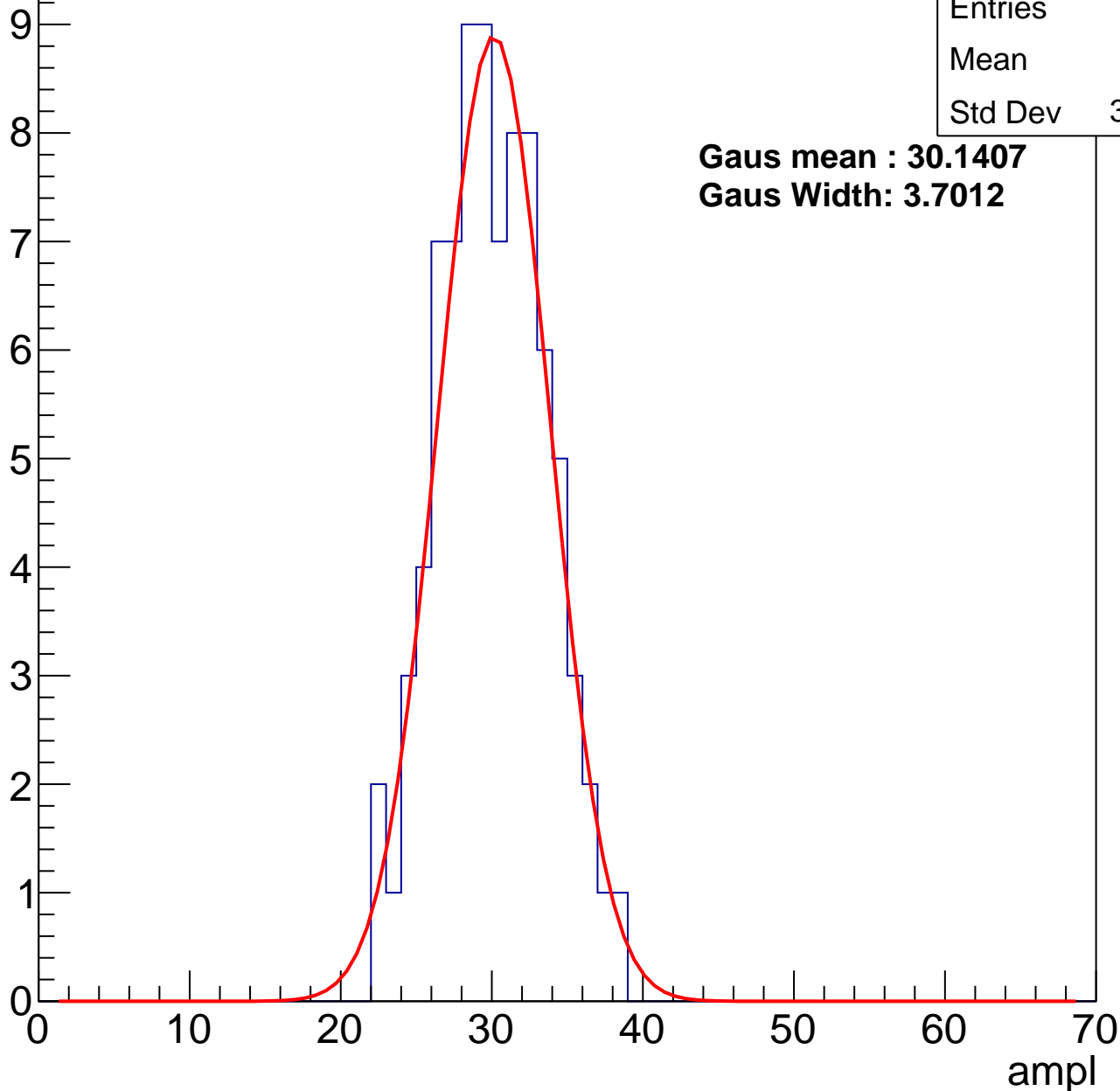
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.6
Std Dev	3.516

Gaus mean : 30.1407

Gaus Width: 3.7012



B0L002S, U7-ch102, adc1

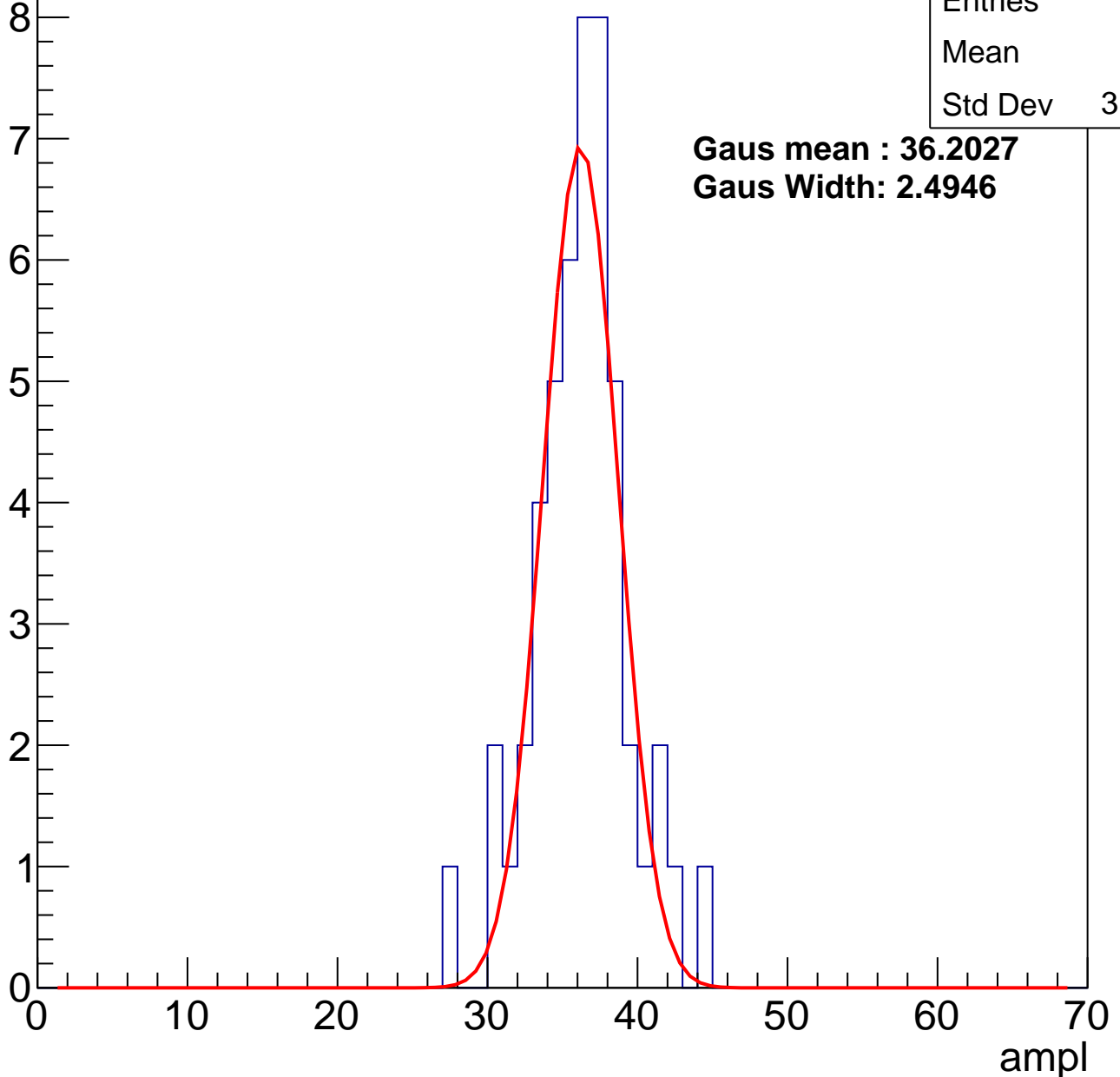
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	35.8
Std Dev	3.136

Gaus mean : 36.2027

Gaus Width: 2.4946



B0L002S, U7-ch102, adc2

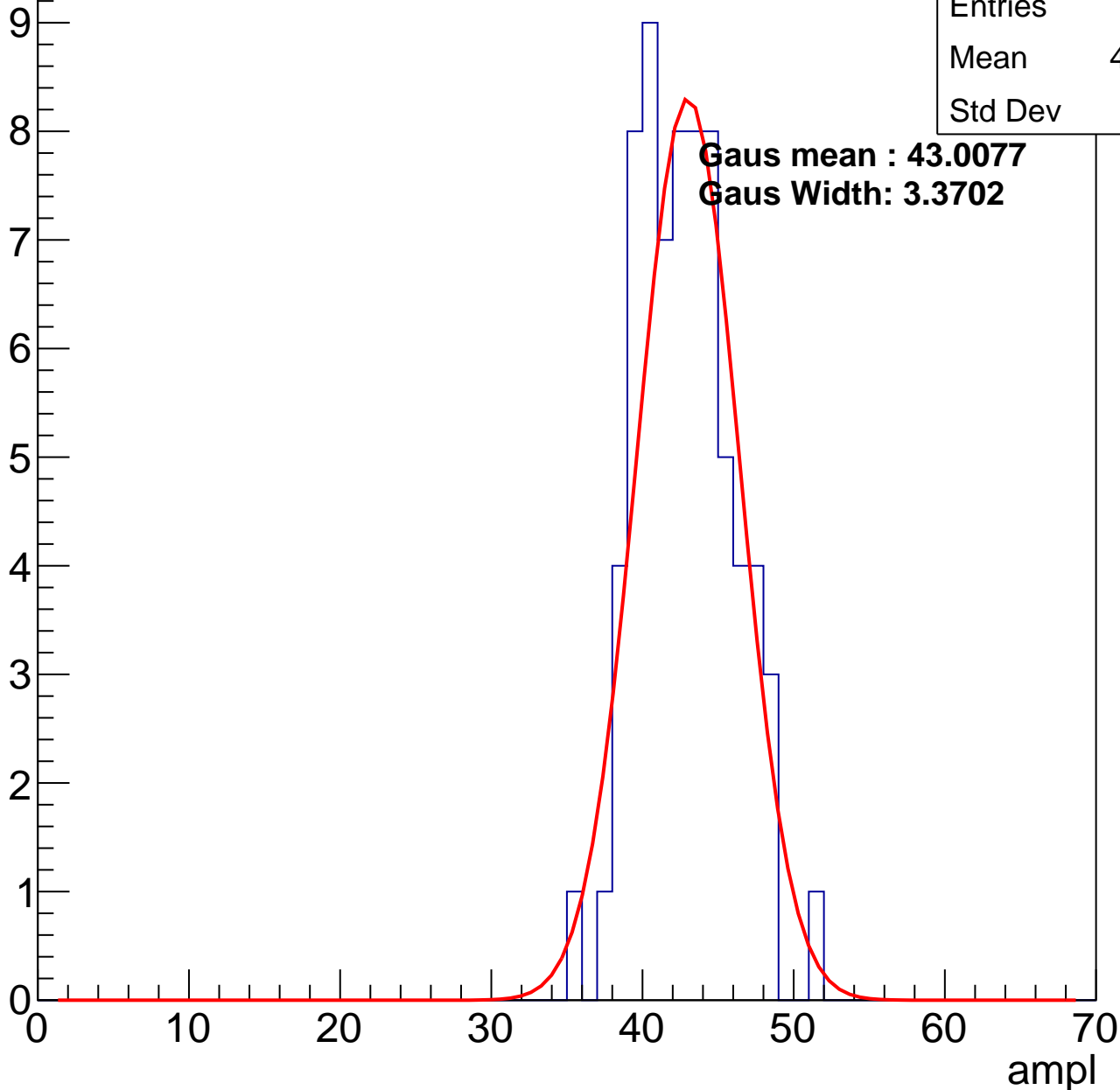
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.35
Std Dev	3.1

Gaus mean : 43.0077

Gaus Width: 3.3702

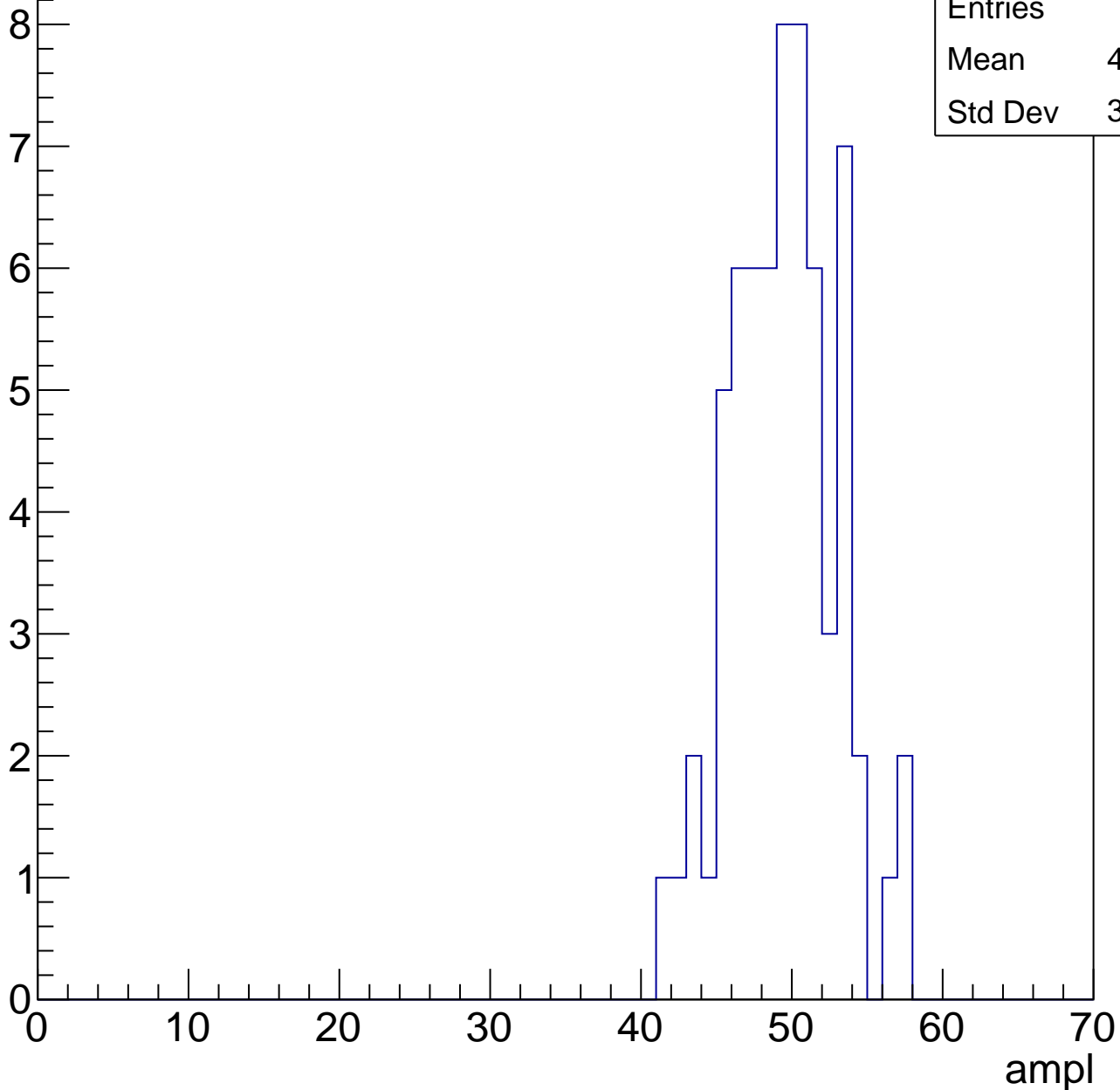


B0L002S, U7-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.03
Std Dev	3.446

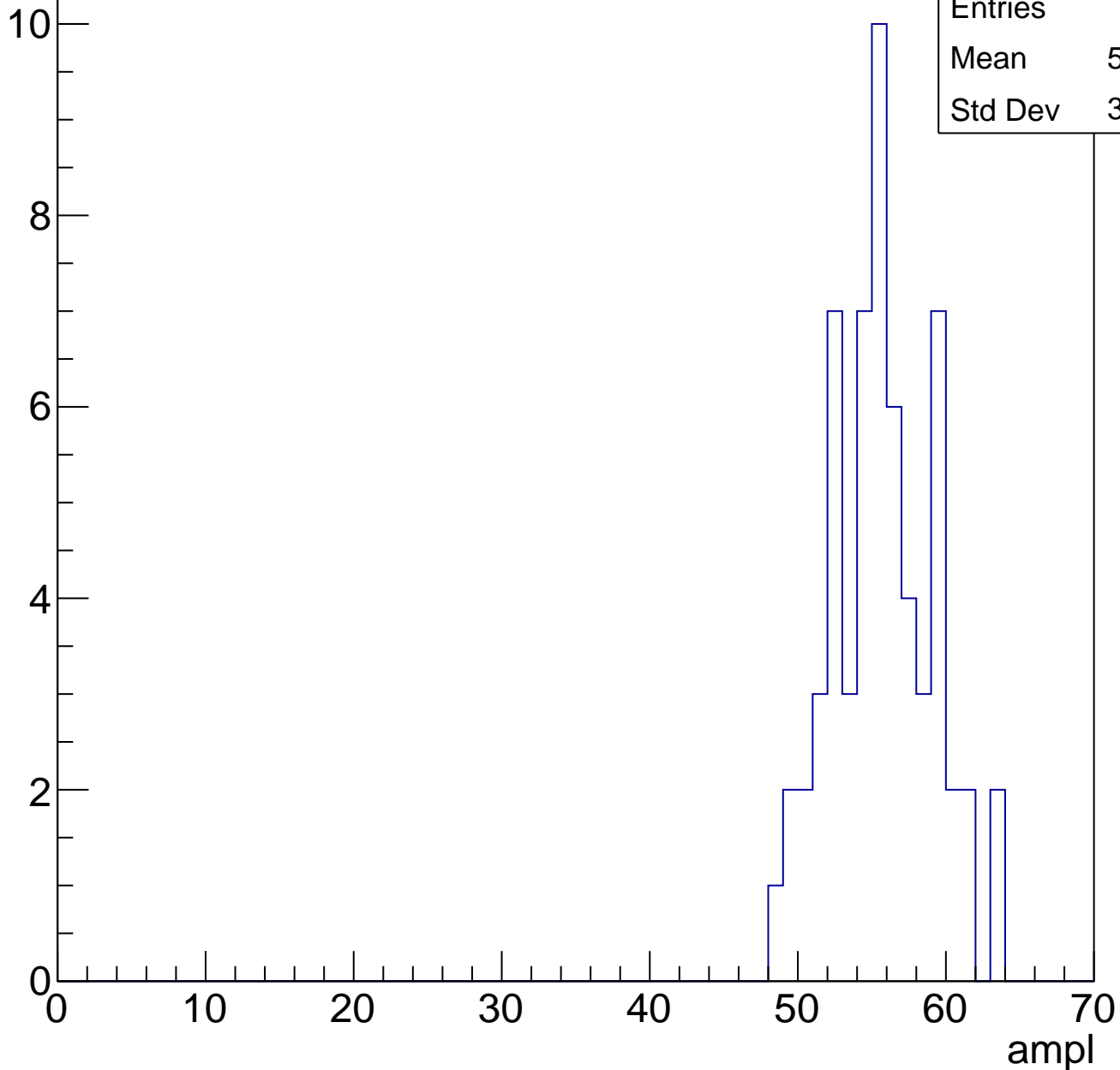


B0L002S, U7-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	55.23
Std Dev	3.409

Entry

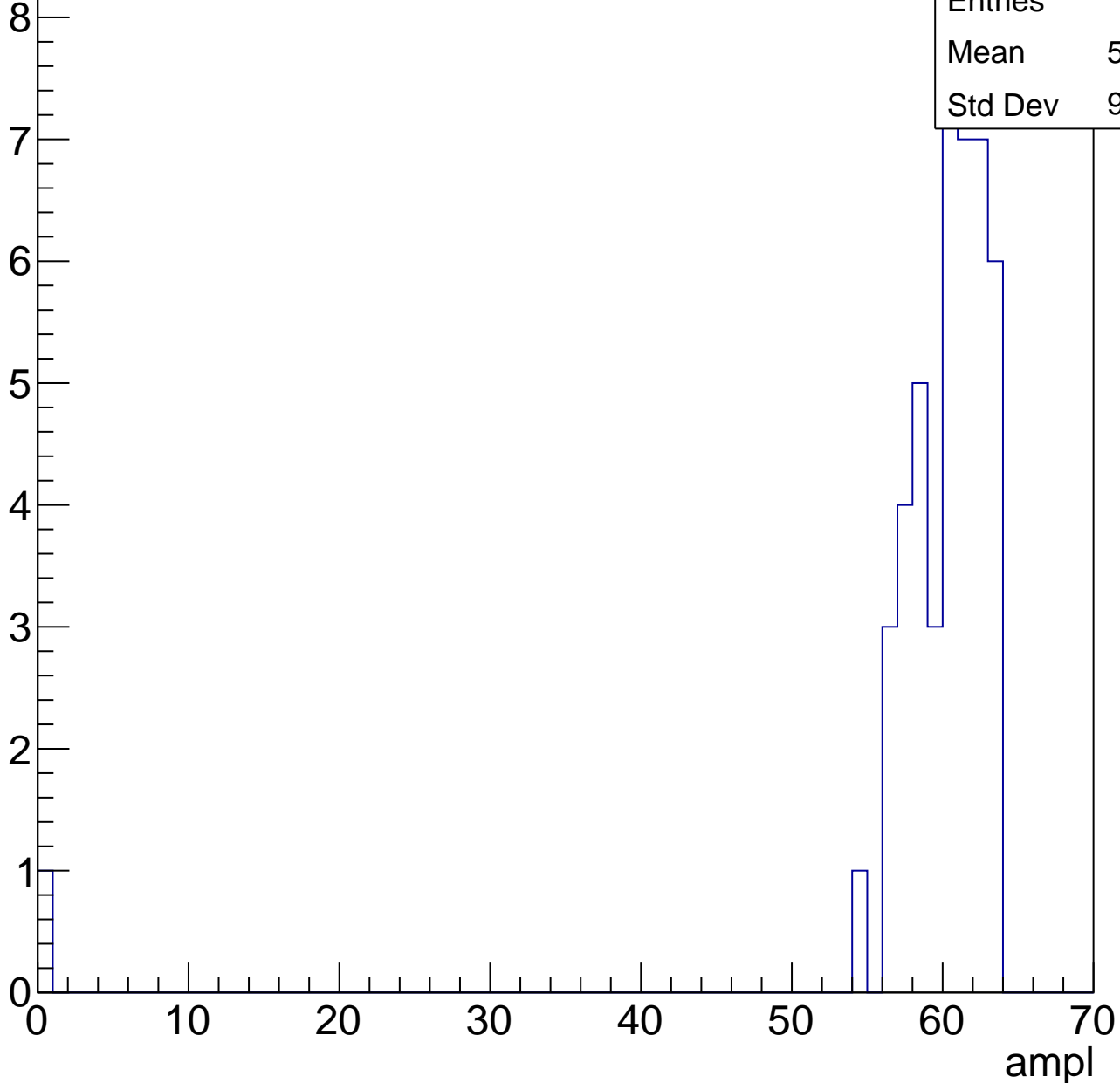


B0L002S, U7-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

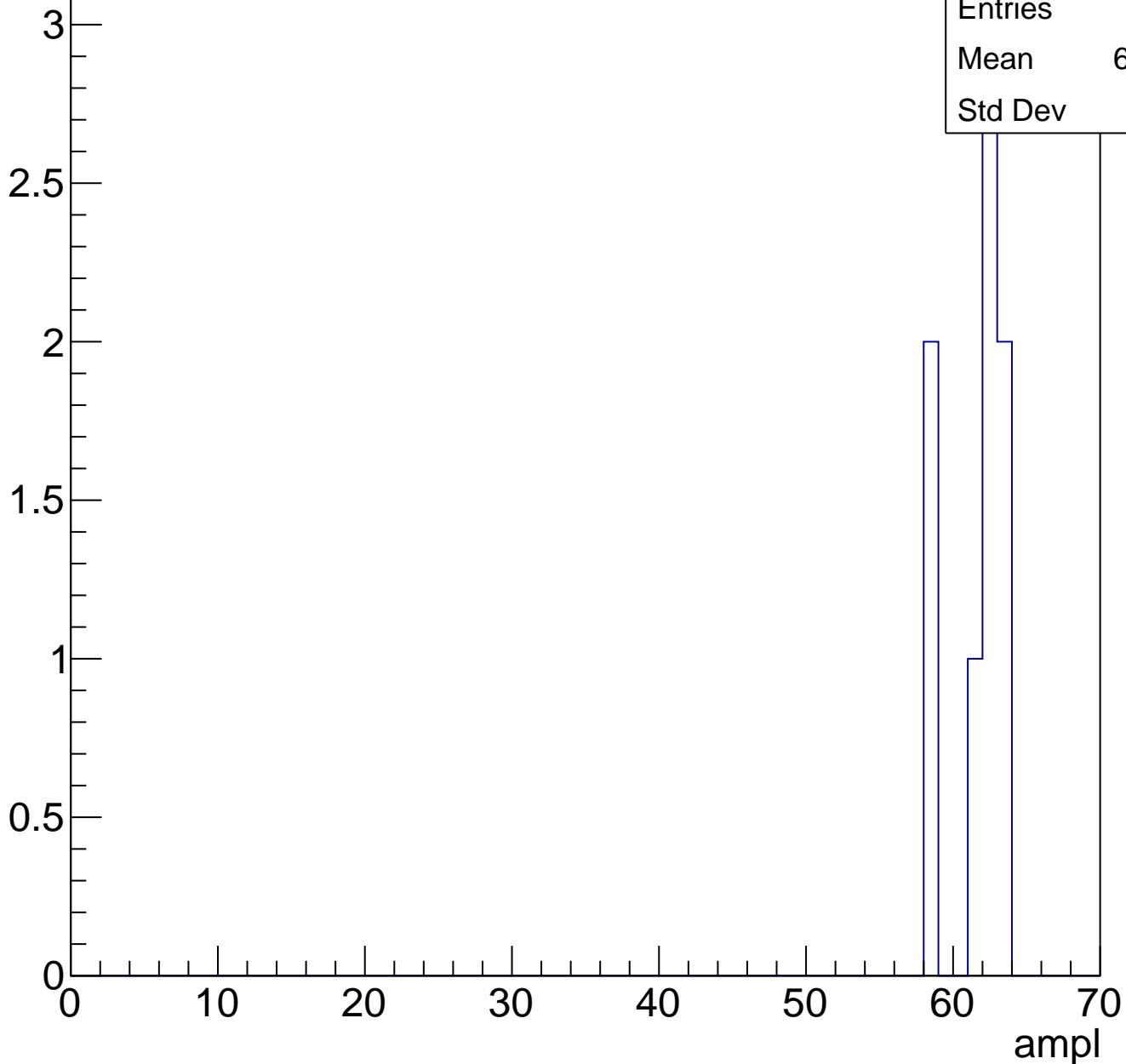
Entries	45
Mean	58.58
Std Dev	9.118



B0L002S, U7-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch103, adc0

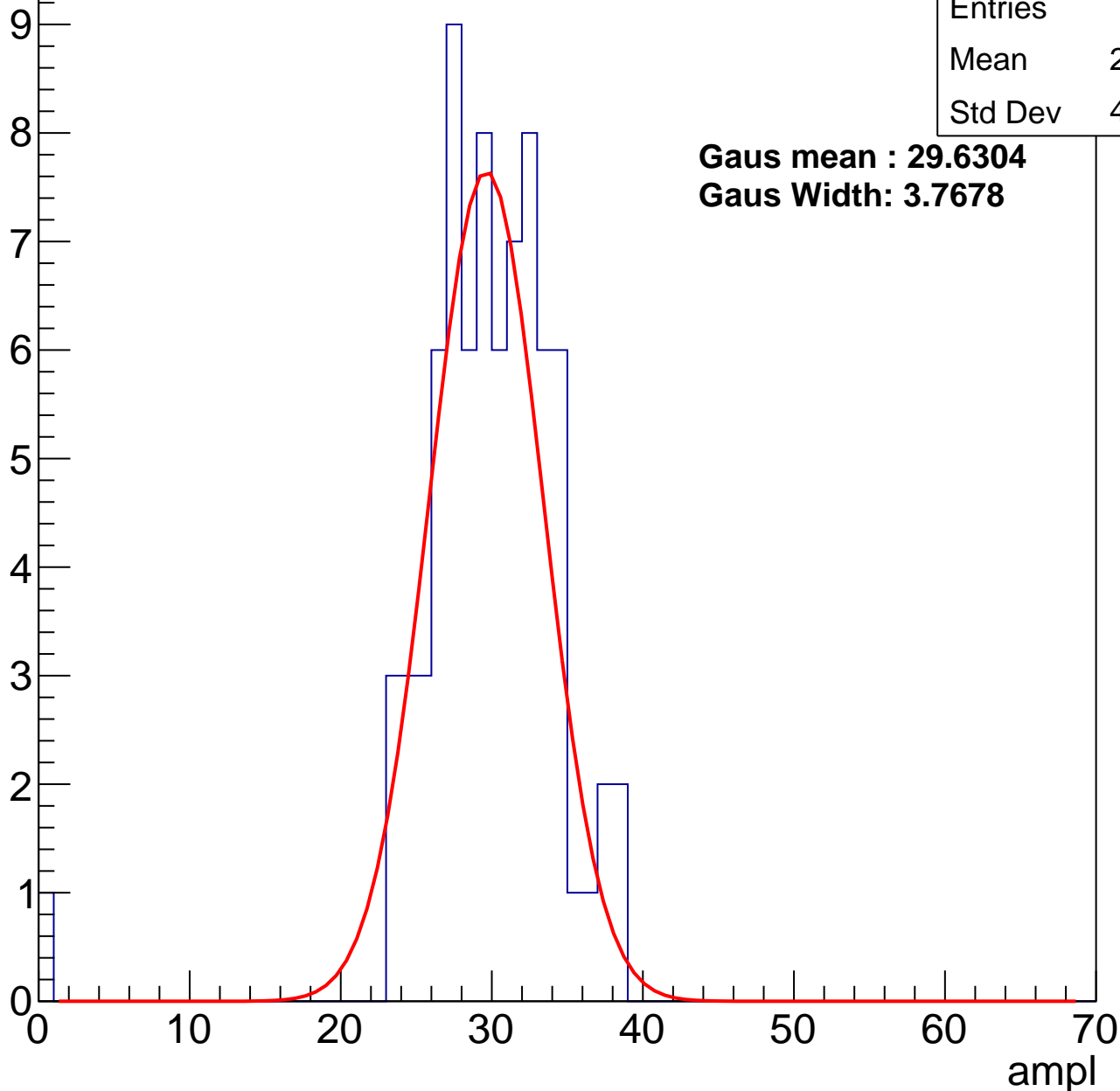
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.37
Std Dev	4.915

Gaus mean : 29.6304

Gaus Width: 3.7678



B0L002S, U7-ch103, adc1

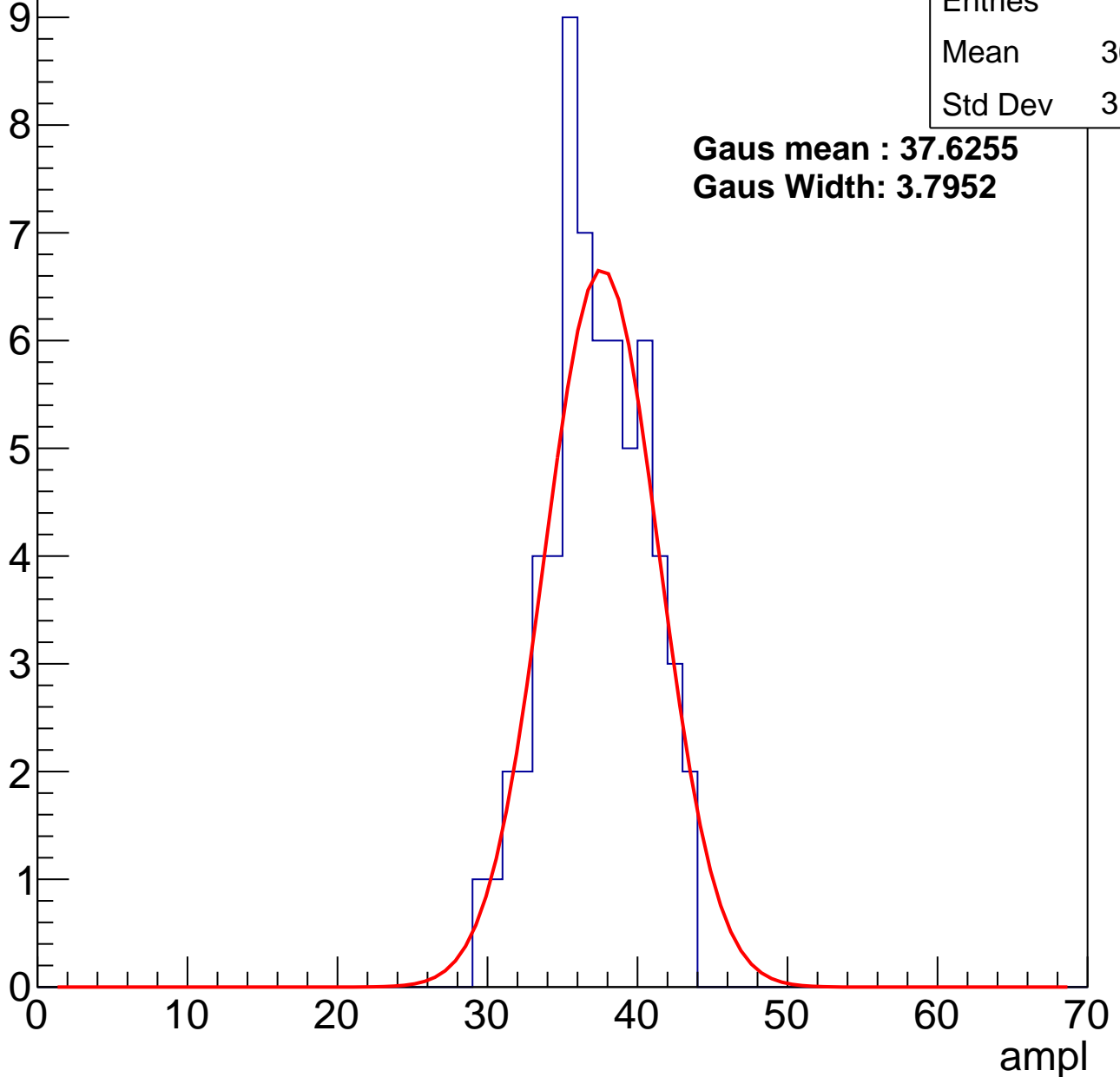
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.79
Std Dev	3.278

Gaus mean : 37.6255

Gaus Width: 3.7952



B0L002S, U7-ch103, adc2

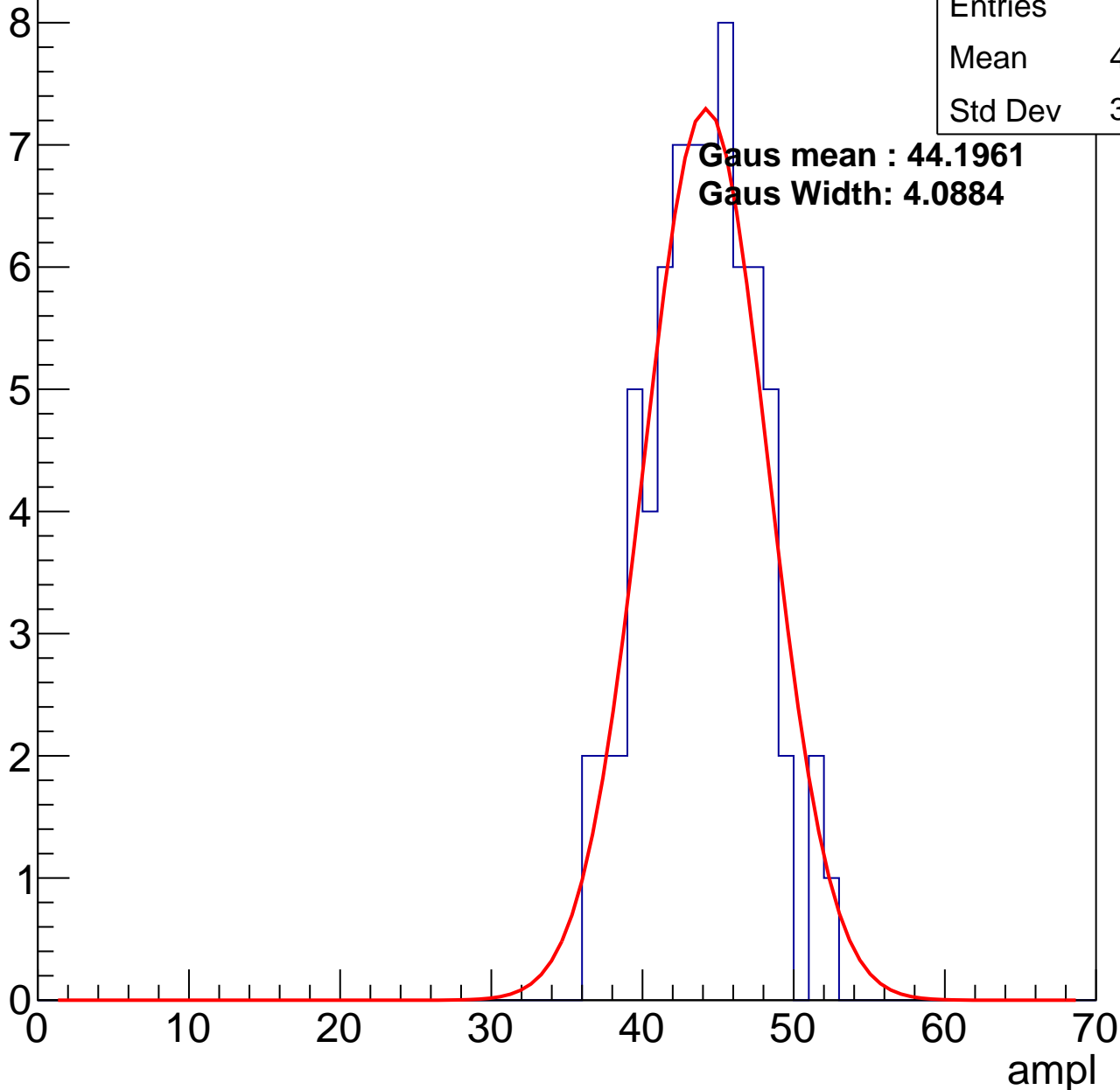
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.56
Std Dev	3.613

Gaus mean : 44.1961

Gaus Width: 4.0884

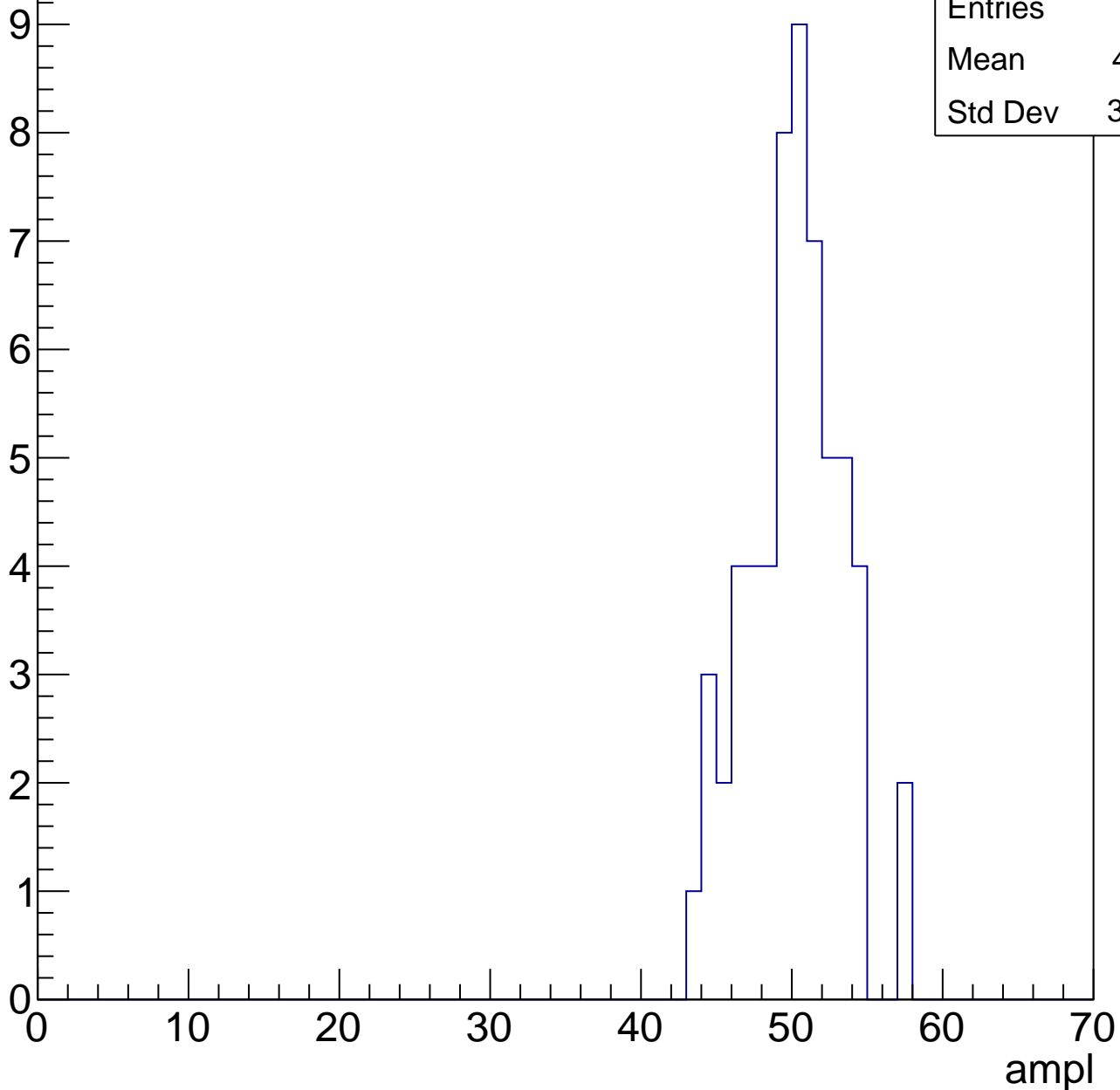


B0L002S, U7-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	49.71
Std Dev	3.107



B0L002S, U7-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

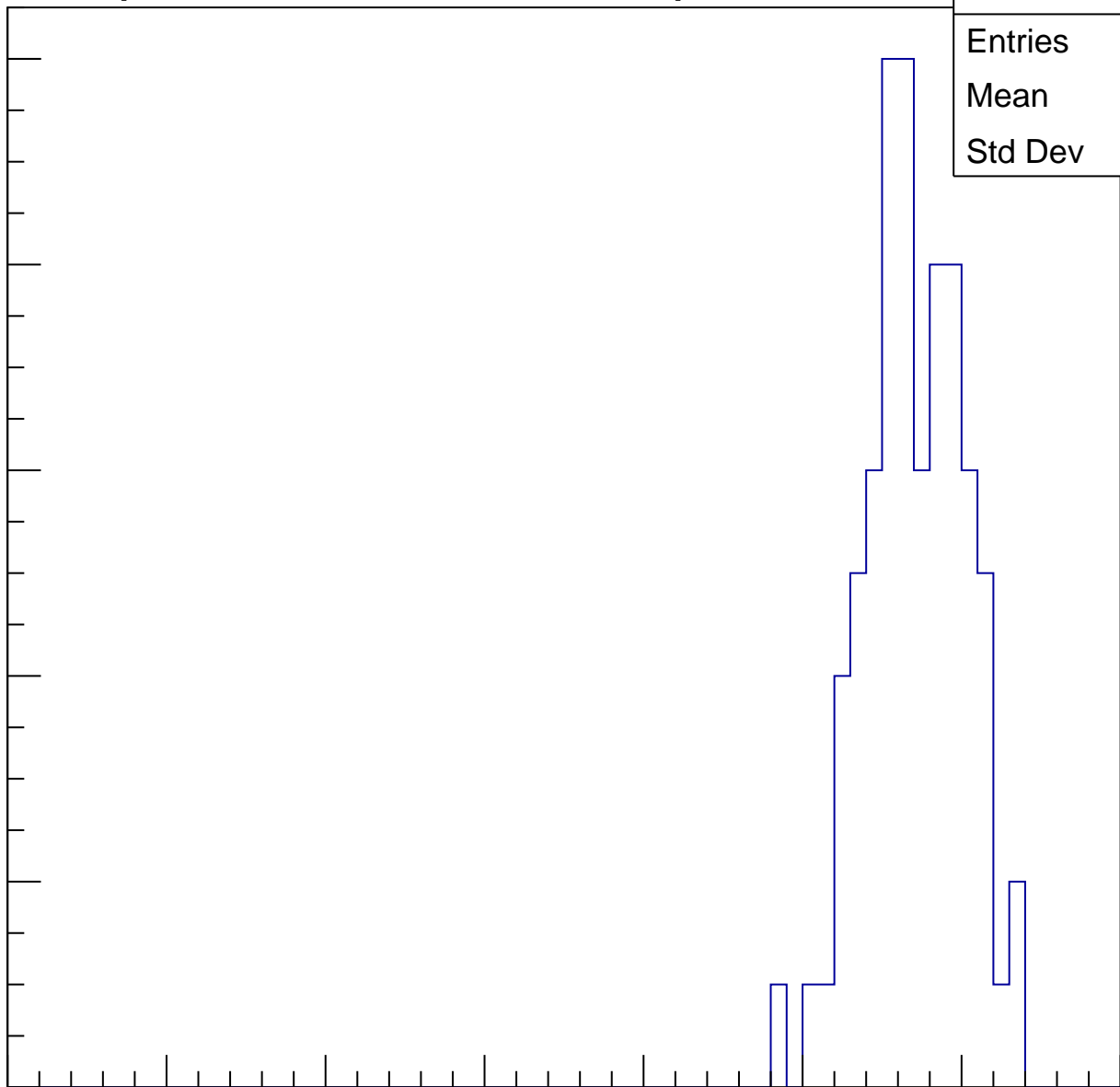
Entries	74
Mean	56.58
Std Dev	3.093

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

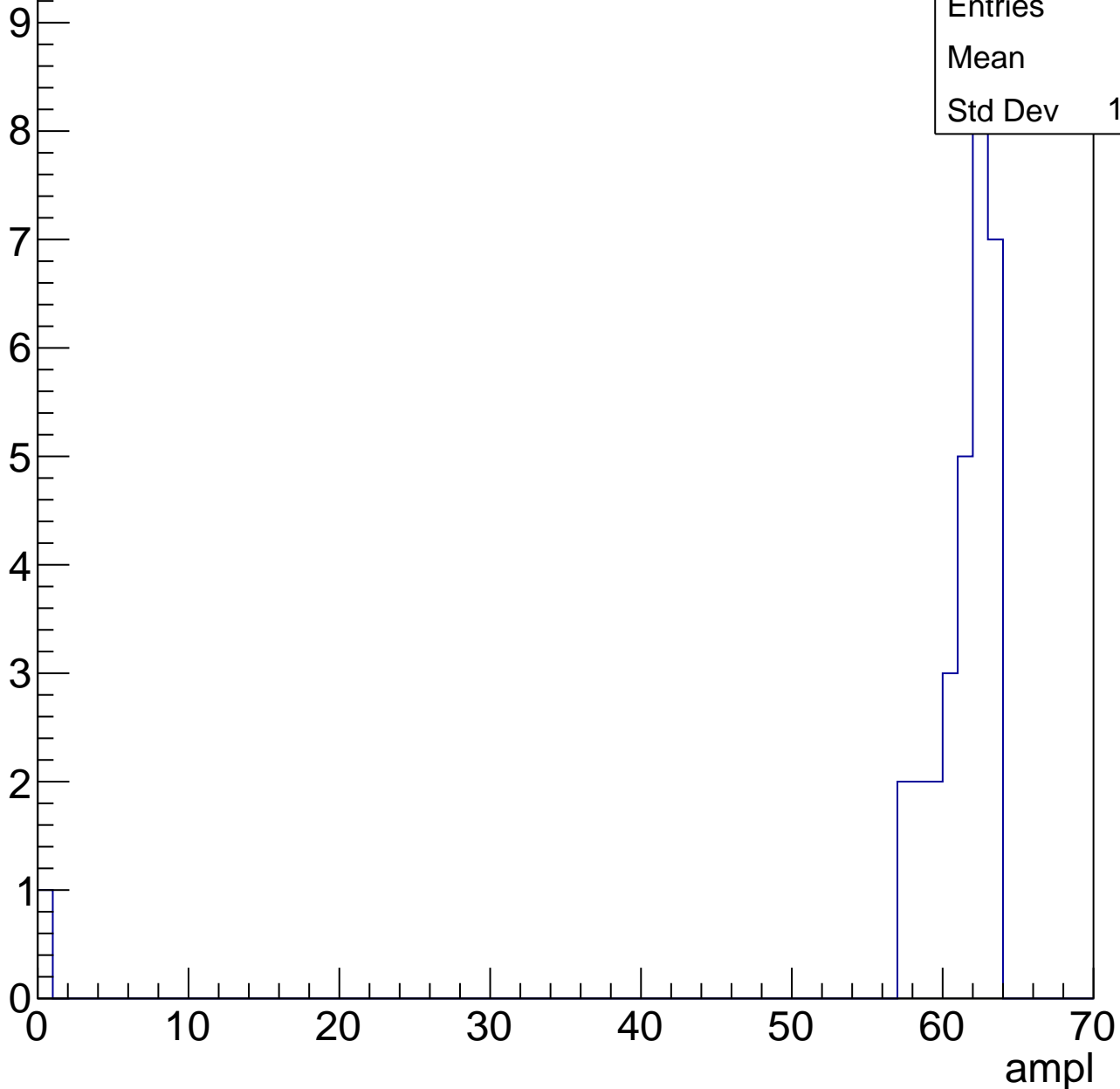


B0L002S, U7-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.1
Std Dev	10.93



B0L002S, U7-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch104, adc0

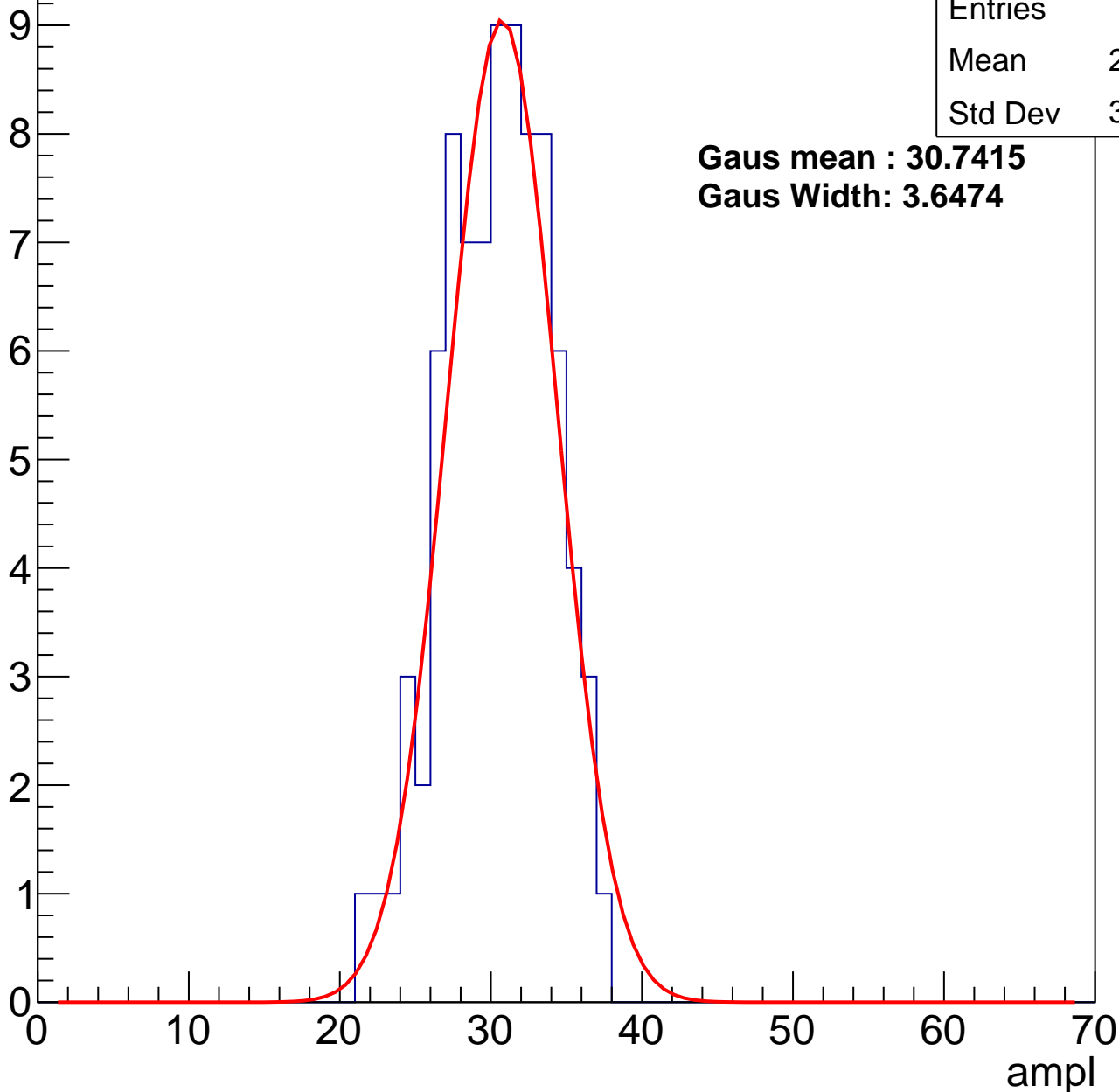
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	29.96
Std Dev	3.486

Gaus mean : 30.7415

Gaus Width: 3.6474



B0L002S, U7-ch104, adc1

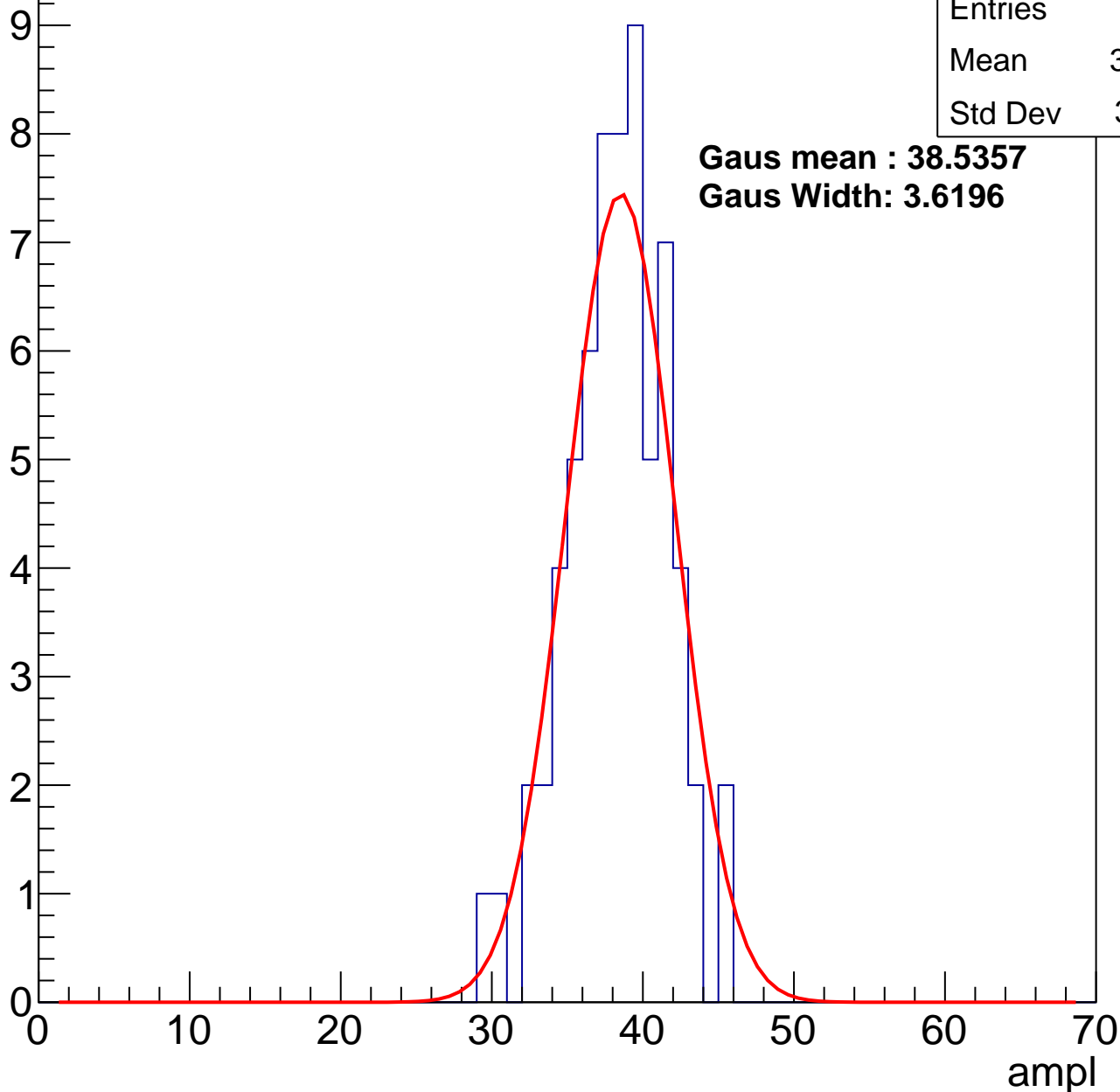
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.85
Std Dev	3.281

Gaus mean : 38.5357

Gaus Width: 3.6196



B0L002S, U7-ch104, adc2

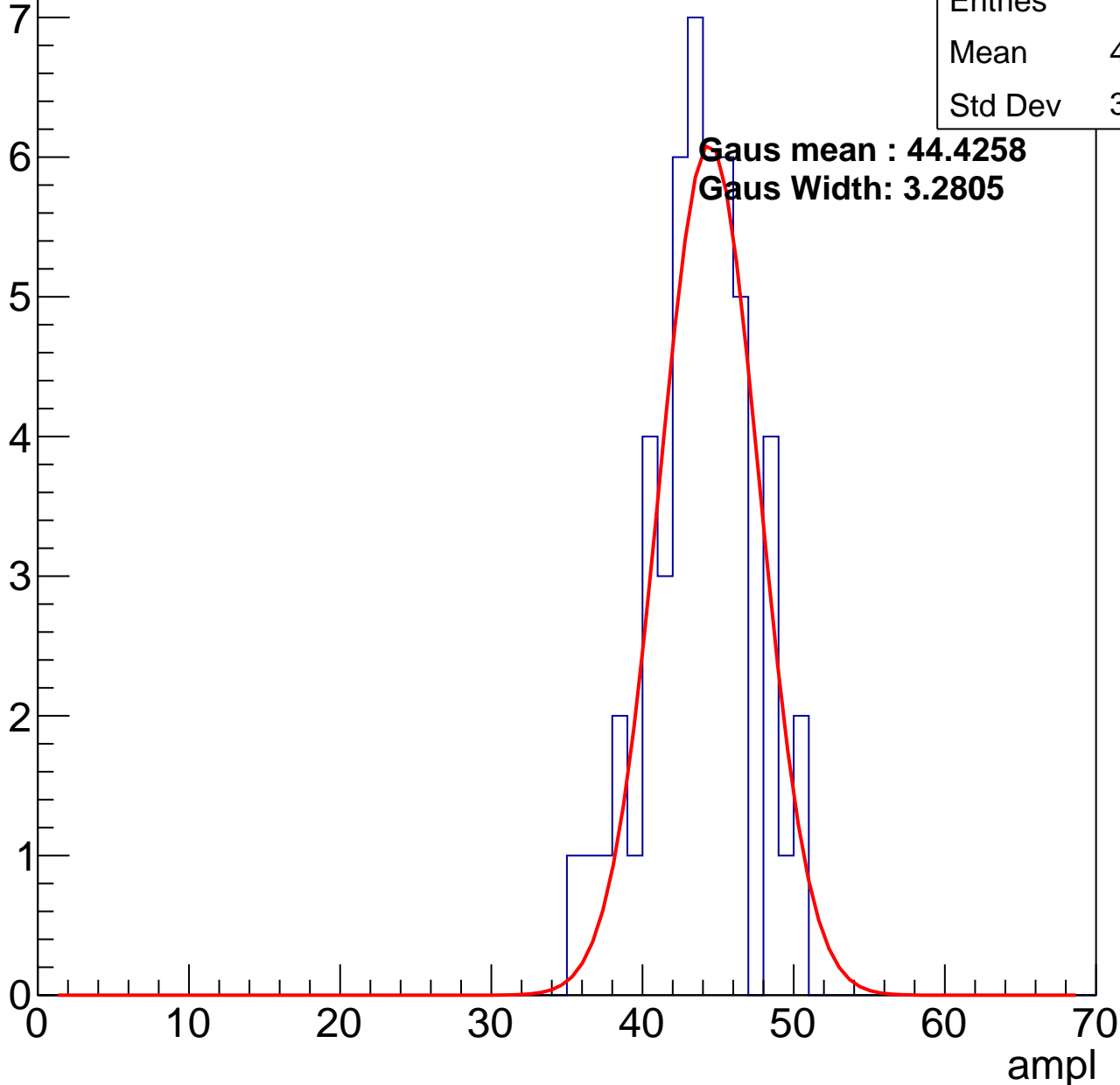
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.28
Std Dev	3.388

Gaus mean : 44.4258

Gaus Width: 3.2805



B0L002S, U7-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

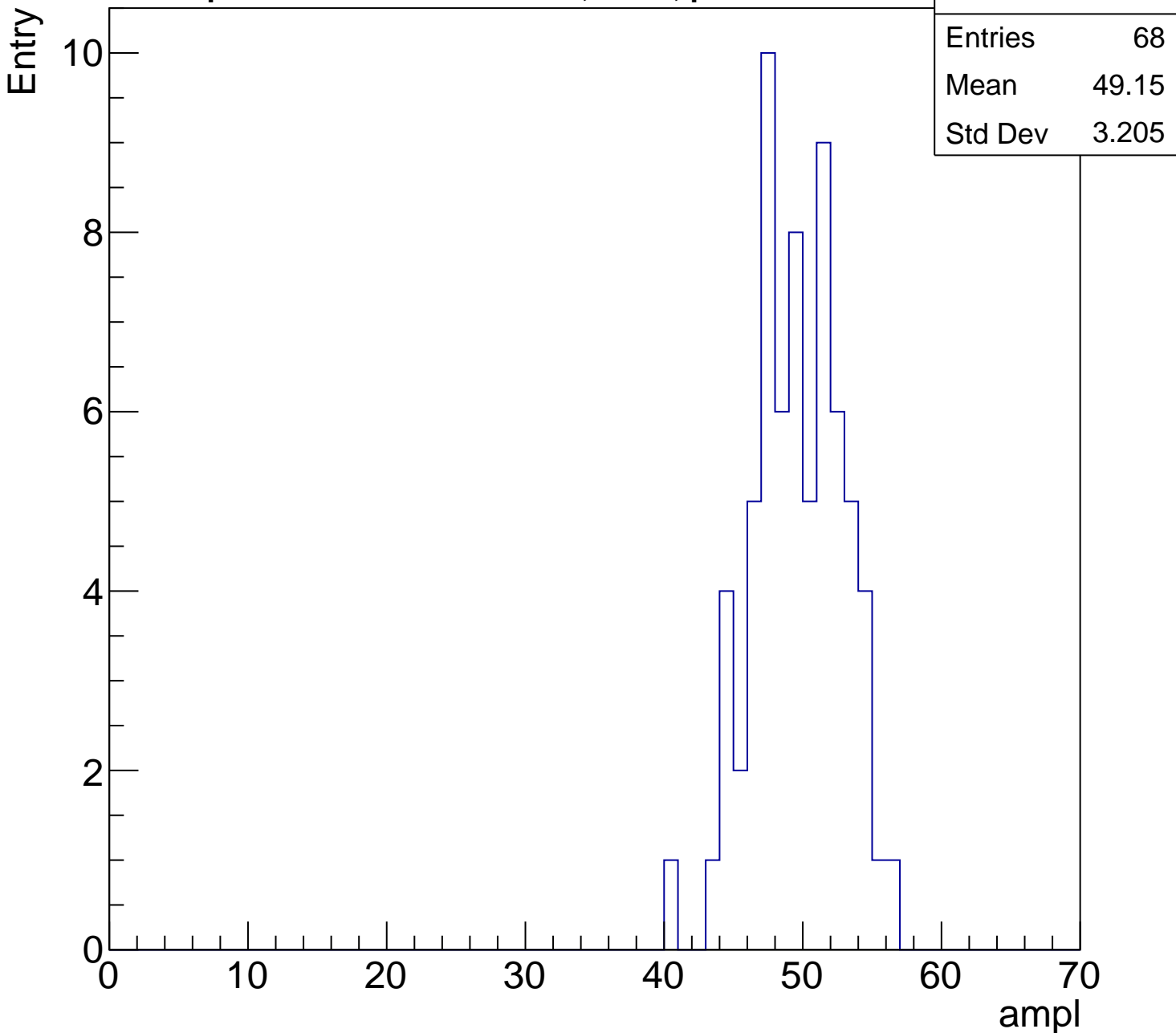
Entries	68
Mean	49.15
Std Dev	3.205

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

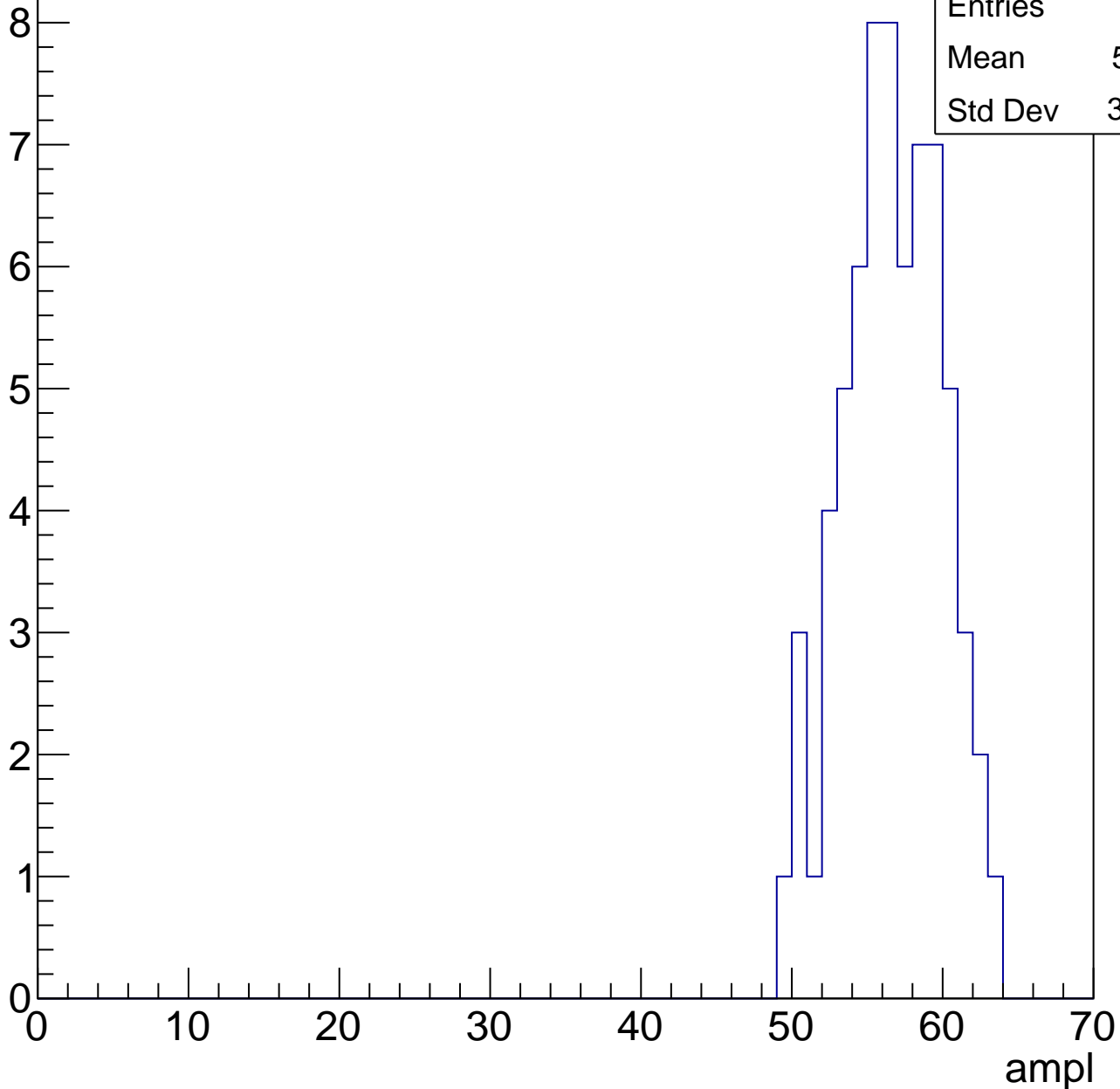


B0L002S, U7-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.21
Std Dev	3.216

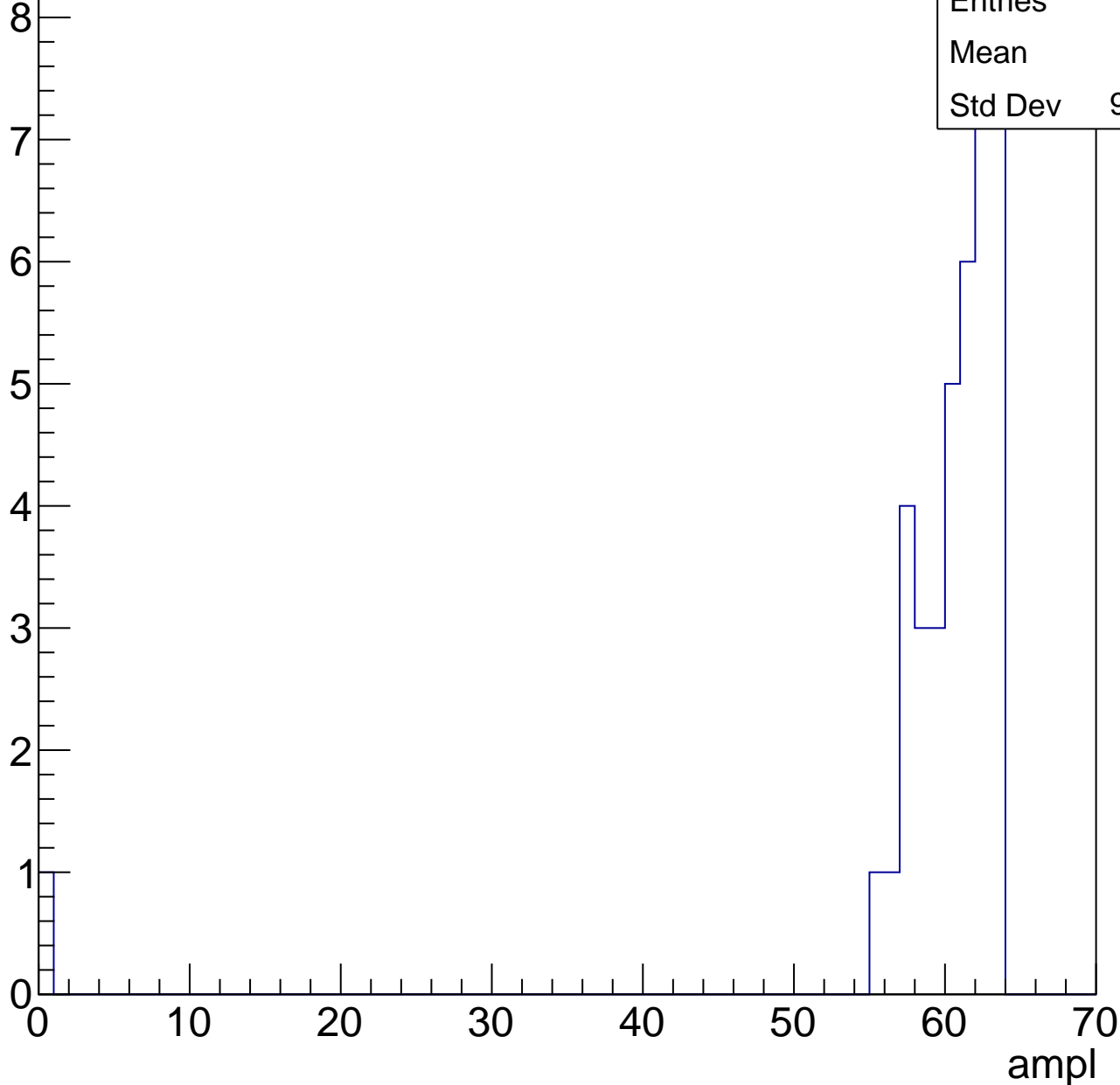


B0L002S, U7-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.9
Std Dev	9.687



B0L002S, U7-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch105, adc0

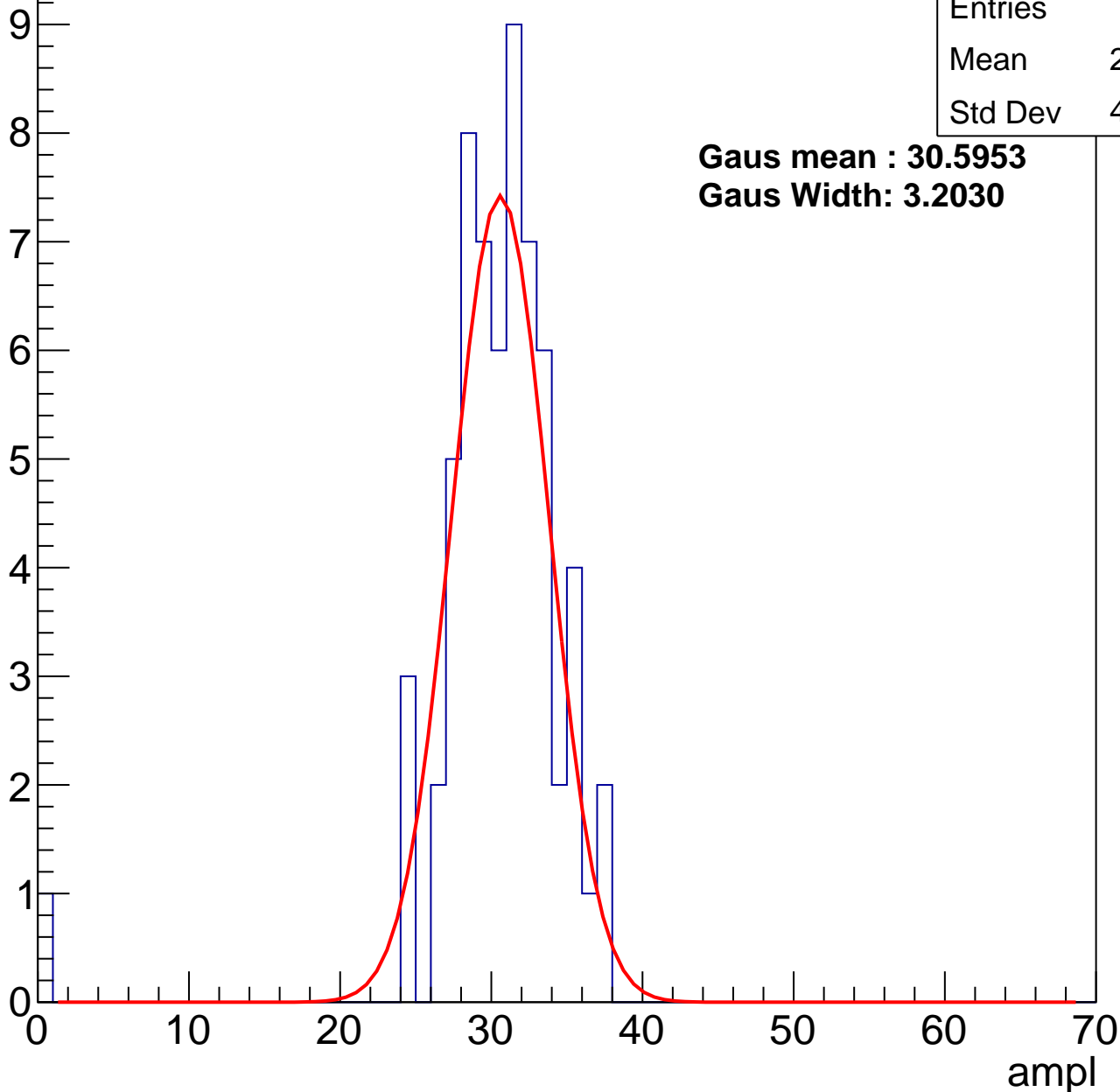
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.92
Std Dev	4.848

Gaus mean : 30.5953

Gaus Width: 3.2030



B0L002S, U7-ch105, adc1

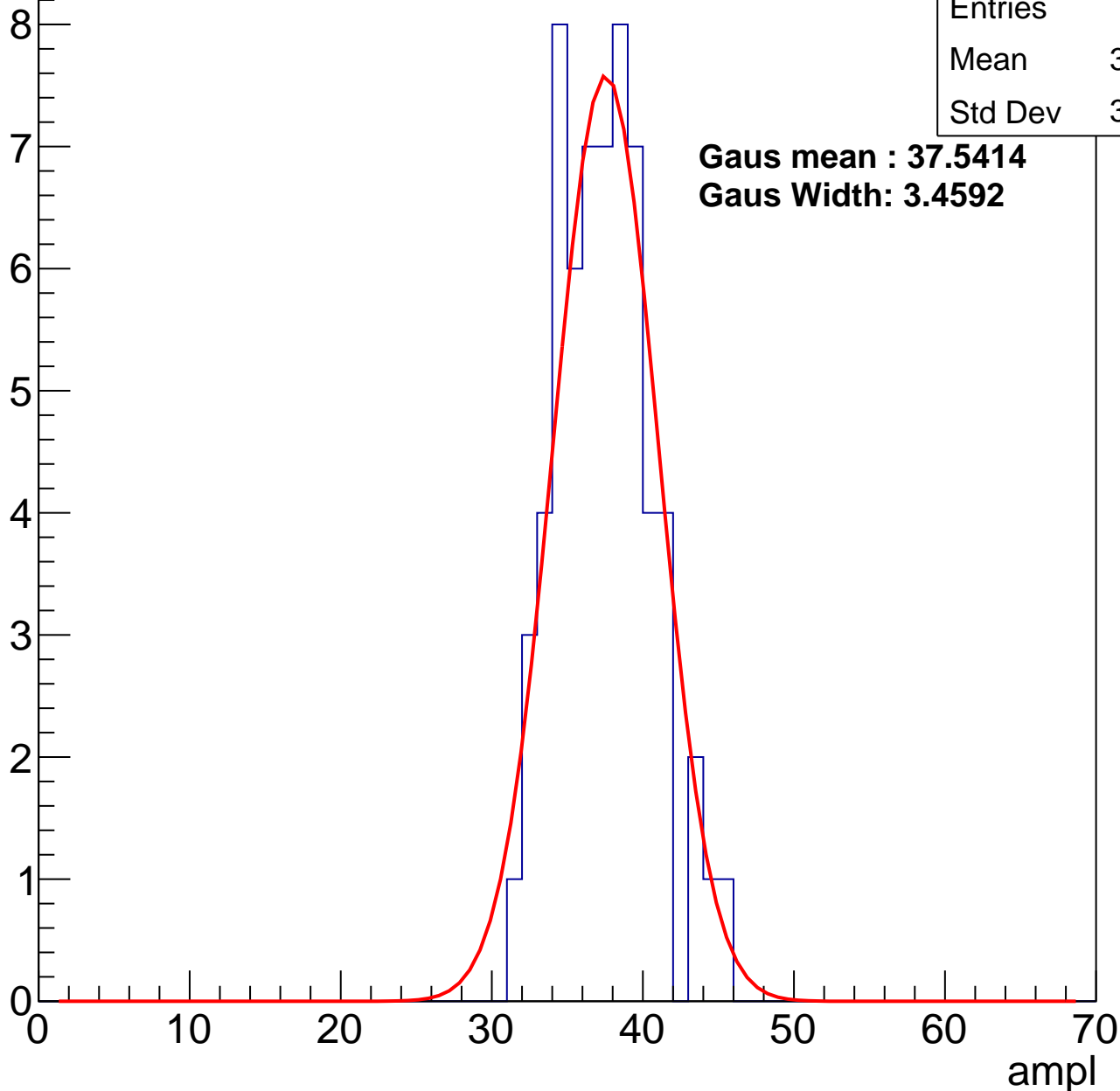
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.95
Std Dev	3.083

Gaus mean : 37.5414

Gaus Width: 3.4592



B0L002S, U7-ch105, adc2

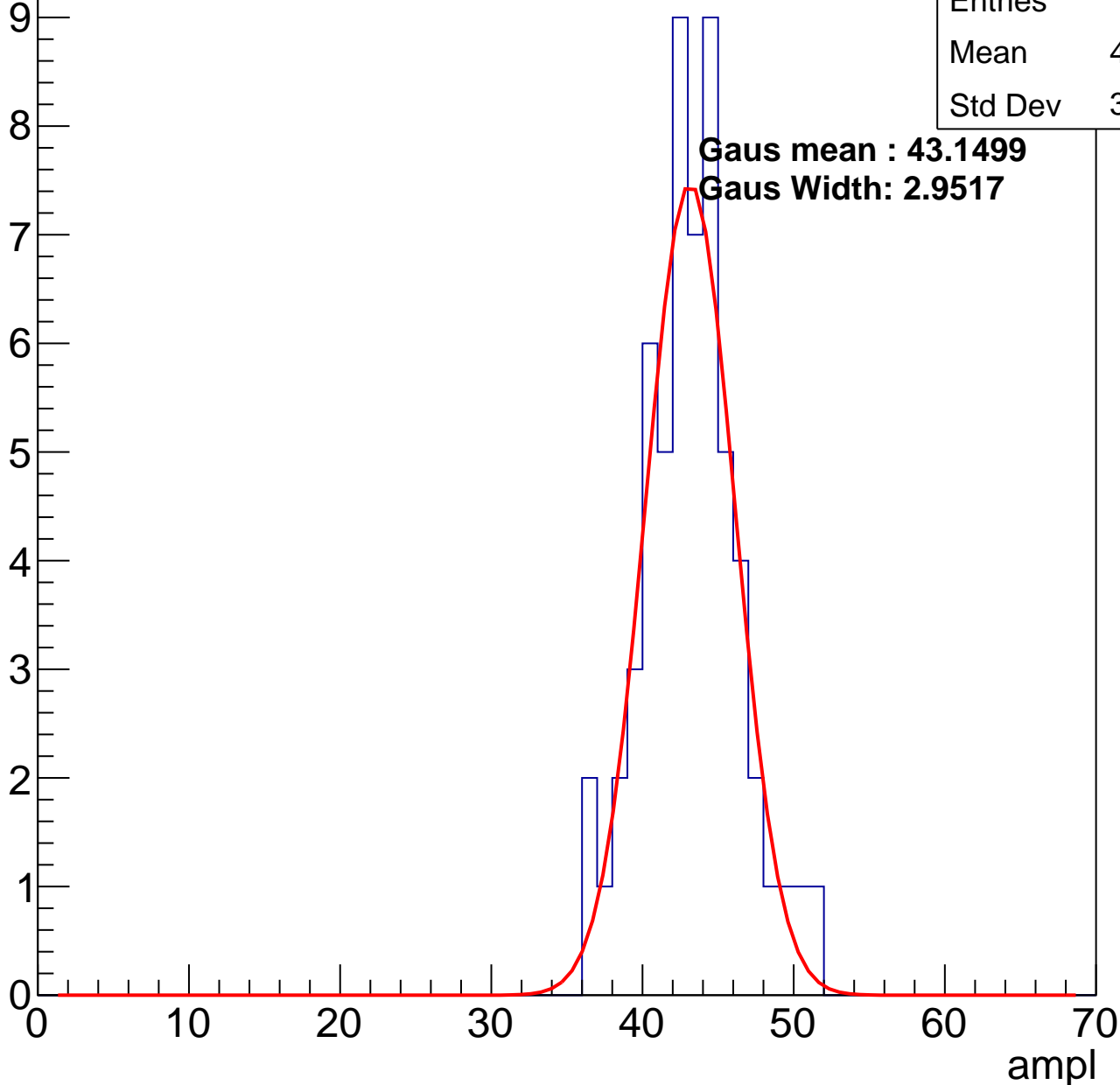
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	42.76
Std Dev	3.143

Gaus mean : 43.1499

Gaus Width: 2.9517

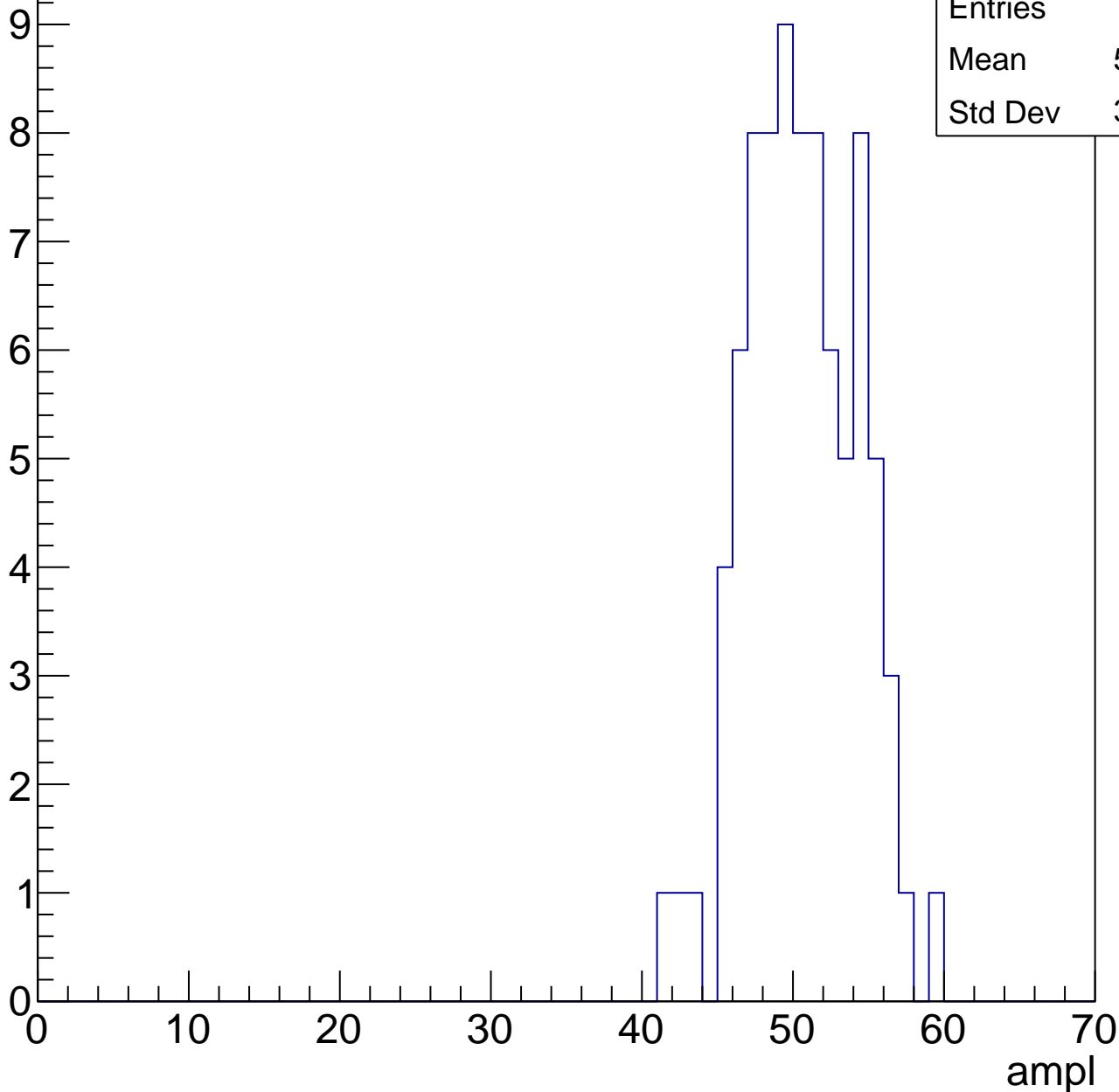


B0L002S, U7-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

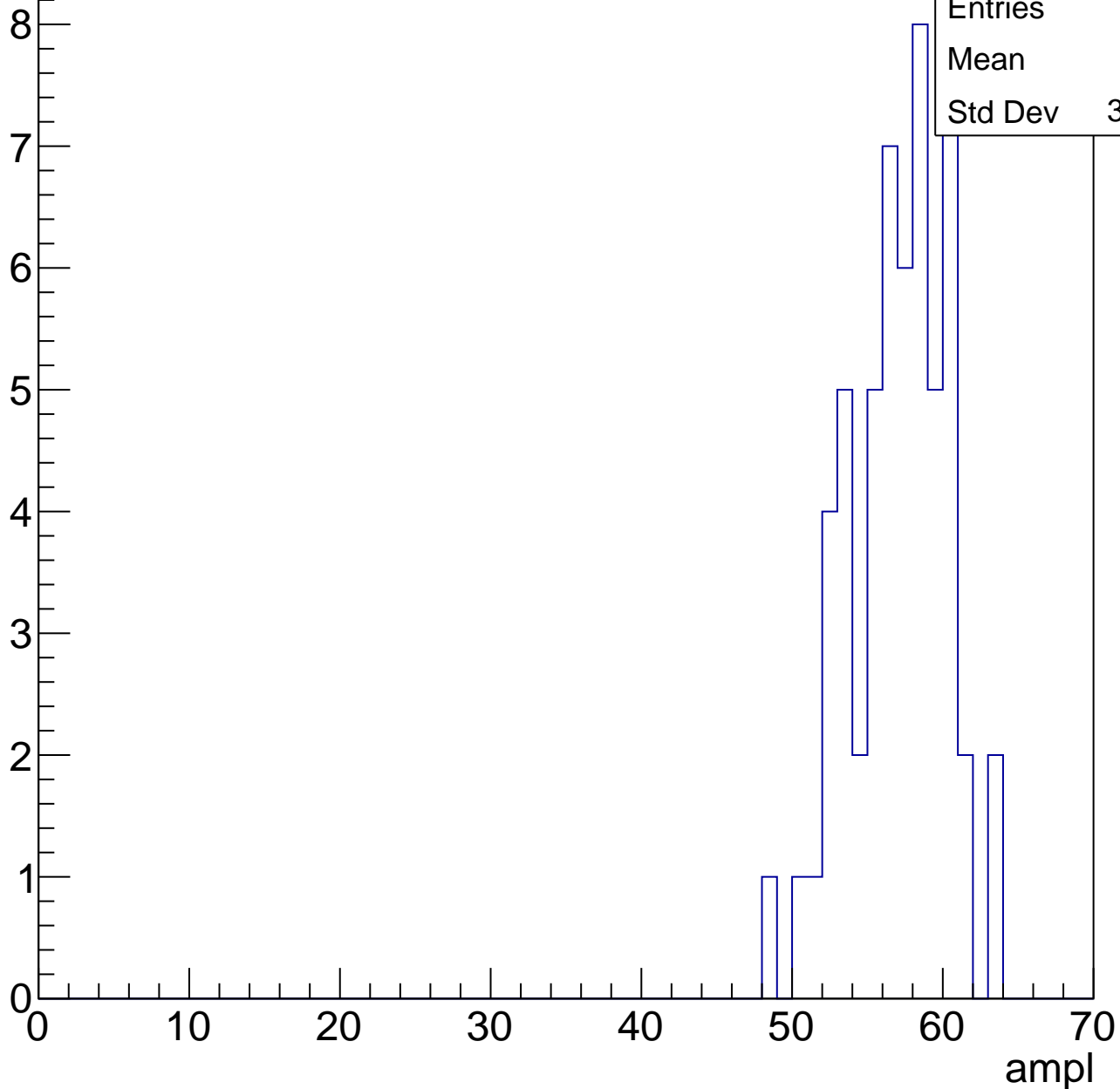
Entries	83
Mean	50.11
Std Dev	3.591



B0L002S, U7-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



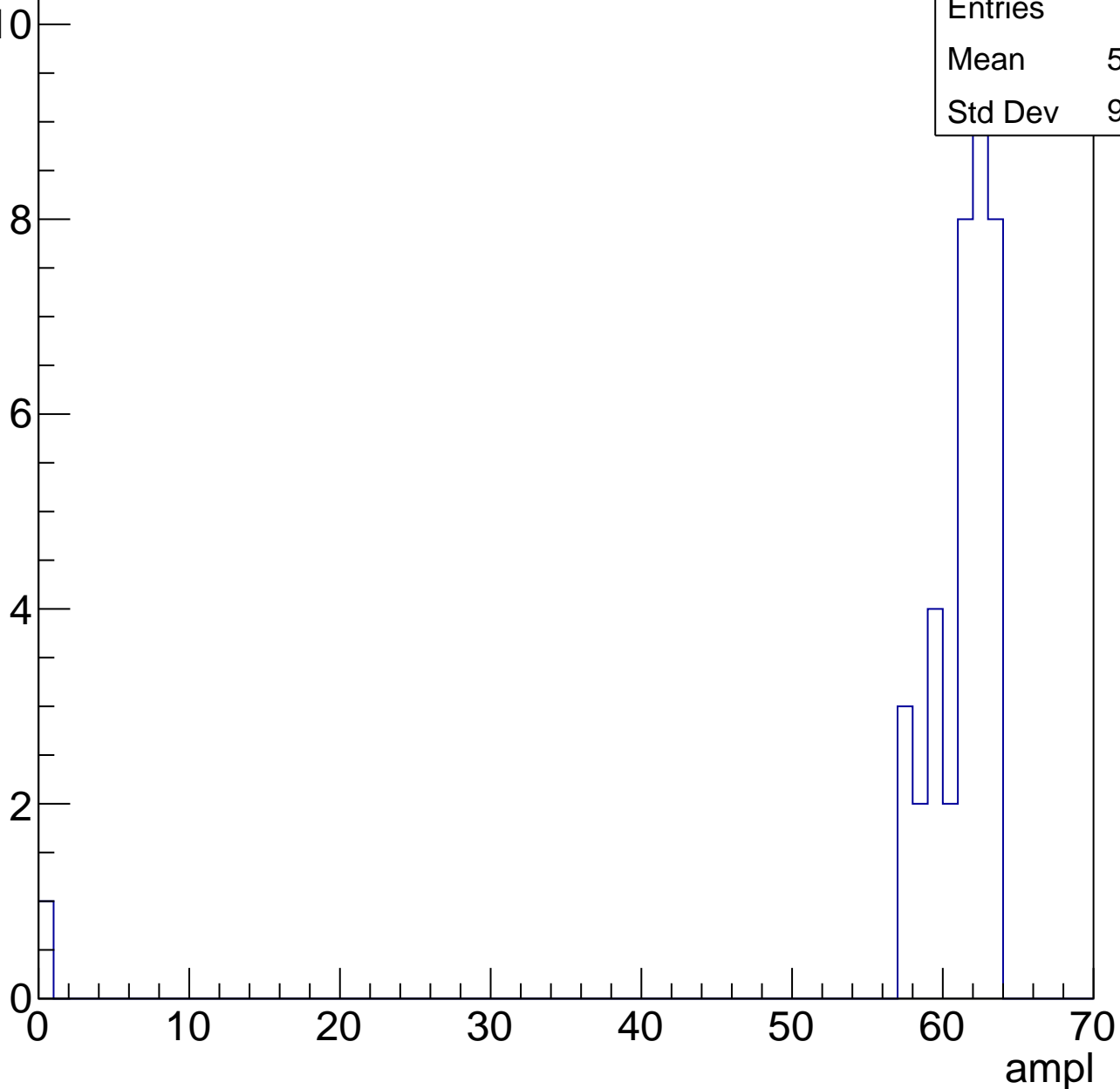
Entries	57
Mean	56.6
Std Dev	3.206

B0L002S, U7-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.34
Std Dev	9.924



B0L002S, U7-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch106, adc0

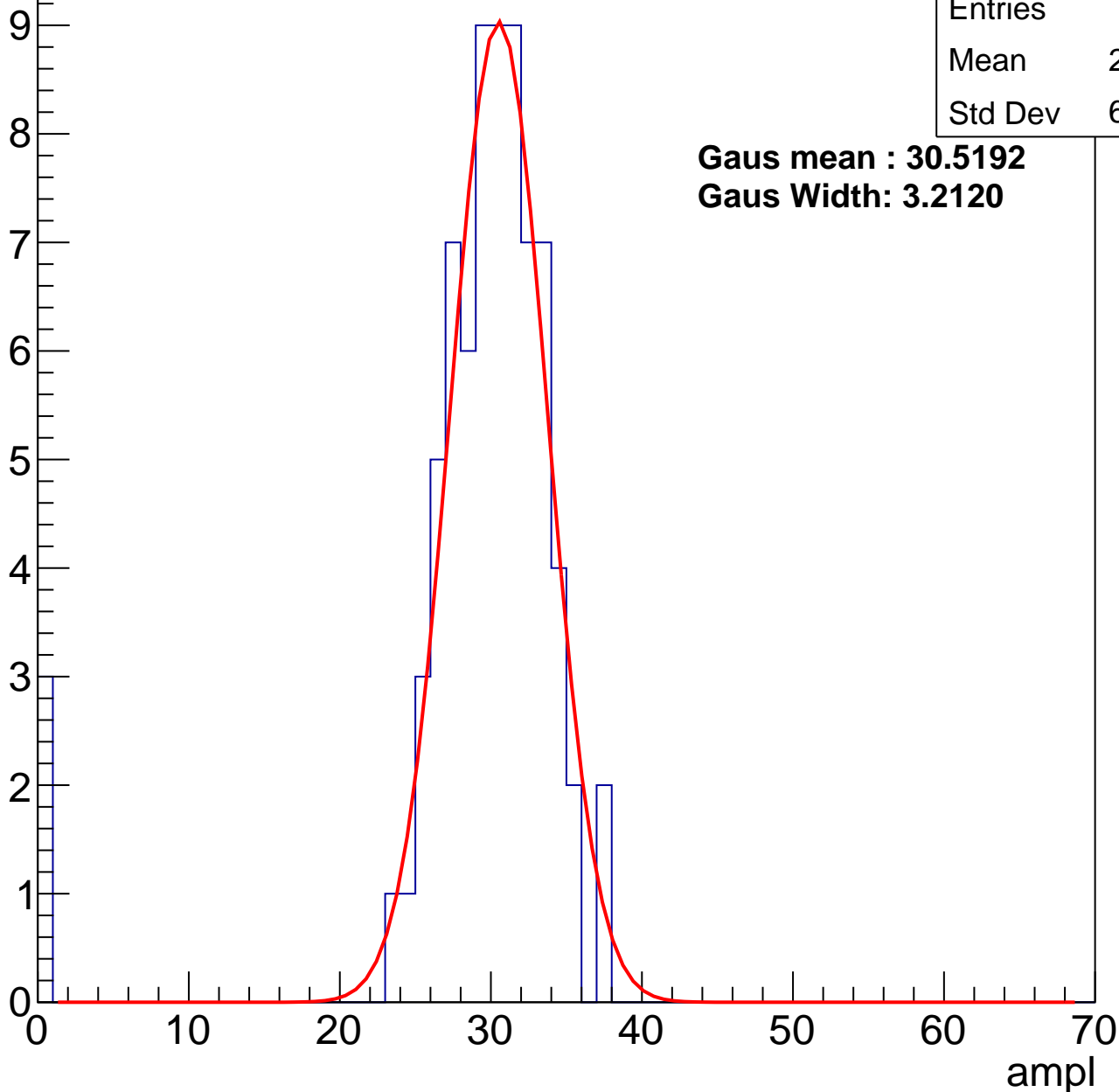
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	28.72
Std Dev	6.558

Gaus mean : 30.5192

Gaus Width: 3.2120



B0L002S, U7-ch106, adc1

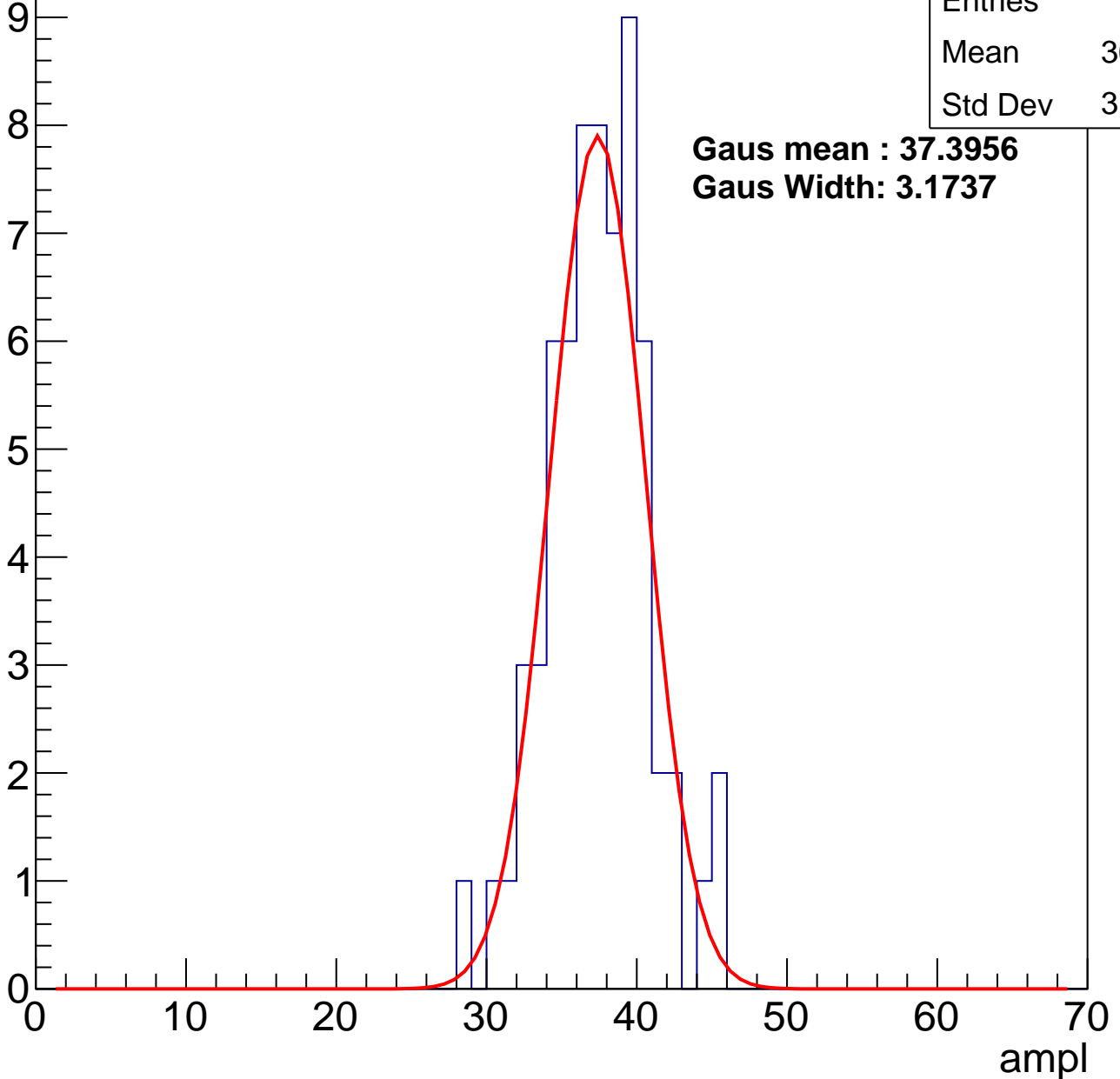
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.95
Std Dev	3.328

Gaus mean : 37.3956

Gaus Width: 3.1737



B0L002S, U7-ch106, adc2

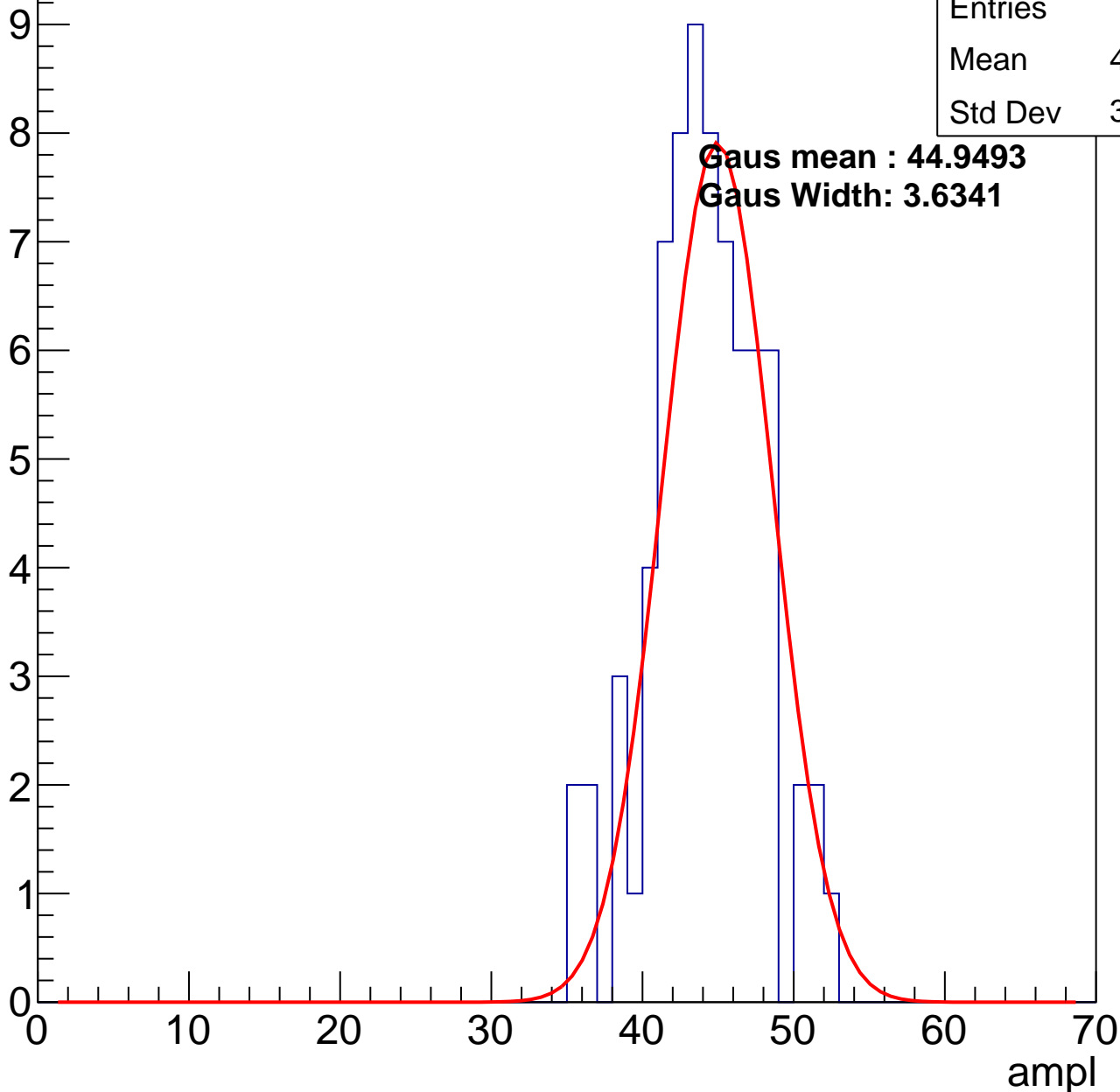
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.68
Std Dev	3.673

Gaus mean : 44.9493

Gaus Width: 3.6341

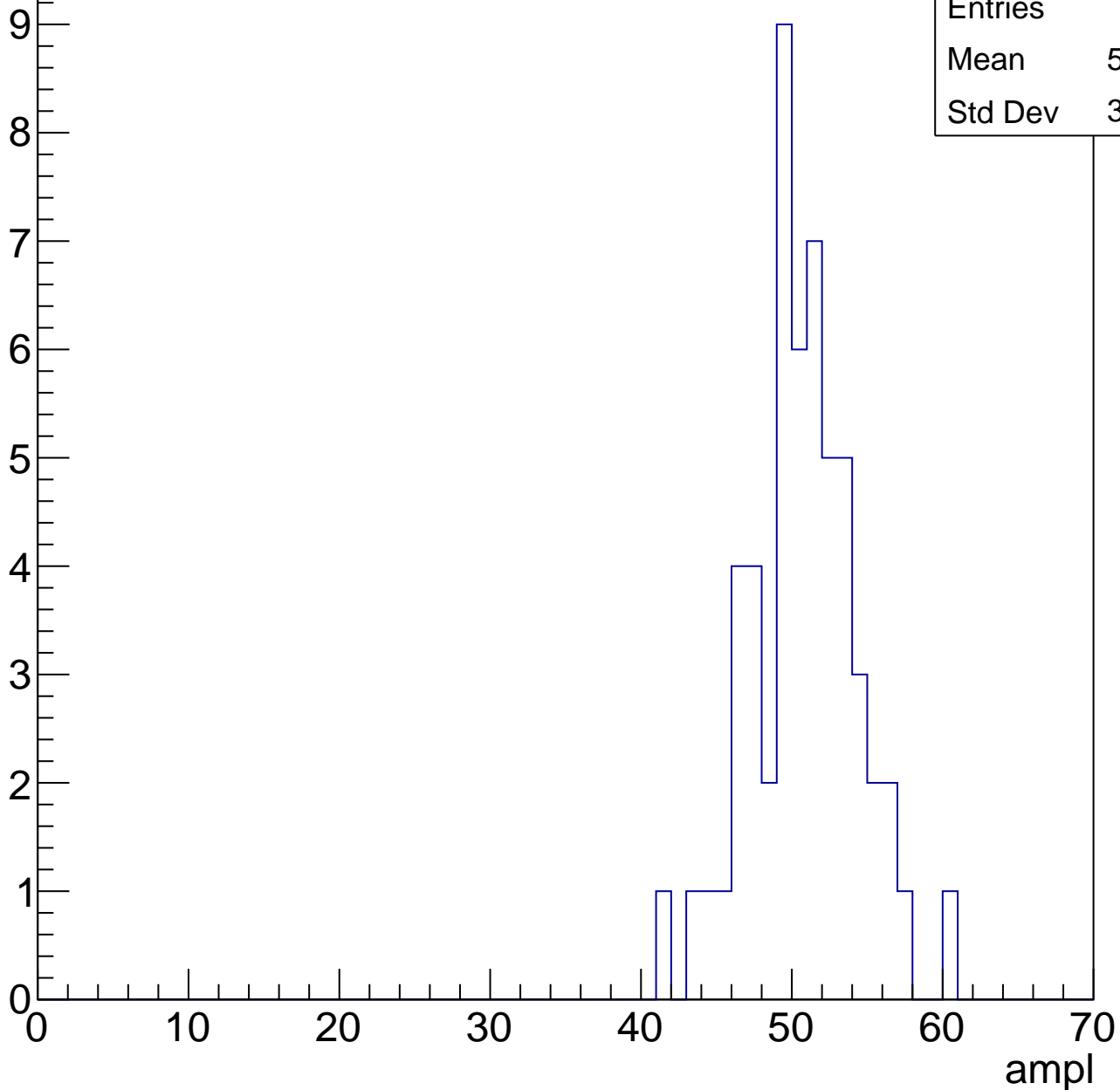


B0L002S, U7-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

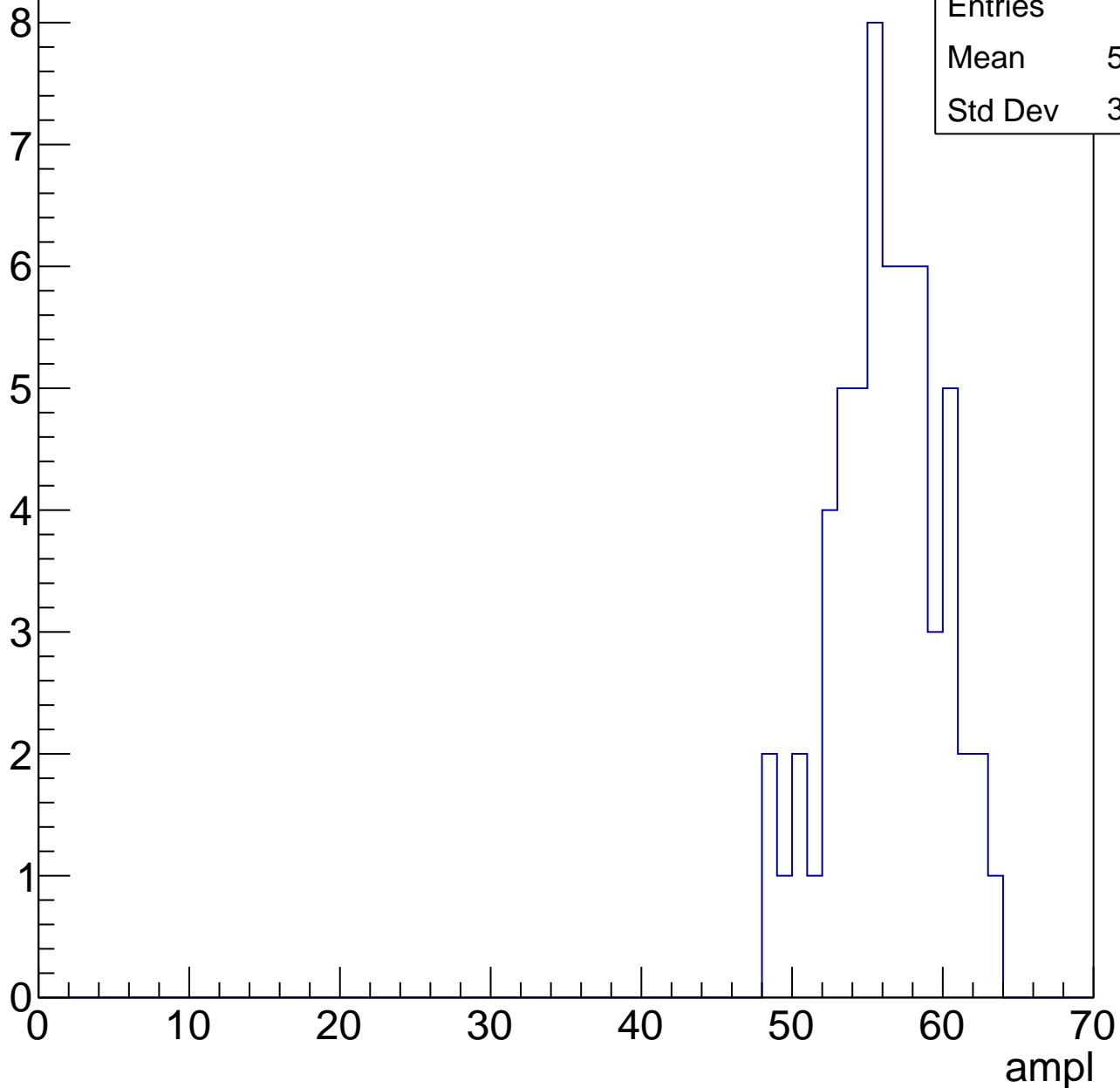
Entries	55
Mean	50.27
Std Dev	3.555



B0L002S, U7-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



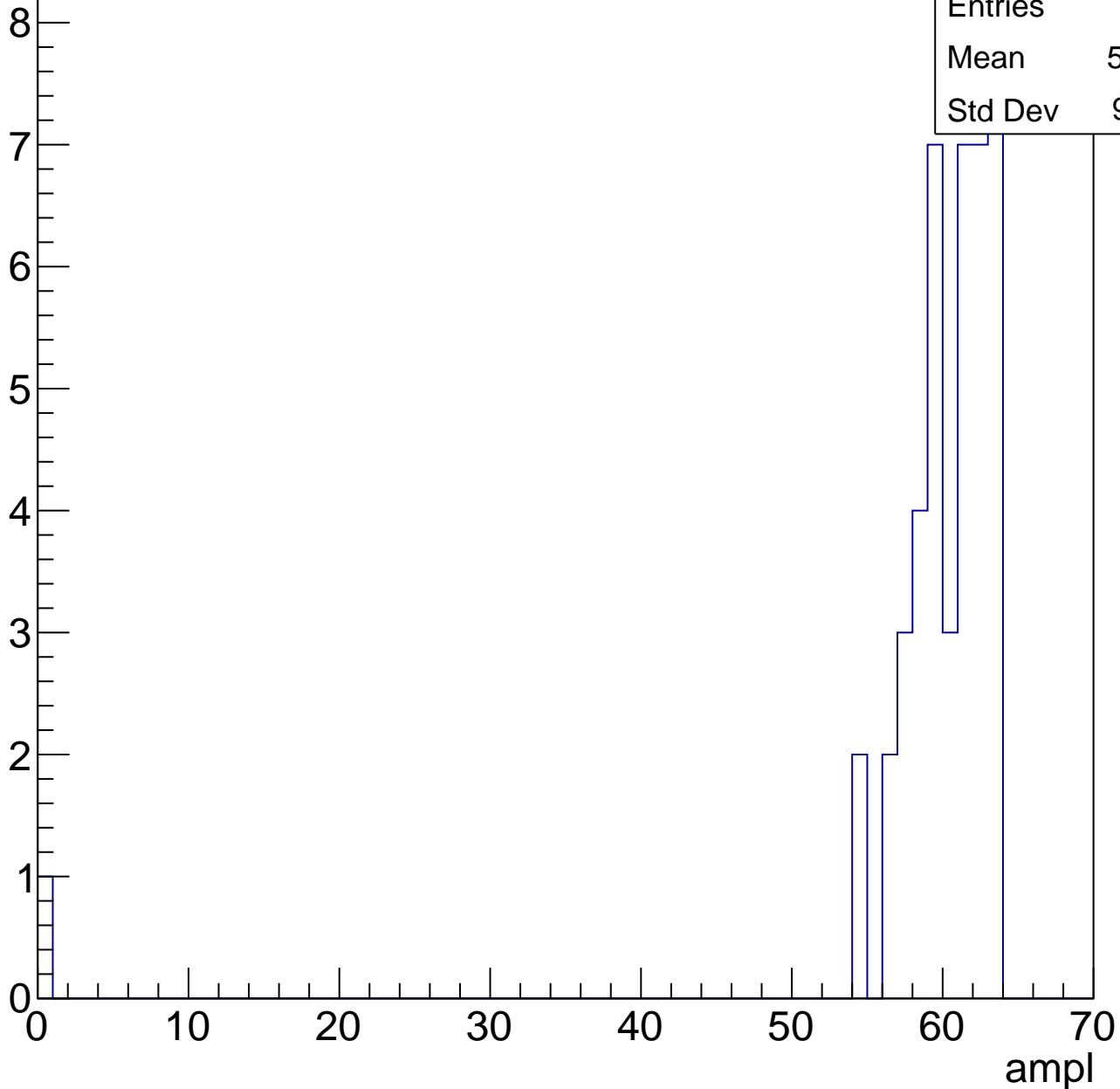
Entries	59
Mean	55.78
Std Dev	3.484

B0L002S, U7-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	58.66
Std Dev	9.271



B0L002S, U7-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch107, adc0

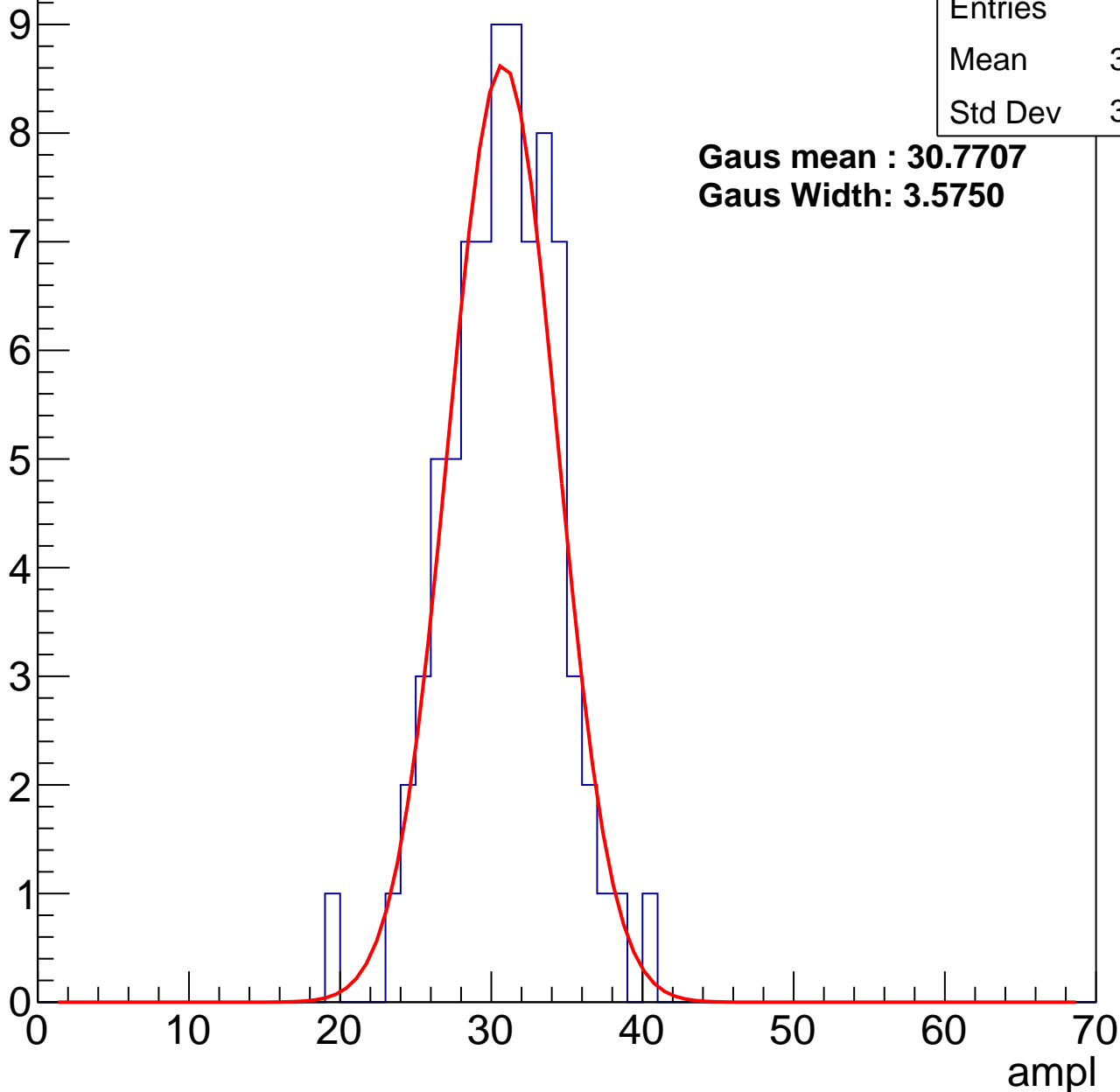
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.33
Std Dev	3.645

Gaus mean : 30.7707

Gaus Width: 3.5750



B0L002S, U7-ch107, adc1

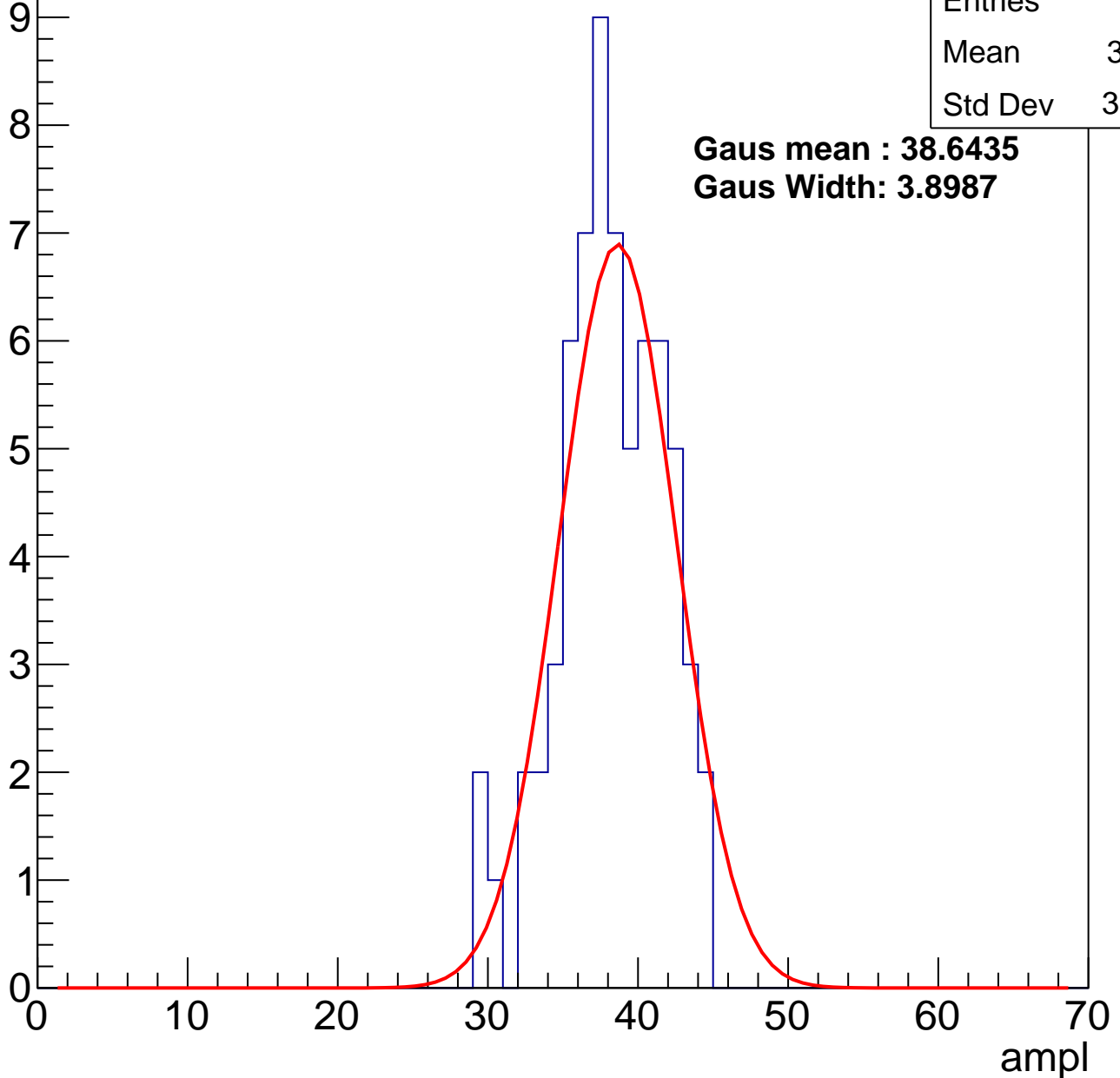
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.71
Std Dev	3.463

Gaus mean : 38.6435

Gaus Width: 3.8987



B0L002S, U7-ch107, adc2

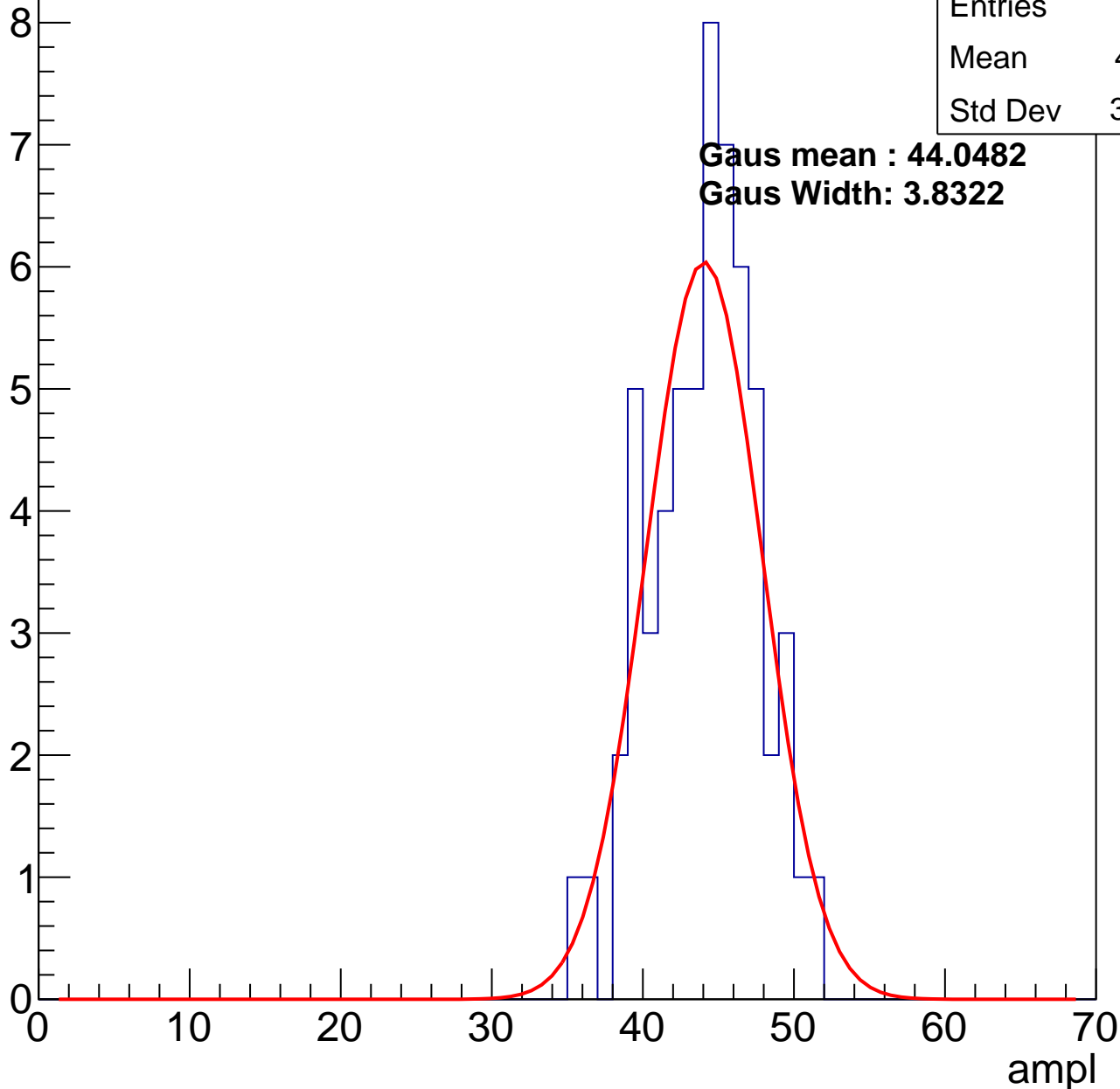
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.61
Std Dev	3.469

Gaus mean : 44.0482

Gaus Width: 3.8322

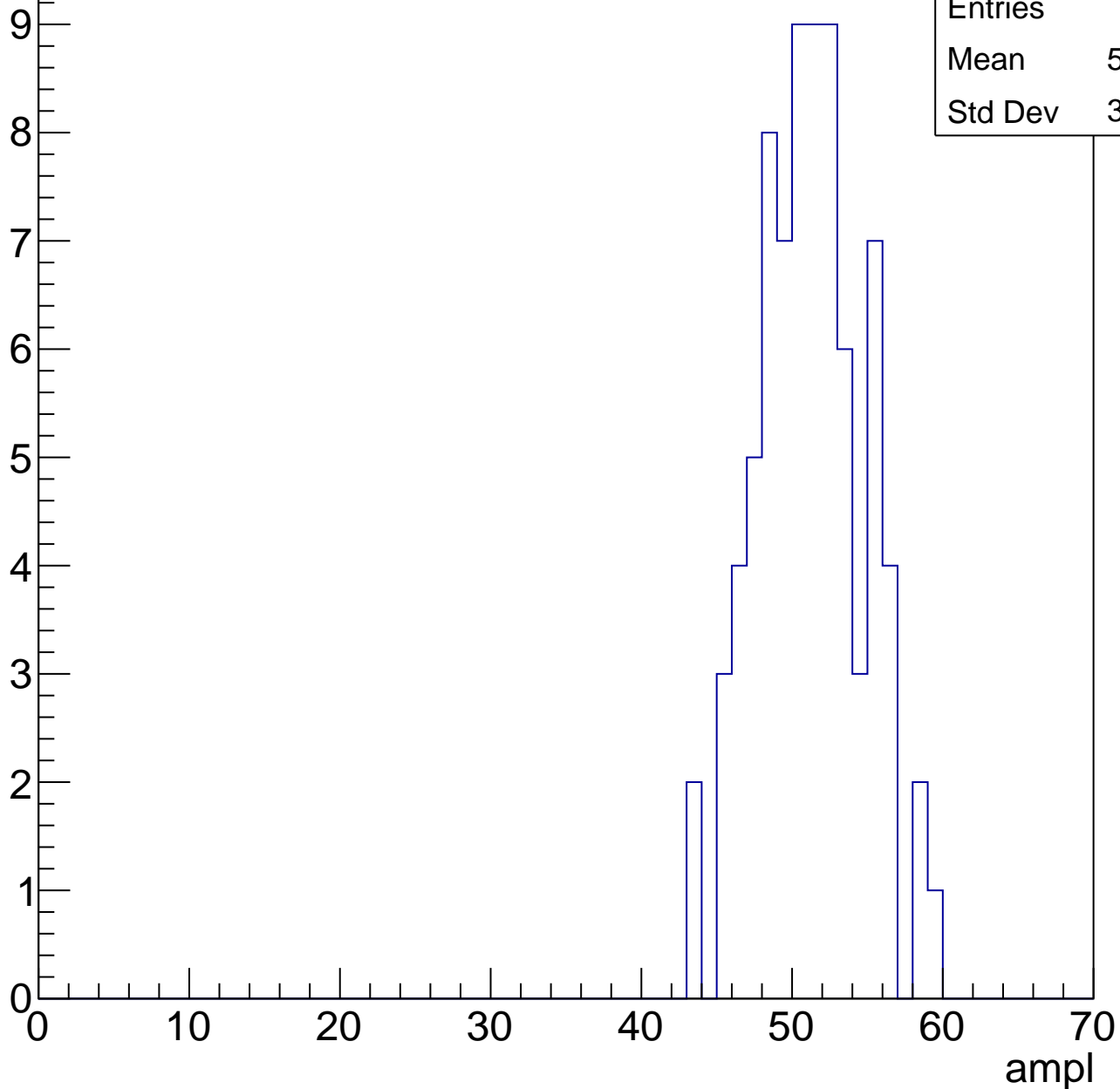


B0L002S, U7-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	50.73
Std Dev	3.474

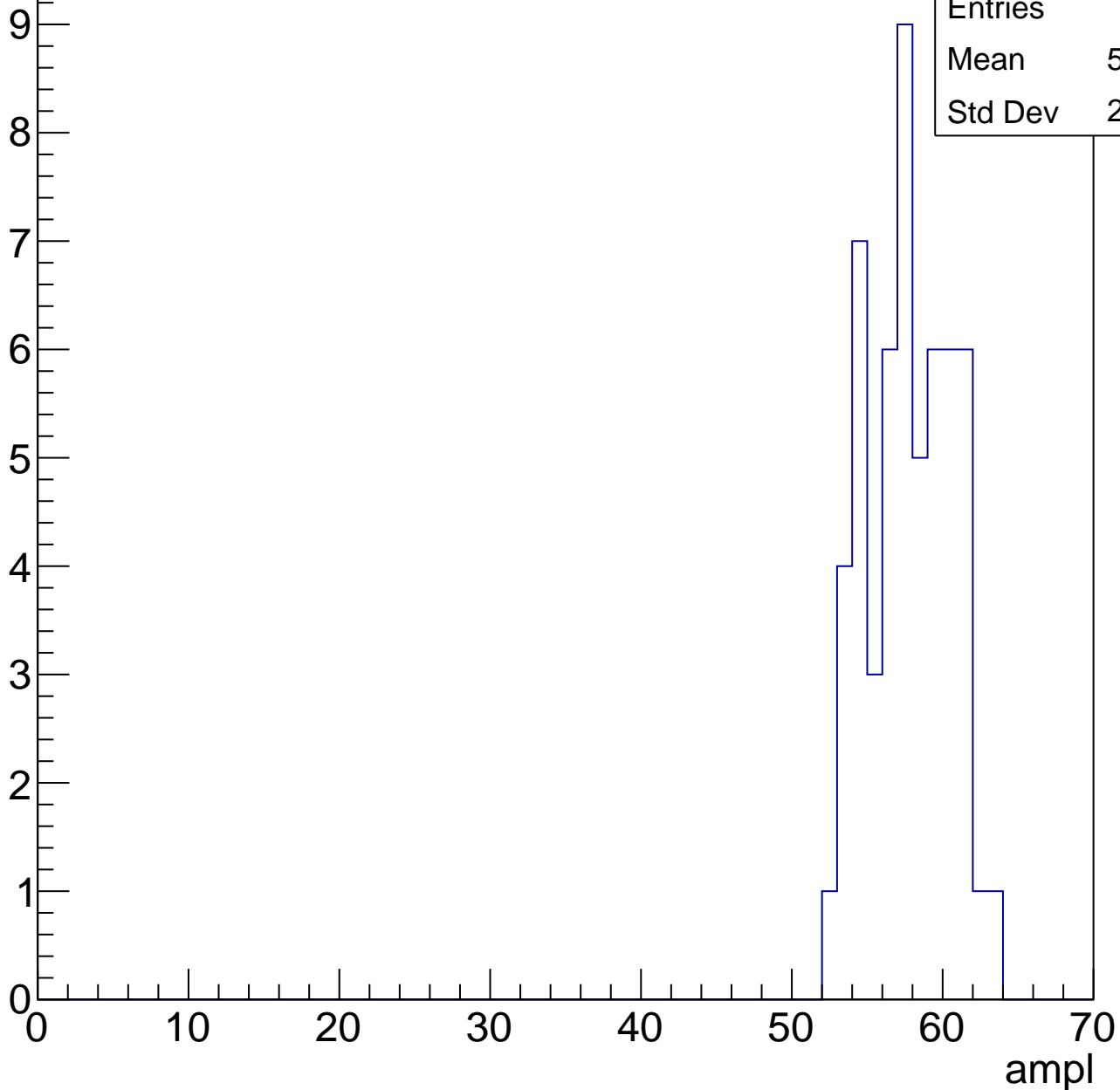


B0L002S, U7-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.29
Std Dev	2.715

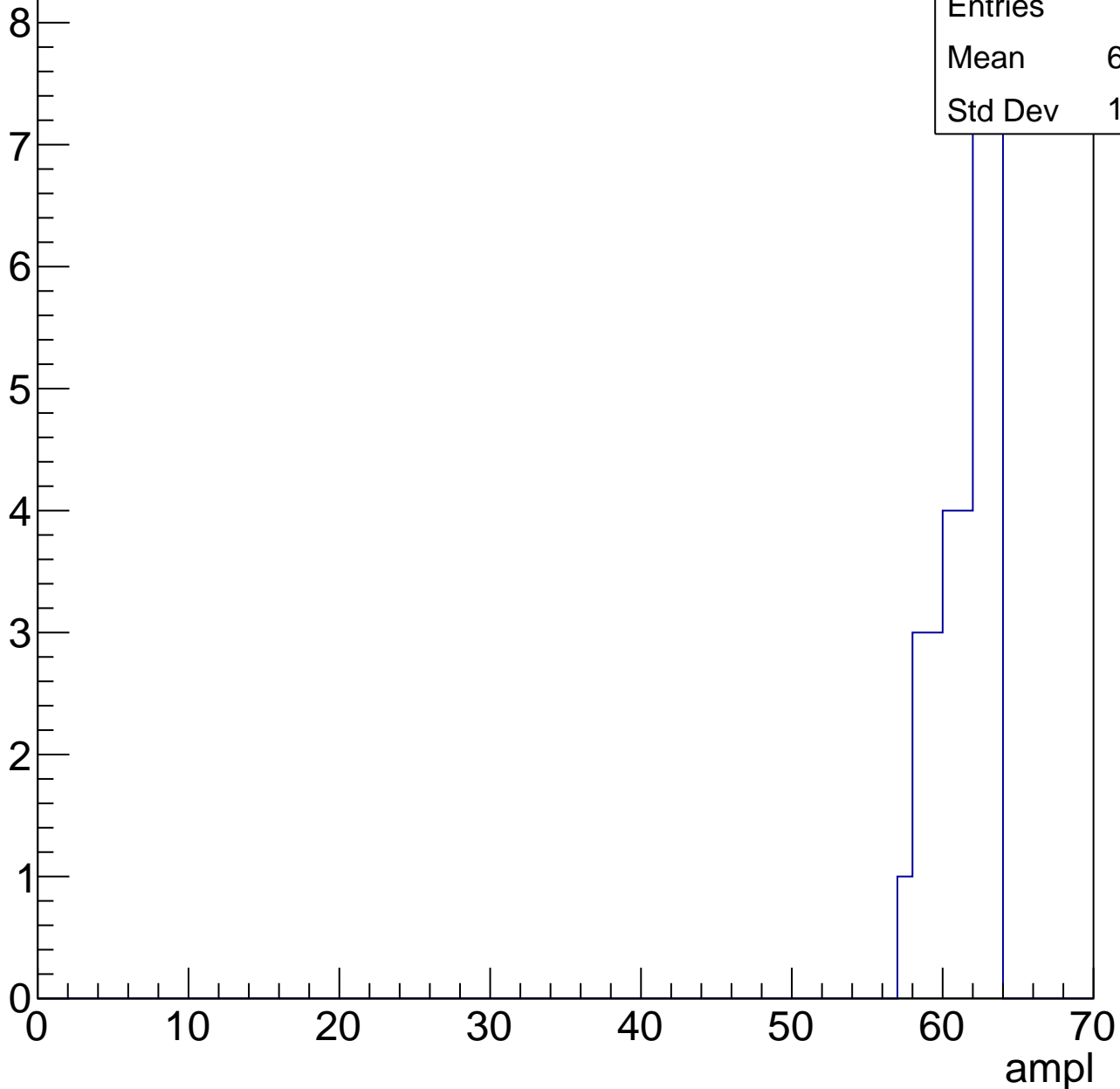


B0L002S, U7-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	61.03
Std Dev	1.787



B0L002S, U7-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch108, adc0

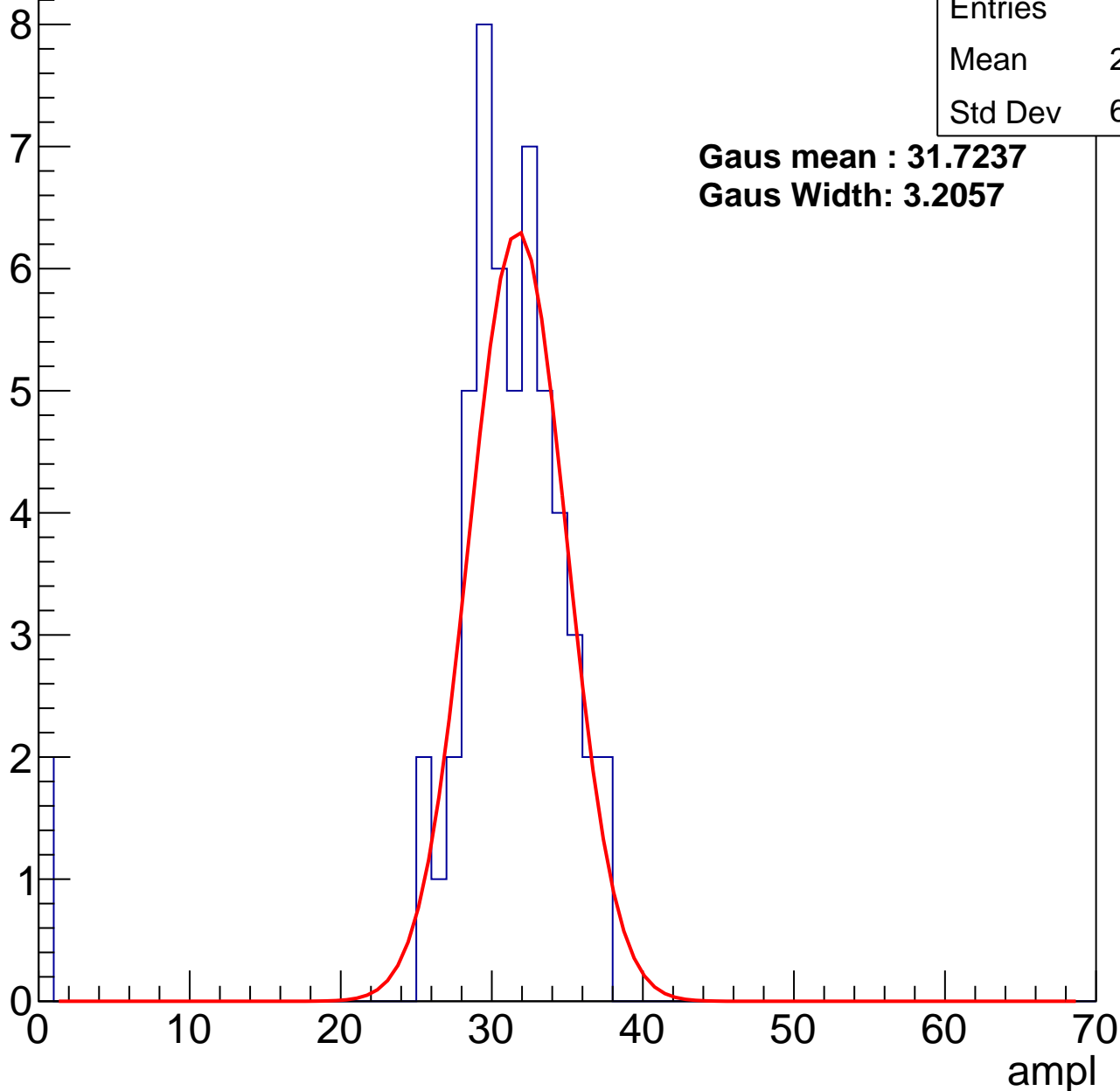
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	29.87
Std Dev	6.524

Gaus mean : 31.7237

Gaus Width: 3.2057



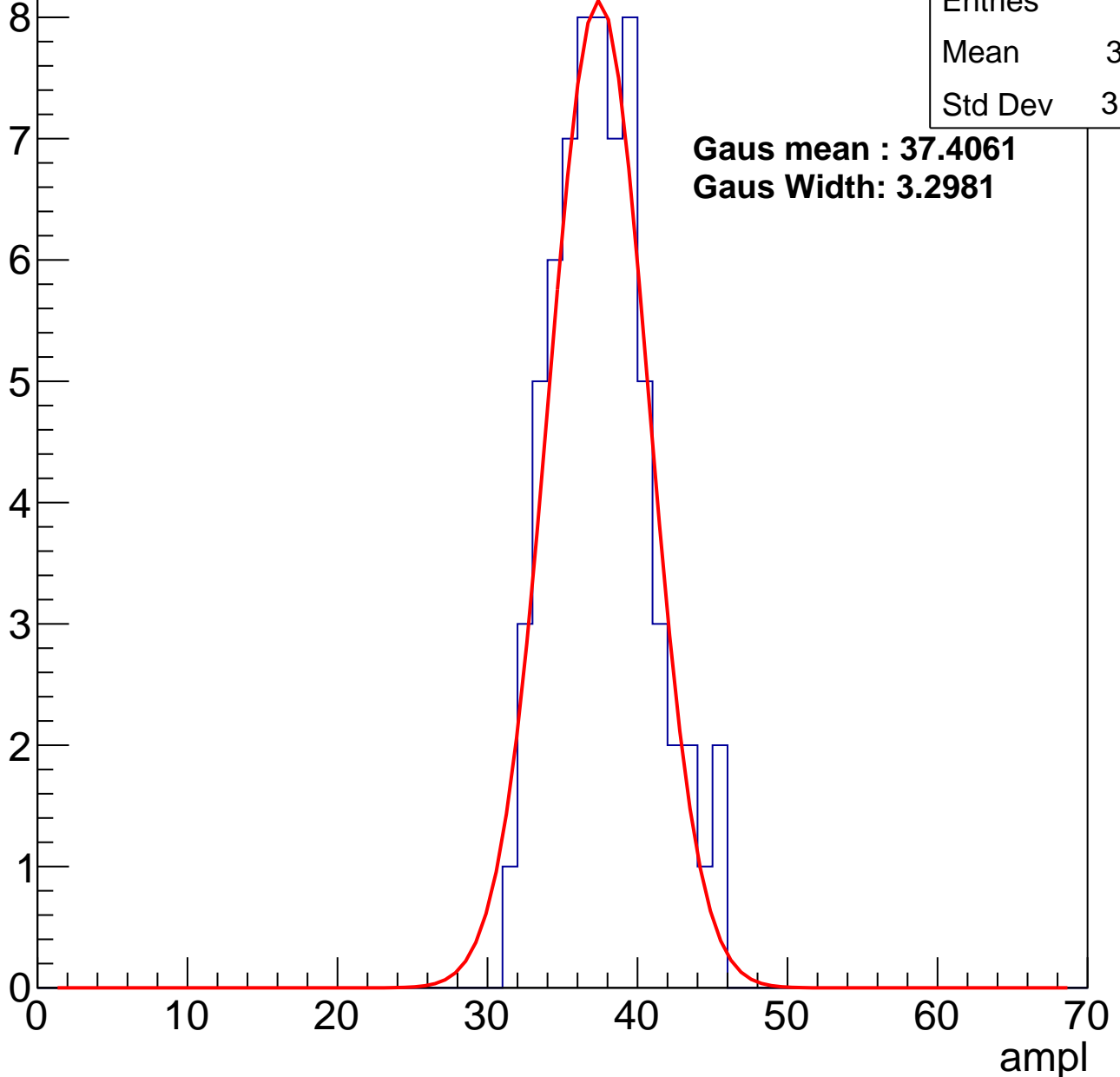
B0L002S, U7-ch108, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.21
Std Dev	3.229

Gaus mean : 37.4061
Gaus Width: 3.2981

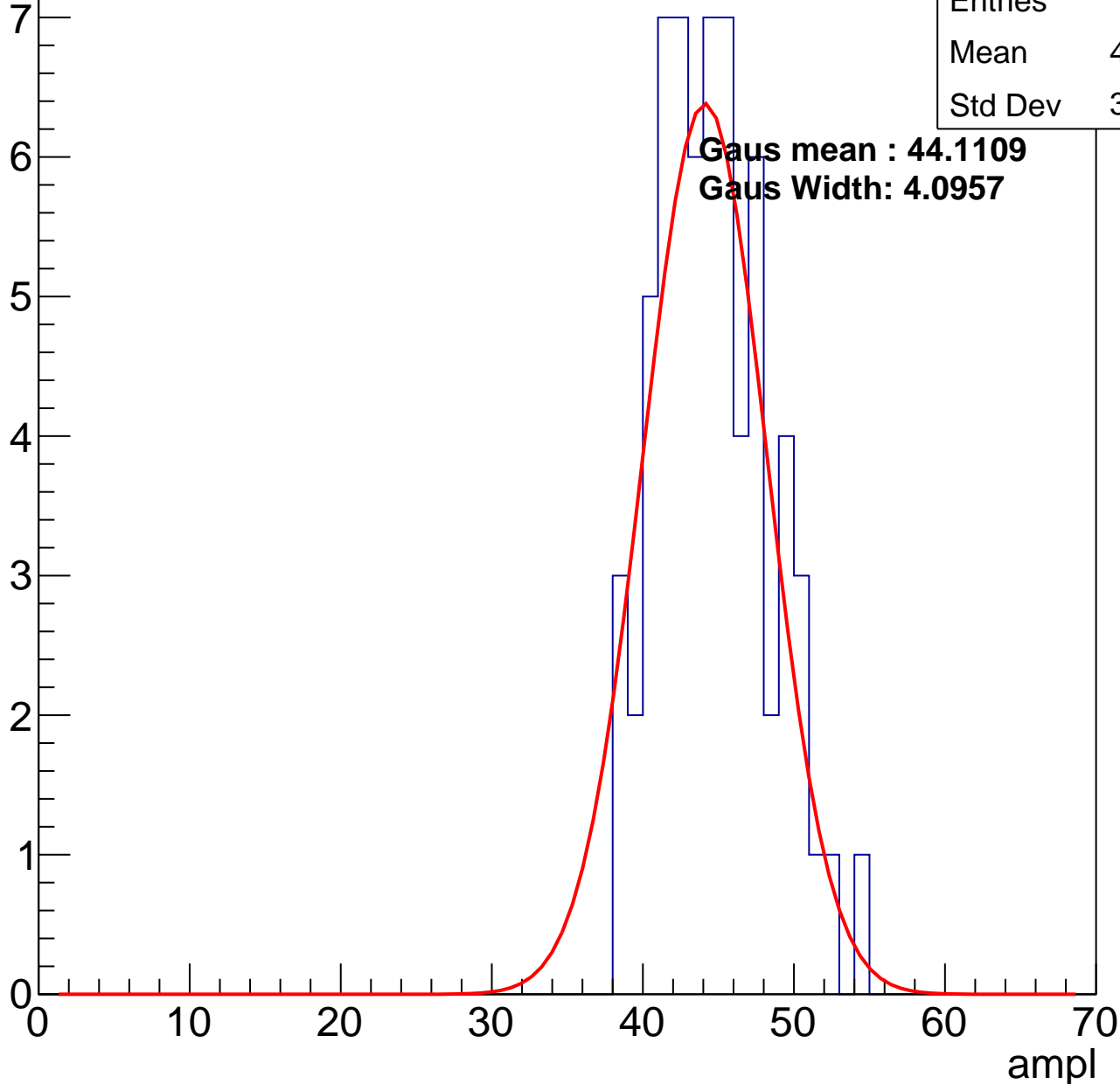


B0L002S, U7-ch108, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.23
Std Dev	3.613

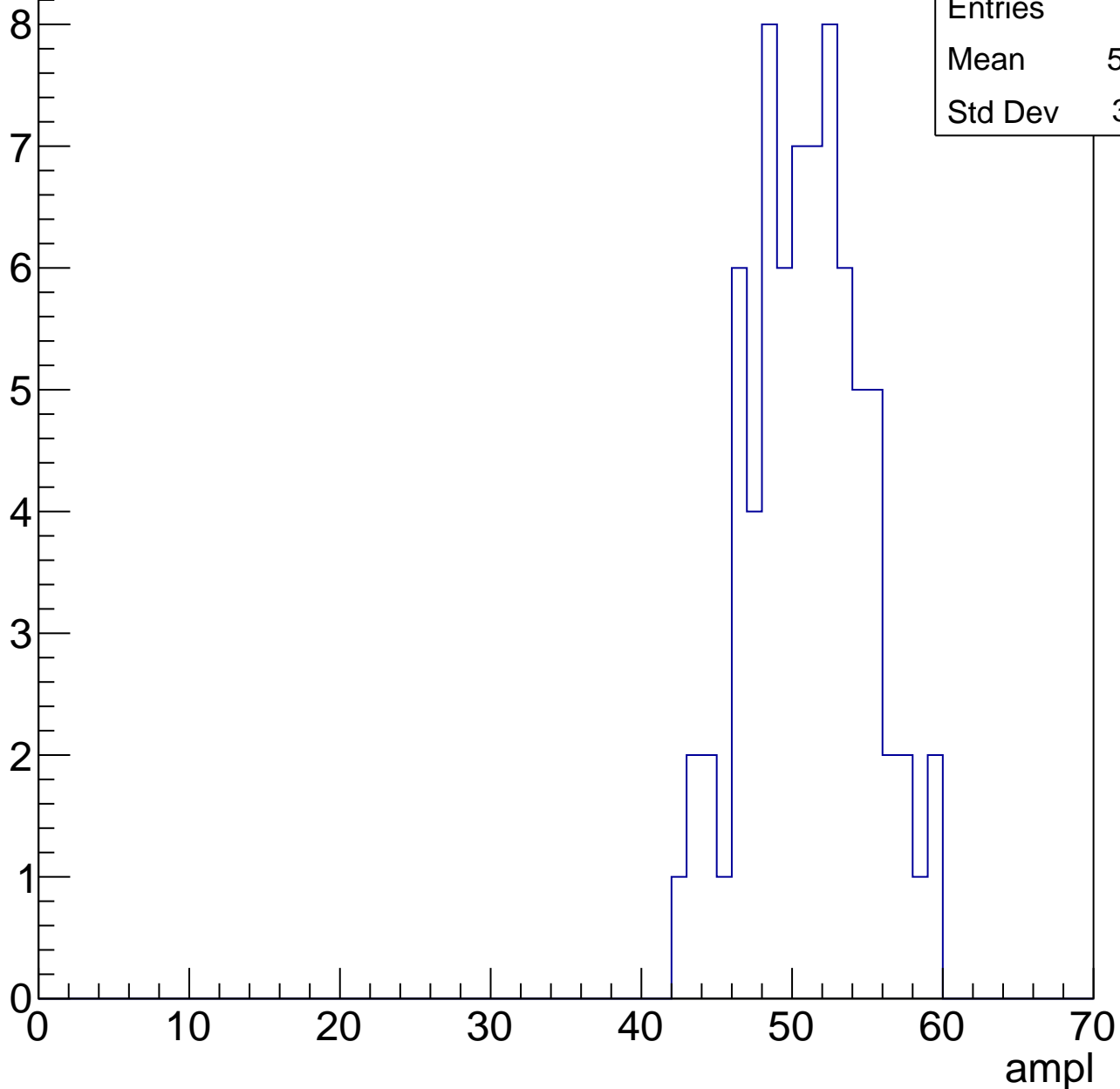


B0L002S, U7-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	50.55
Std Dev	3.831

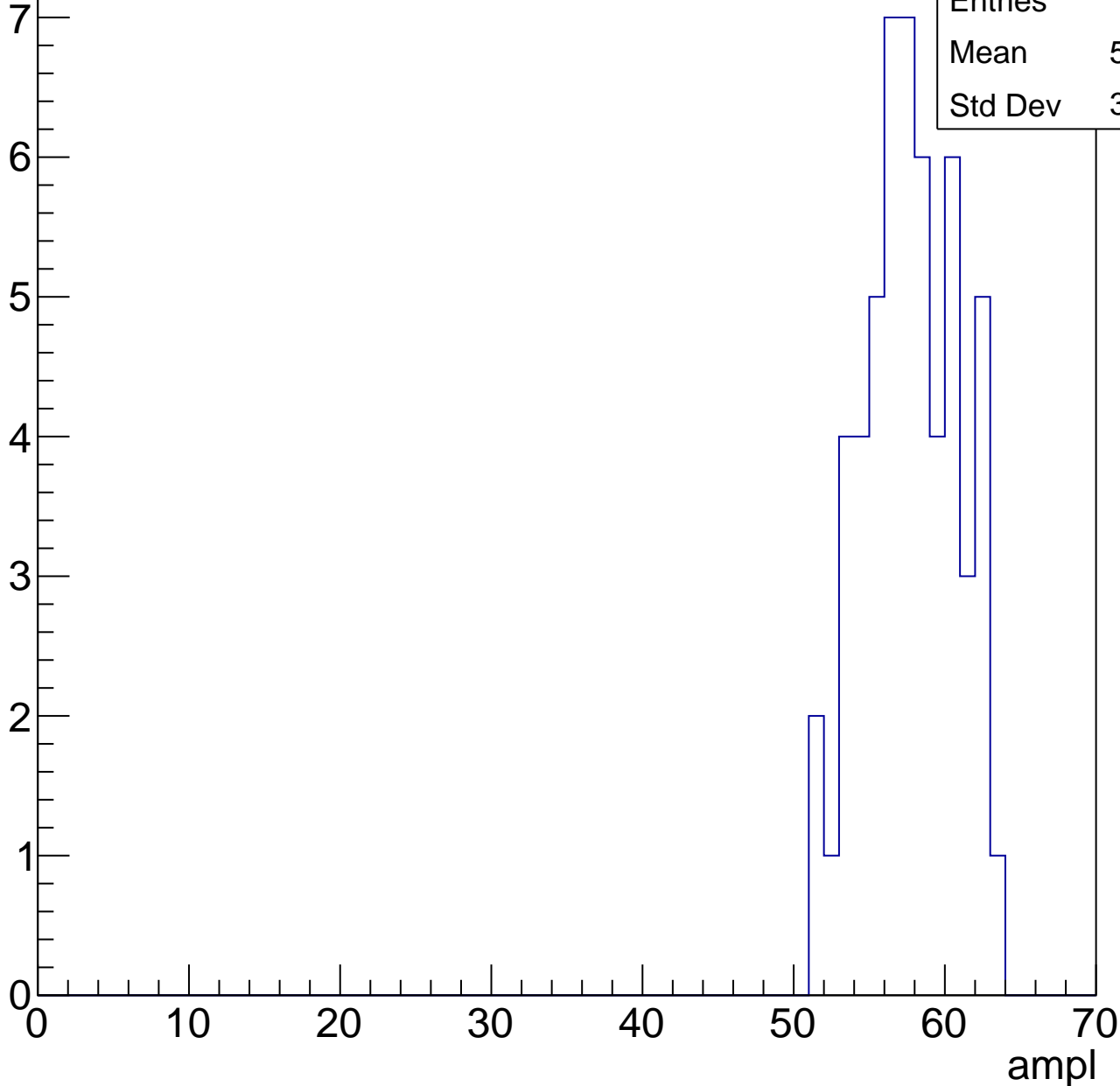


B0L002S, U7-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.24
Std Dev	3.033

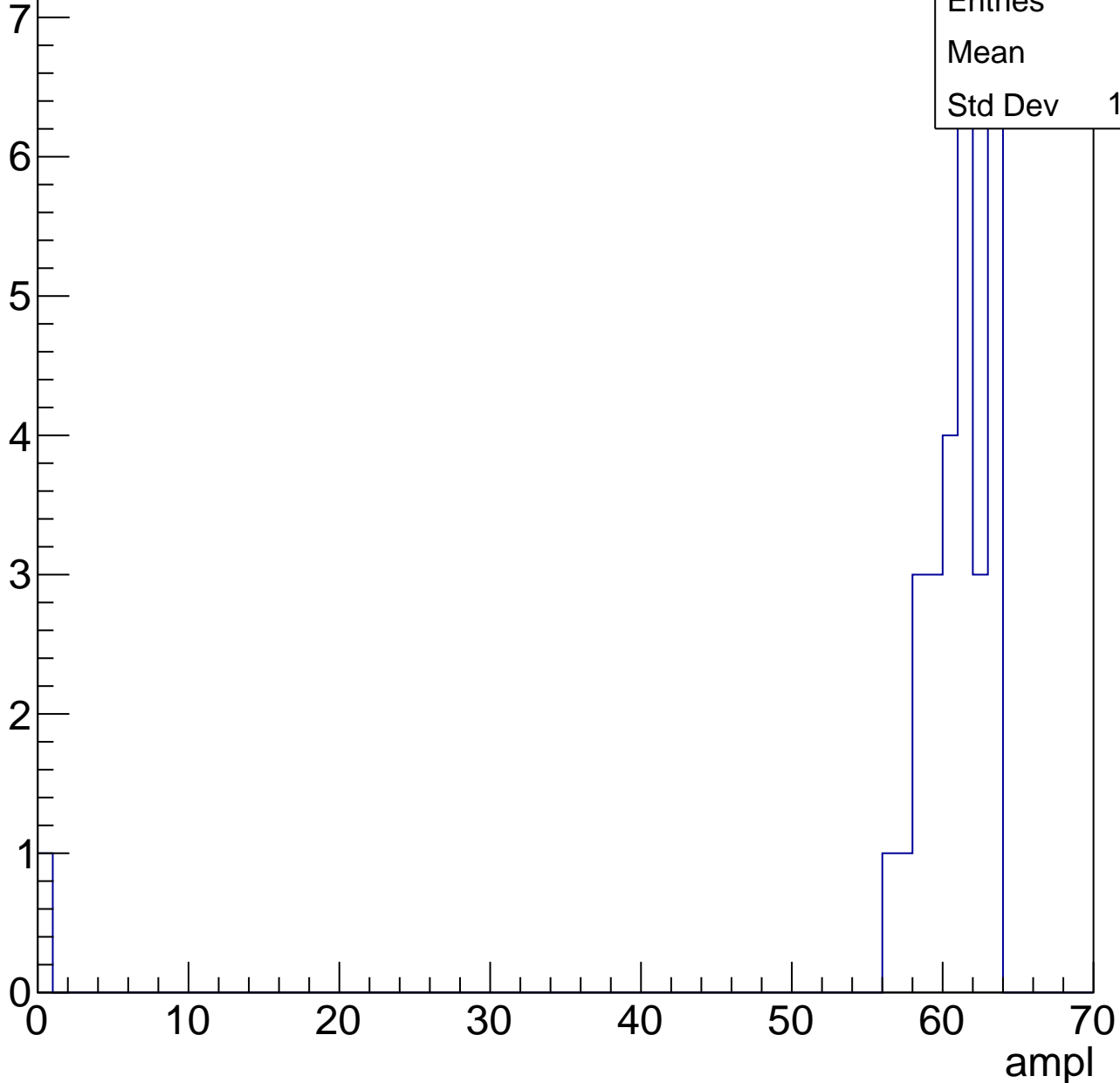


B0L002S, U7-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.6
Std Dev	11.05



B0L002S, U7-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch109, adc0

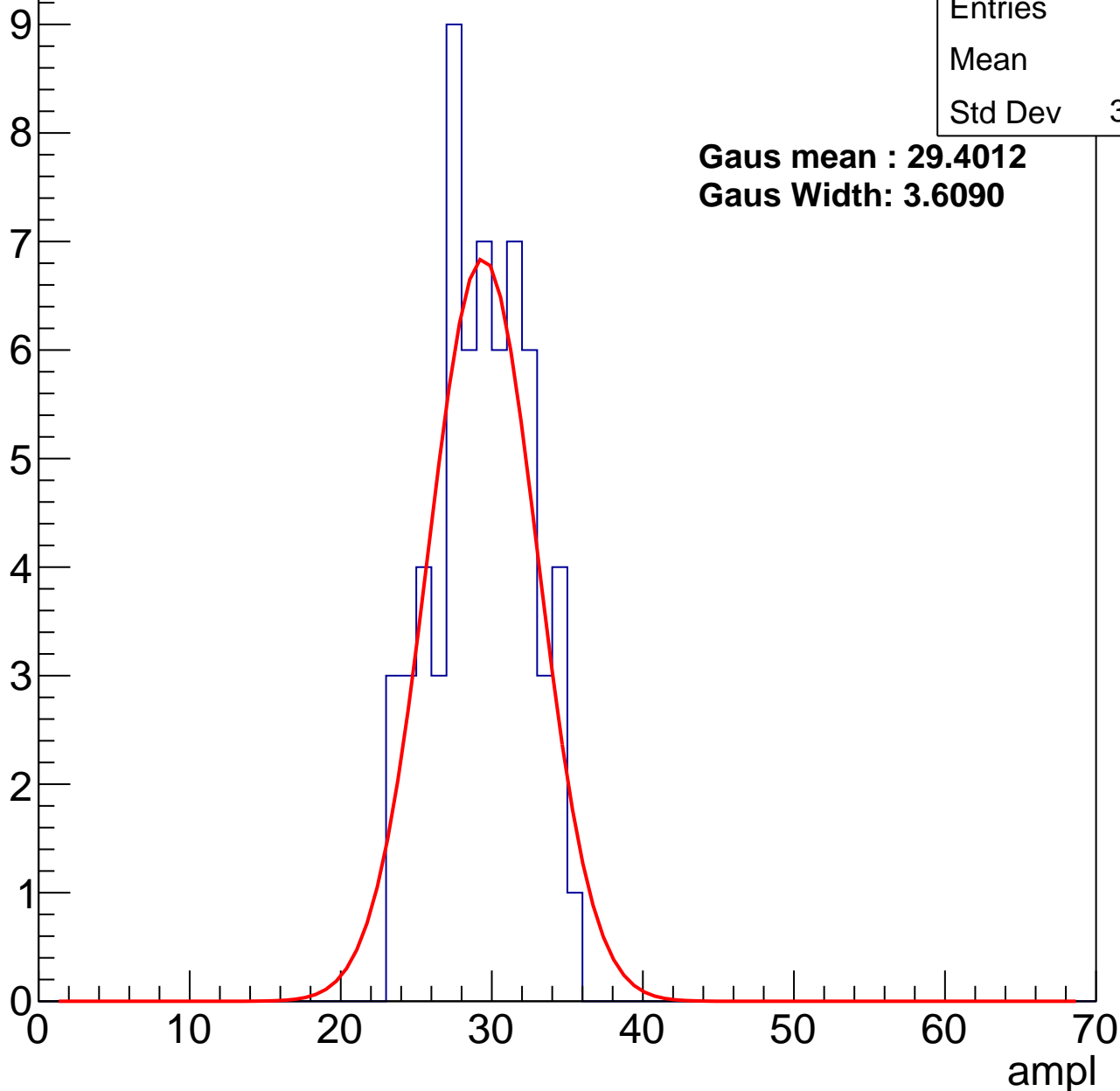
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	28.9
Std Dev	3.078

Gaus mean : 29.4012

Gaus Width: 3.6090



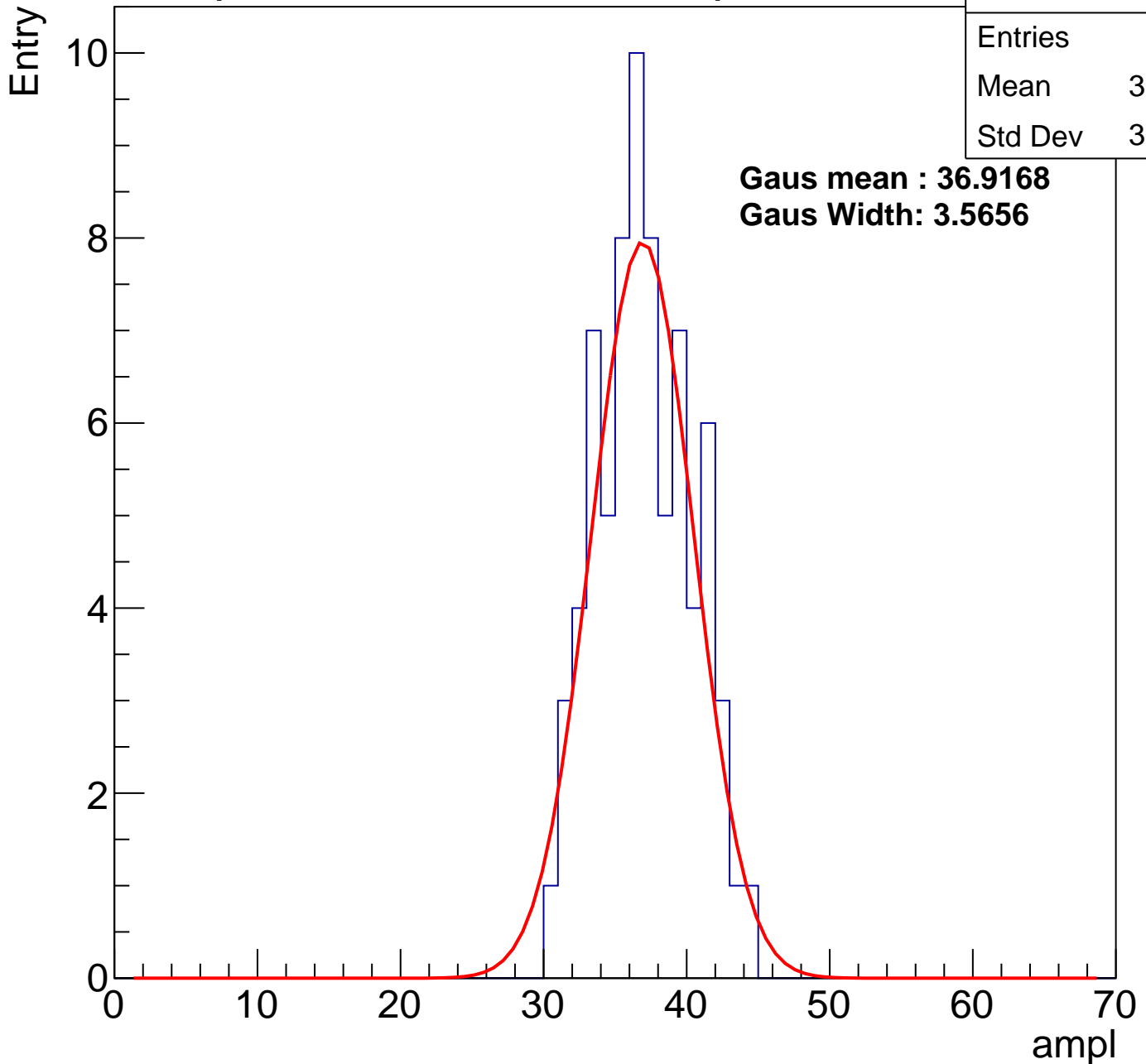
B0L002S, U7-ch109, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	36.58
Std Dev	3.243

Gaus mean : 36.9168

Gaus Width: 3.5656

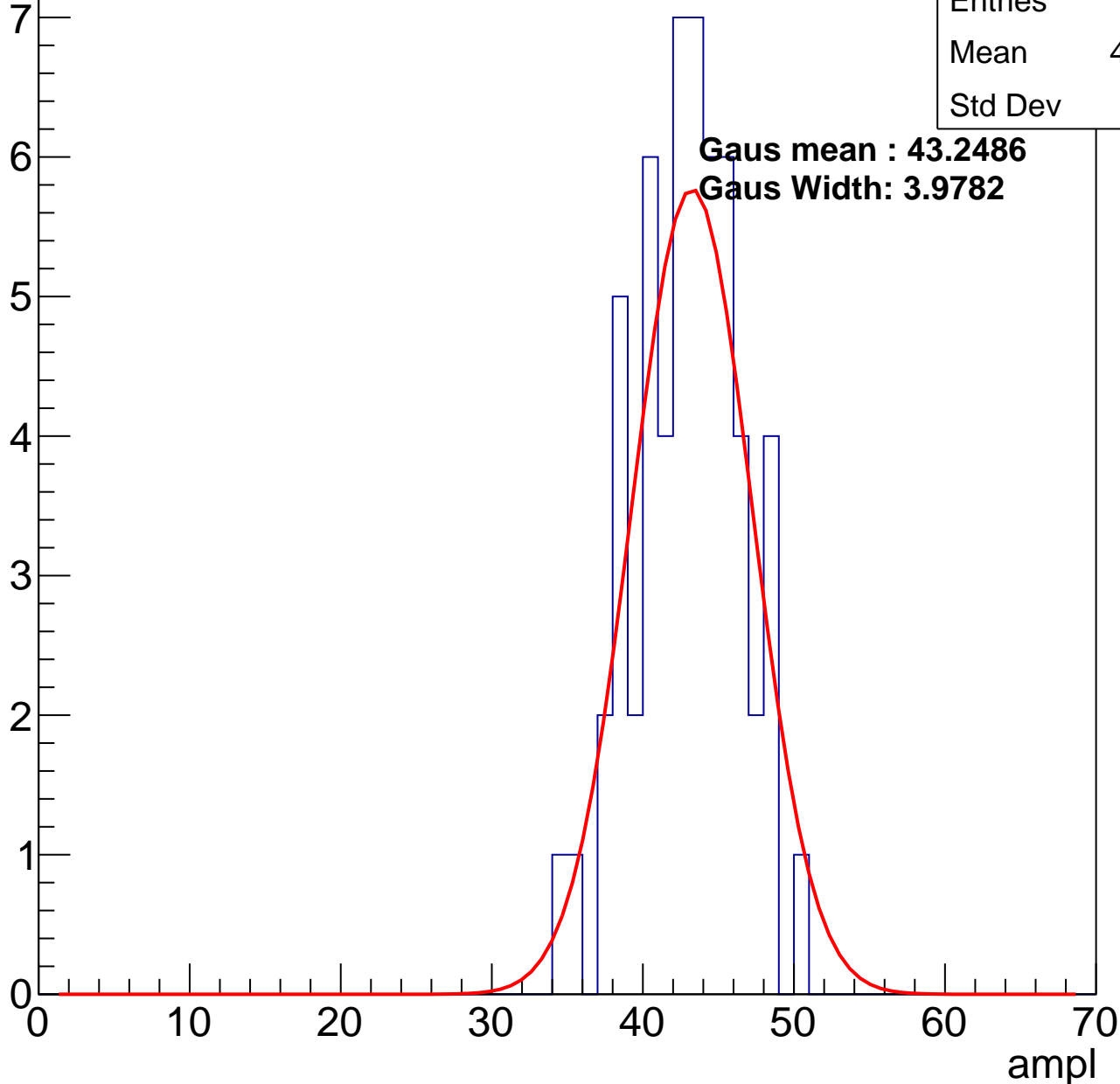


B0L002S, U7-ch109, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.48
Std Dev	3.43

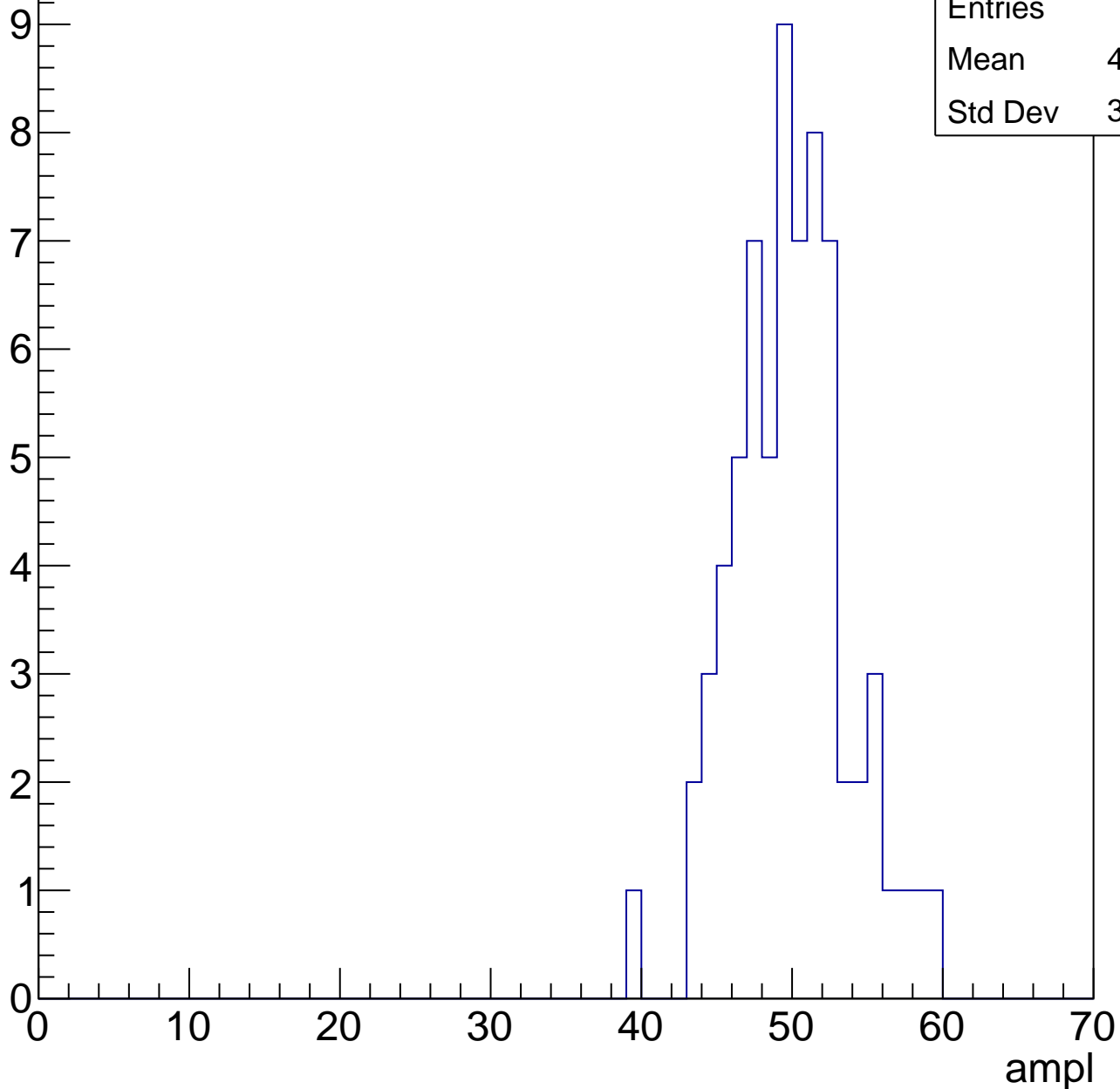


B0L002S, U7-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.39
Std Dev	3.739

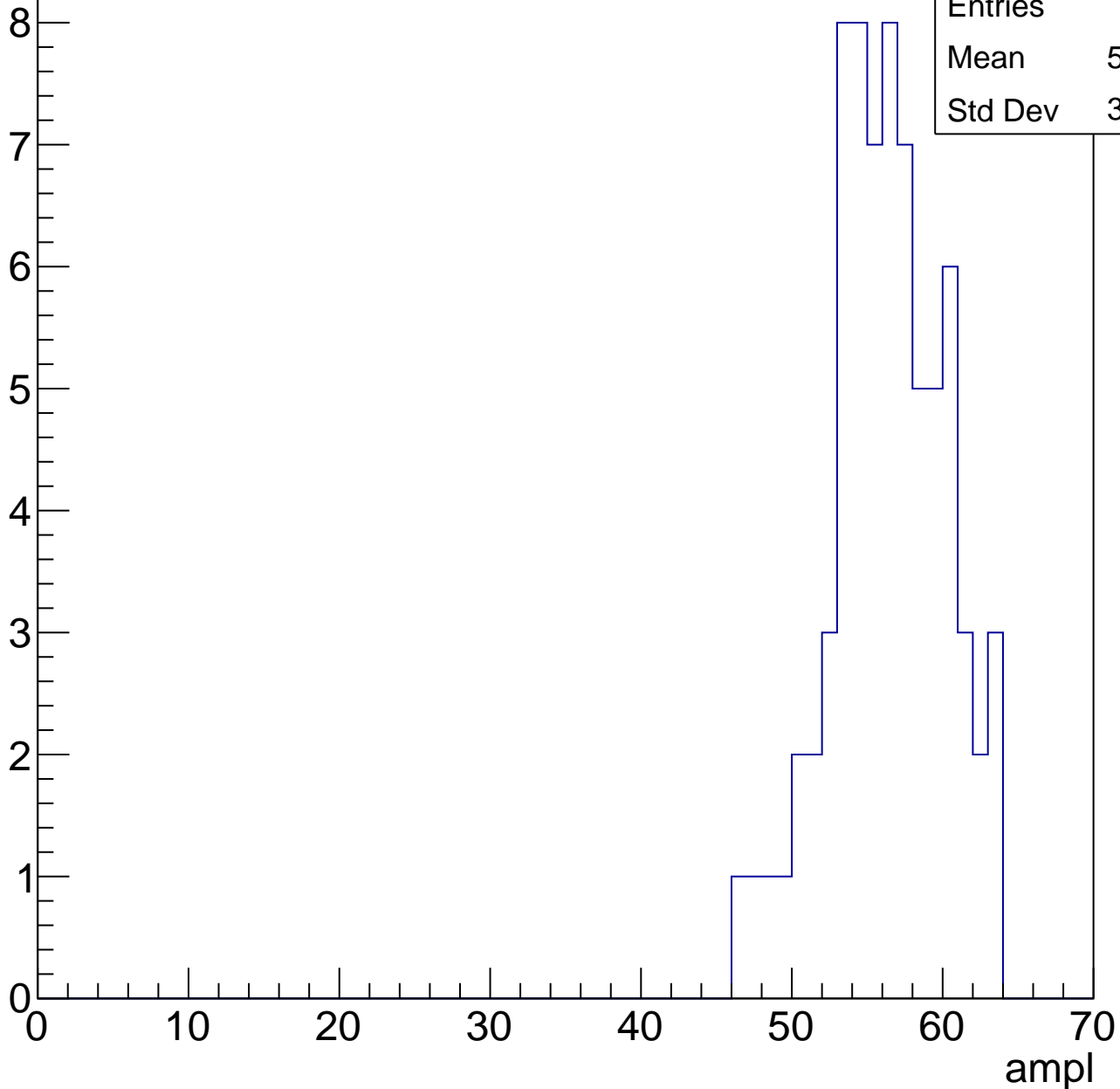


B0L002S, U7-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	55.85
Std Dev	3.766

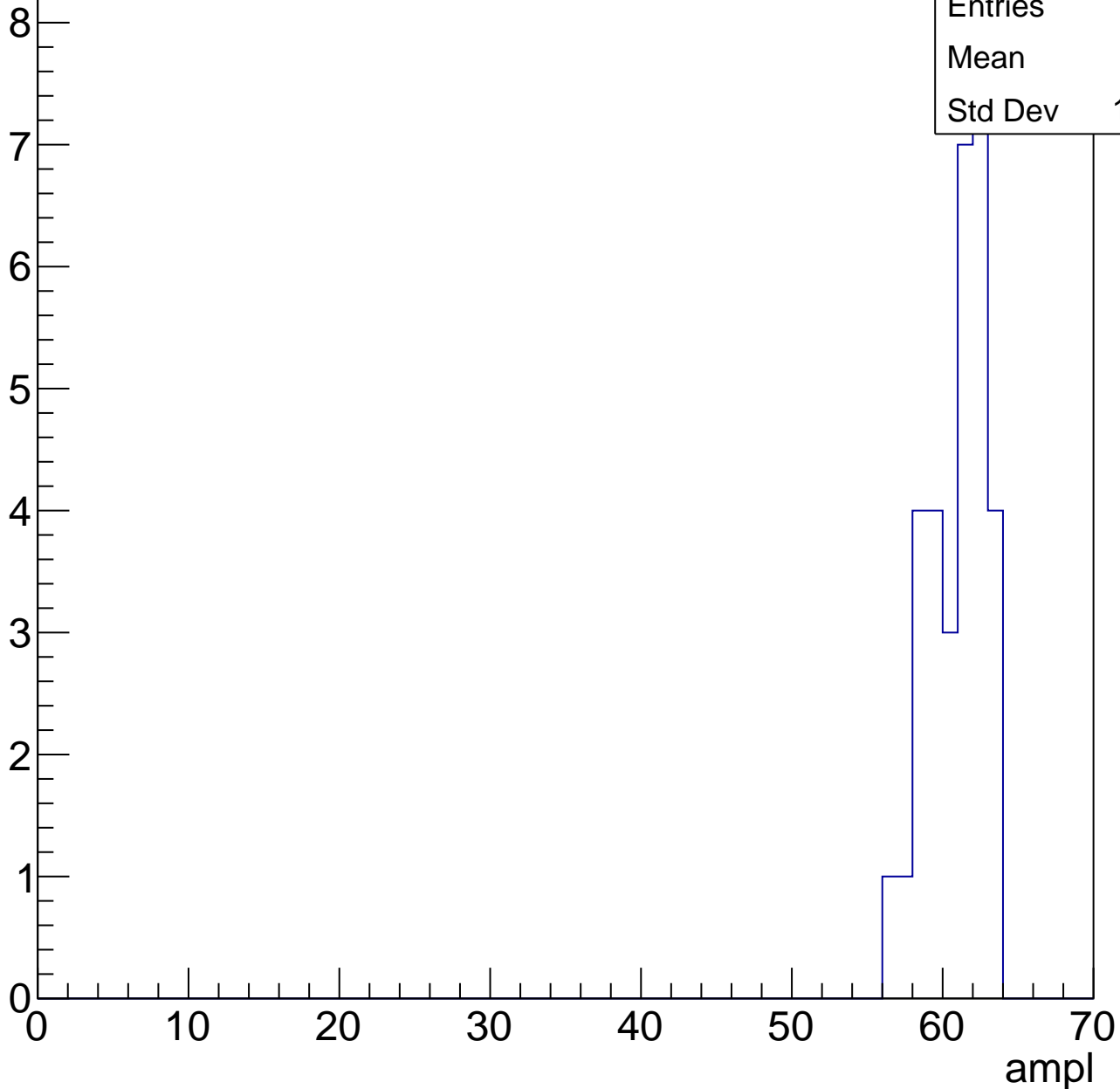


B0L002S, U7-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

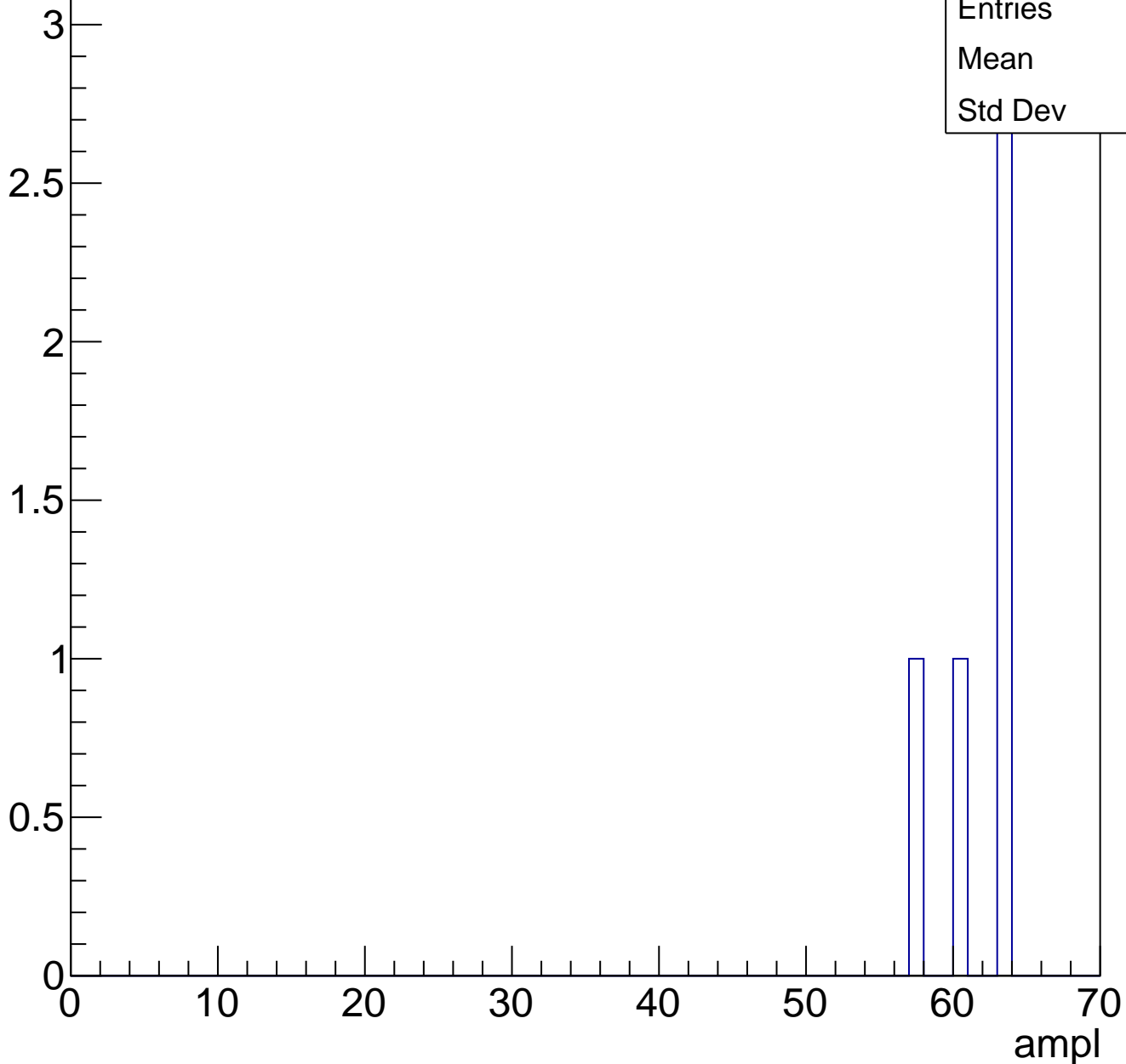
Entries	32
Mean	60.5
Std Dev	1.871



B0L002S, U7-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

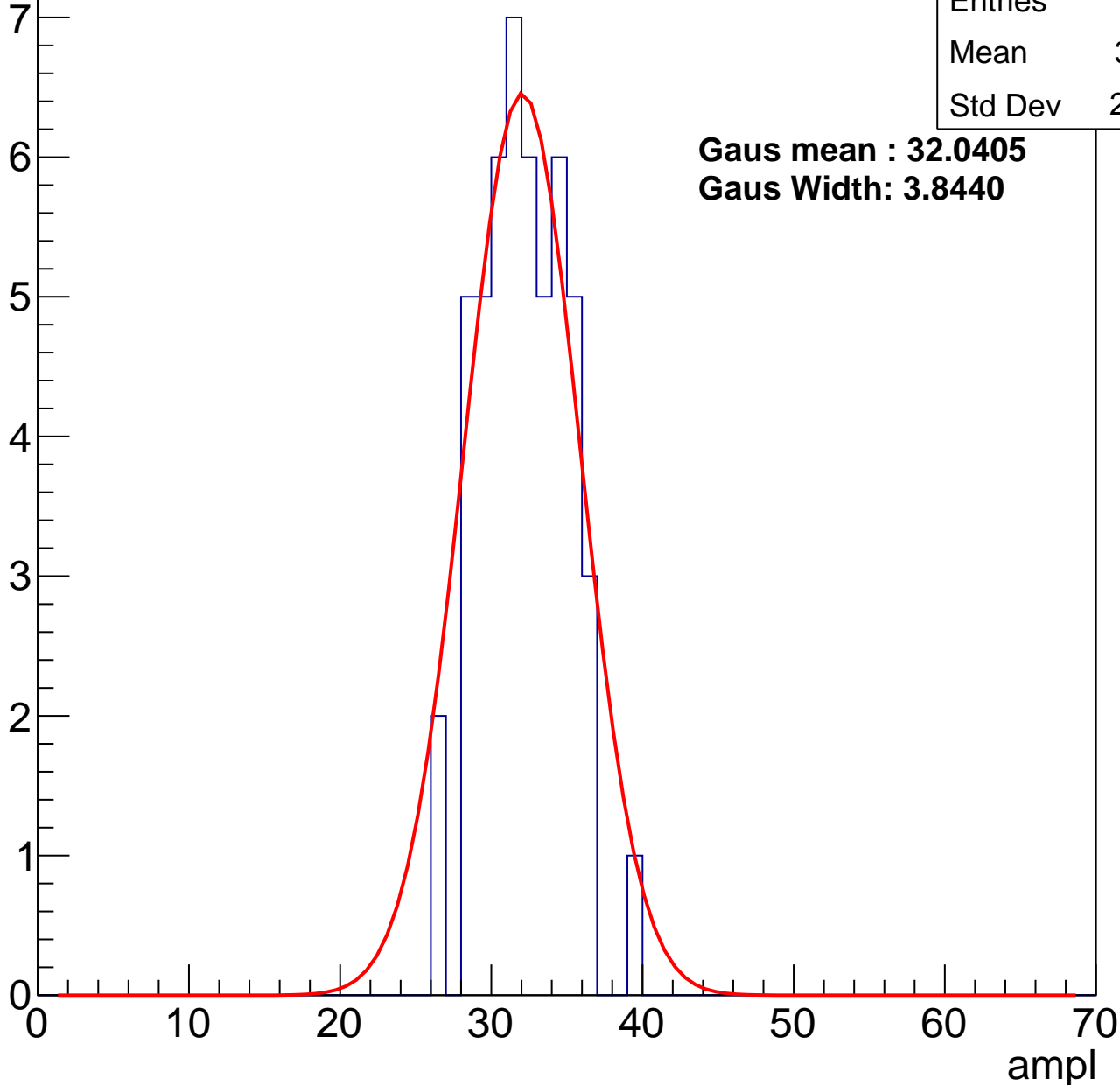
B0L002S, U7-ch110, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	31.71
Std Dev	2.782

Gaus mean : 32.0405
Gaus Width: 3.8440

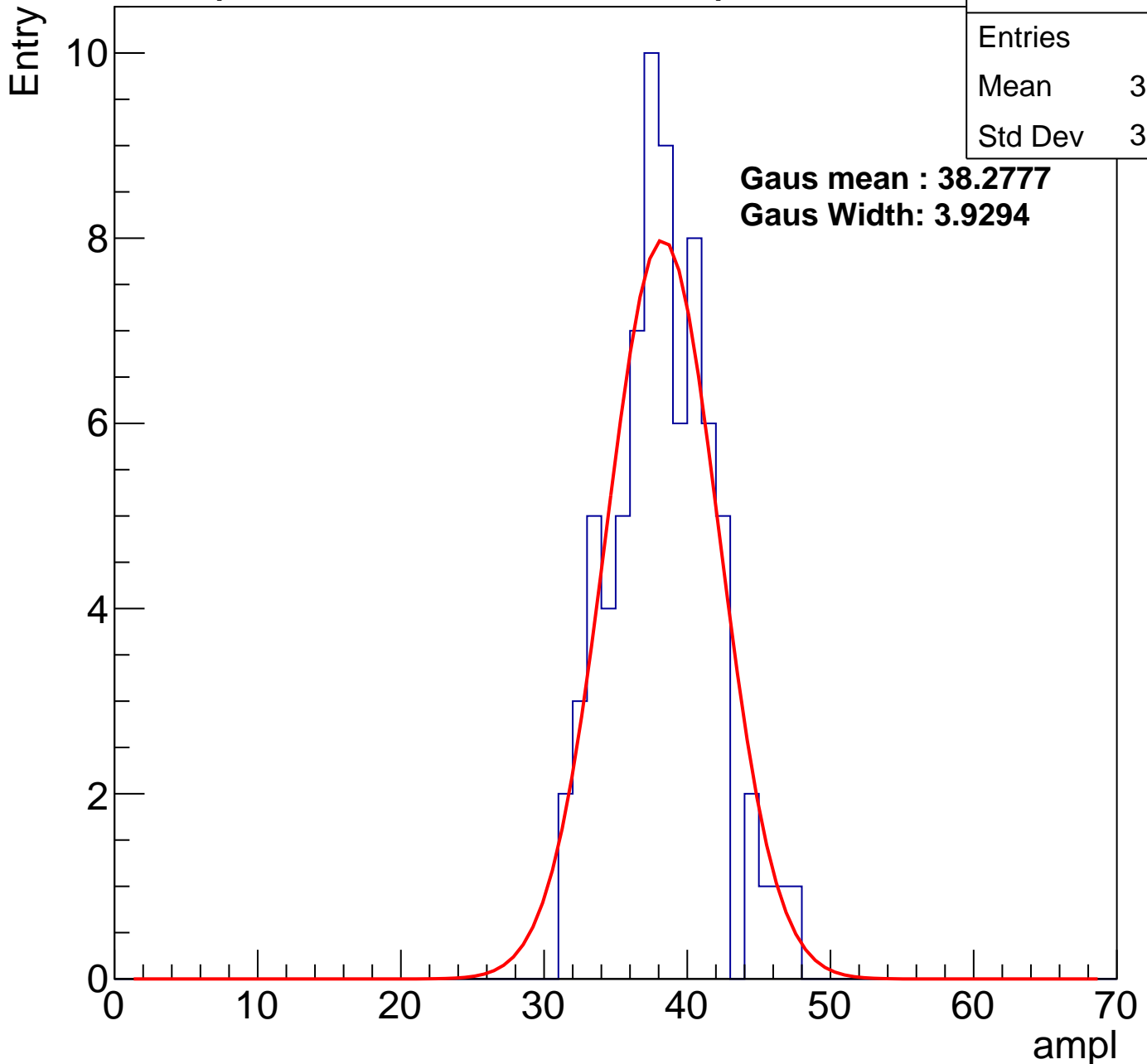


B0L002S, U7-ch110, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.79
Std Dev	3.488

Gaus mean : 38.2777
Gaus Width: 3.9294



B0L002S, U7-ch110, adc2

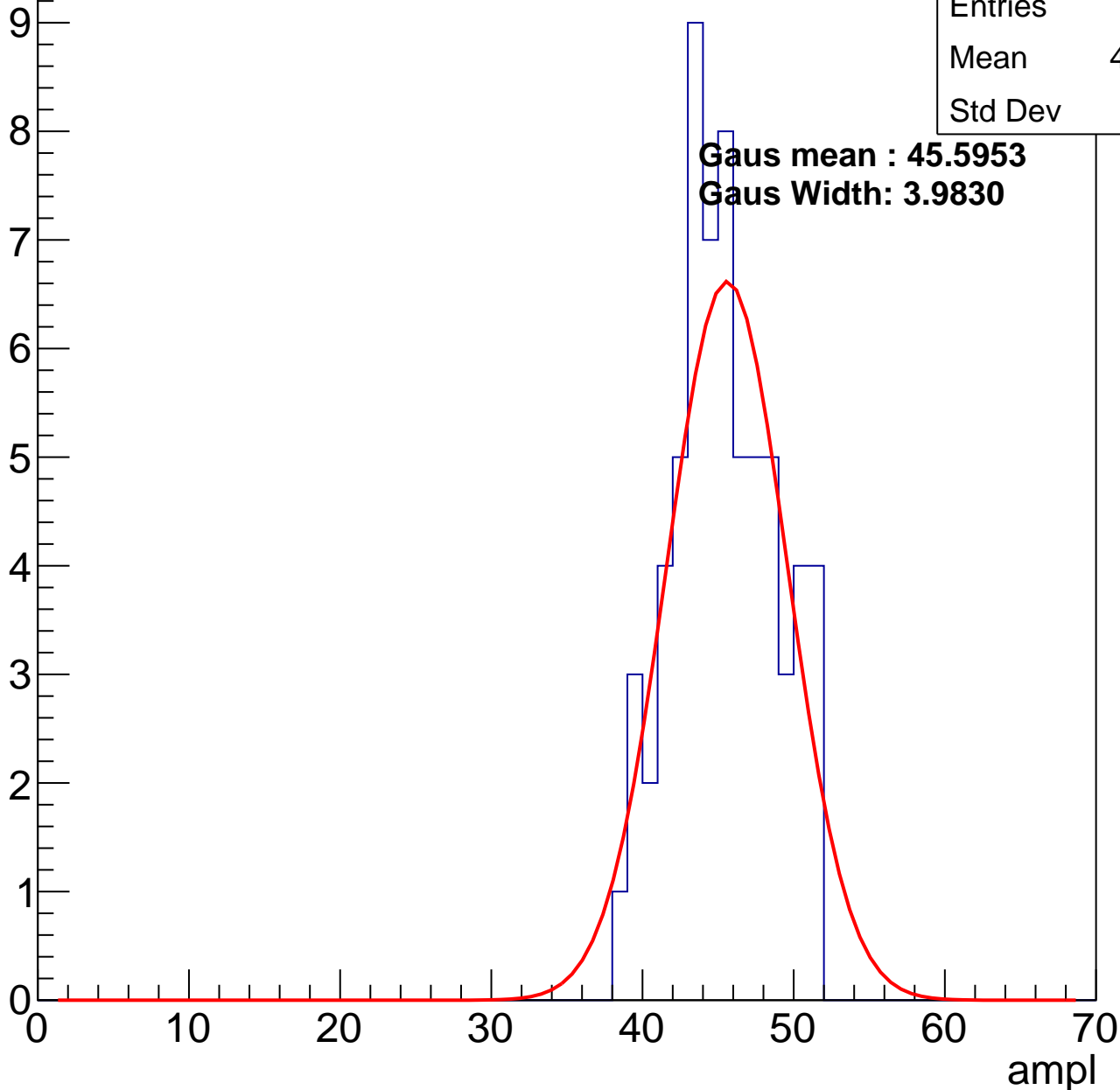
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.92
Std Dev	3.33

Gaus mean : 45.5953

Gaus Width: 3.9830



B0L002S, U7-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

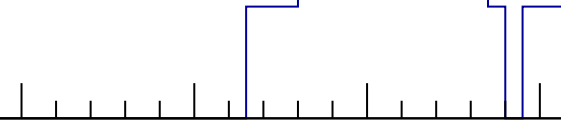
Entries	73
Mean	51.42
Std Dev	3.641

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

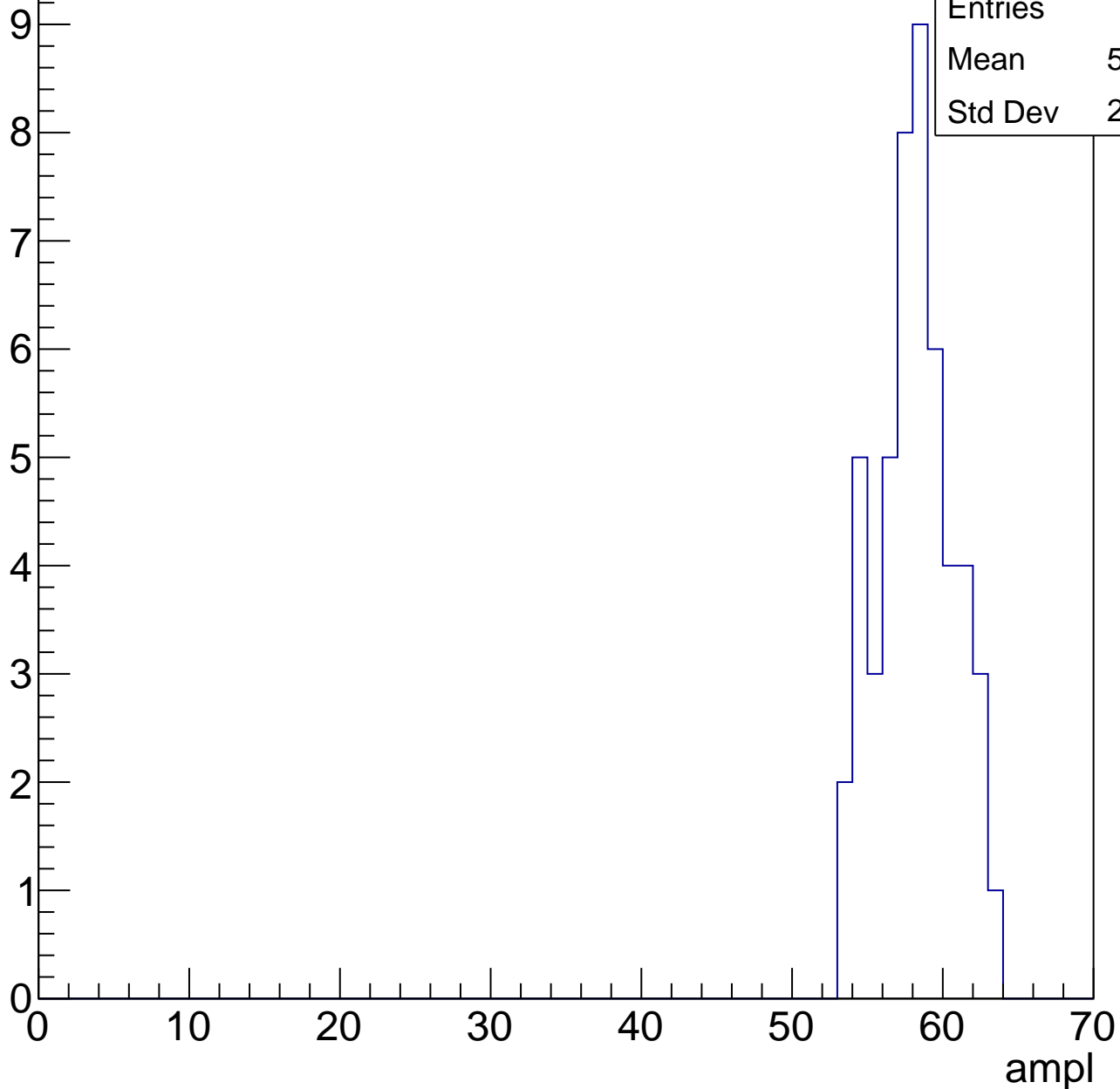
ampl



B0L002S, U7-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



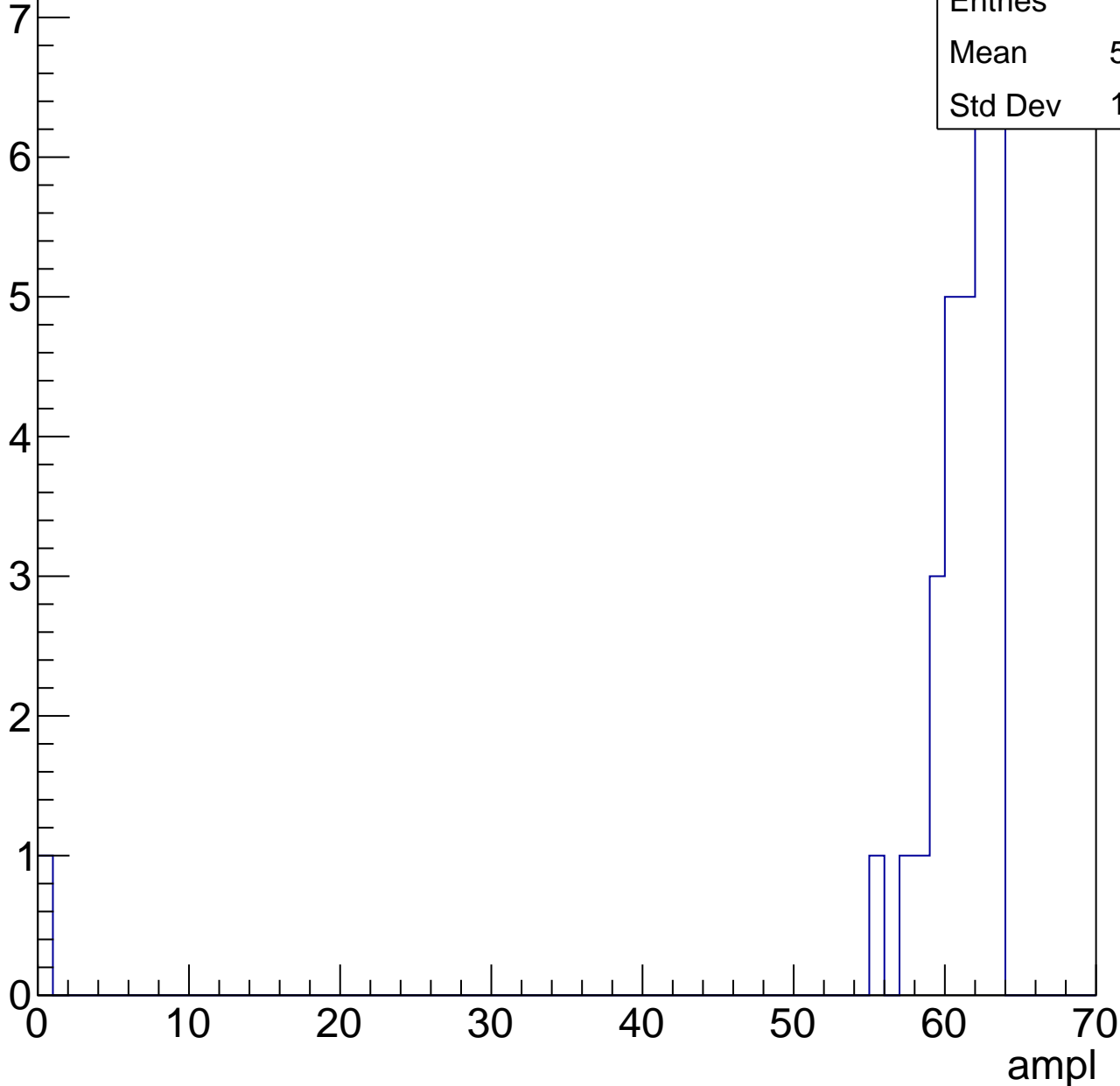
Entries	50
Mean	57.72
Std Dev	2.498

B0L002S, U7-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.94
Std Dev	10.93



B0L002S, U7-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L002S, U7-ch110, adc7

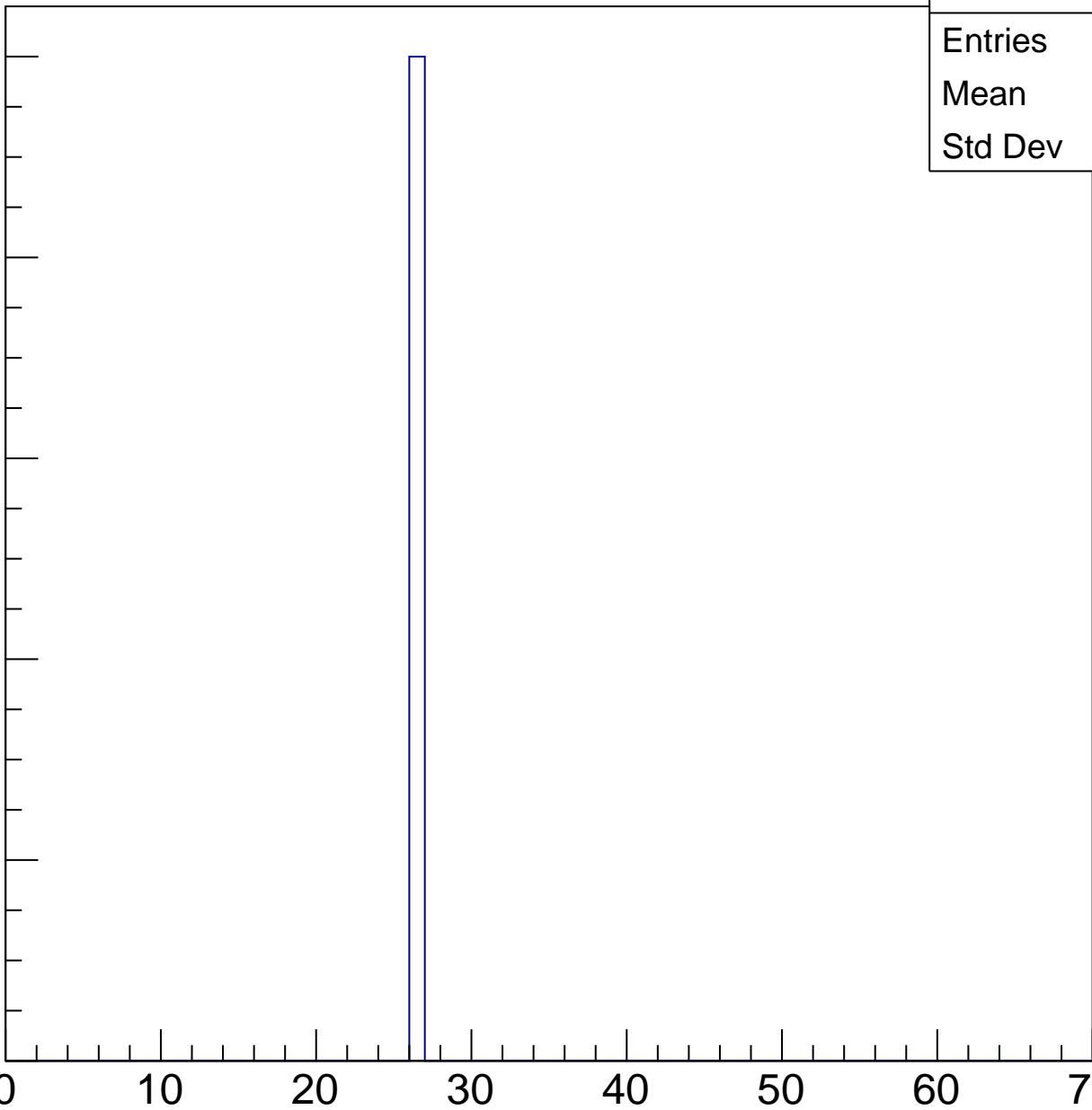
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl



B0L002S, U7-ch111, adc0

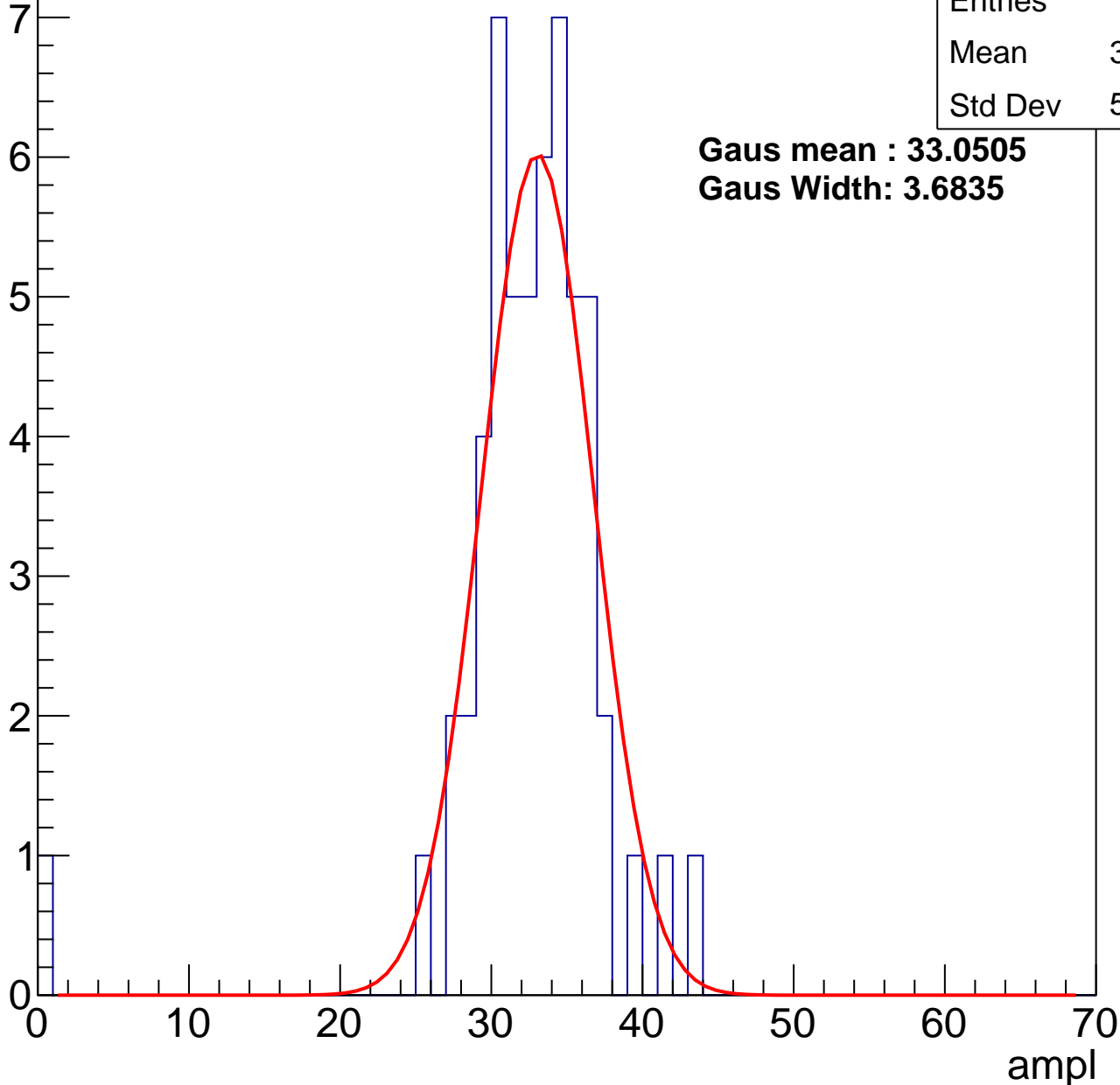
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	32.07
Std Dev	5.533

Gaus mean : 33.0505

Gaus Width: 3.6835



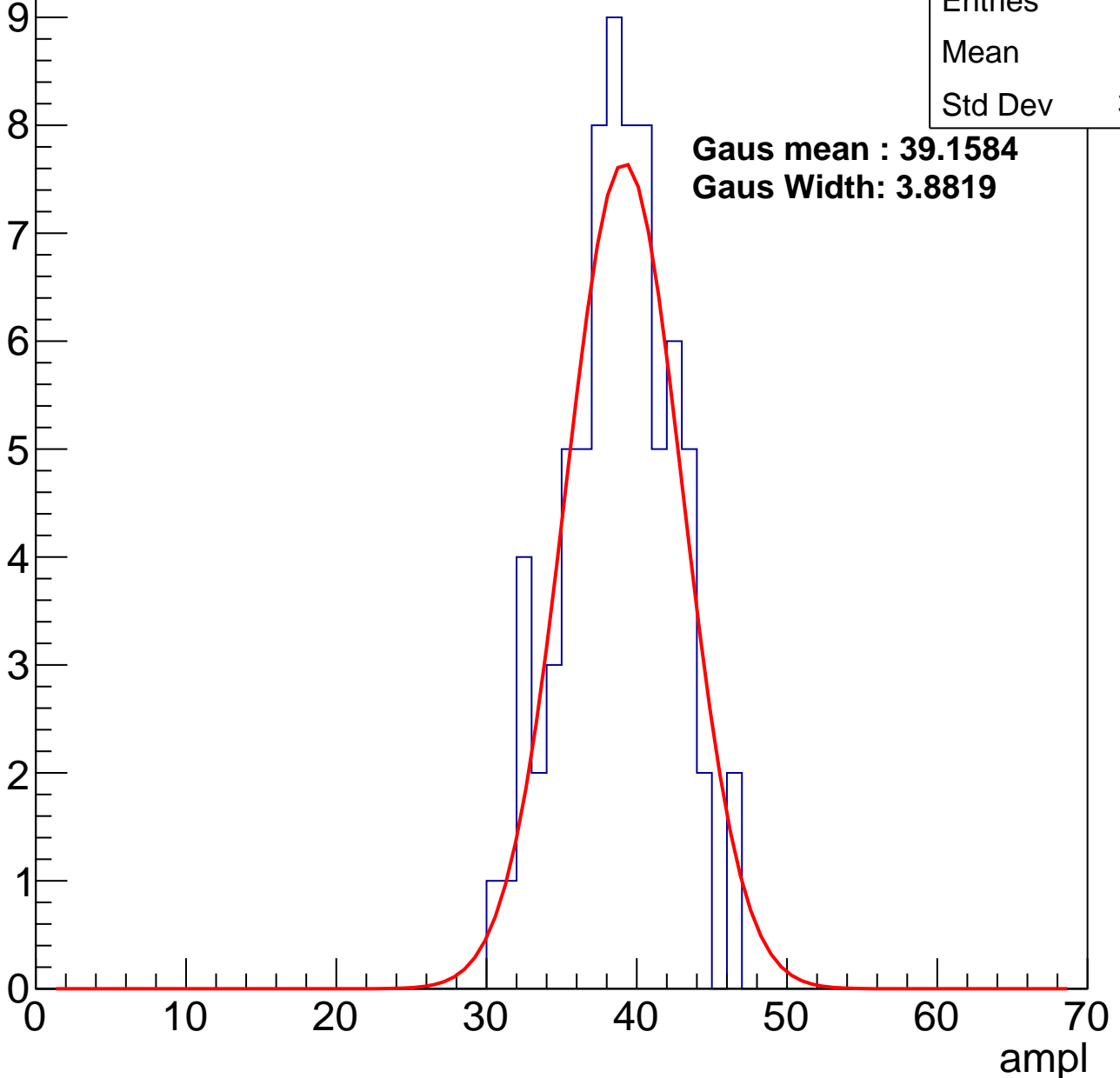
B0L002S, U7-ch111, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.3
Std Dev	3.54

Gaus mean : 39.1584
Gaus Width: 3.8819



B0L002S, U7-ch111, adc2

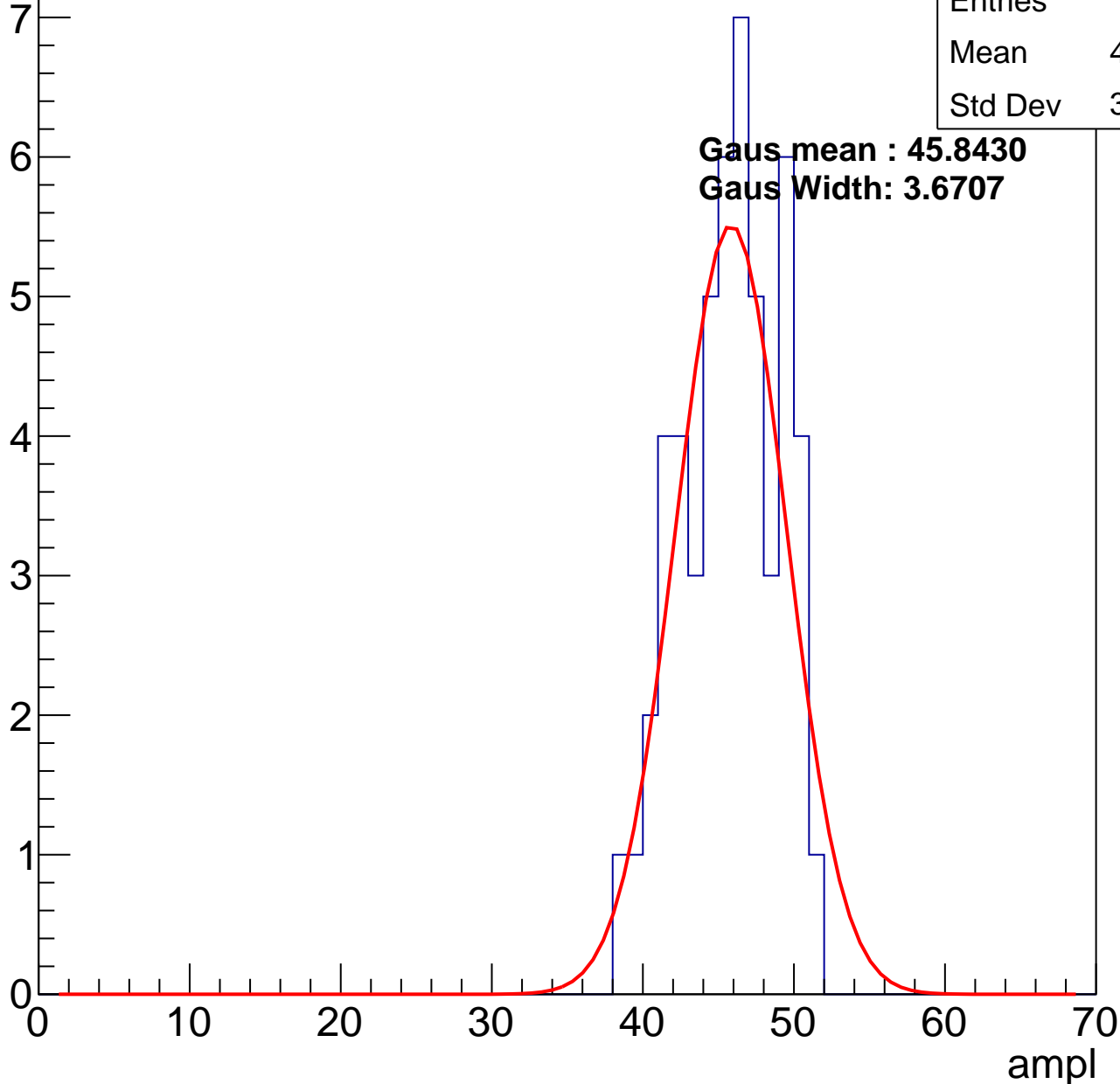
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	45.27
Std Dev	3.205

Gaus mean : 45.8430

Gaus Width: 3.6707

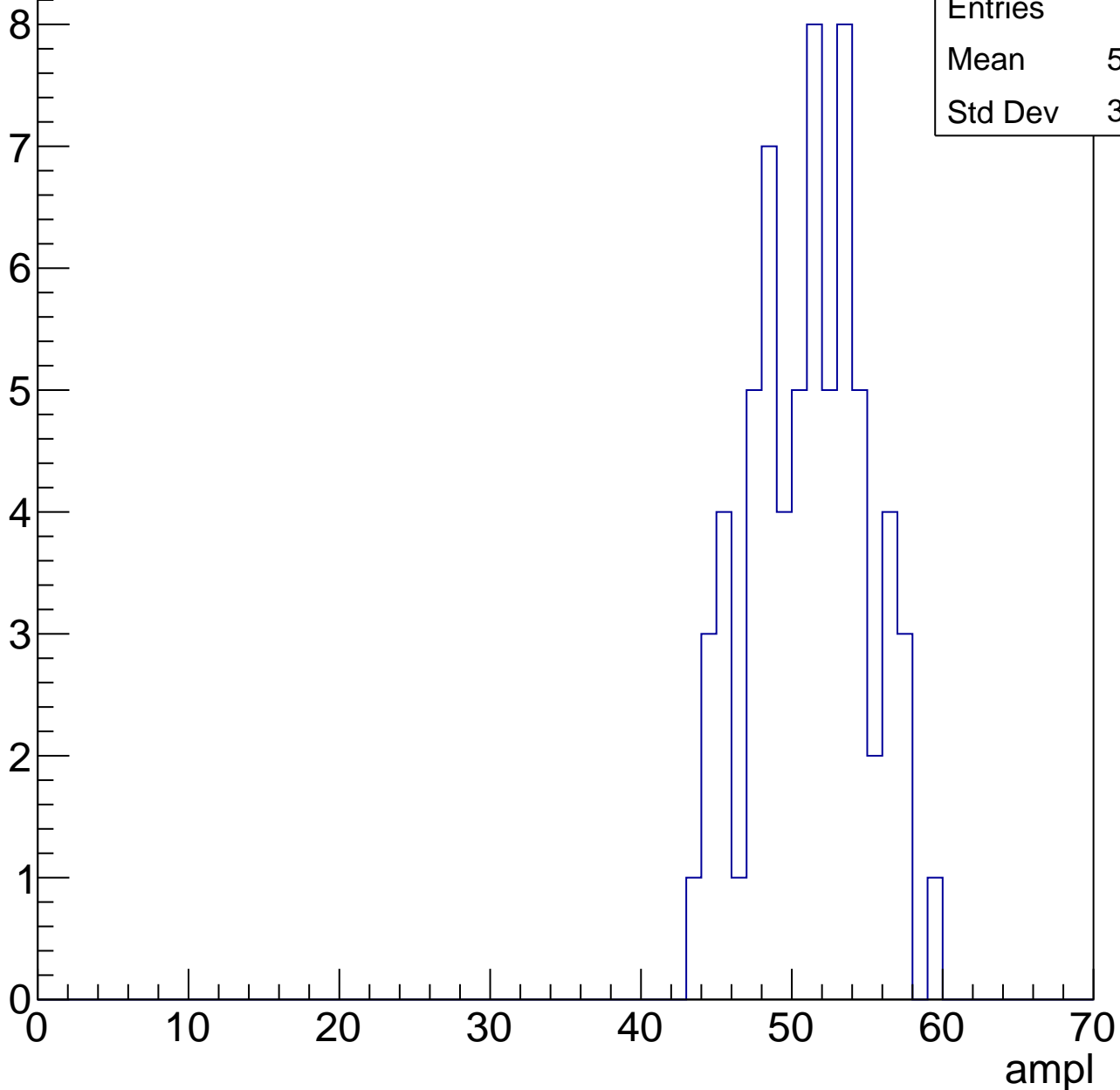


B0L002S, U7-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.67
Std Dev	3.739

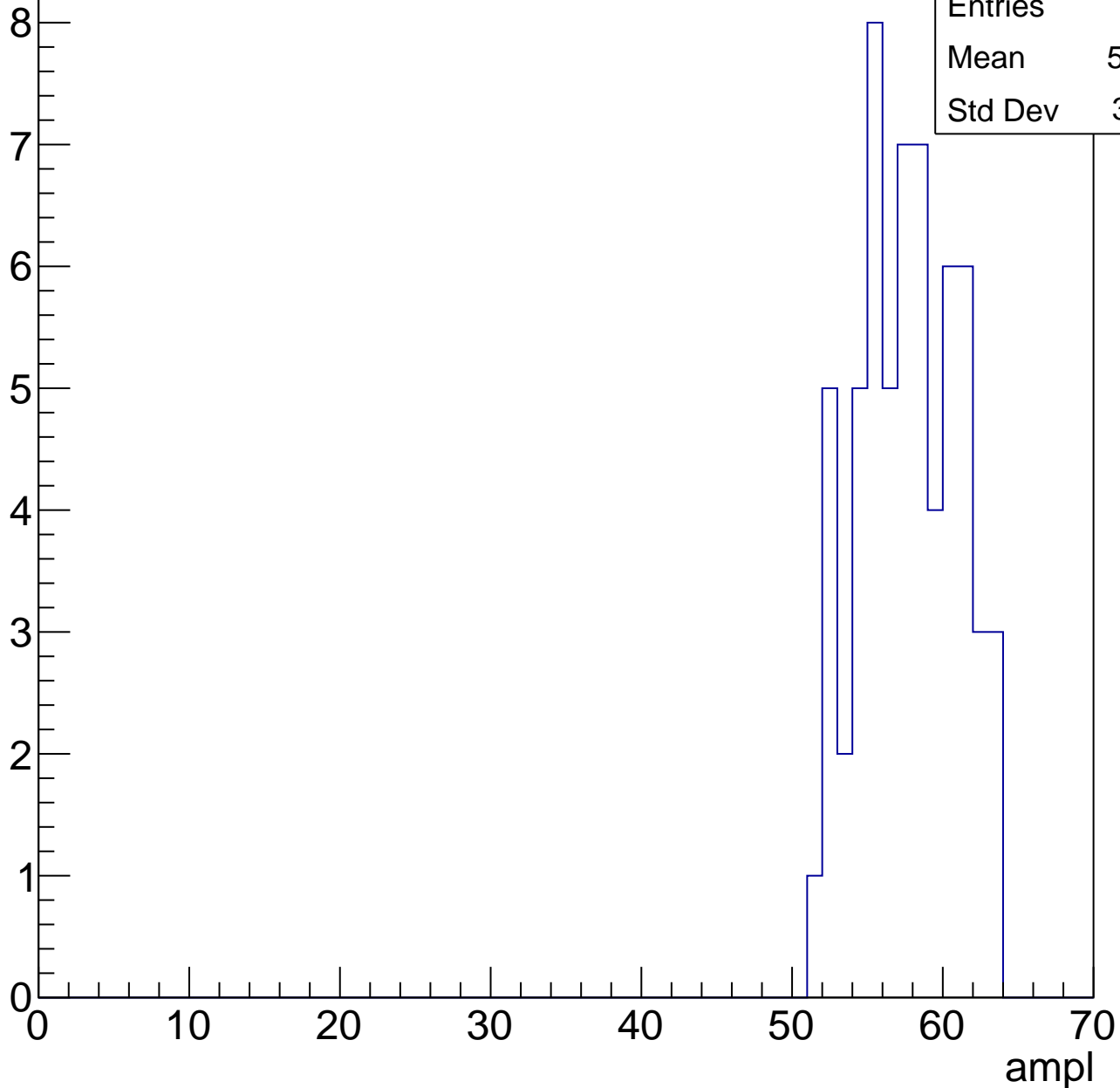


B0L002S, U7-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	57.24
Std Dev	3.181

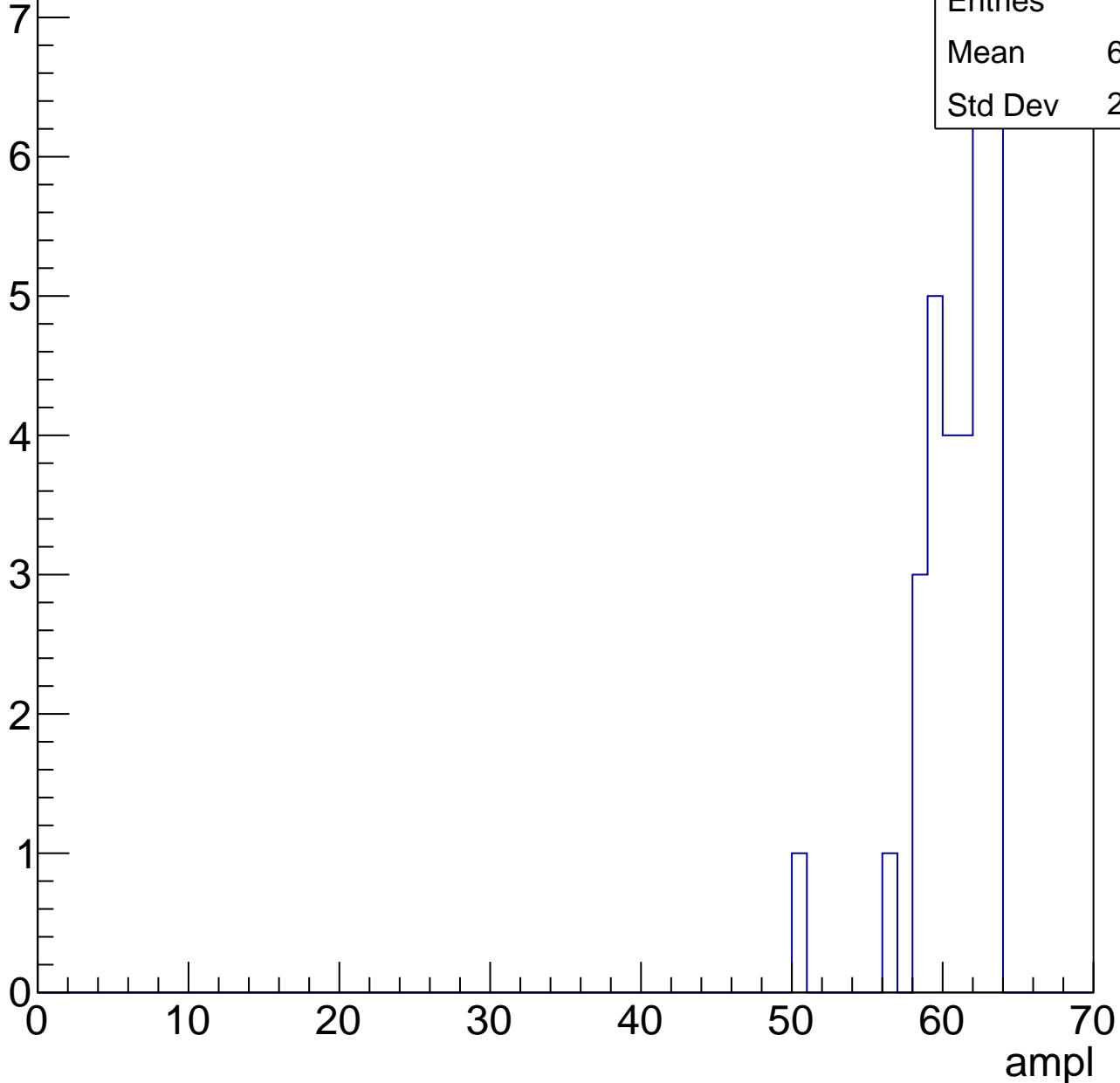


B0L002S, U7-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	60.44
Std Dev	2.633



B0L002S, U7-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch112, adc0

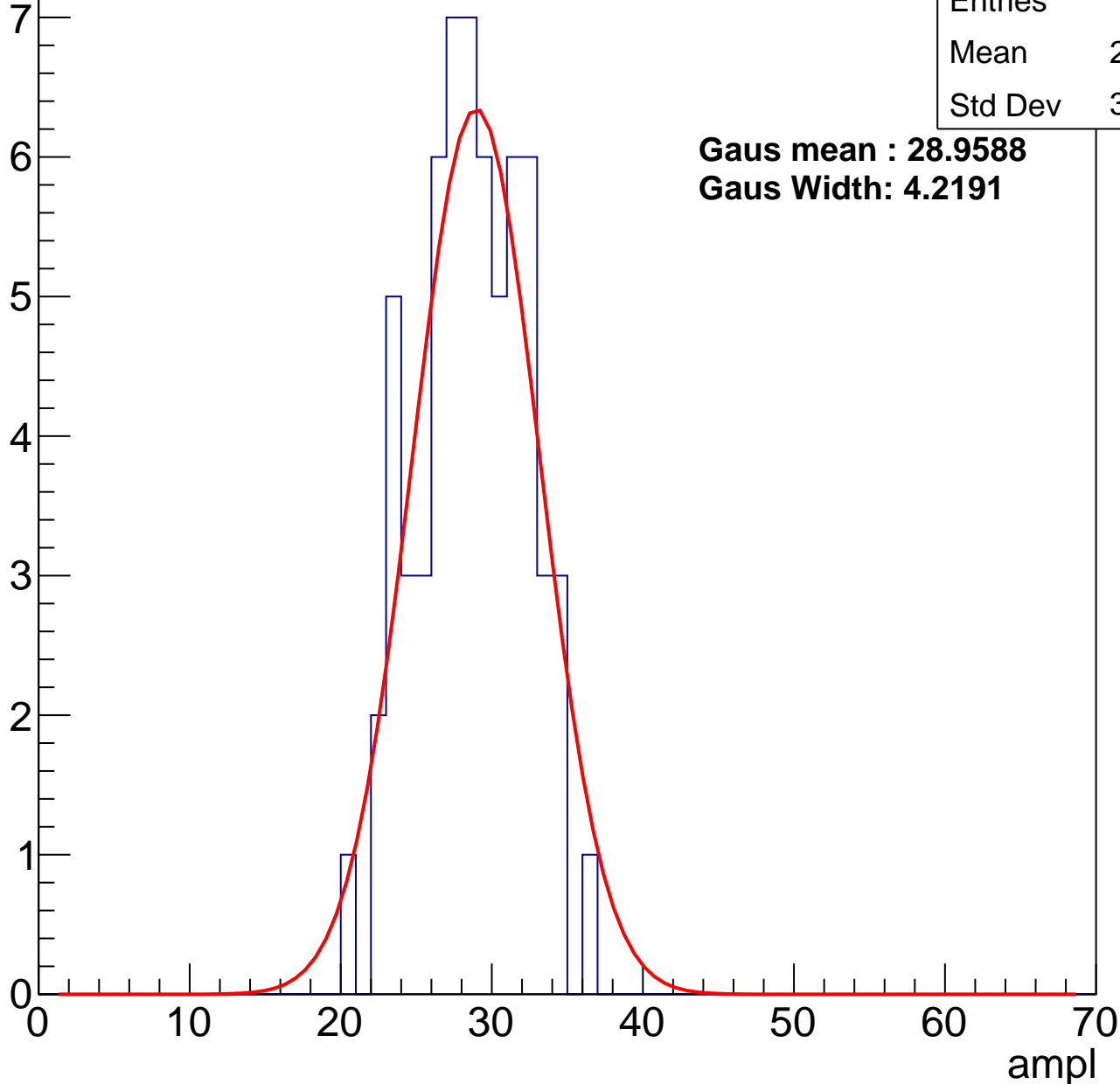
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	28.22
Std Dev	3.507

Gaus mean : 28.9588

Gaus Width: 4.2191



B0L002S, U7-ch112, adc1

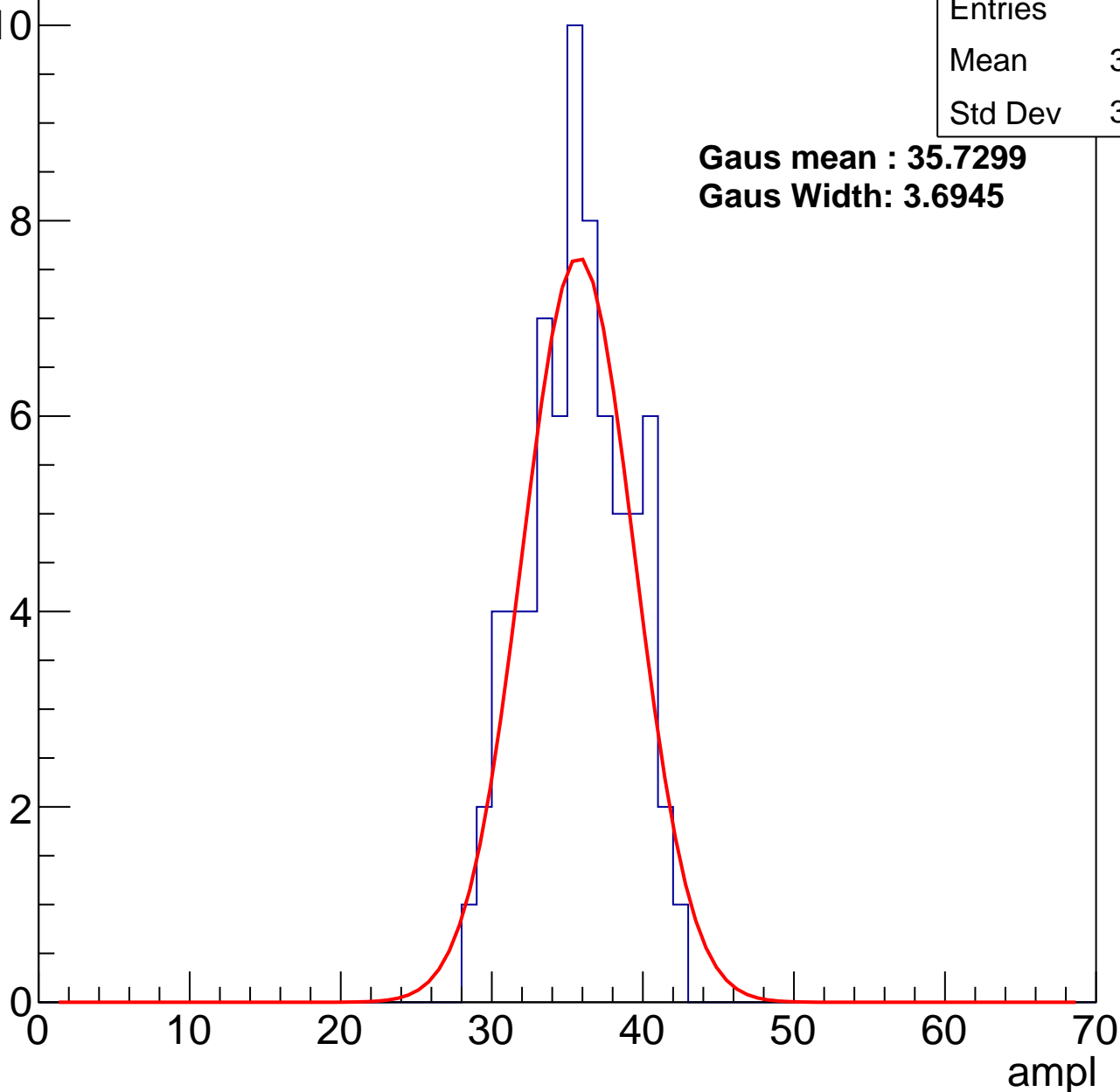
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	35.24
Std Dev	3.312

Gaus mean : 35.7299

Gaus Width: 3.6945



B0L002S, U7-ch112, adc2

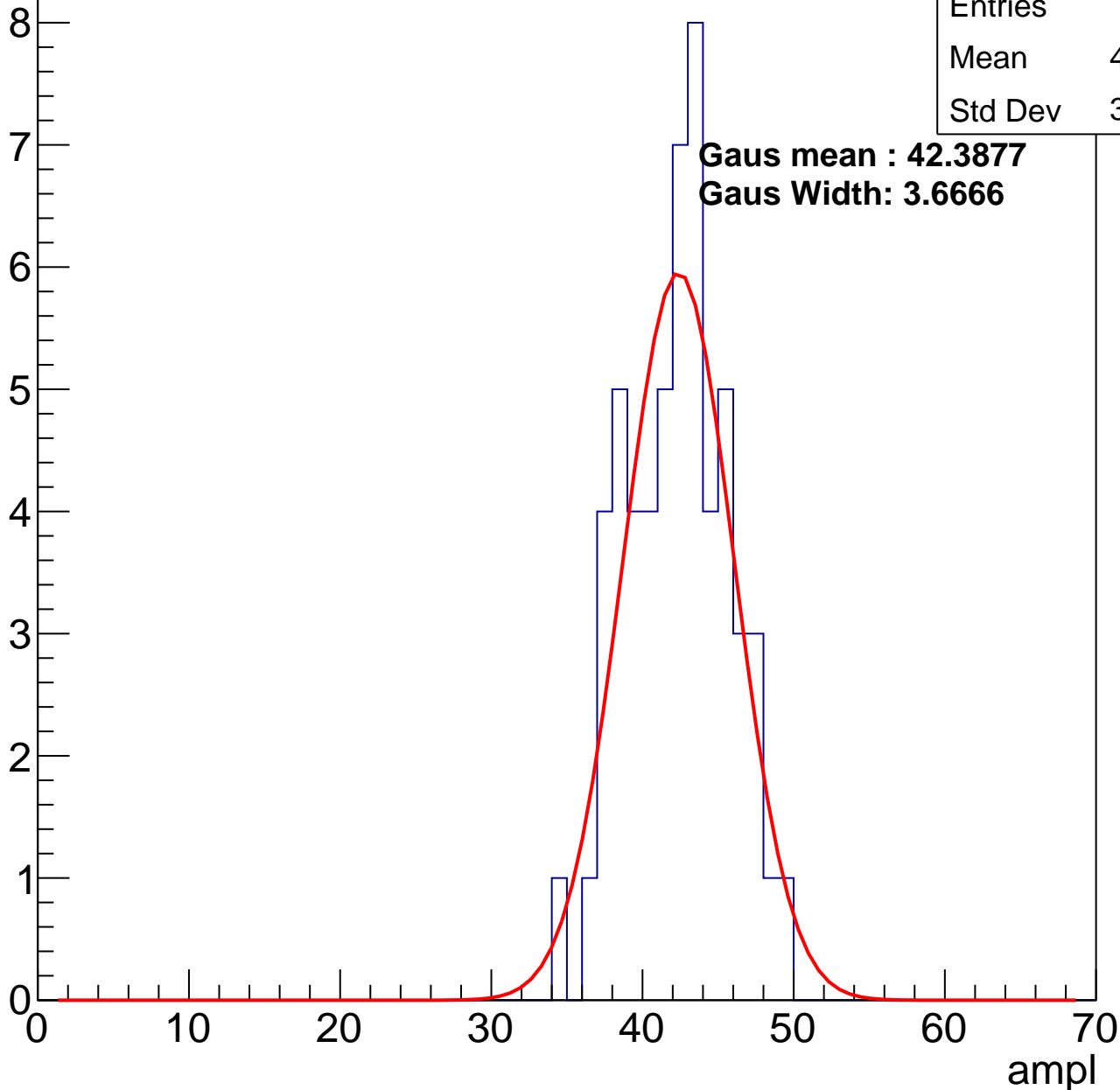
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	41.86
Std Dev	3.308

Gaus mean : 42.3877

Gaus Width: 3.6666

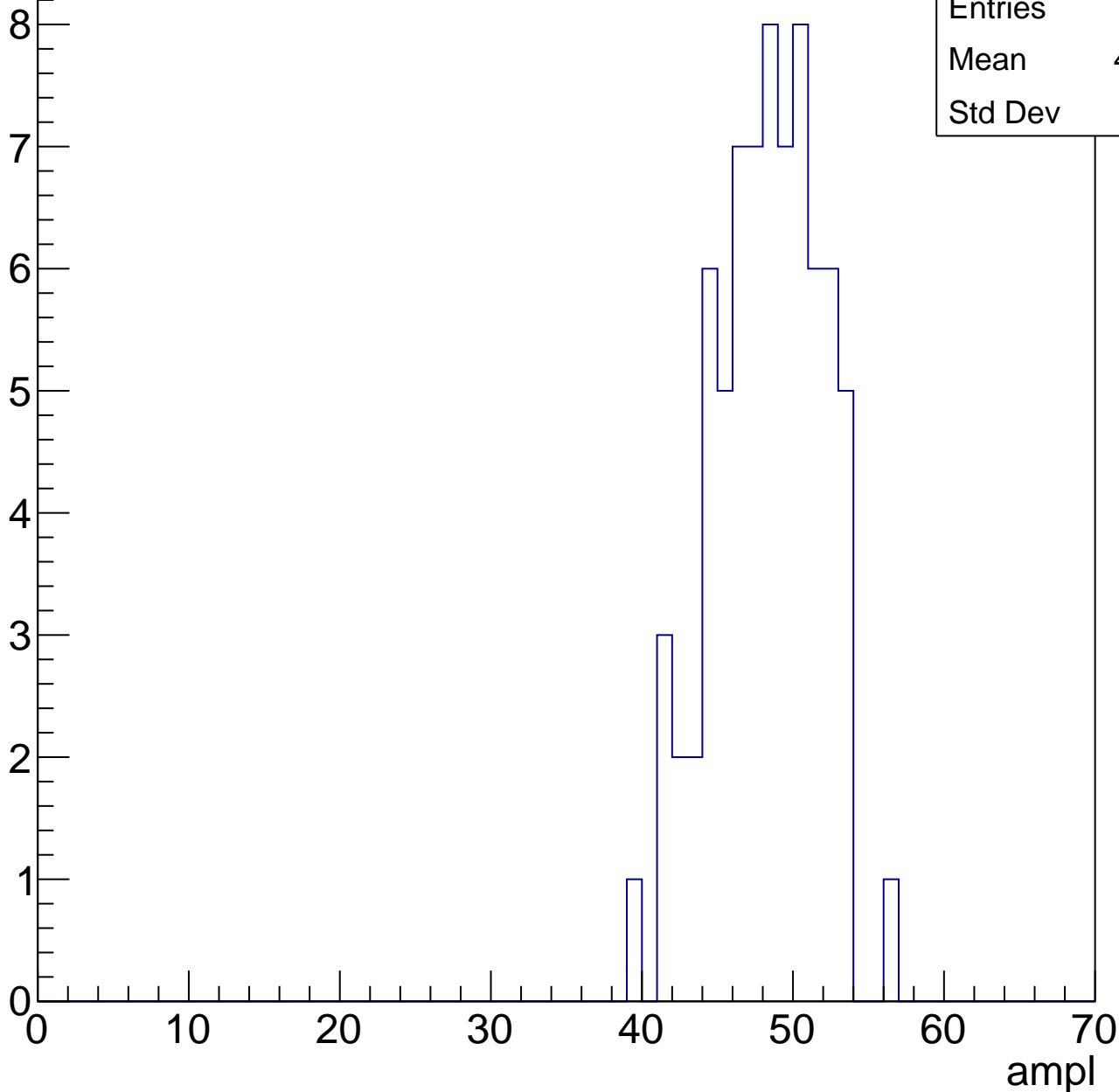


B0L002S, U7-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	47.81
Std Dev	3.49

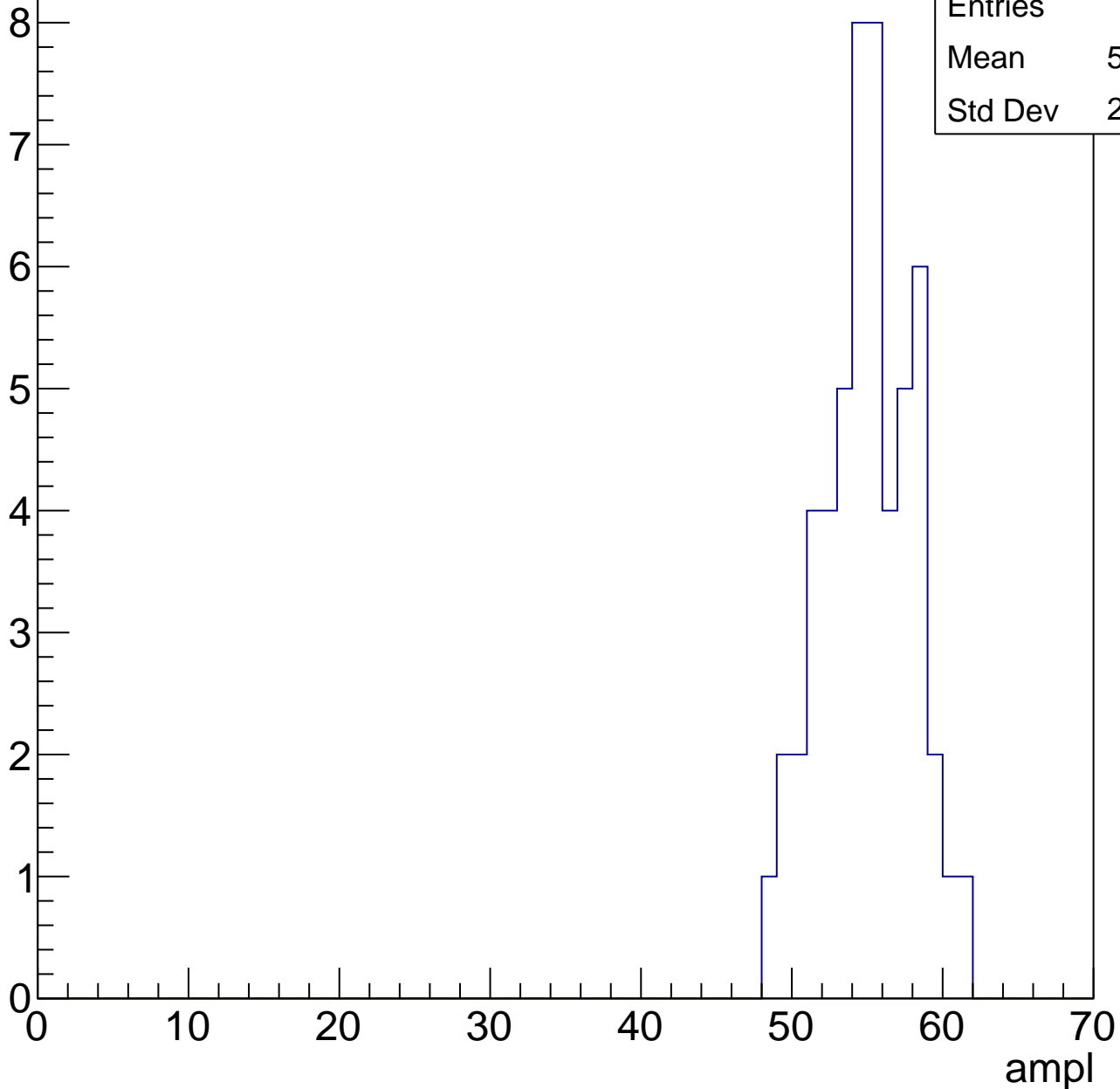


B0L002S, U7-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

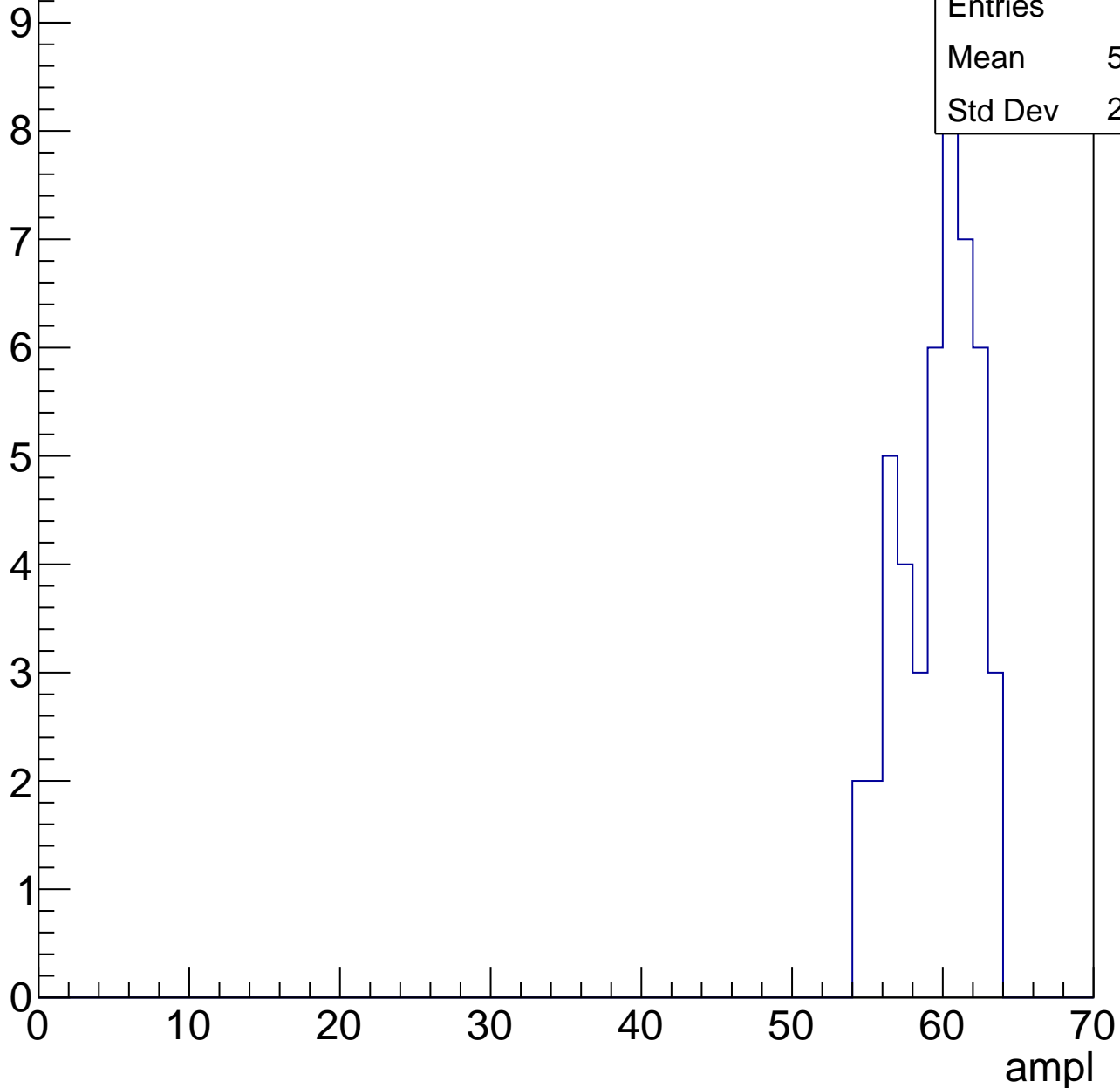
Entries	53
Mean	54.55
Std Dev	2.943



B0L002S, U7-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

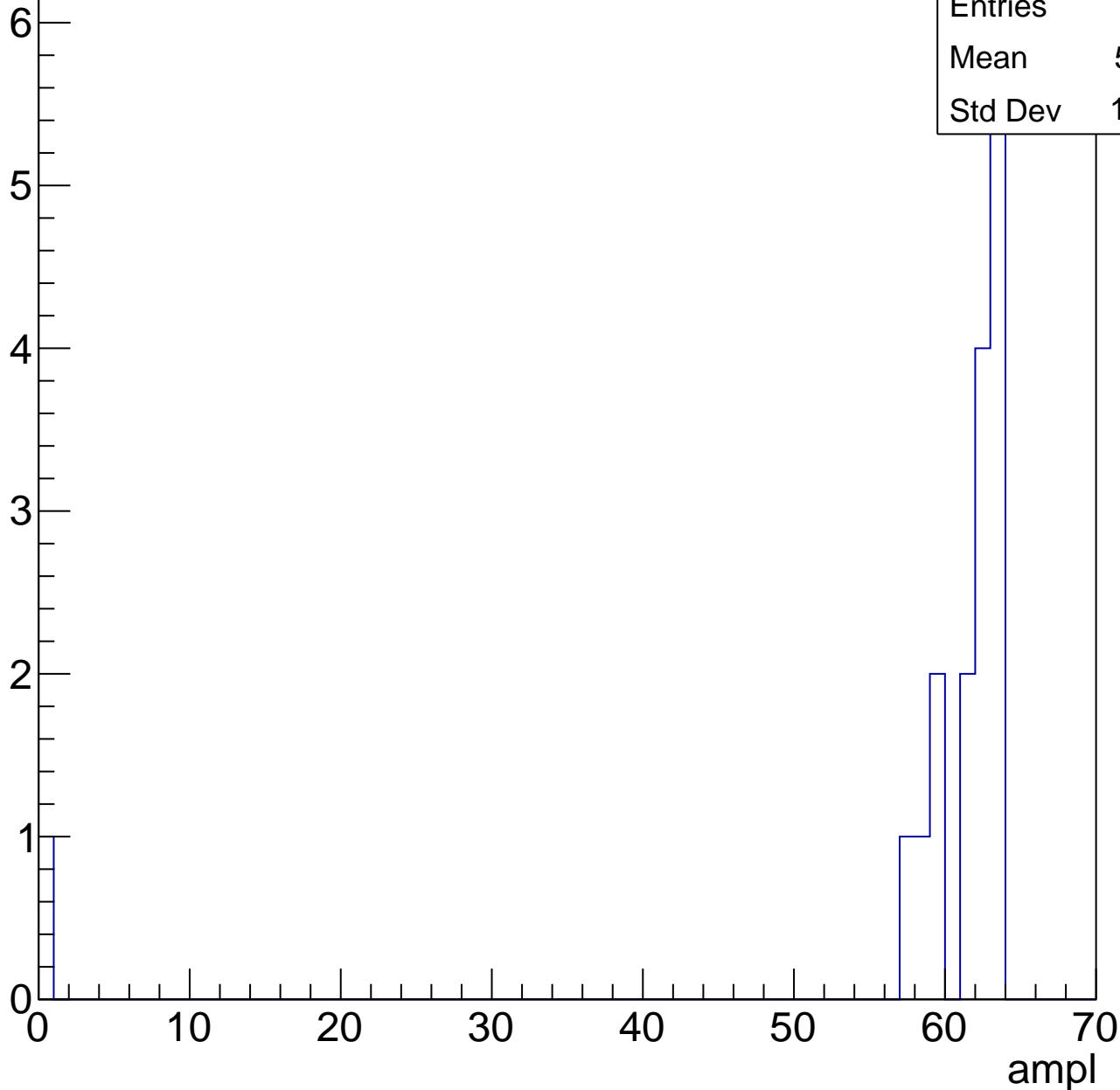


B0L002S, U7-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	57.71
Std Dev	14.55



B0L002S, U7-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L002S, U7-ch113, adc0

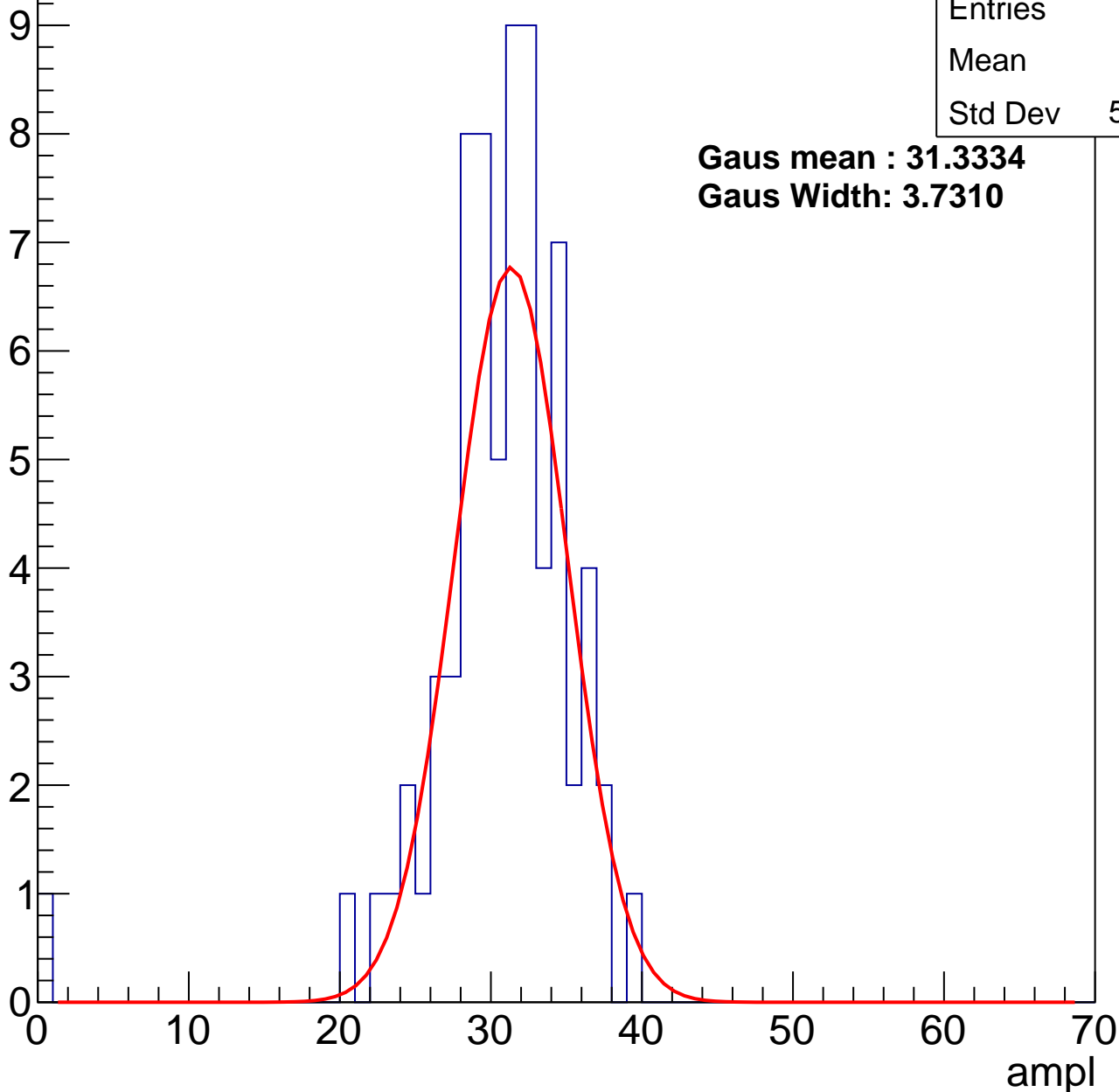
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.1
Std Dev	5.124

Gaus mean : 31.3334

Gaus Width: 3.7310



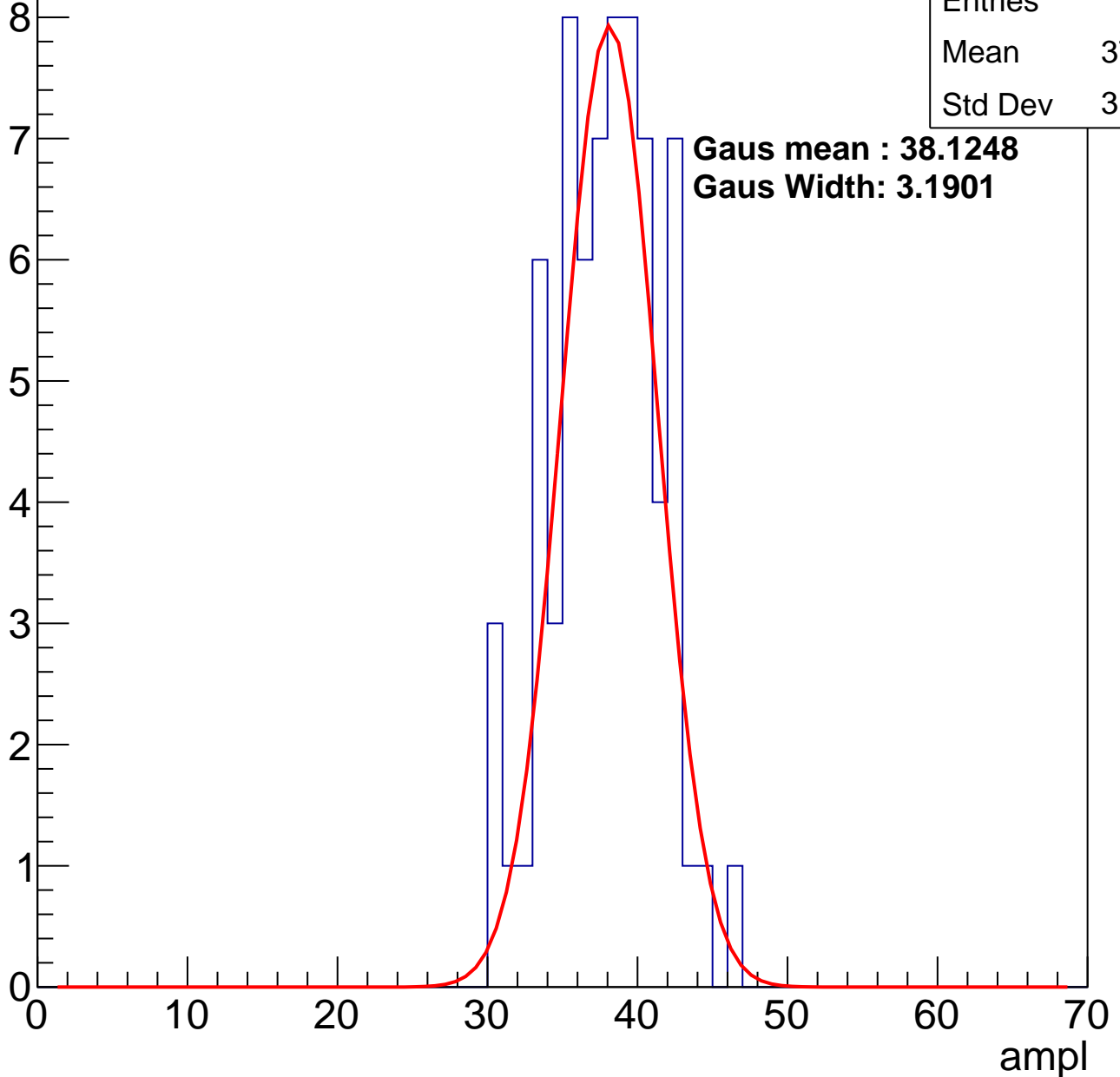
B0L002S, U7-ch113, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.43
Std Dev	3.463

Gaus mean : 38.1248
Gaus Width: 3.1901



B0L002S, U7-ch113, adc2

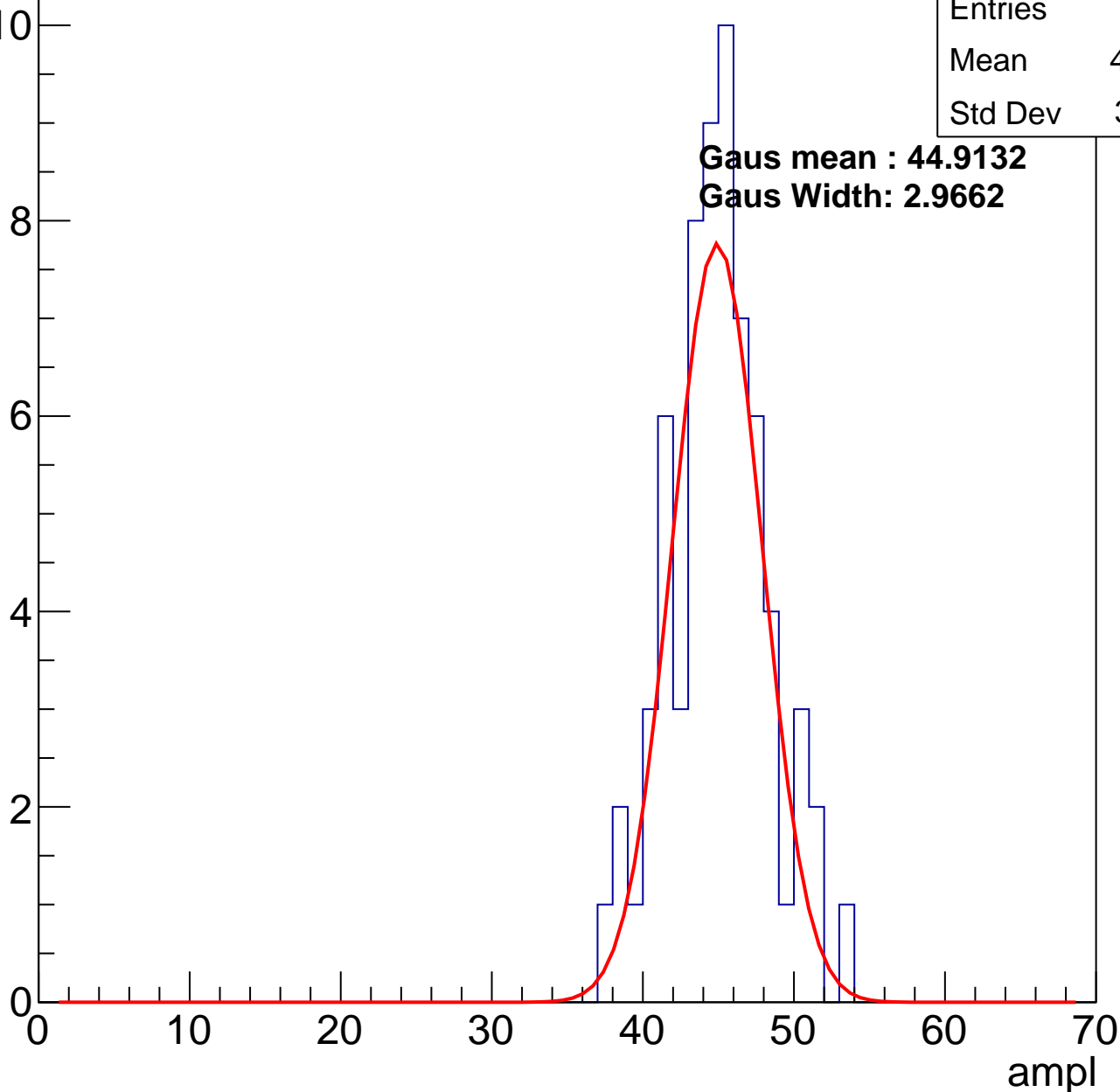
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.54
Std Dev	3.271

Gaus mean : 44.9132

Gaus Width: 2.9662

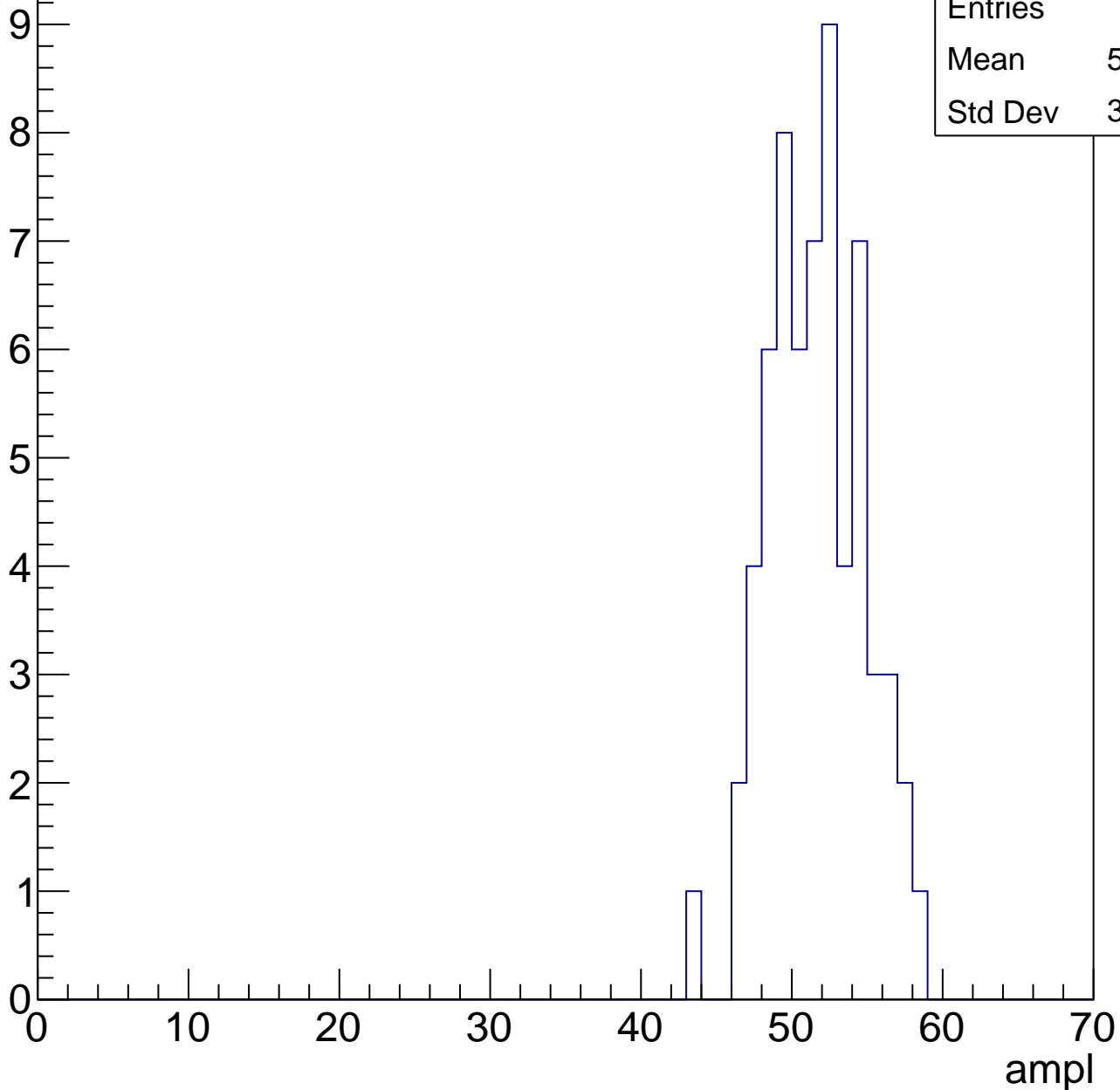


B0L002S, U7-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

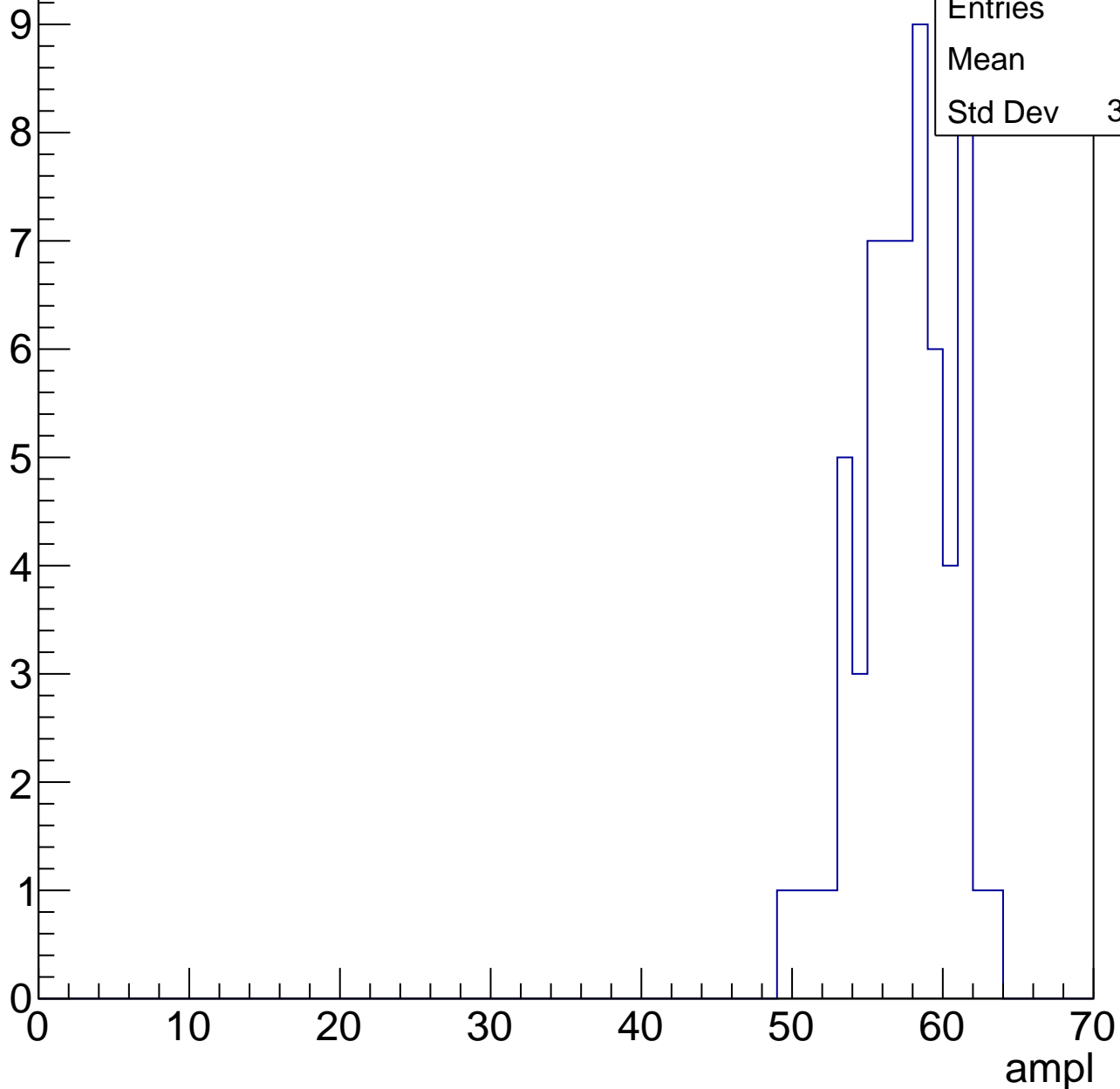
Entries	63
Mean	51.16
Std Dev	3.087



B0L002S, U7-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

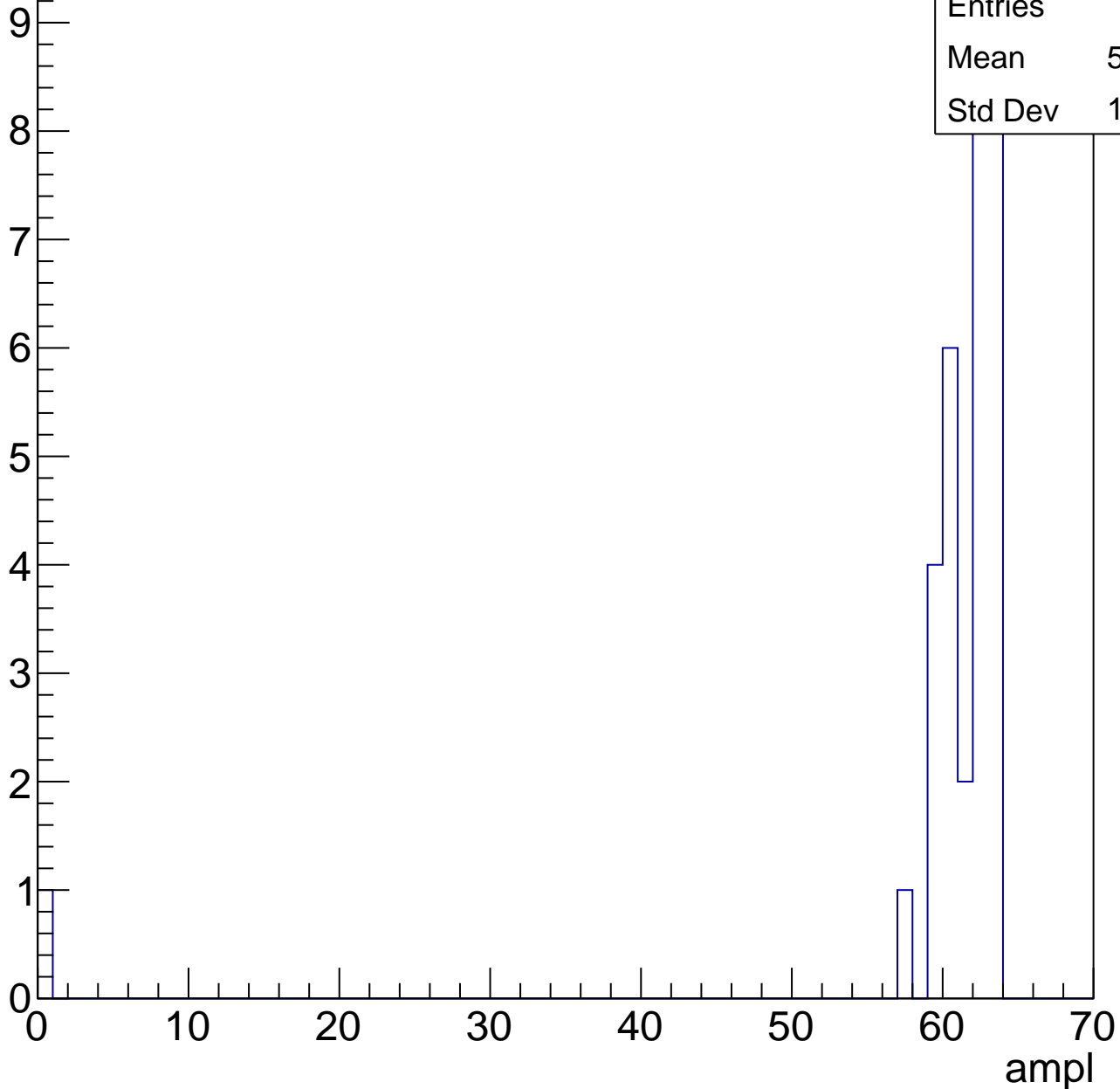


B0L002S, U7-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.29
Std Dev	10.94



B0L002S, U7-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch114, adc0

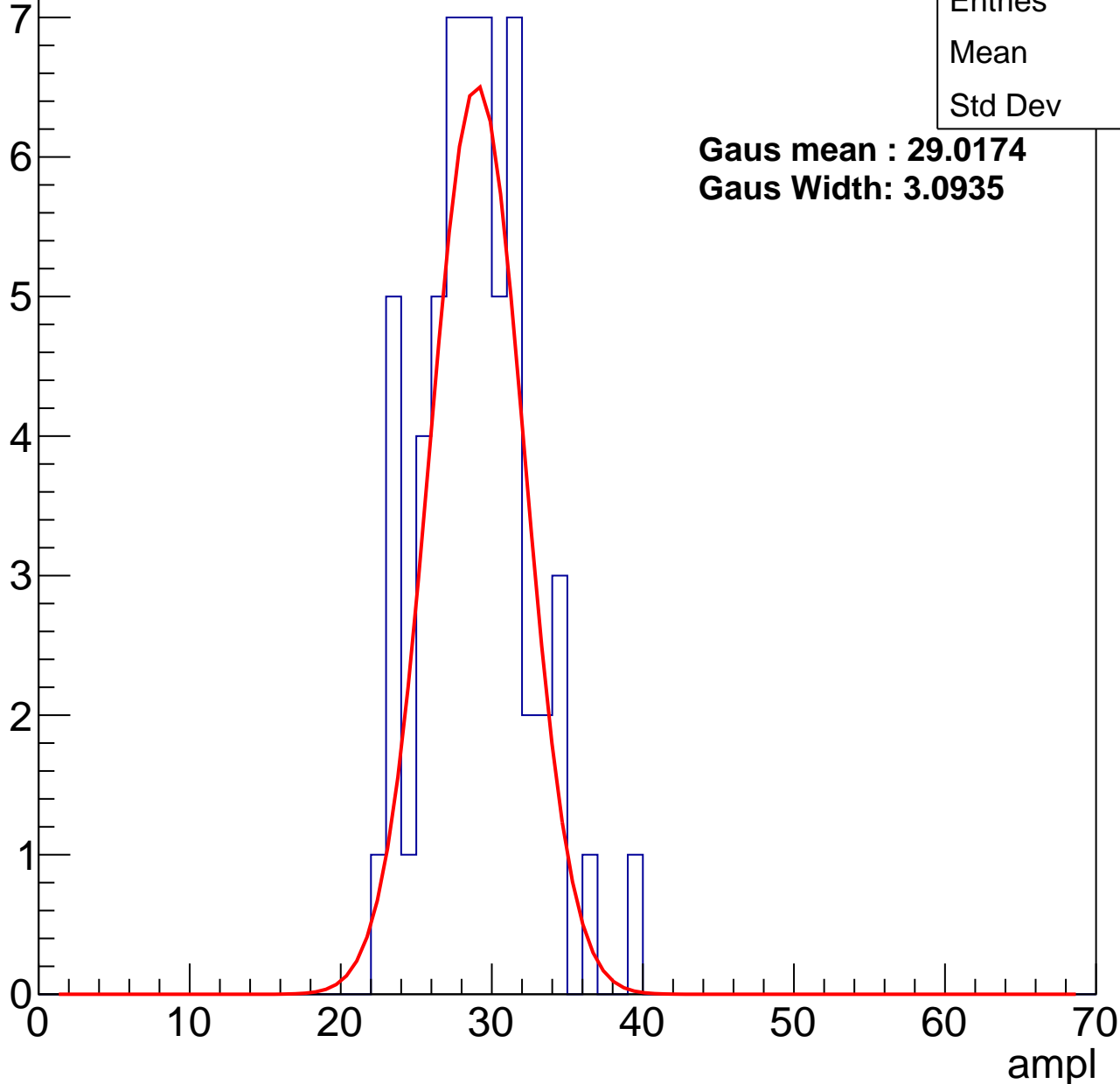
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	28.5
Std Dev	3.45

Gaus mean : 29.0174

Gaus Width: 3.0935



B0L002S, U7-ch114, adc1

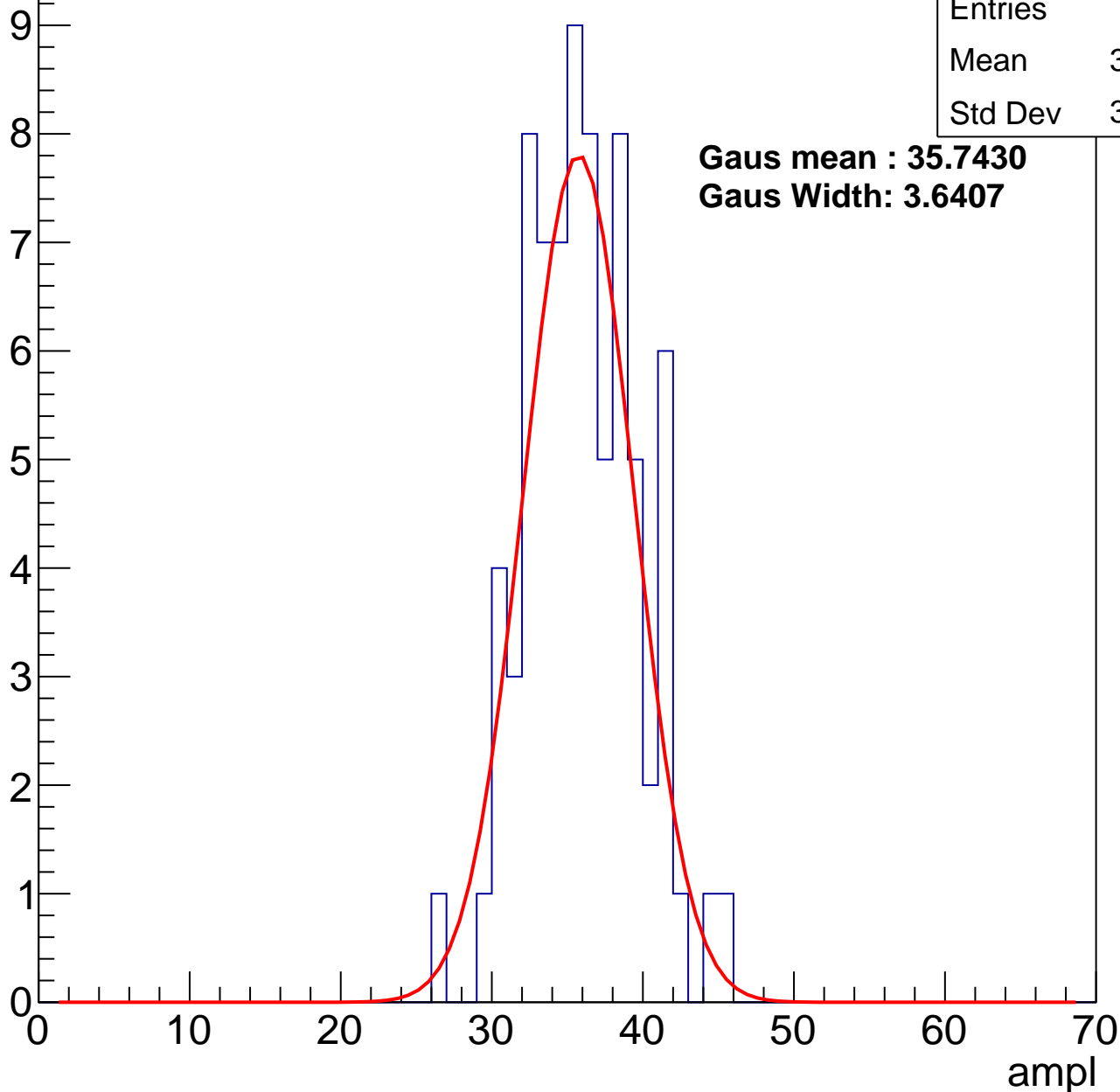
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	35.55
Std Dev	3.649

Gaus mean : 35.7430

Gaus Width: 3.6407



B0L002S, U7-ch114, adc2

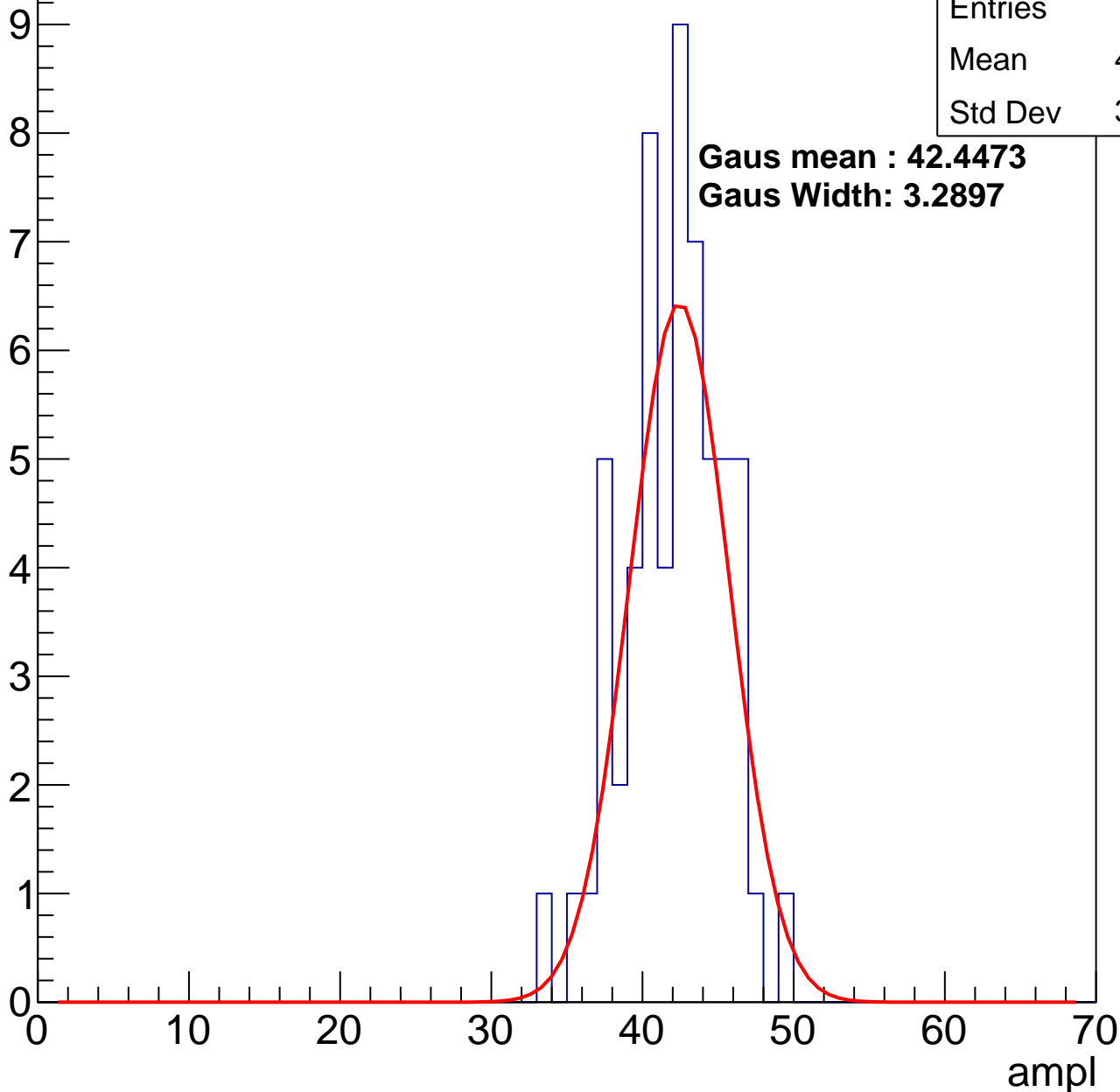
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	41.61
Std Dev	3.221

Gaus mean : 42.4473

Gaus Width: 3.2897

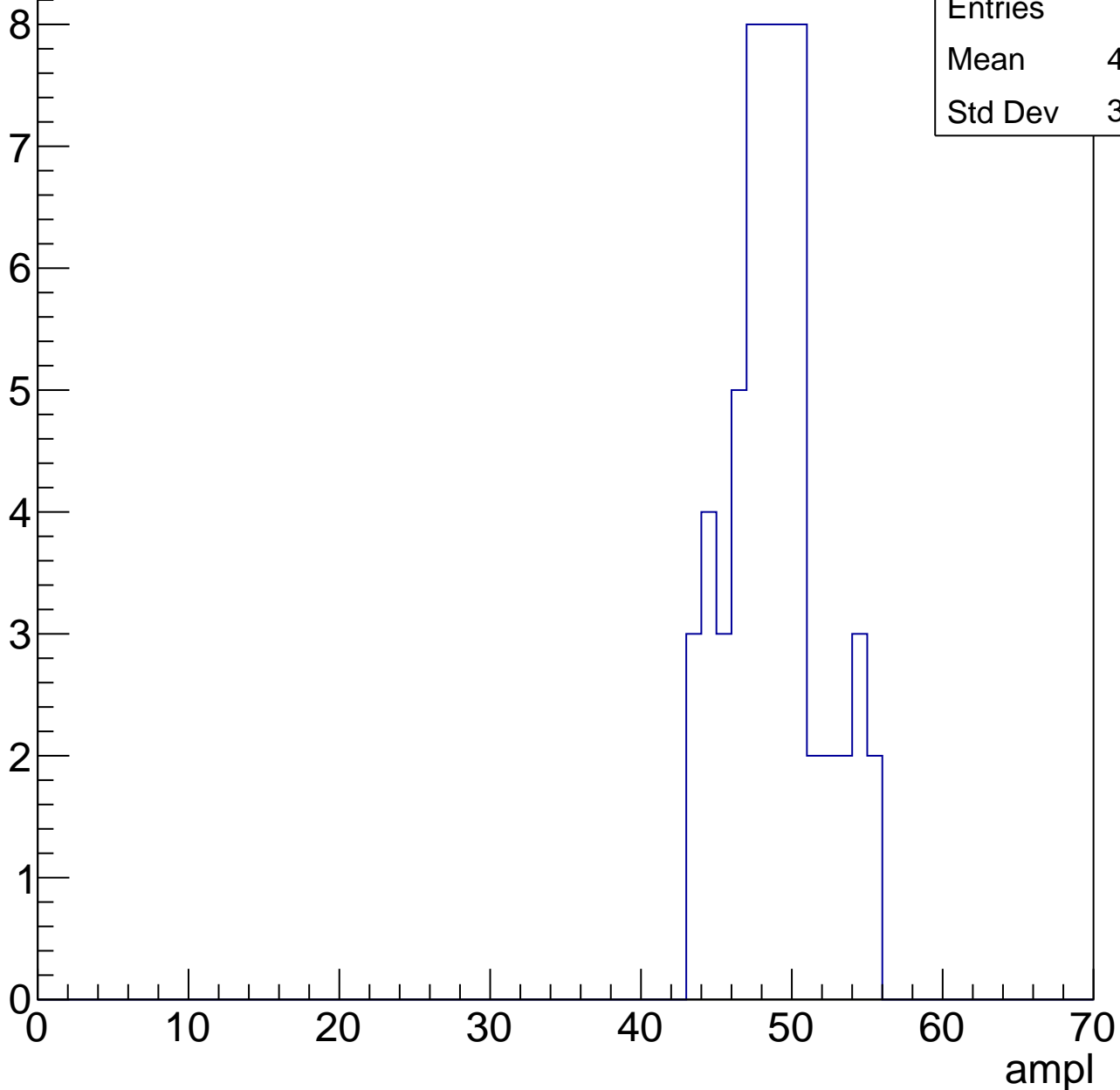


B0L002S, U7-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	48.38
Std Dev	3.028



B0L002S, U7-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

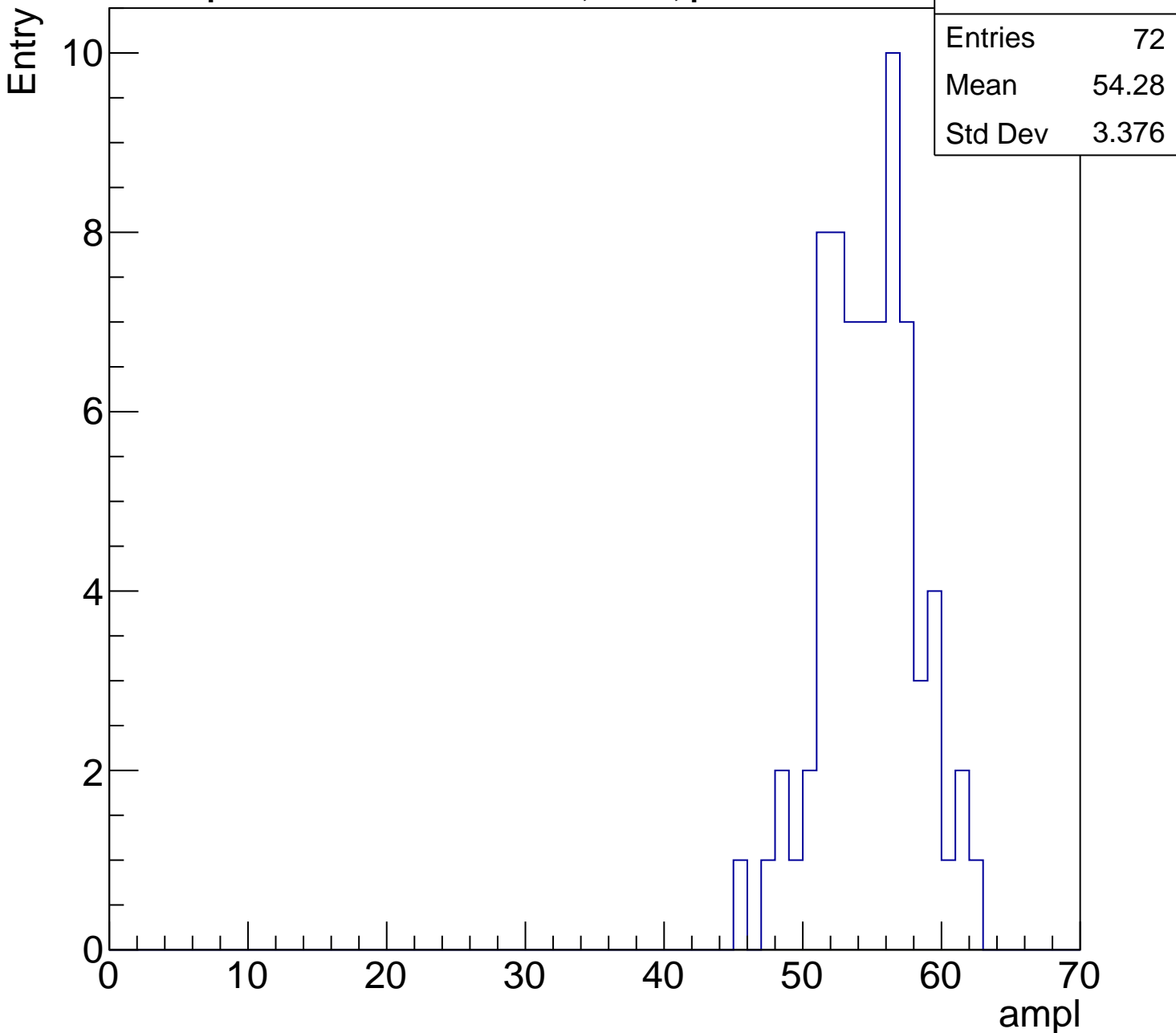
Entries	72
Mean	54.28
Std Dev	3.376

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

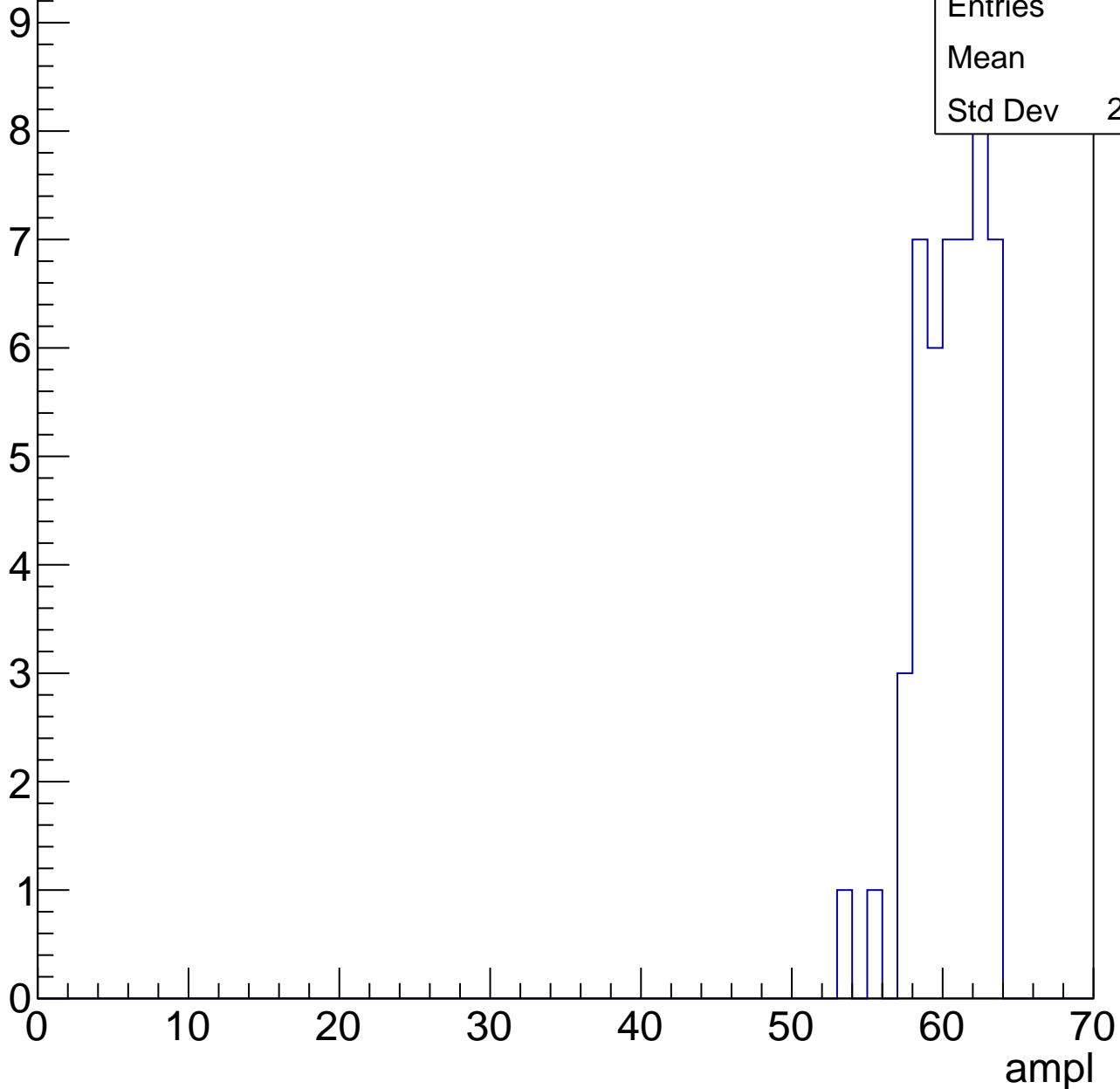


B0L002S, U7-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

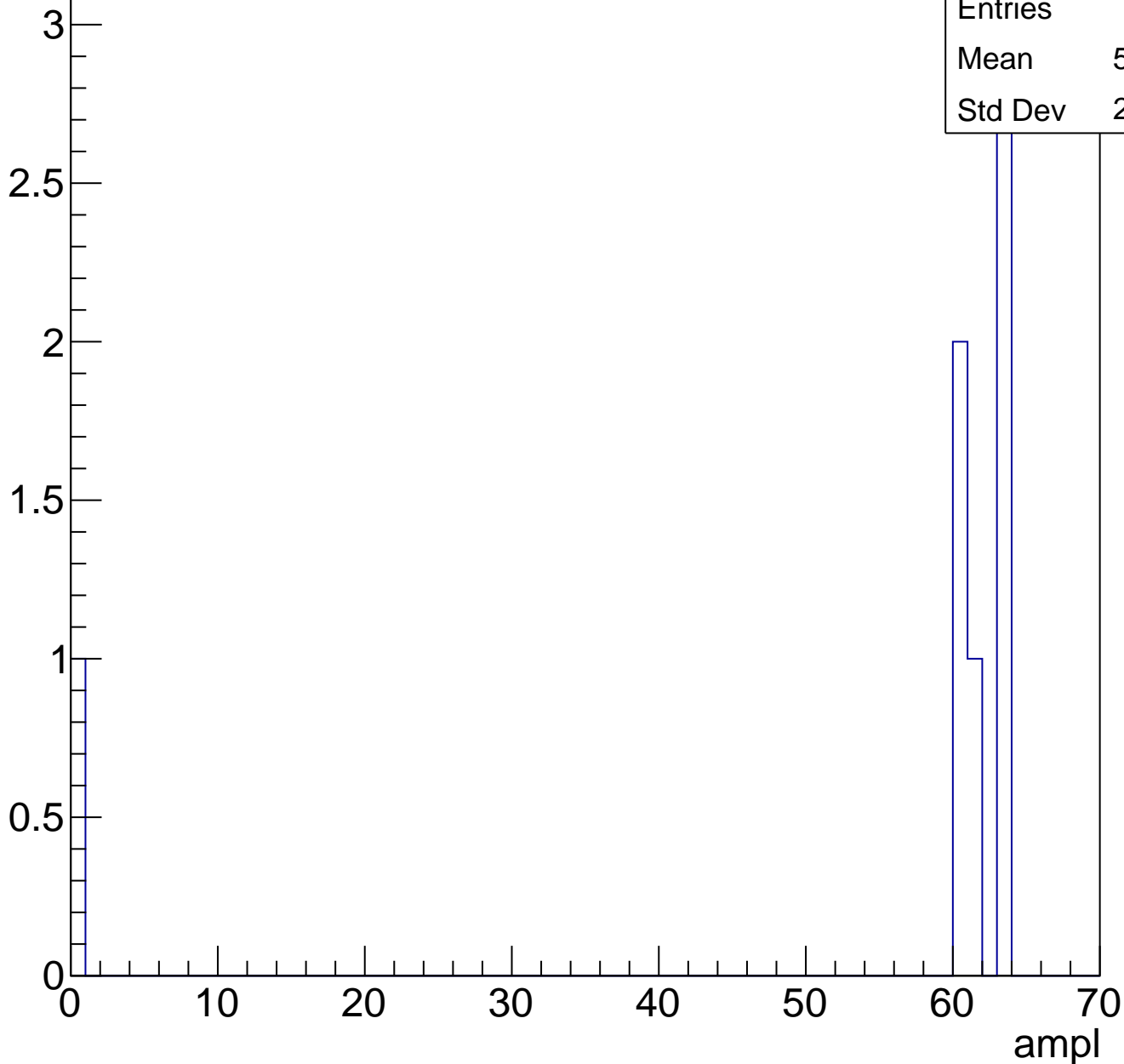
Entries	48
Mean	60.1
Std Dev	2.238



B0L002S, U7-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U7-ch115, adc0

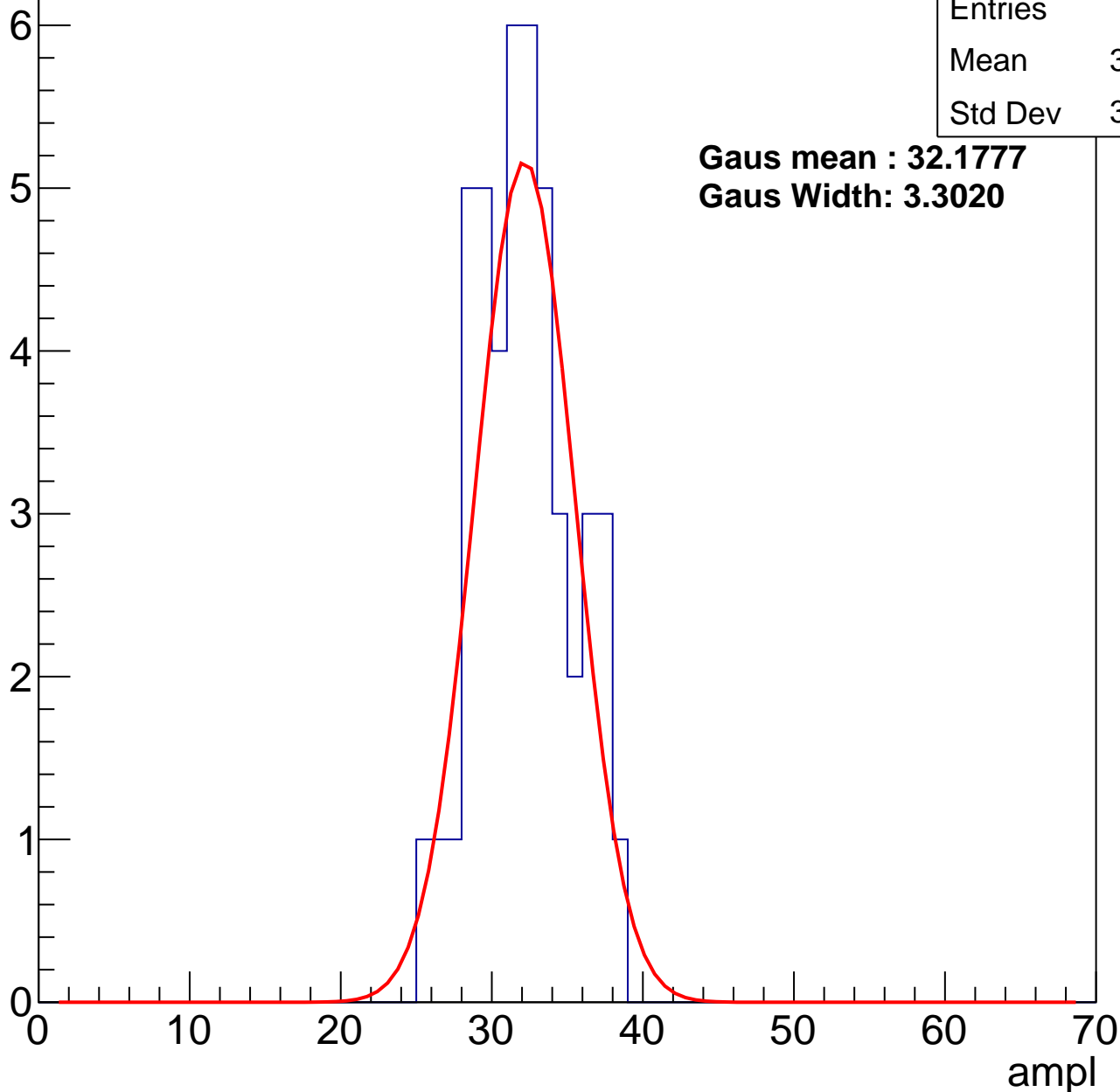
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	31.63
Std Dev	3.109

Gaus mean : 32.1777

Gaus Width: 3.3020



B0L002S, U7-ch115, adc1

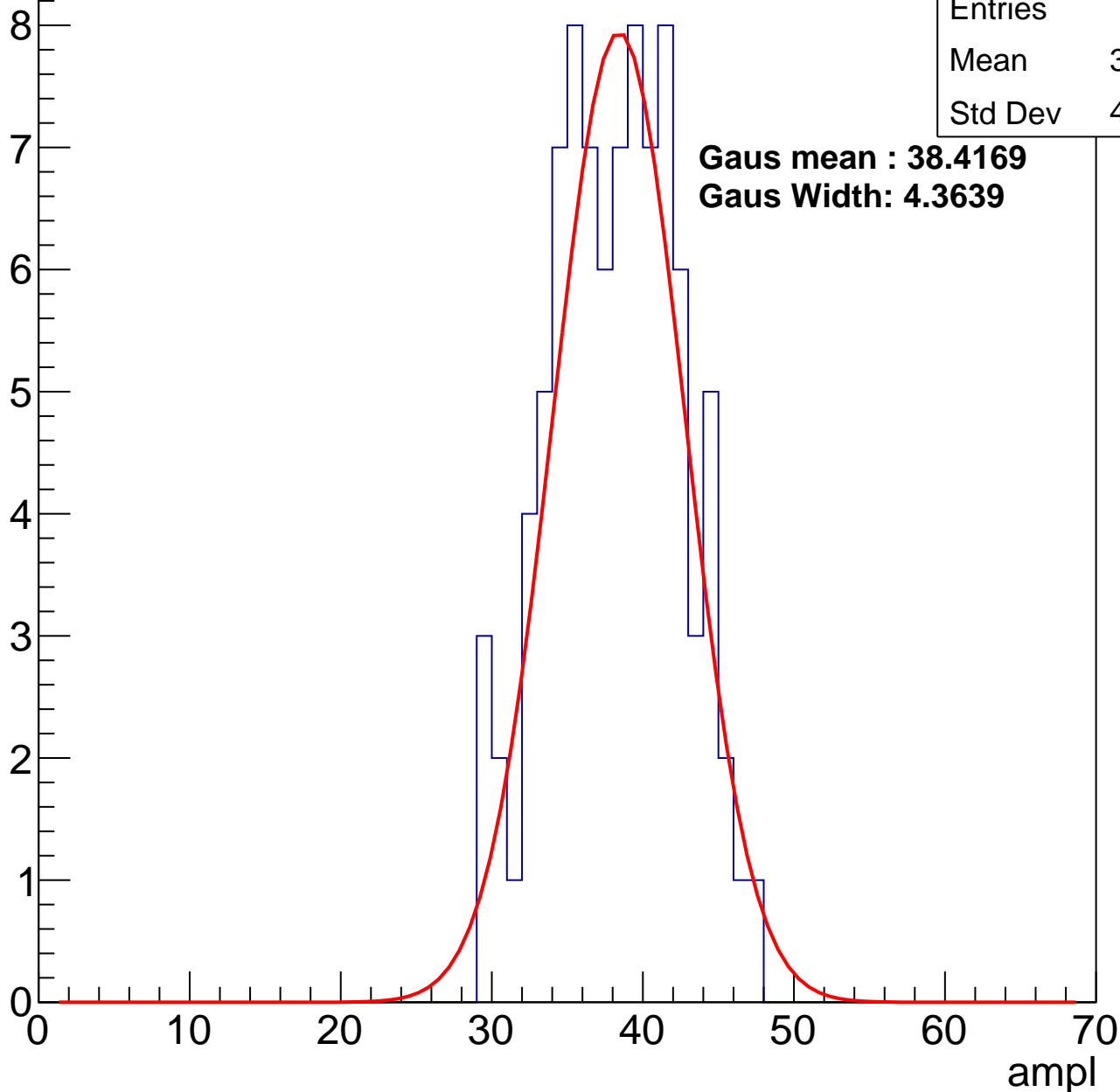
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	91
Mean	37.73
Std Dev	4.196

Gaus mean : 38.4169

Gaus Width: 4.3639



B0L002S, U7-ch115, adc2

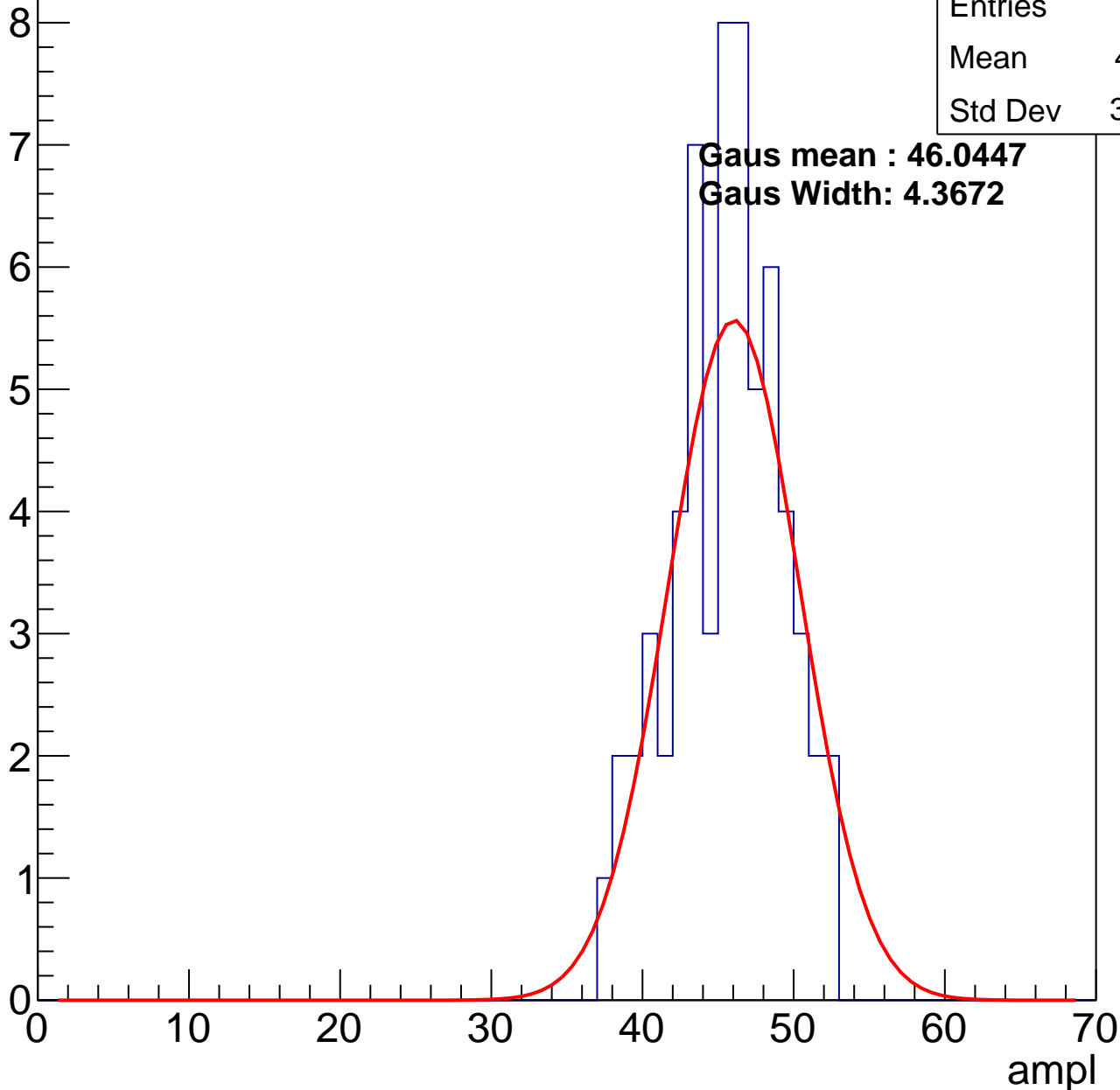
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.11
Std Dev	3.588

Gaus mean : 46.0447

Gaus Width: 4.3672

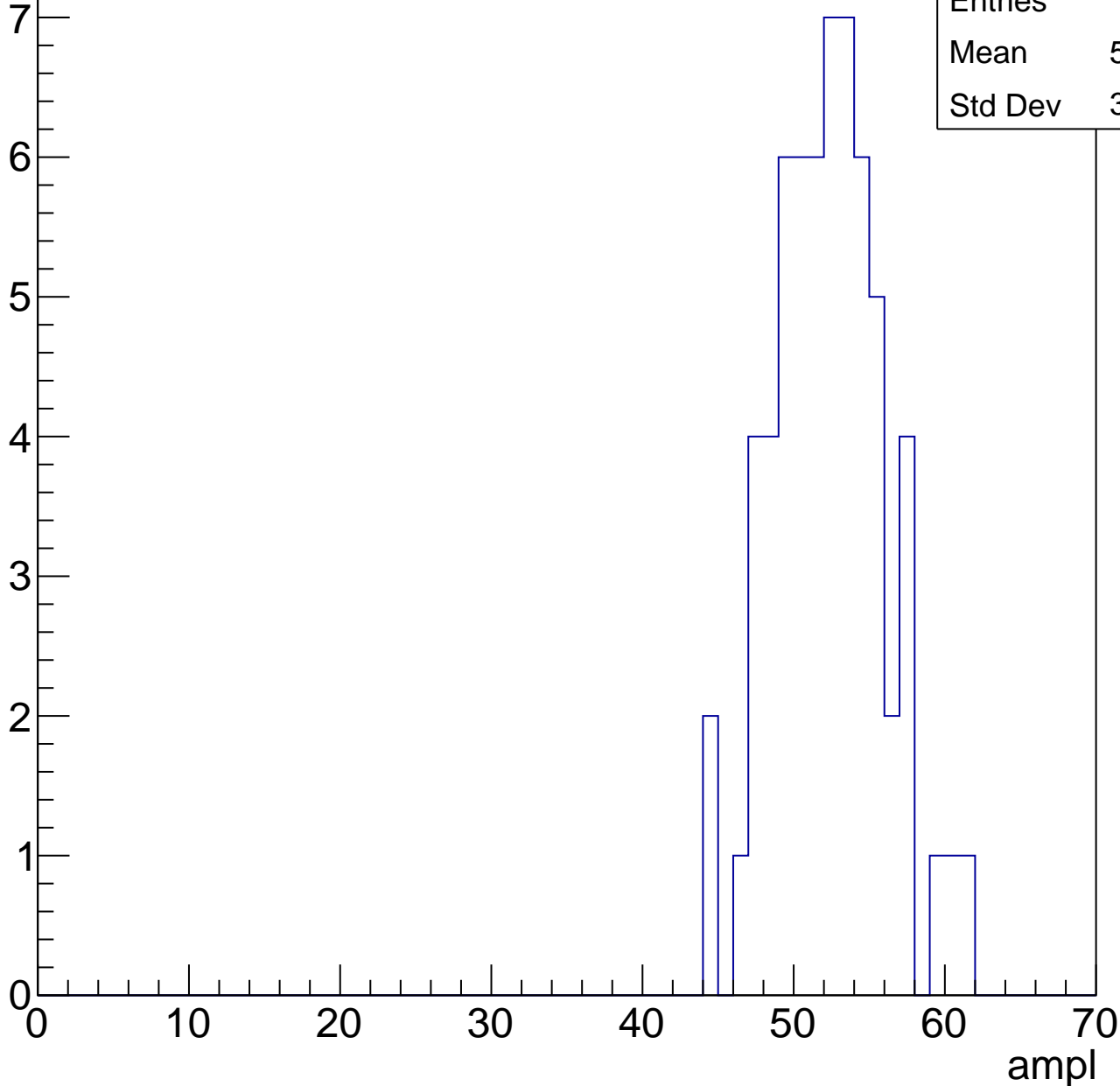


B0L002S, U7-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	51.87
Std Dev	3.592

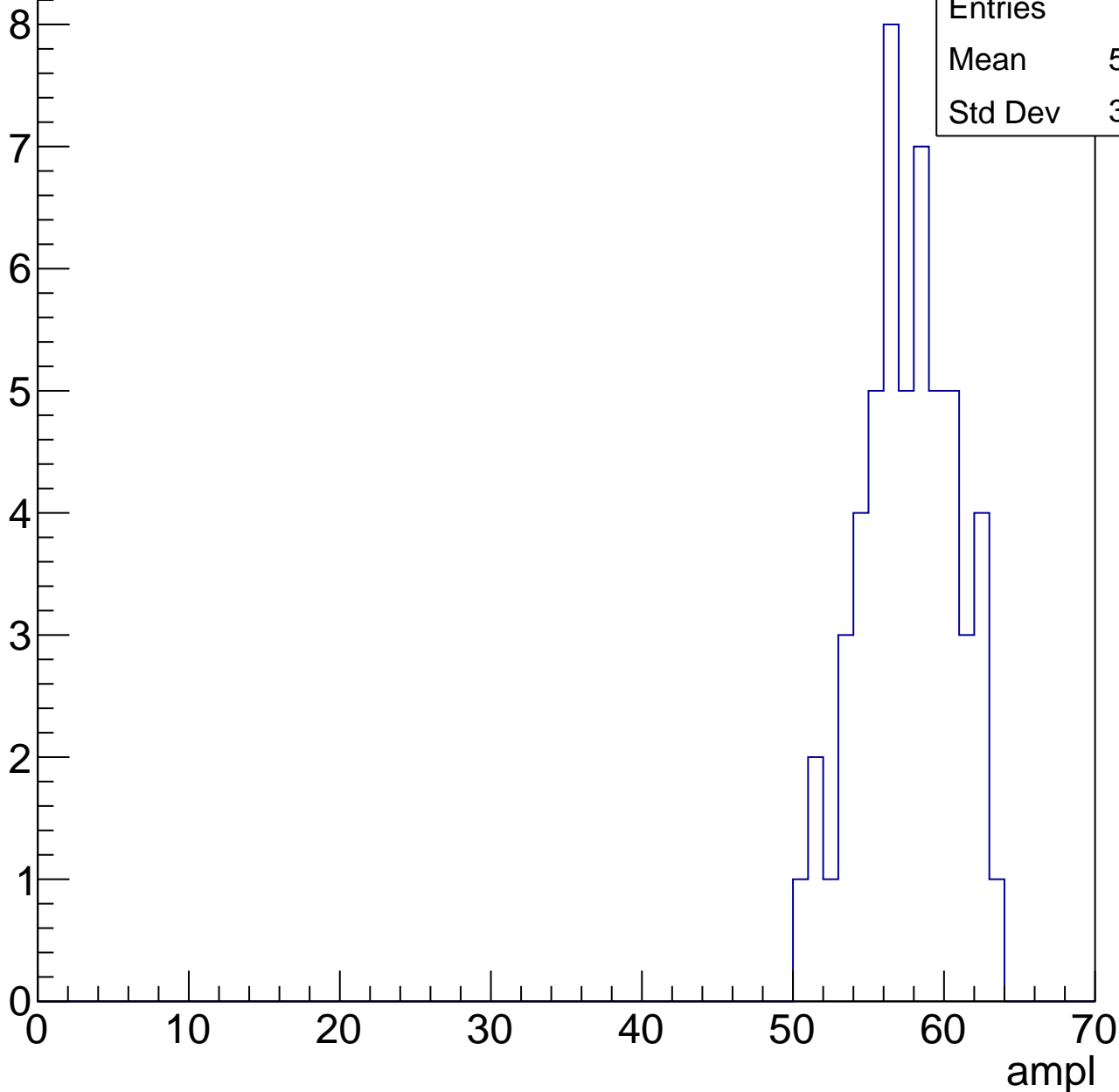


B0L002S, U7-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57.07
Std Dev	3.084

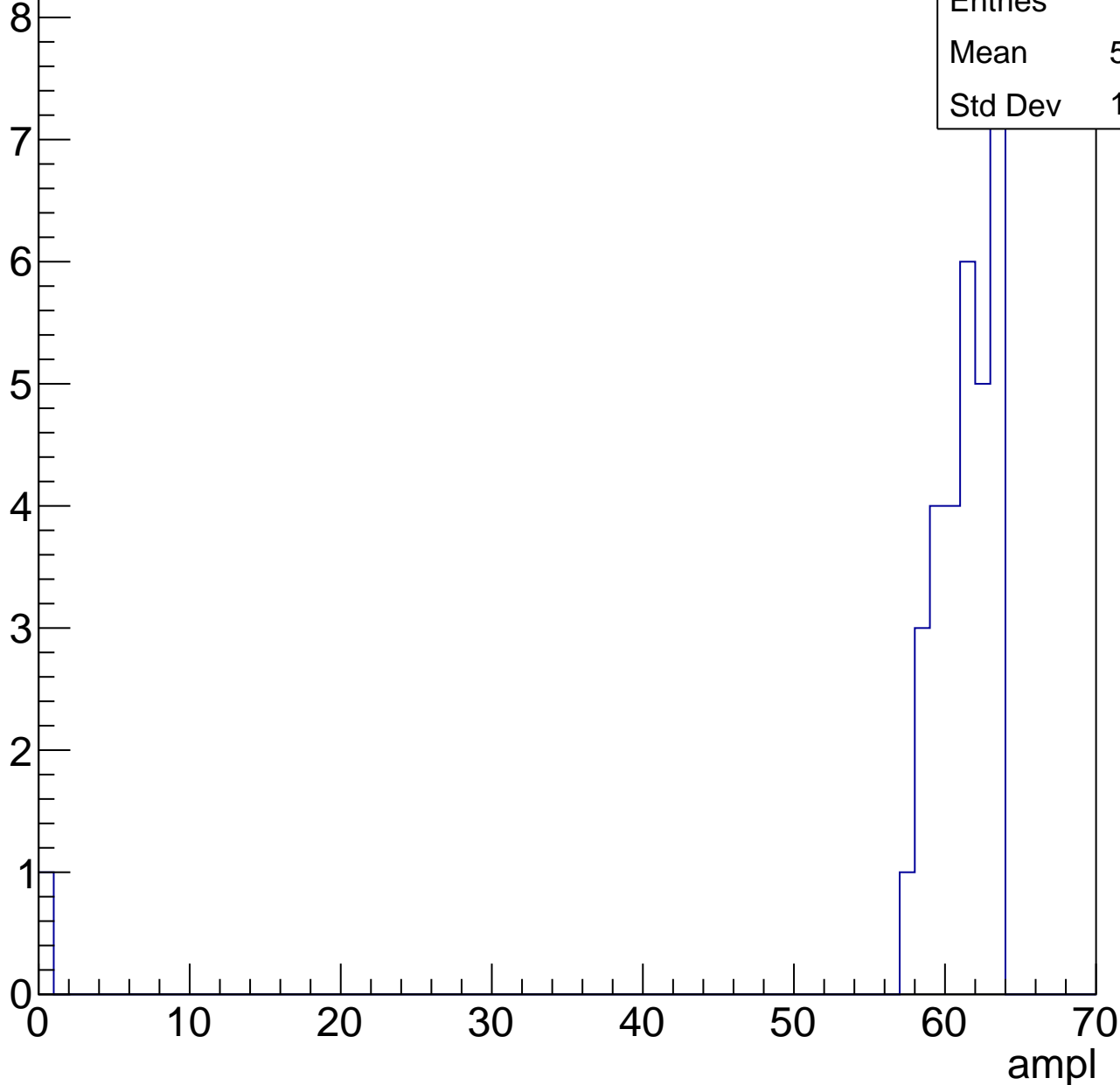


B0L002S, U7-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.97
Std Dev	10.74



B0L002S, U7-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch116, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	30.82
Std Dev	3.176

Gaus mean : 30.3402

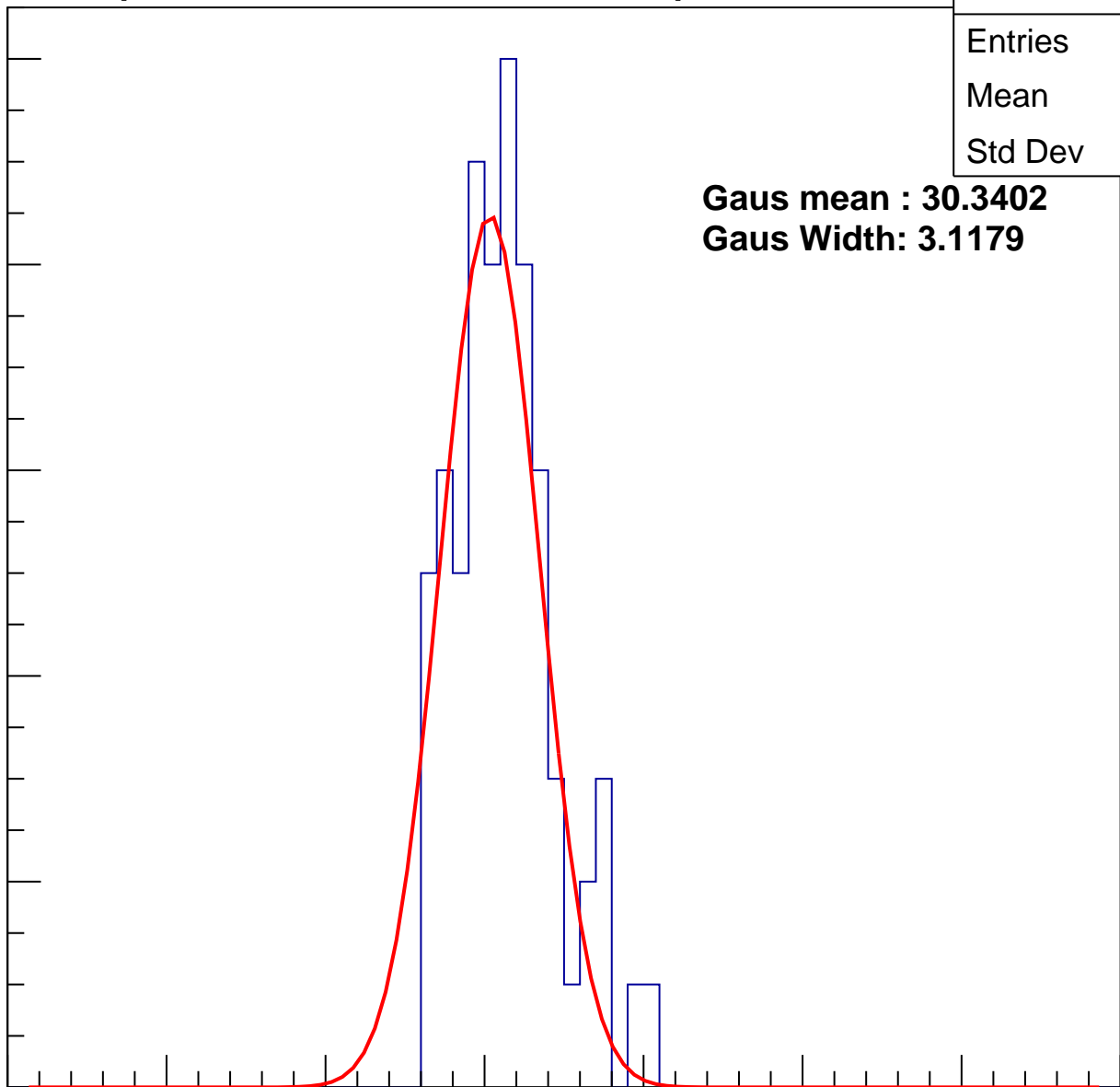
Gaus Width: 3.1179

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch116, adc1

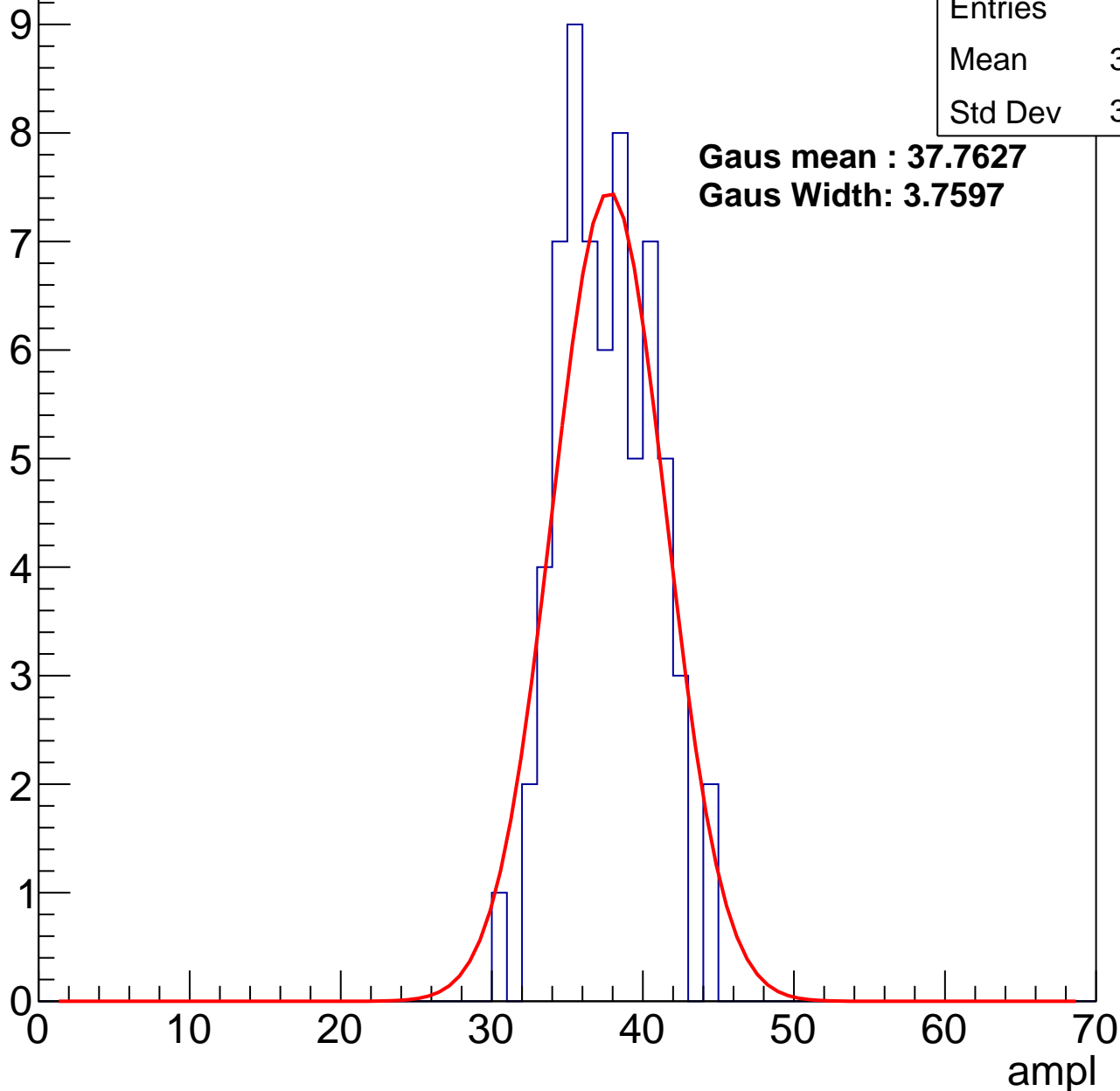
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.14
Std Dev	3.045

Gaus mean : 37.7627

Gaus Width: 3.7597



B0L002S, U7-ch116, adc2

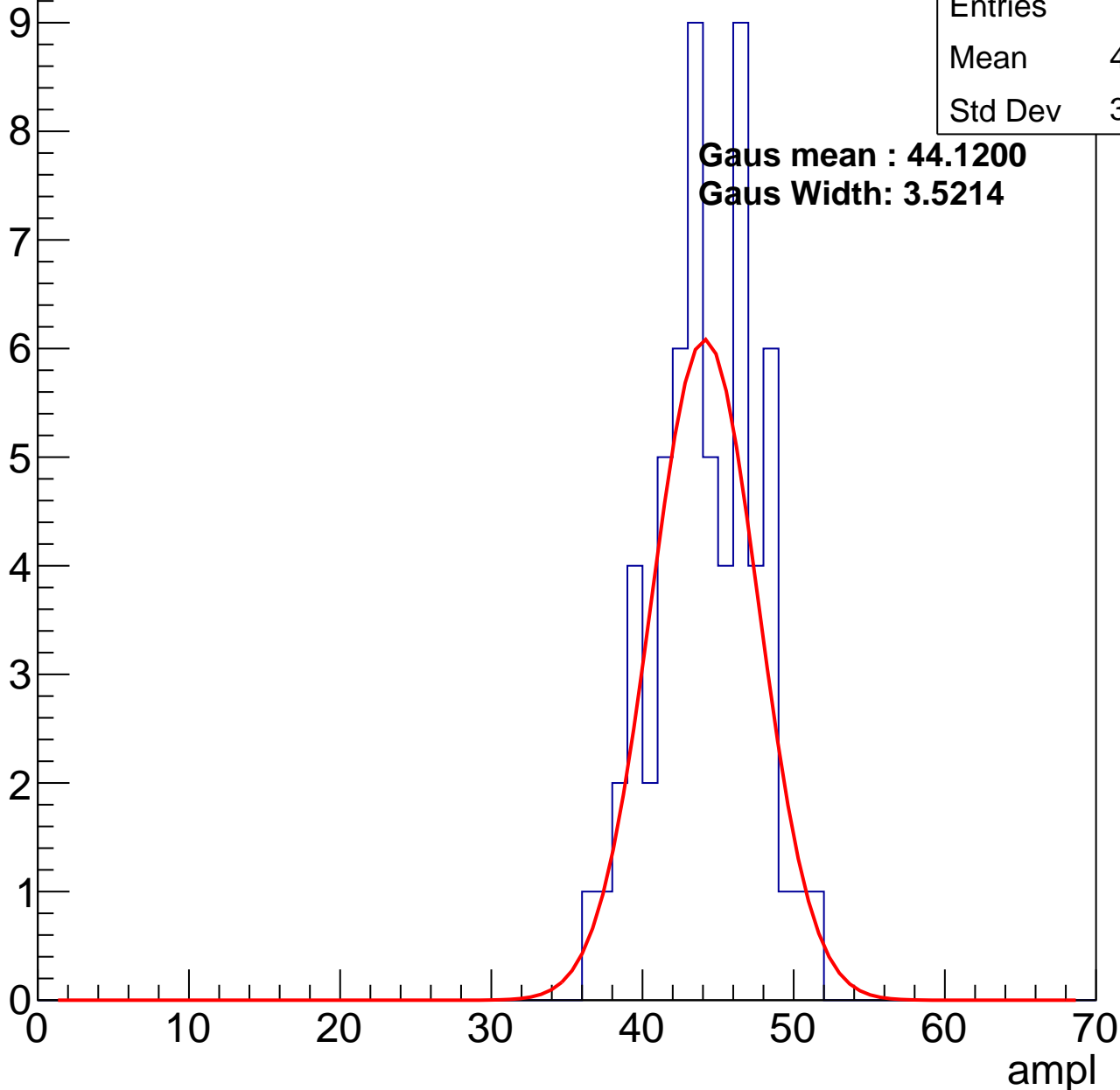
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.75
Std Dev	3.322

Gaus mean : 44.1200

Gaus Width: 3.5214

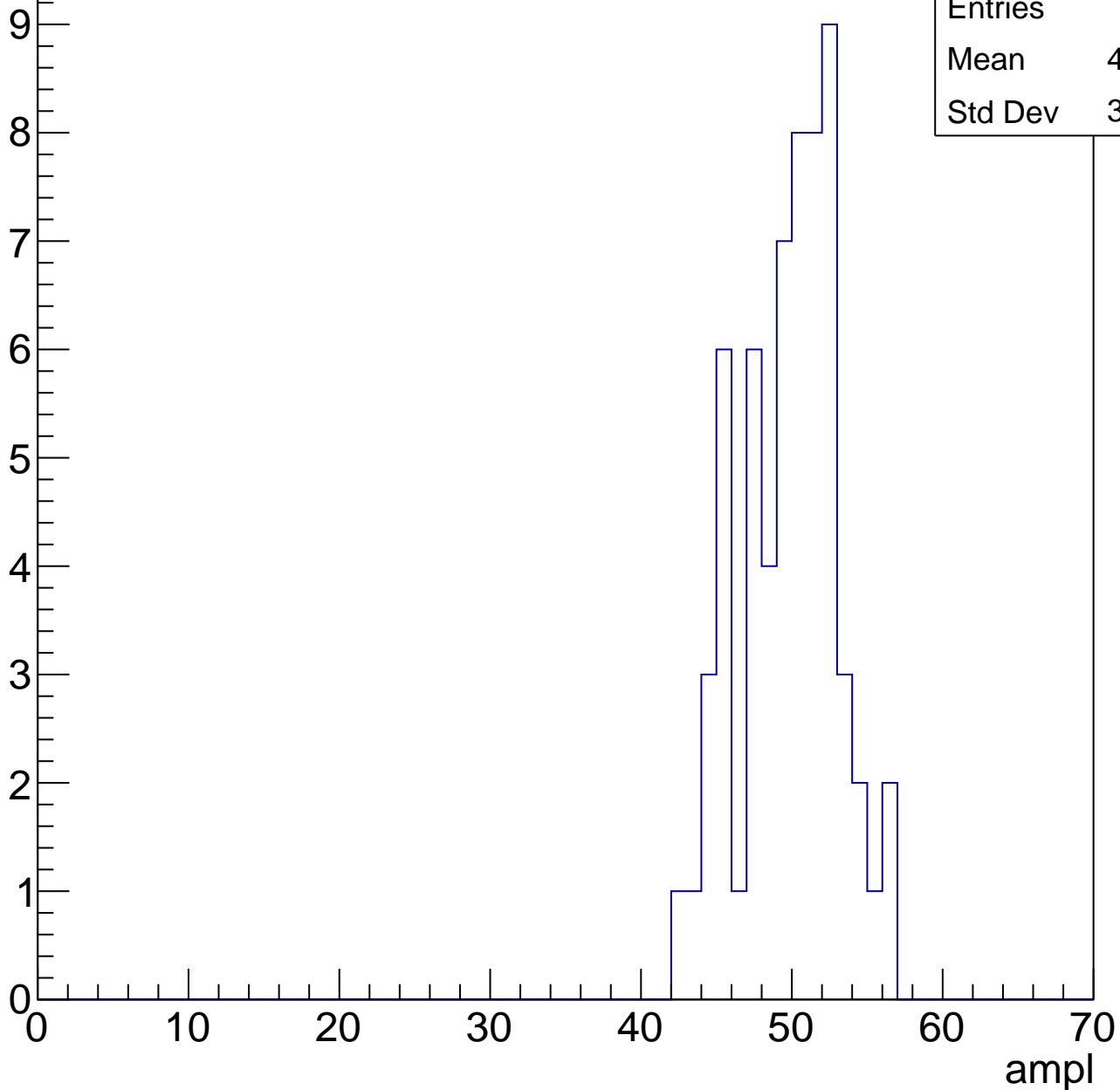


B0L002S, U7-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

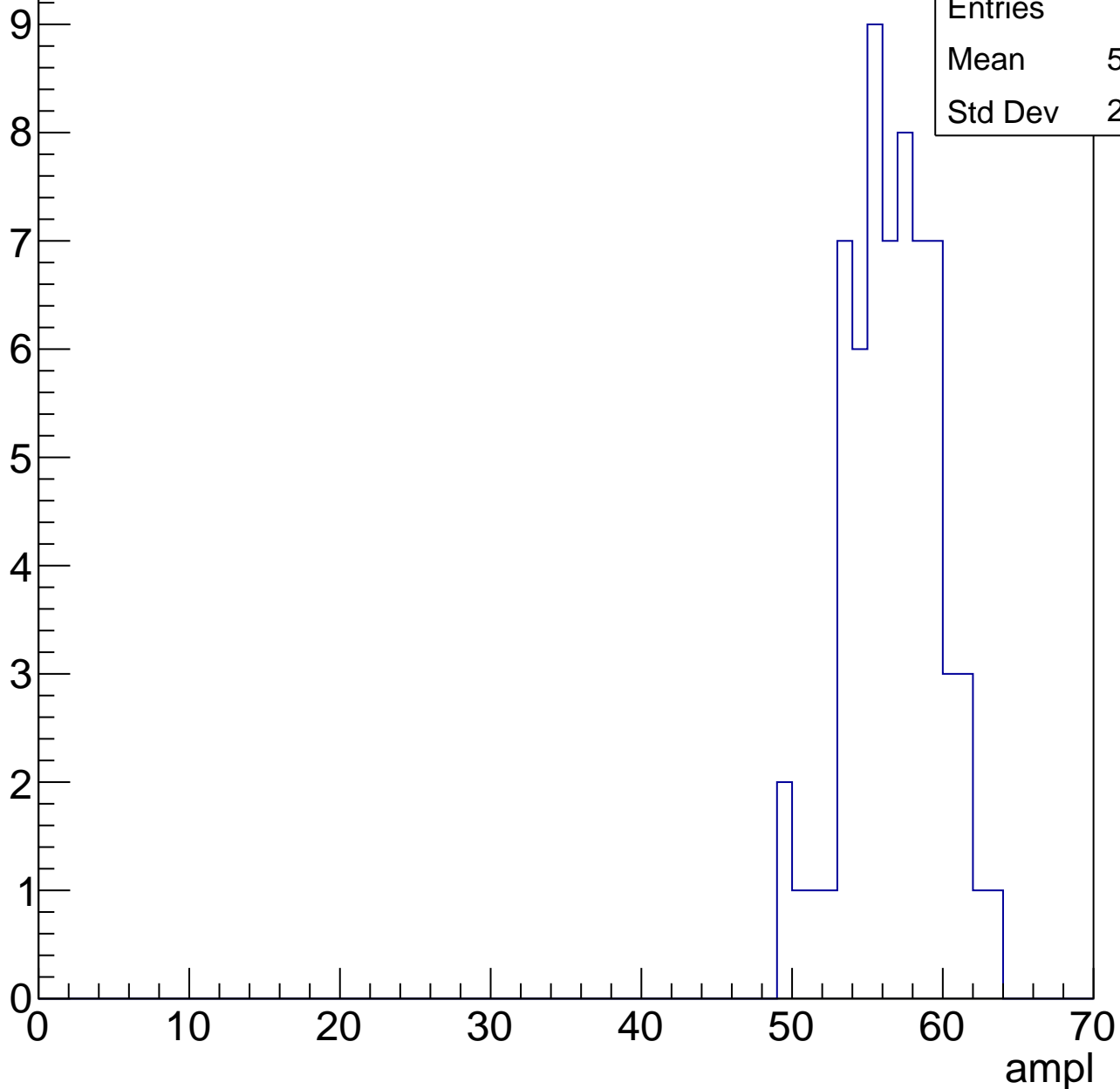
Entries	62
Mean	49.35
Std Dev	3.208



B0L002S, U7-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

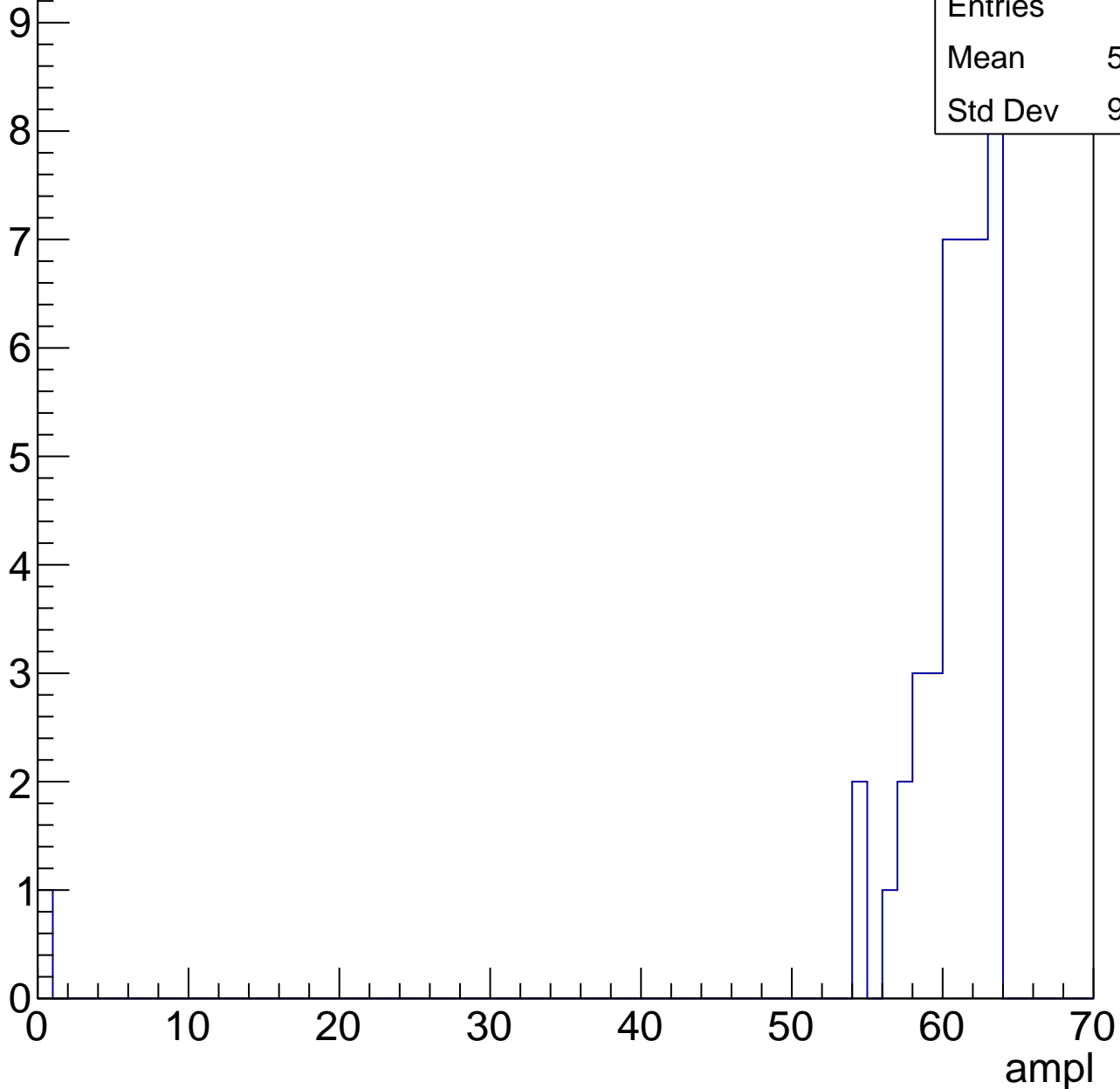


B0L002S, U7-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.98
Std Dev	9.503



B0L002S, U7-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch116, adc7

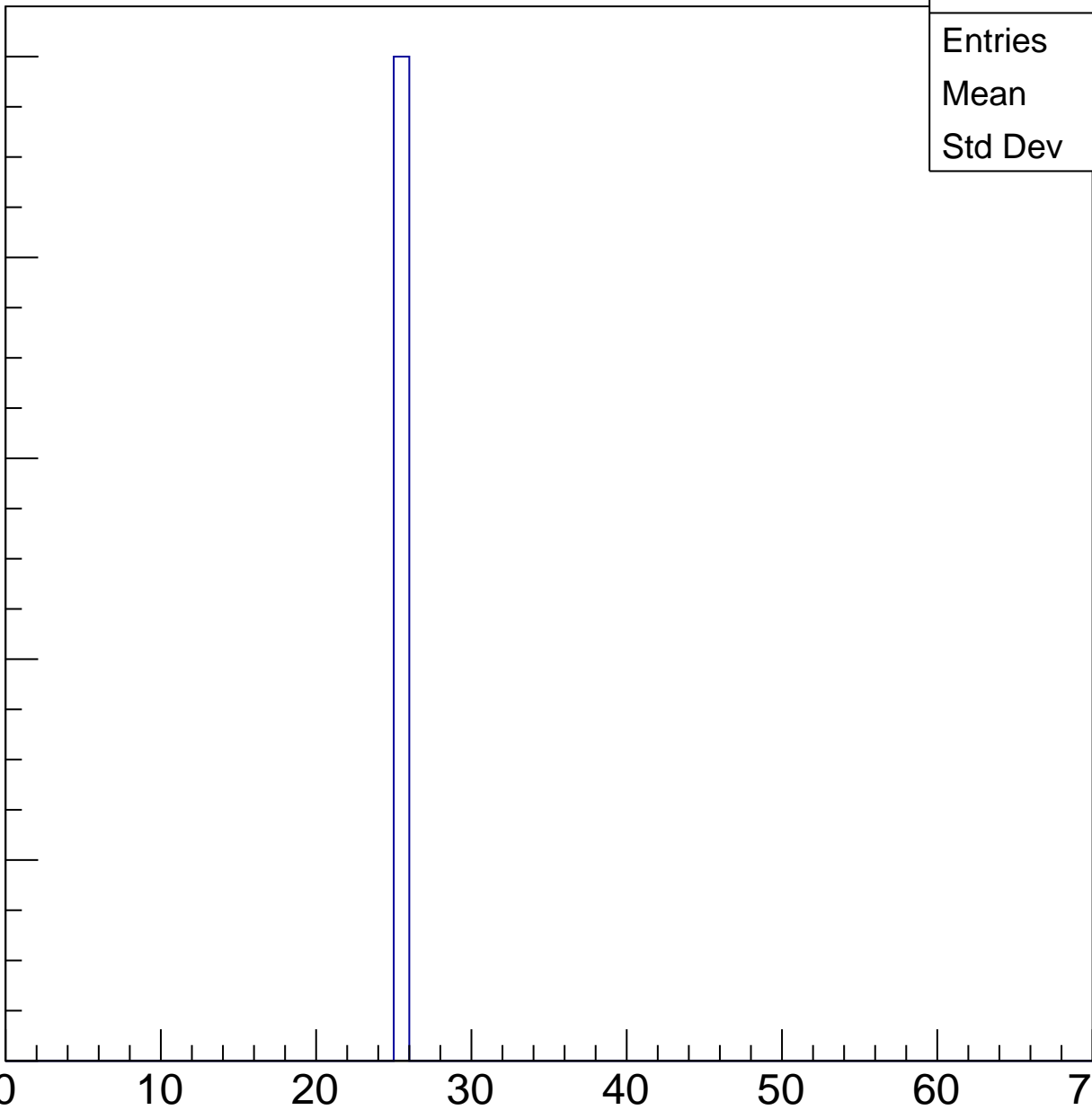
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L002S, U7-ch117, adc0

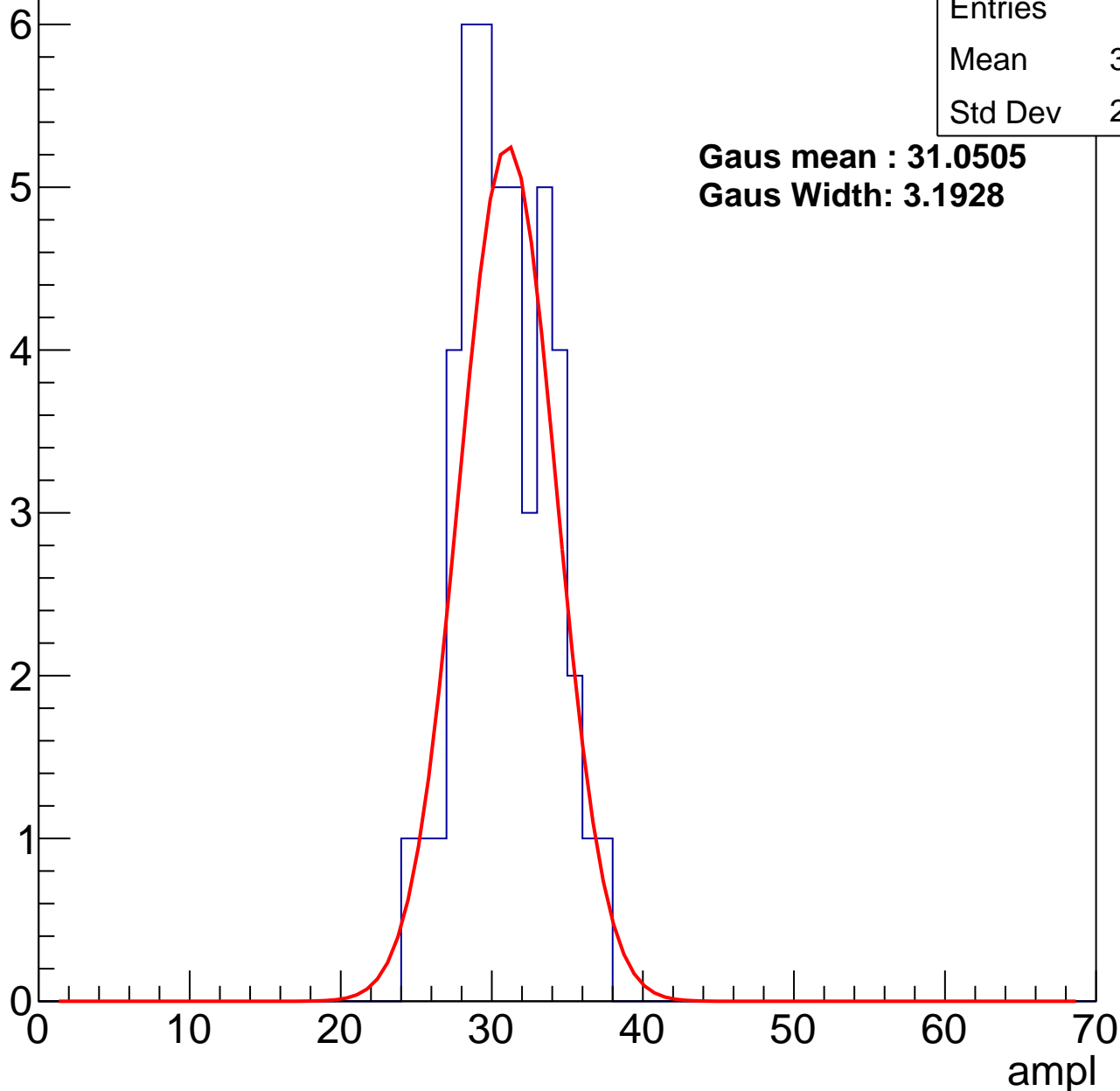
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	30.44
Std Dev	2.963

Gaus mean : 31.0505

Gaus Width: 3.1928



B0L002S, U7-ch117, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	37.03
Std Dev	3.317

Gaus mean : 37.4342

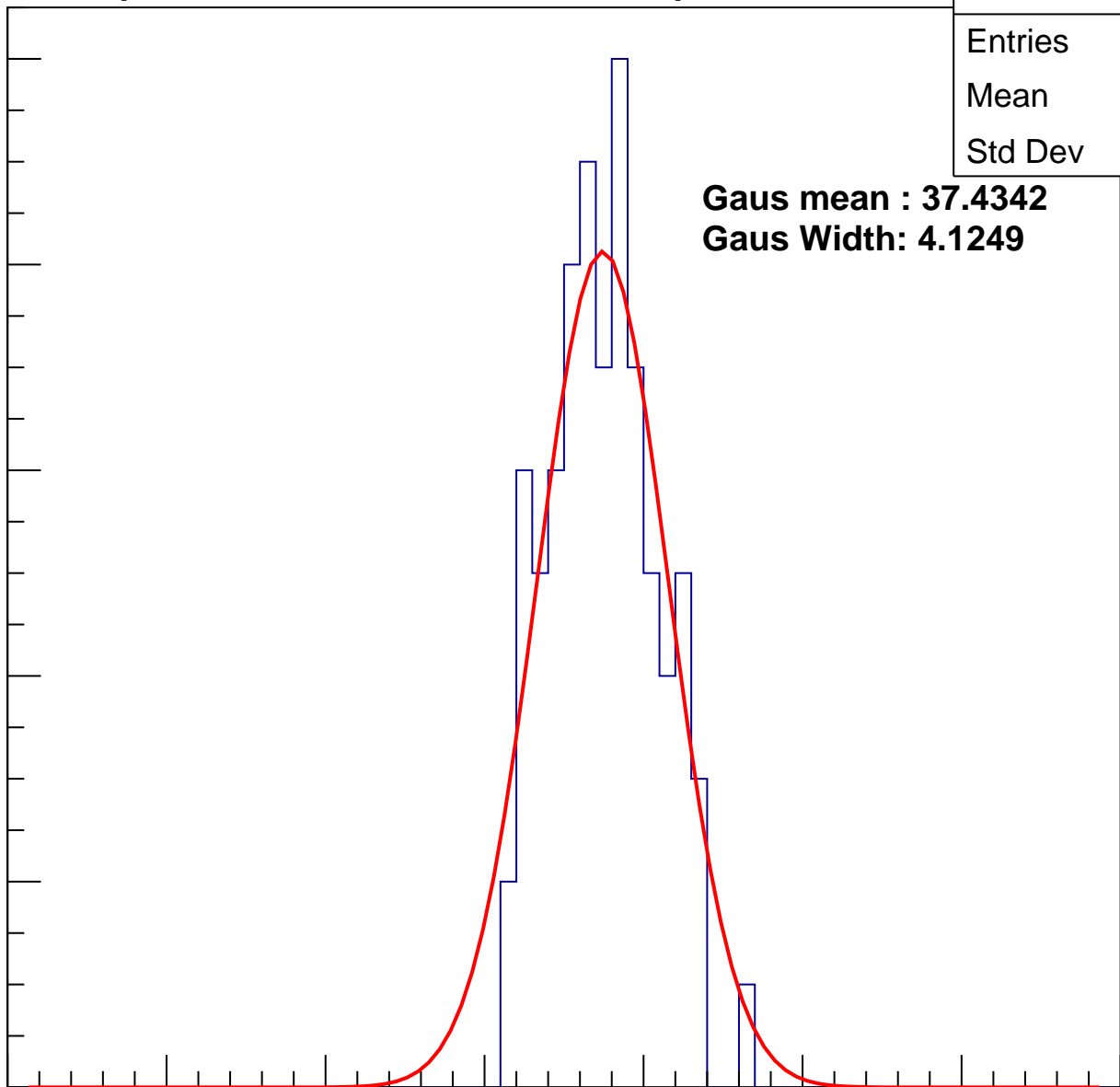
Gaus Width: 4.1249

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch117, adc2

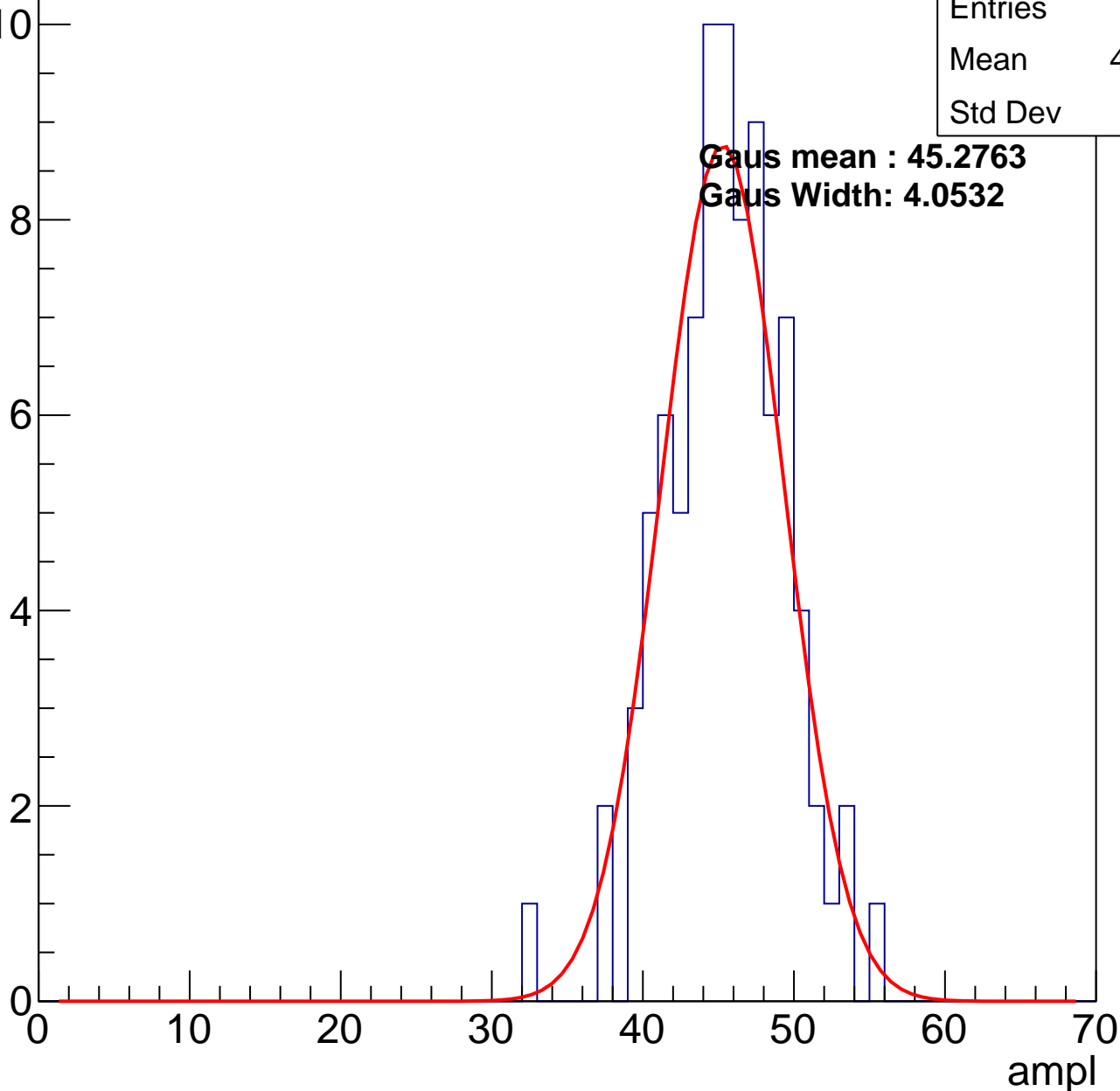
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	89
Mean	45.02
Std Dev	3.92

Gaus mean : 45.2763

Gaus Width: 4.0532

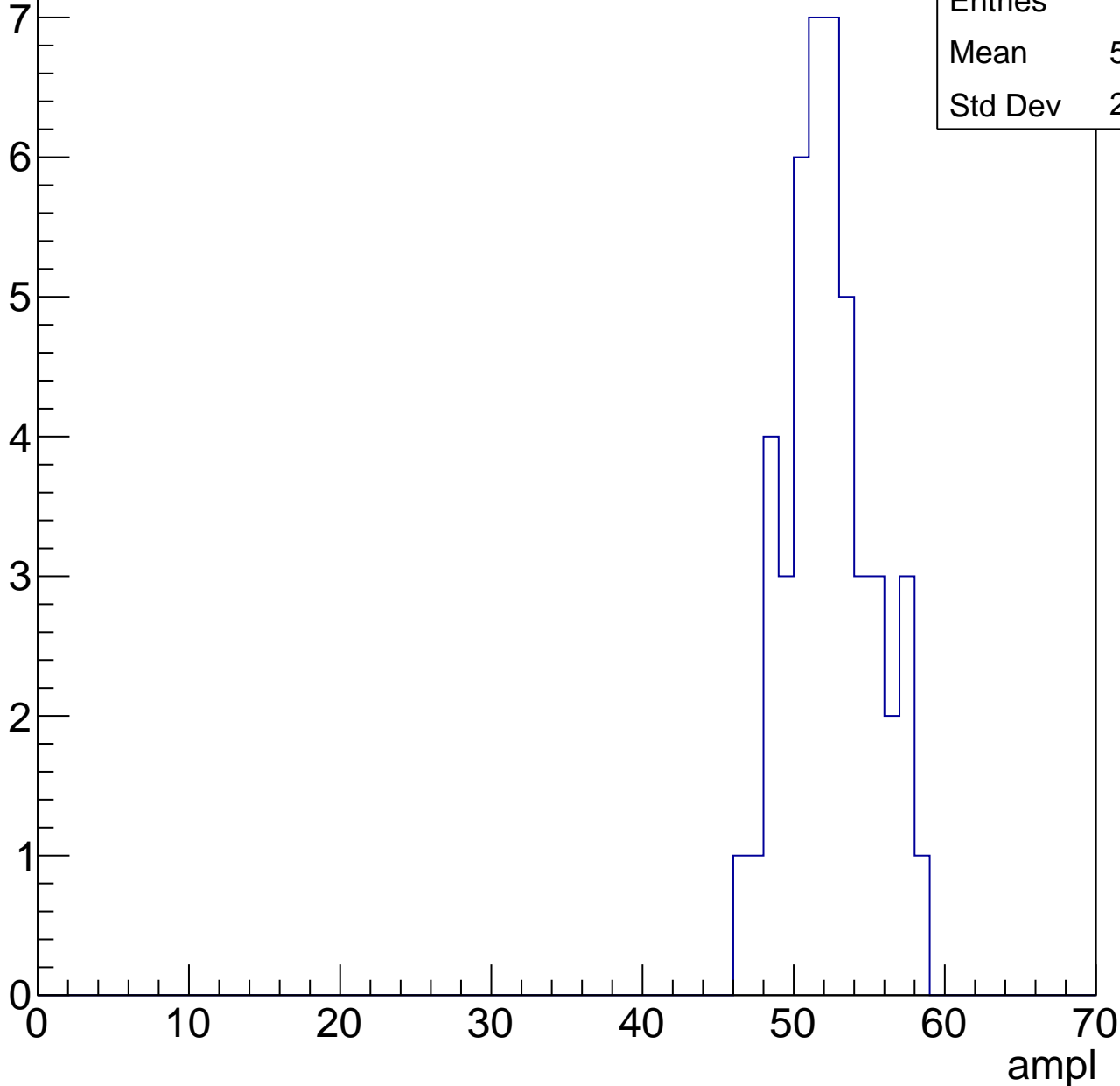


B0L002S, U7-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	51.87
Std Dev	2.833

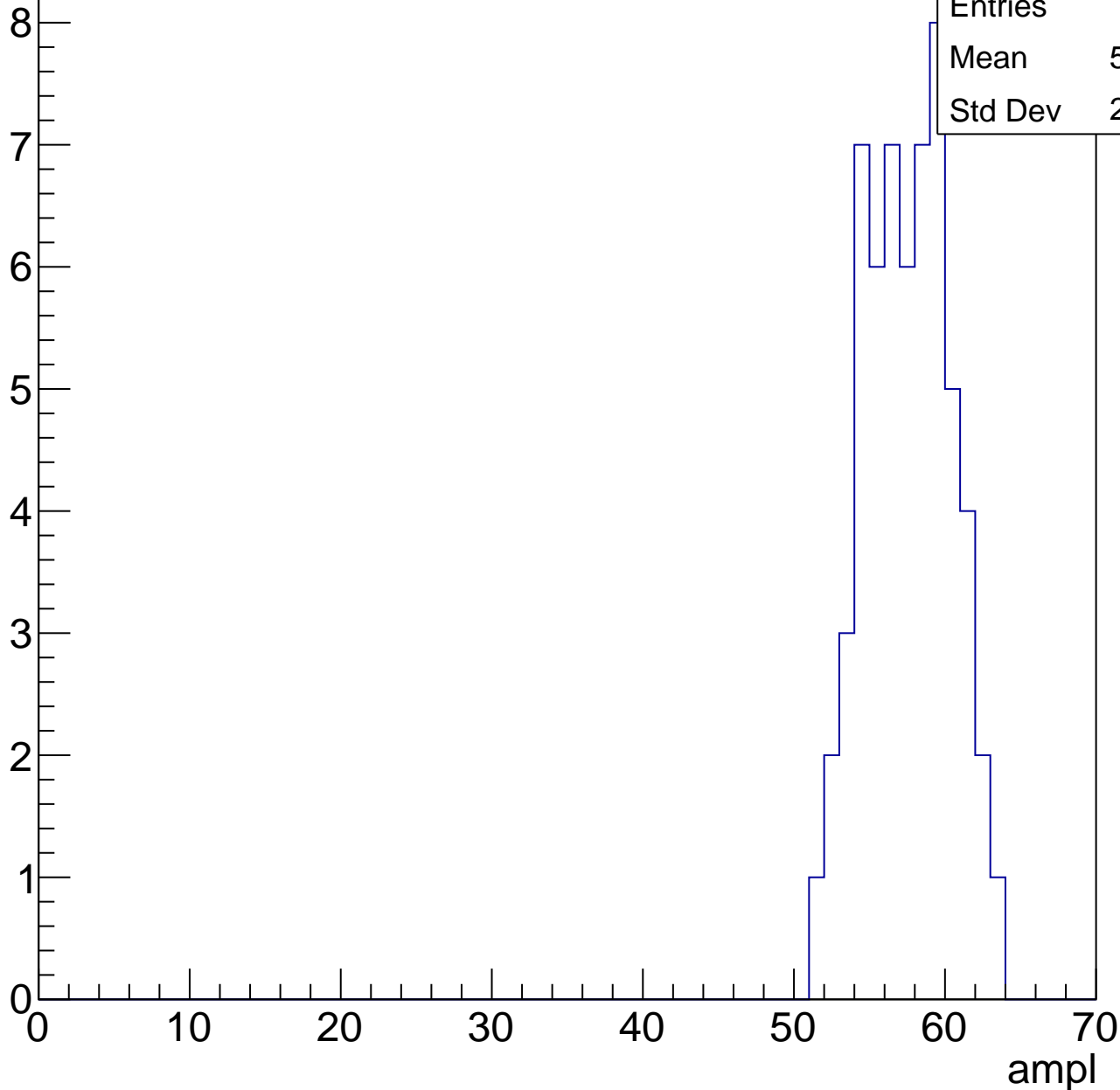


B0L002S, U7-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	57.03
Std Dev	2.798

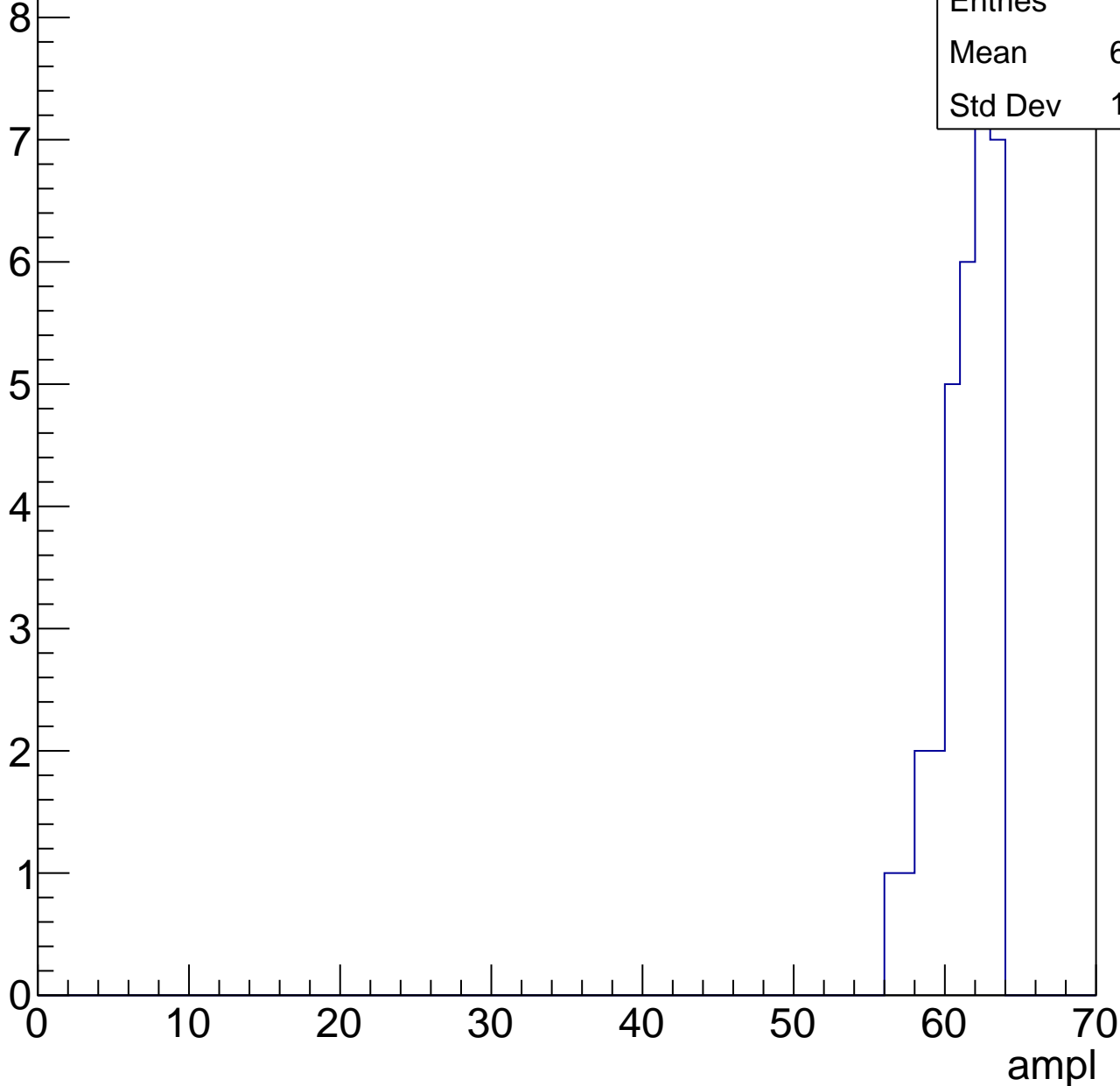


B0L002S, U7-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	60.94
Std Dev	1.836



B0L002S, U7-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U7-ch118, adc0

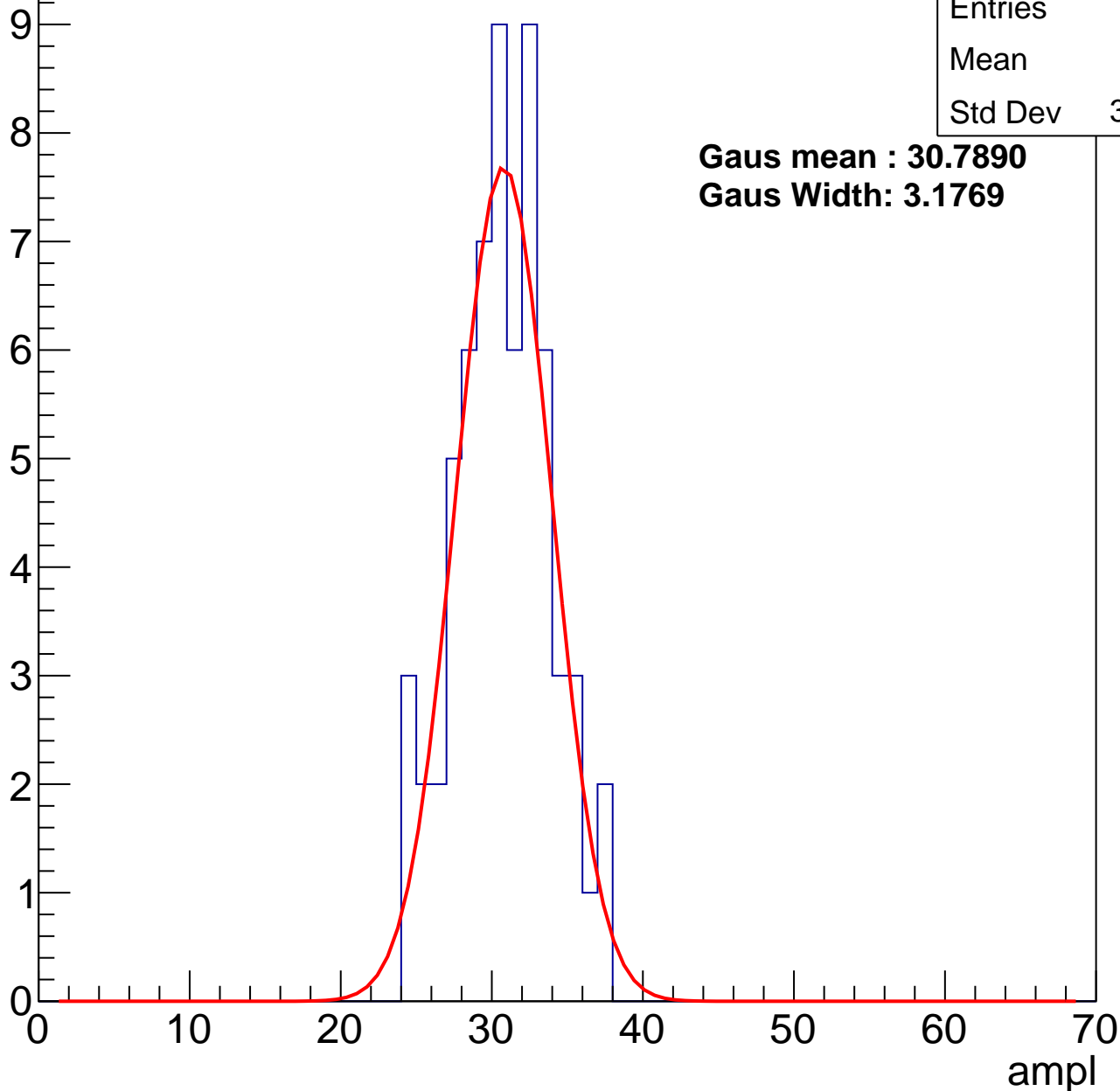
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.3
Std Dev	3.096

Gaus mean : 30.7890

Gaus Width: 3.1769

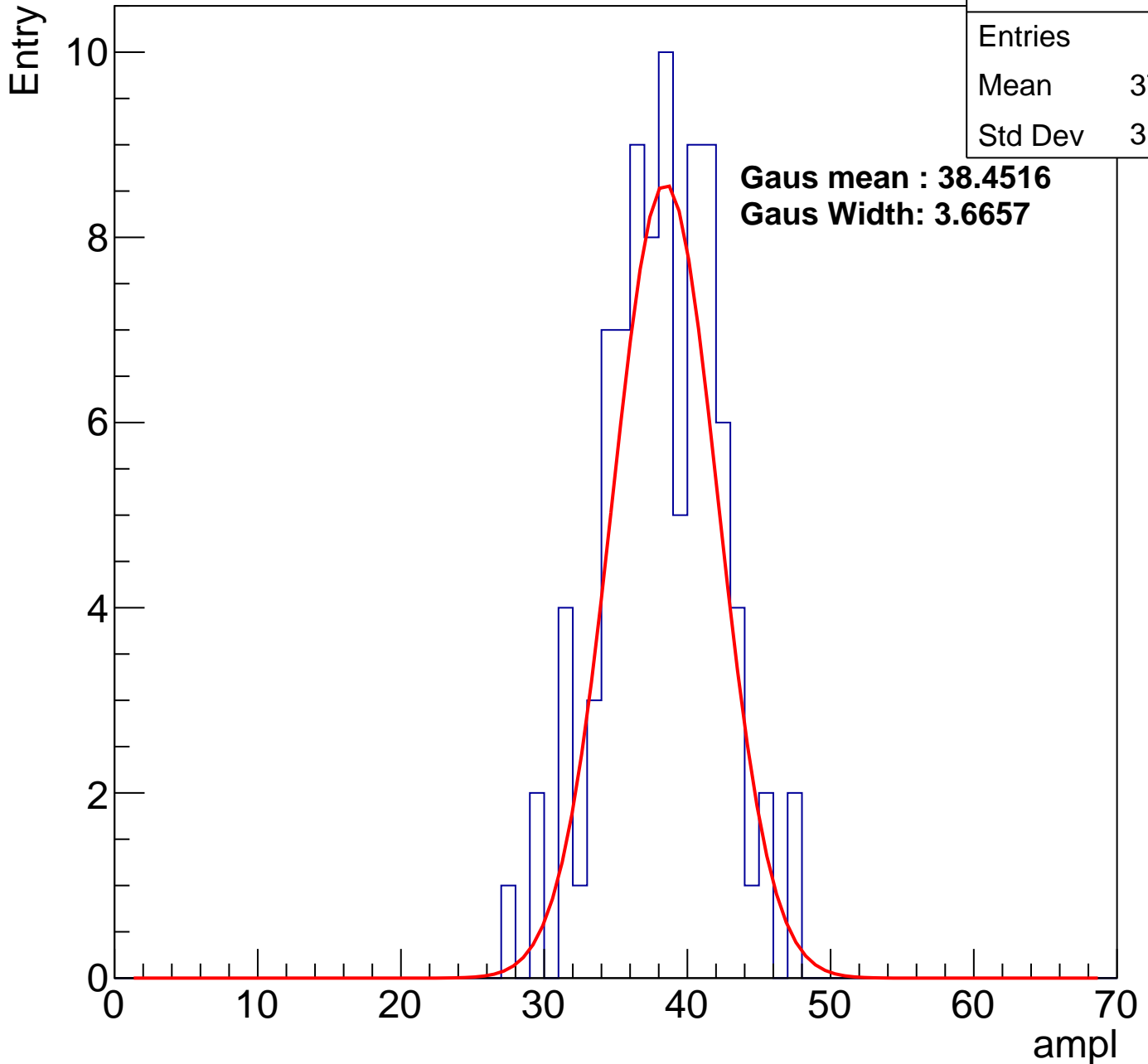


B0L002S, U7-ch118, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	90
Mean	37.77
Std Dev	3.969

Gaus mean : 38.4516
Gaus Width: 3.6657



B0L002S, U7-ch118, adc2

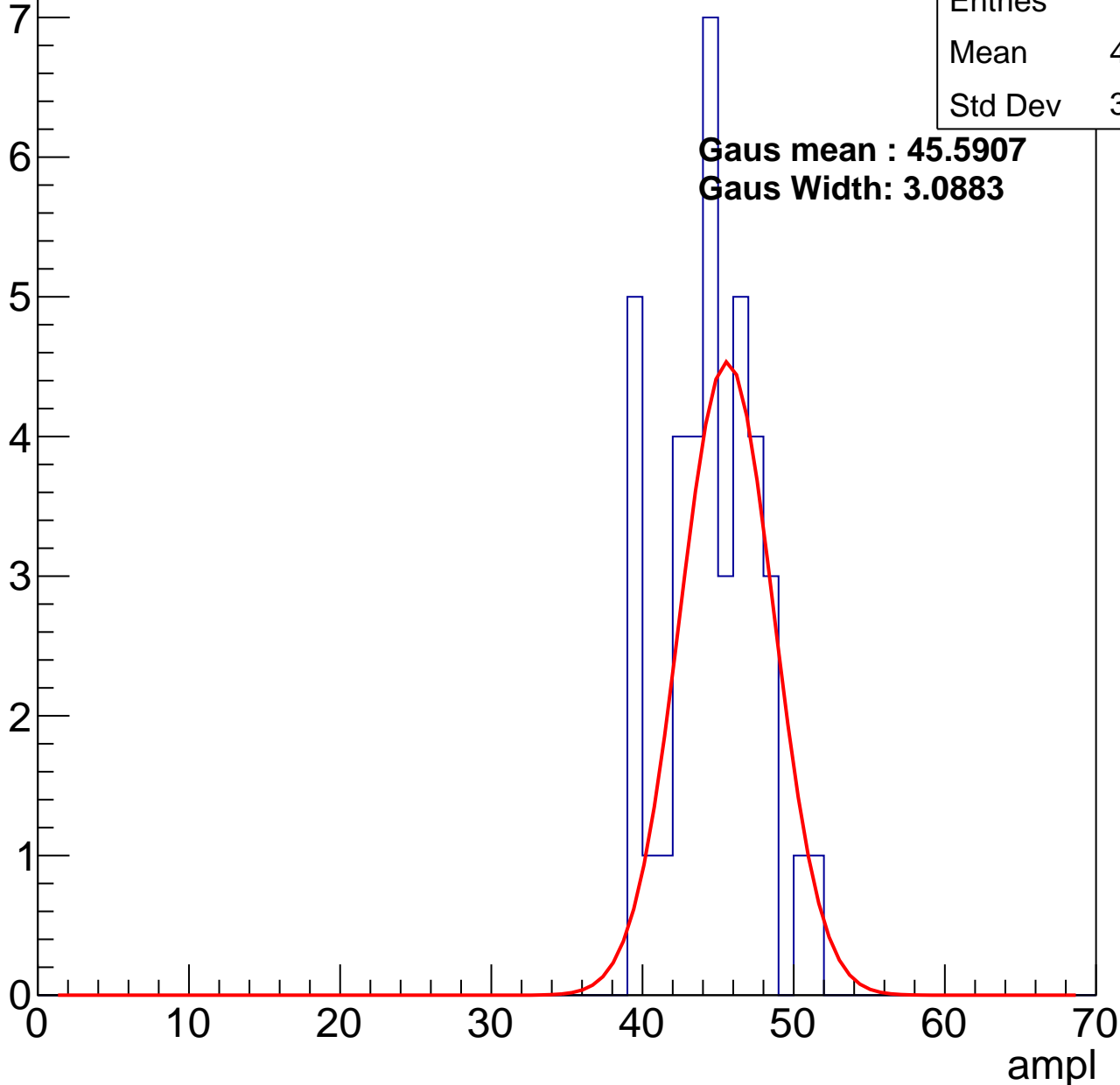
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	44.15
Std Dev	3.043

Gaus mean : 45.5907

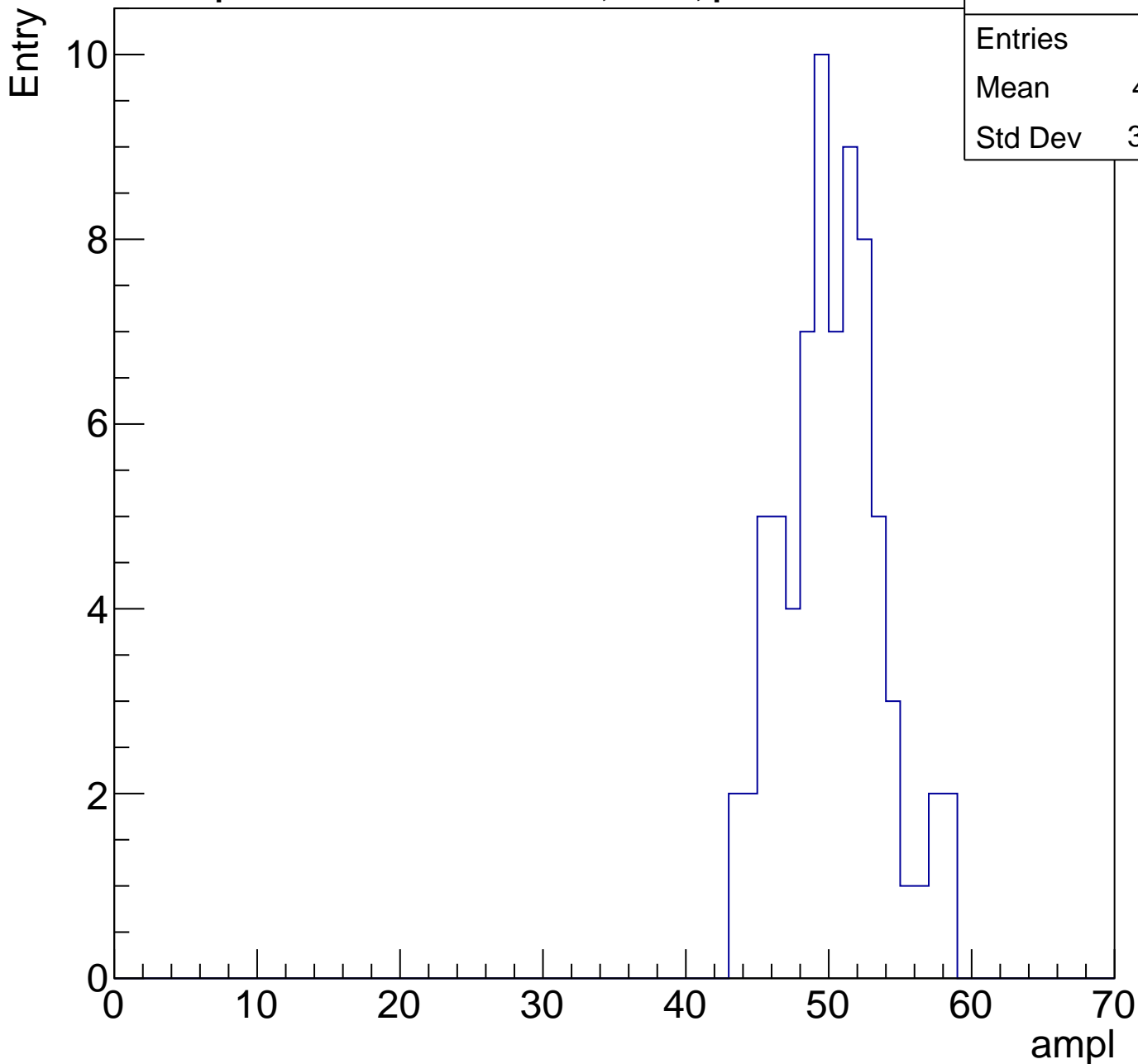
Gaus Width: 3.0883



B0L002S, U7-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

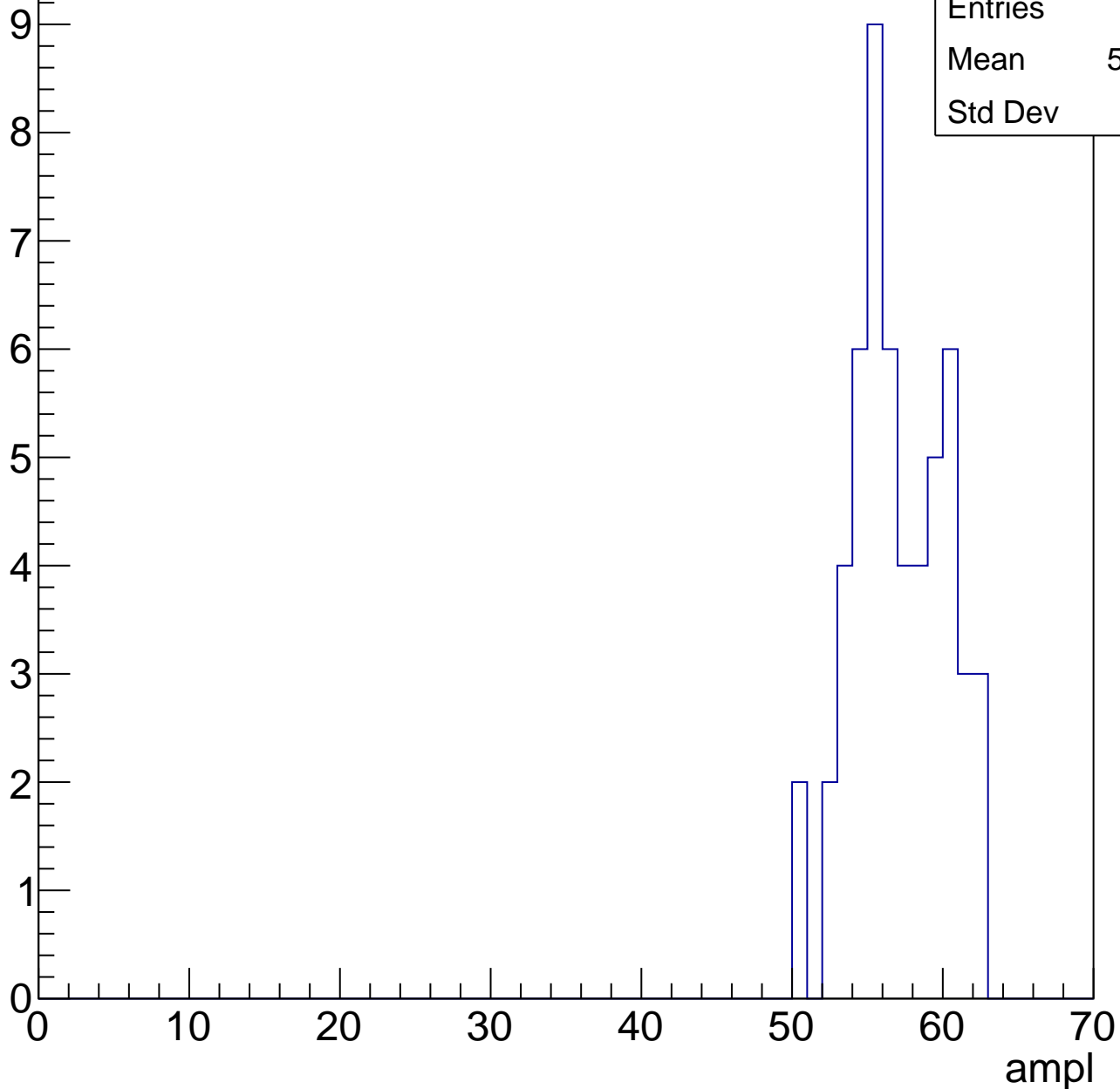
Entries	73
Mean	49.81
Std Dev	3.447



B0L002S, U7-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



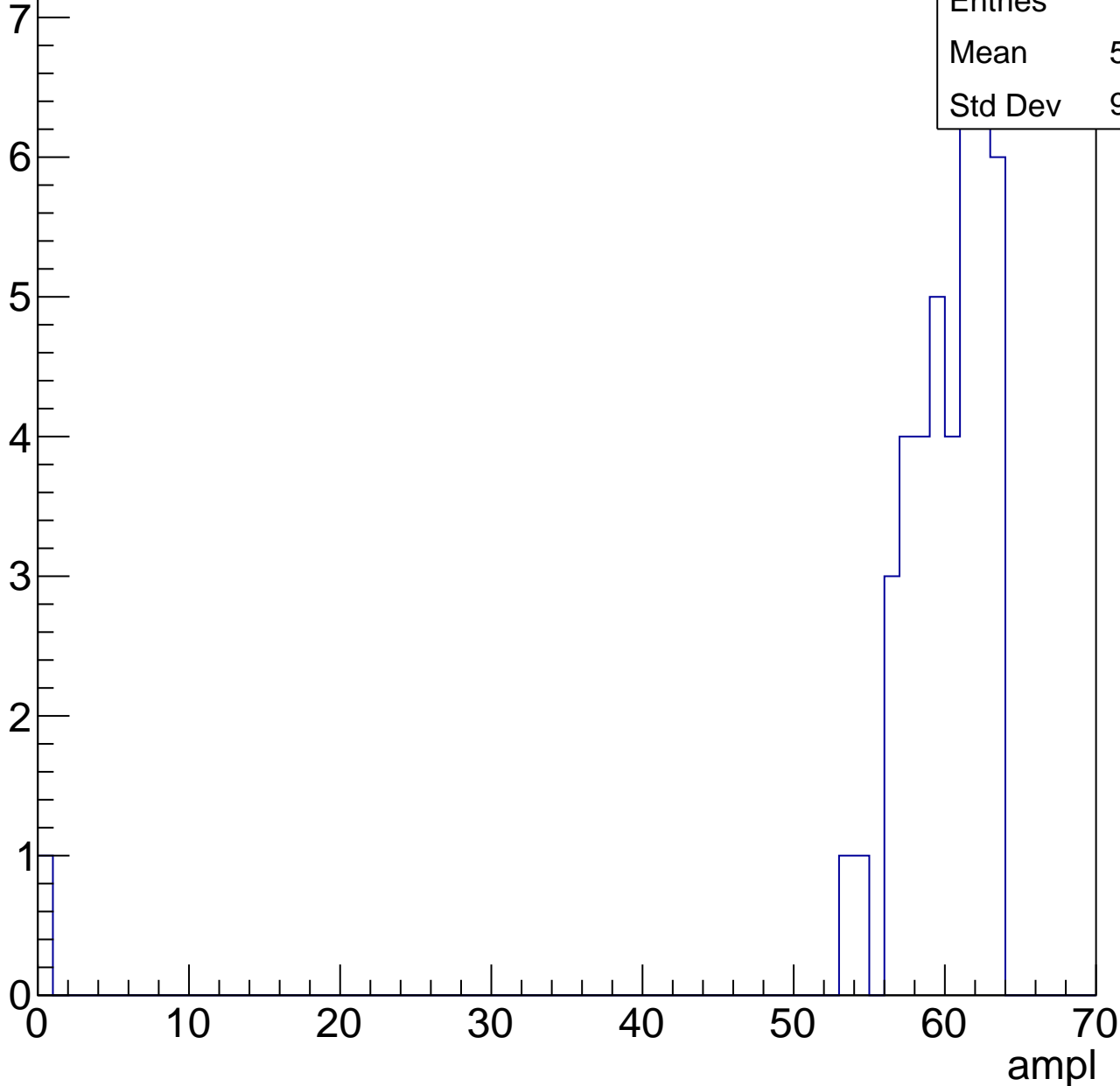
Entries	54
Mean	56.57
Std Dev	3.04

B0L002S, U7-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.35
Std Dev	9.353



B0L002S, U7-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch119, adc0

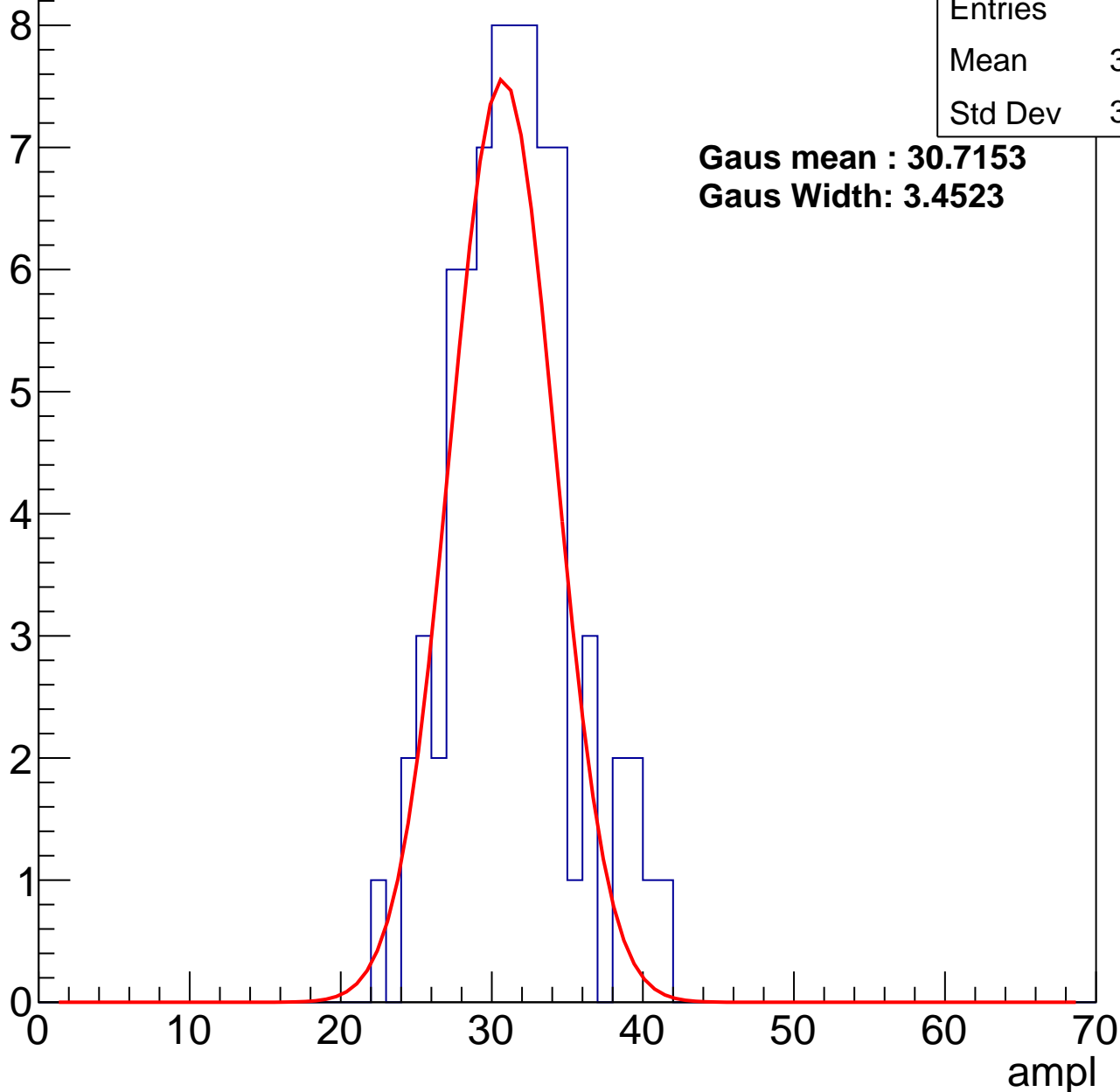
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.95
Std Dev	3.864

Gaus mean : 30.7153

Gaus Width: 3.4523



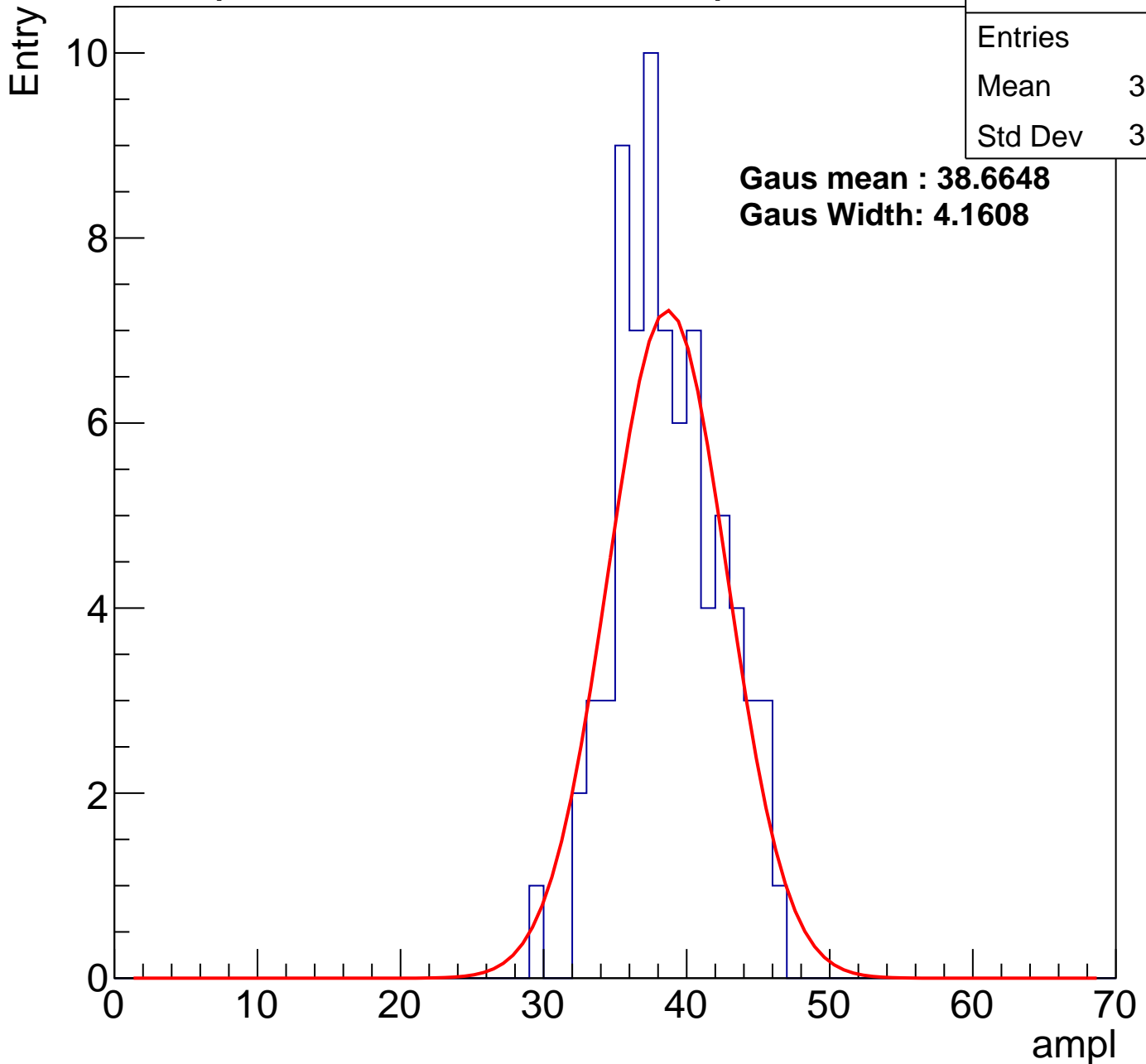
B0L002S, U7-ch119, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	38.27
Std Dev	3.575

Gaus mean : 38.6648

Gaus Width: 4.1608

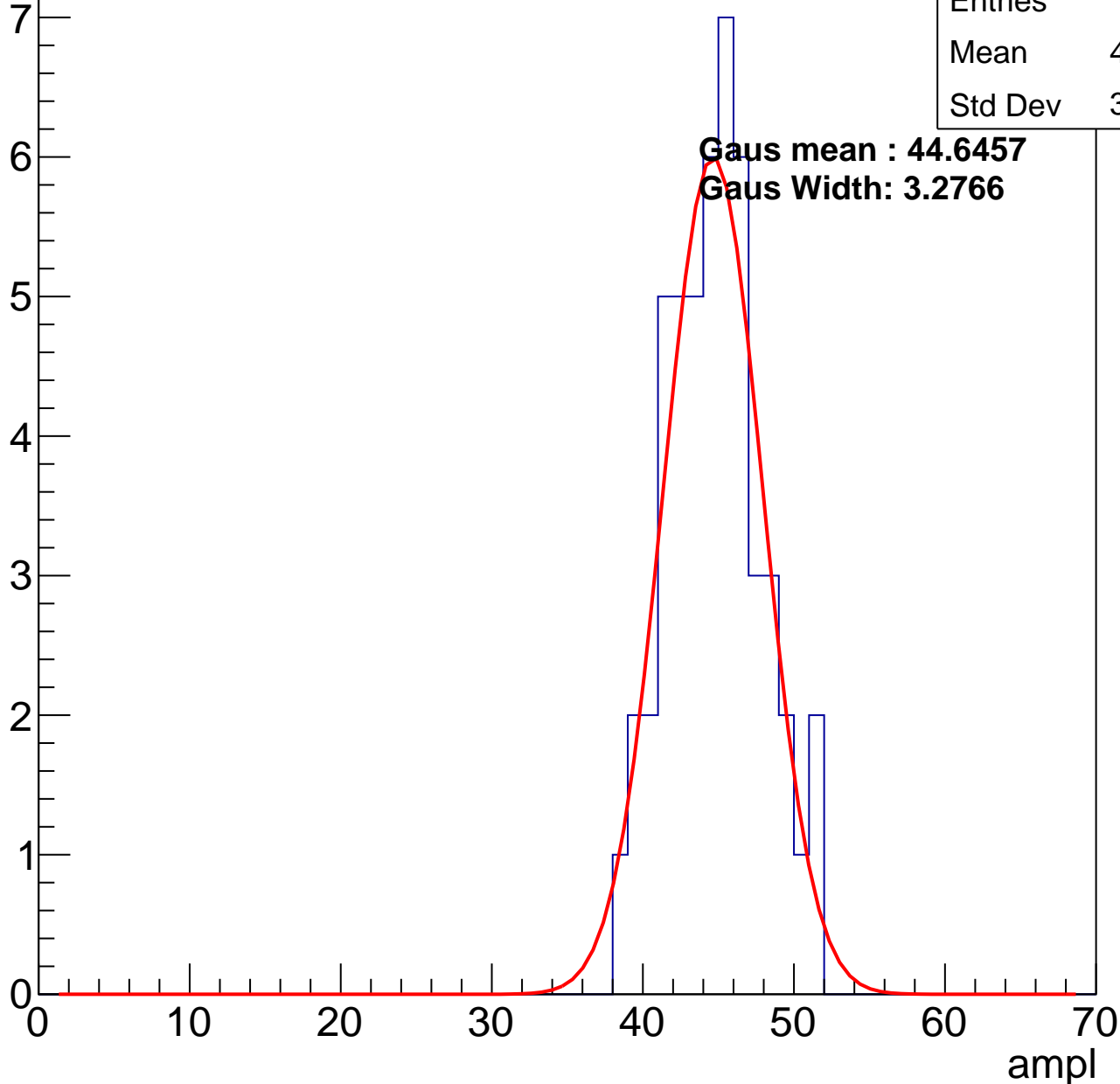


B0L002S, U7-ch119, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	44.32
Std Dev	3.075

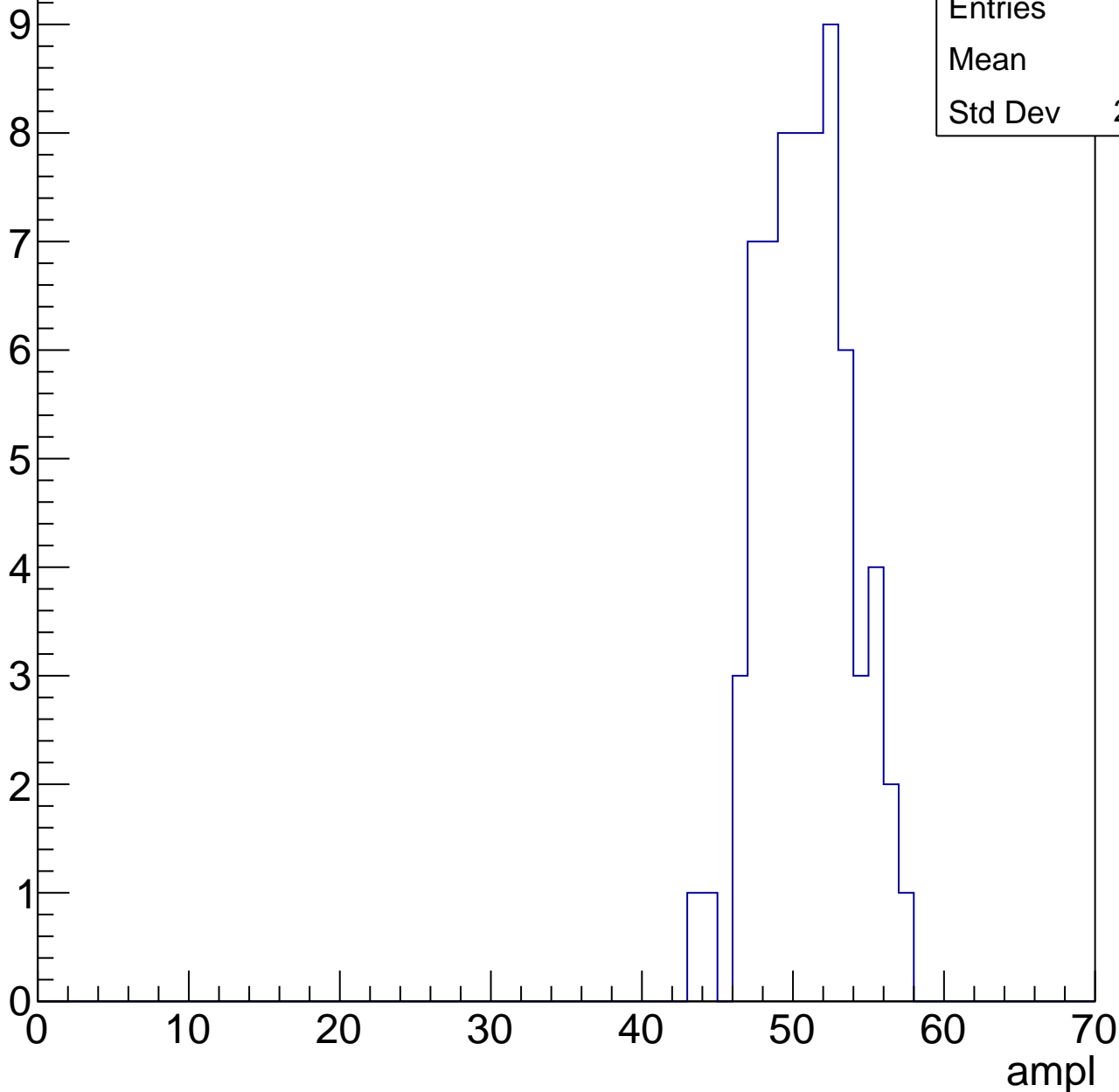


B0L002S, U7-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

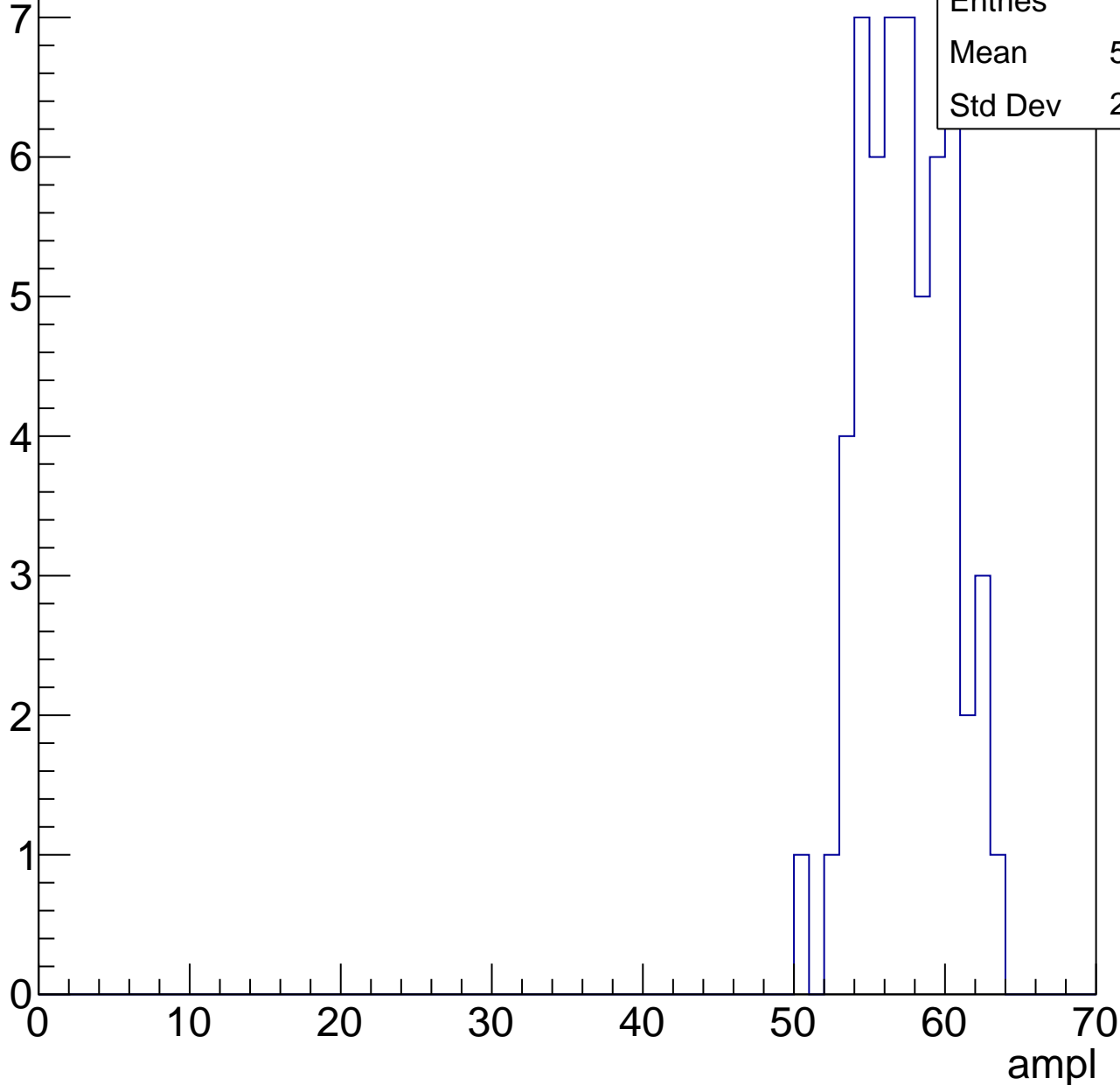
Entries	68
Mean	50.4
Std Dev	2.941



B0L002S, U7-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

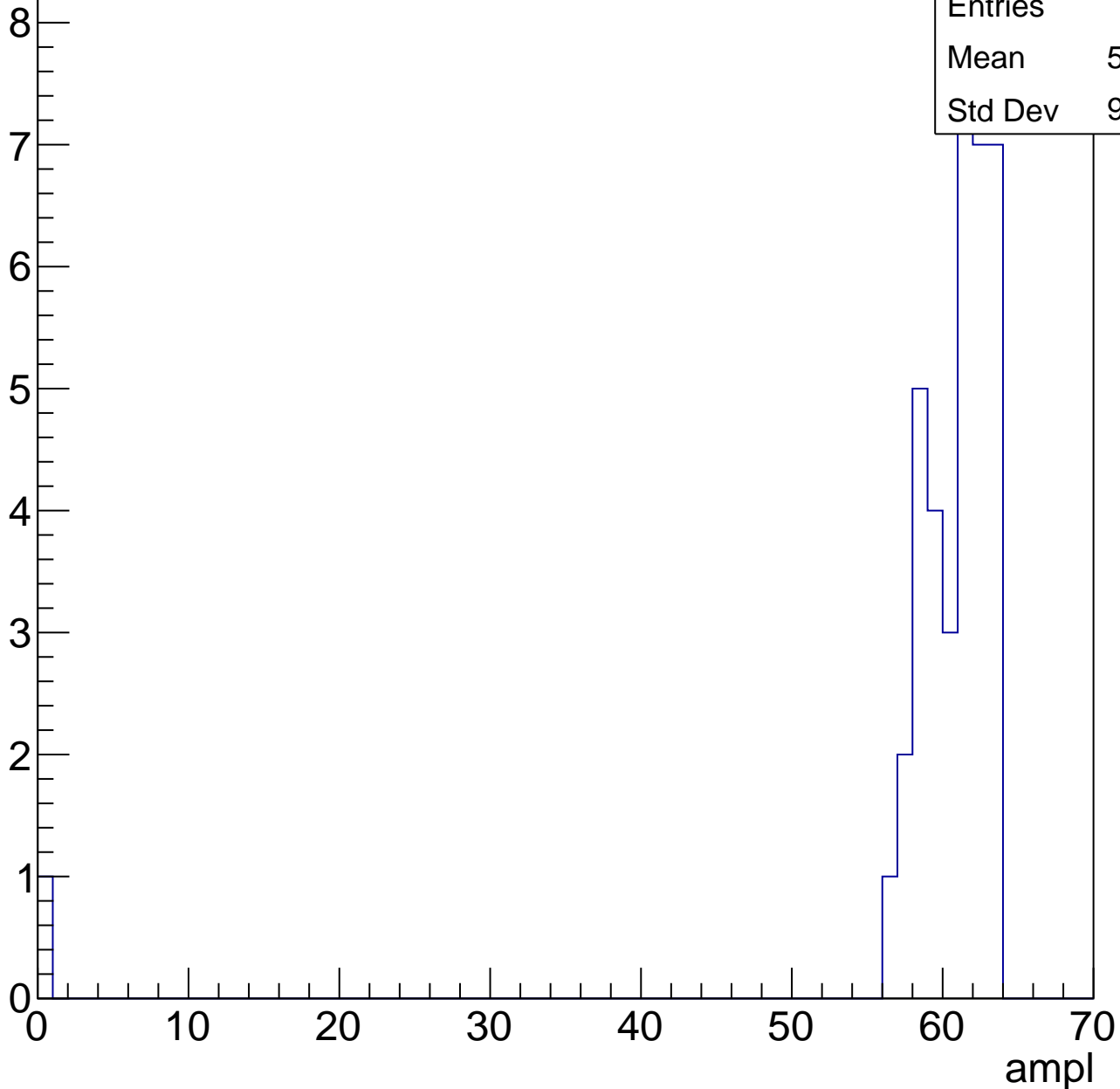


B0L002S, U7-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.92
Std Dev	9.885



B0L002S, U7-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch120, adc0

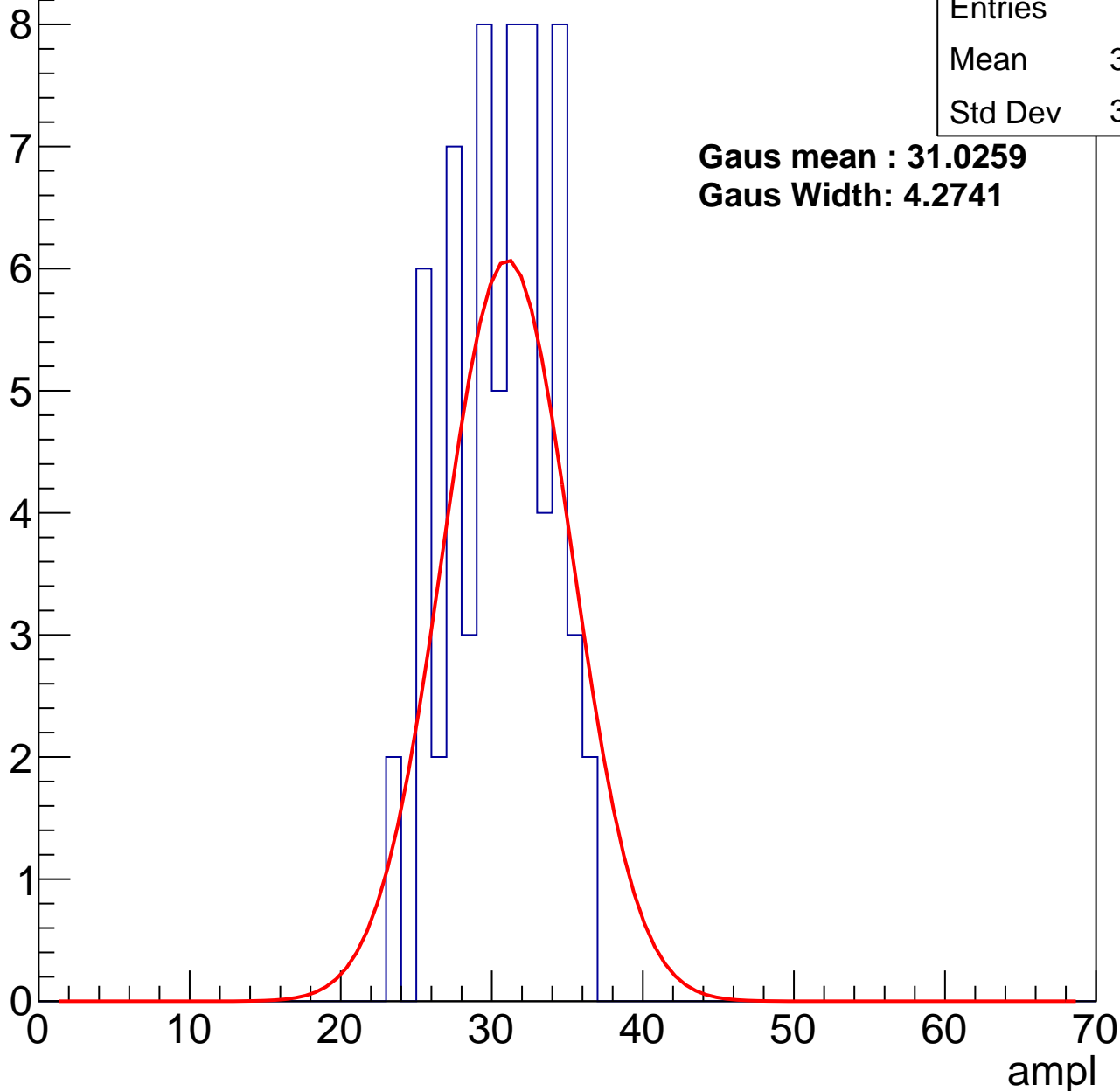
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.12
Std Dev	3.287

Gaus mean : 31.0259

Gaus Width: 4.2741



B0L002S, U7-ch120, adc1

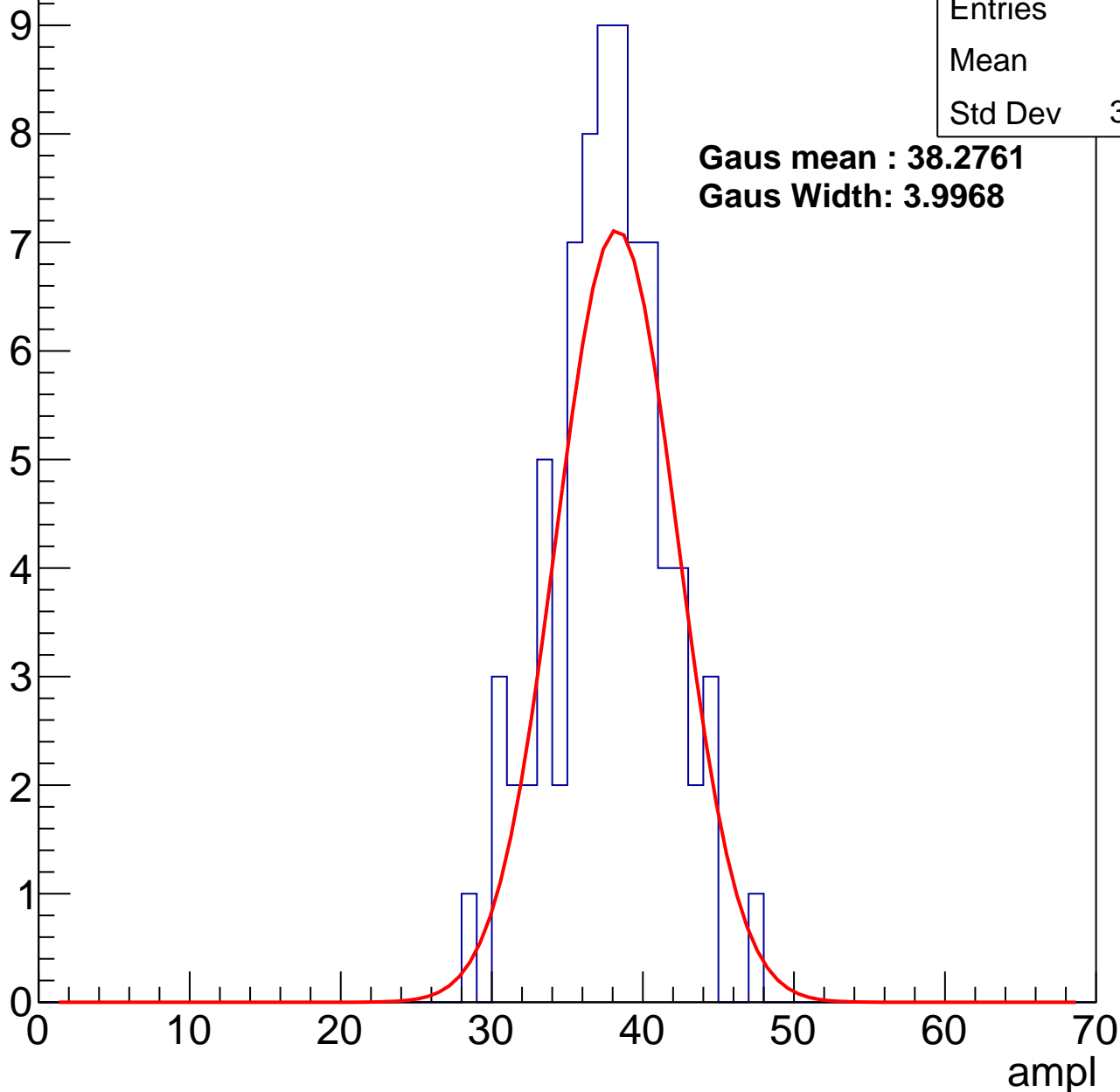
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.3
Std Dev	3.735

Gaus mean : 38.2761

Gaus Width: 3.9968



B0L002S, U7-ch120, adc2

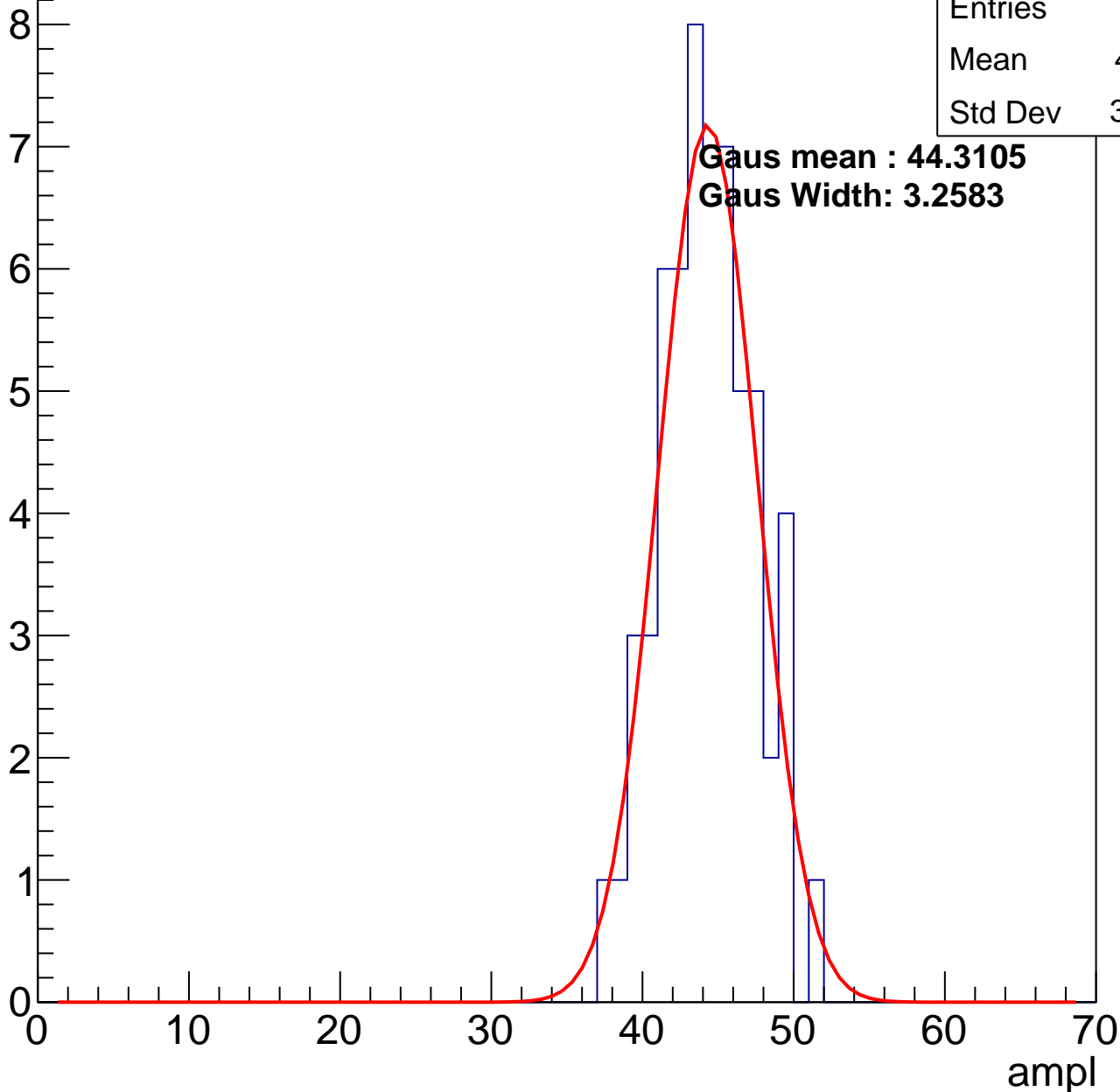
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.81
Std Dev	3.039

Gaus mean : 44.3105

Gaus Width: 3.2583

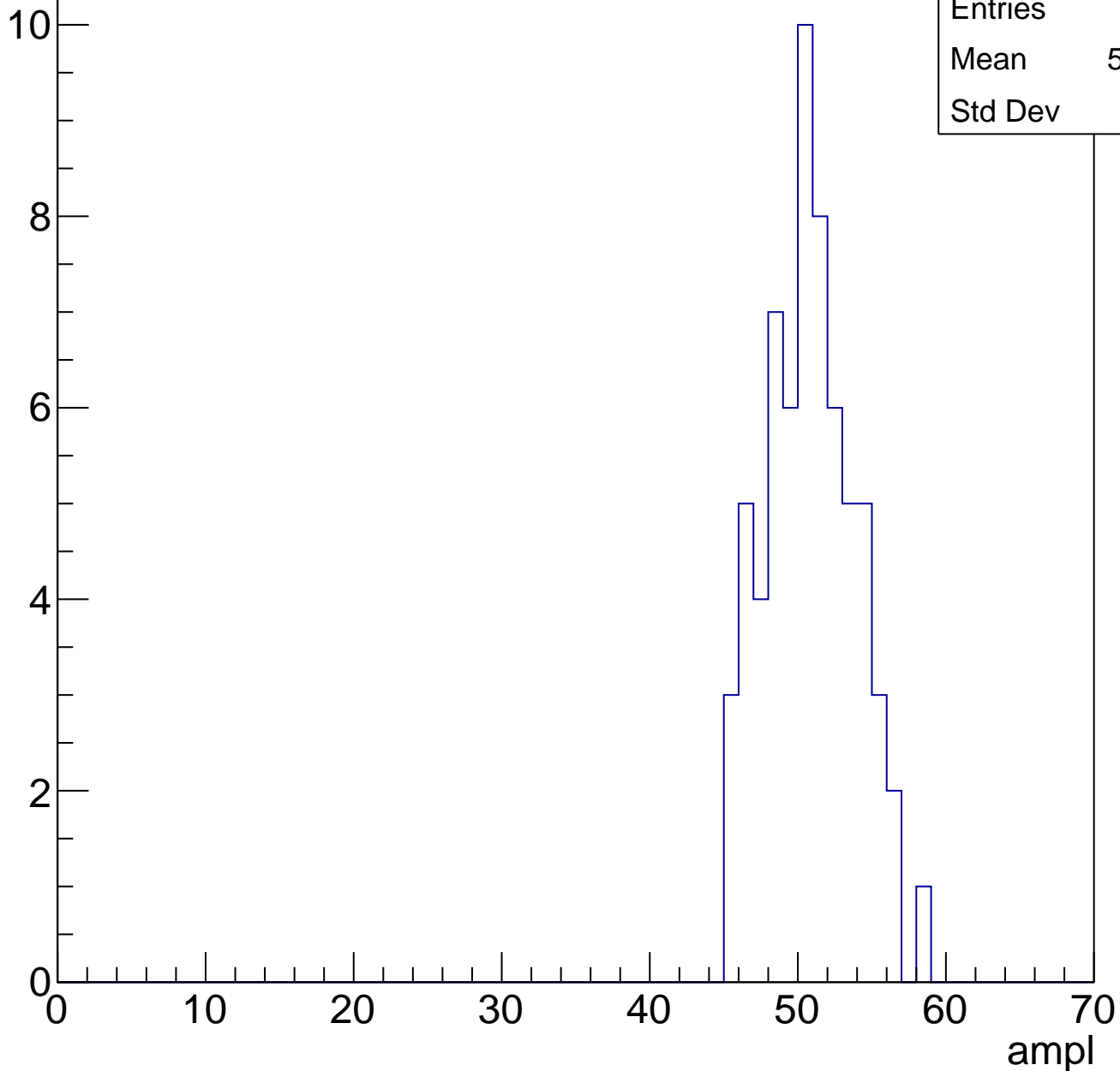


B0L002S, U7-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	50.35
Std Dev	3

Entry

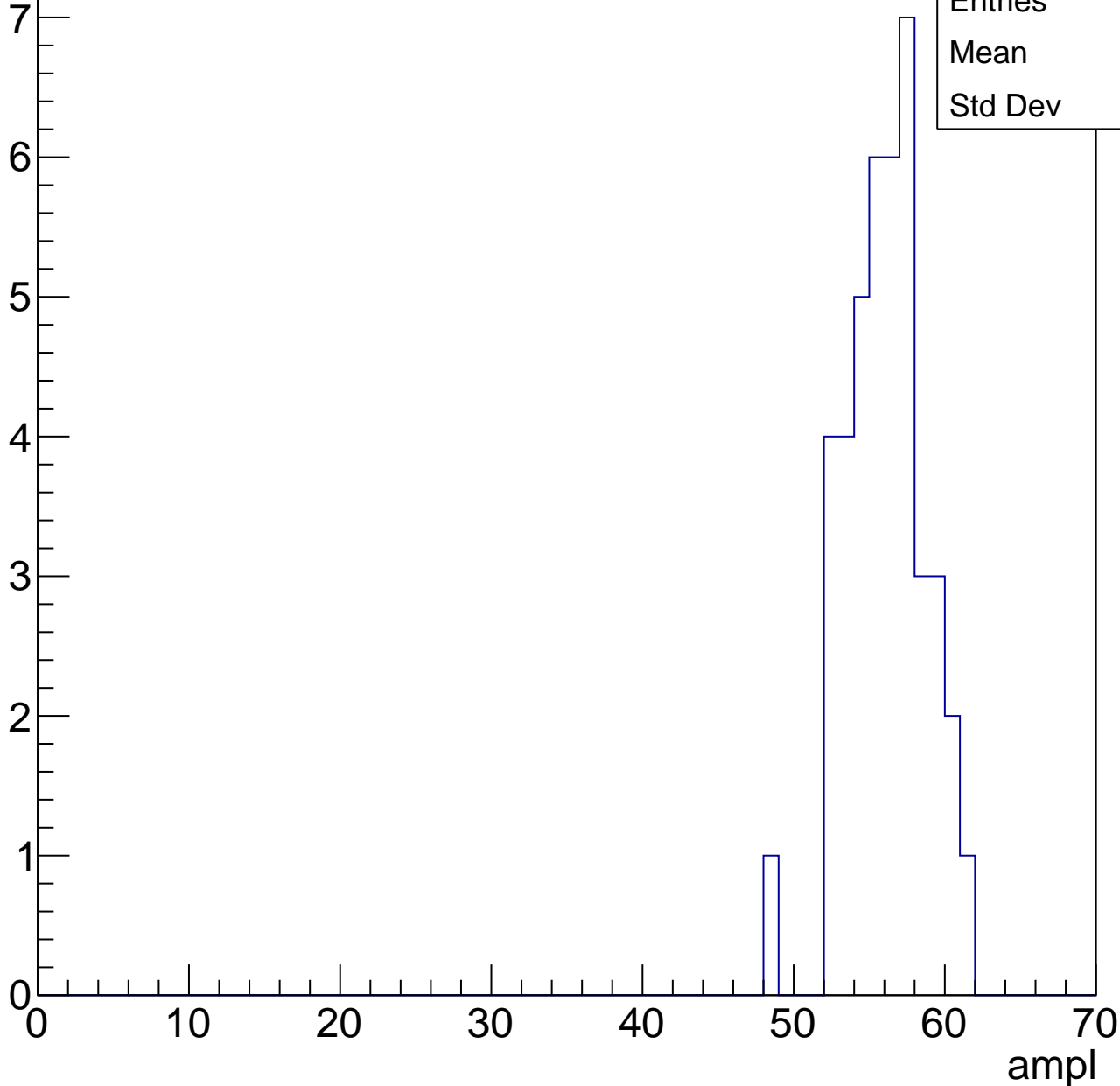


B0L002S, U7-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	55.6
Std Dev	2.61

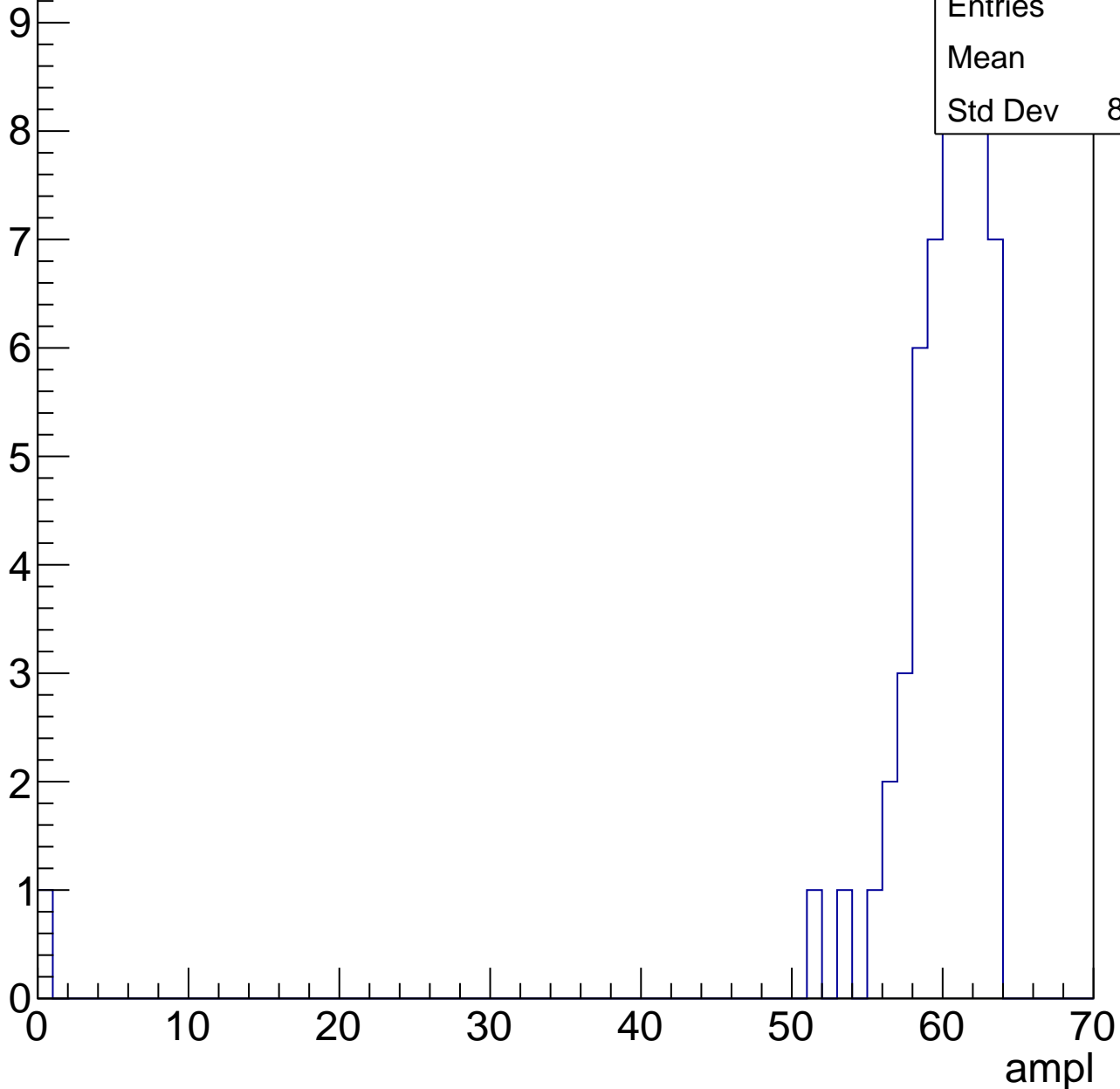


B0L002S, U7-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.7
Std Dev	8.456



B0L002S, U7-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

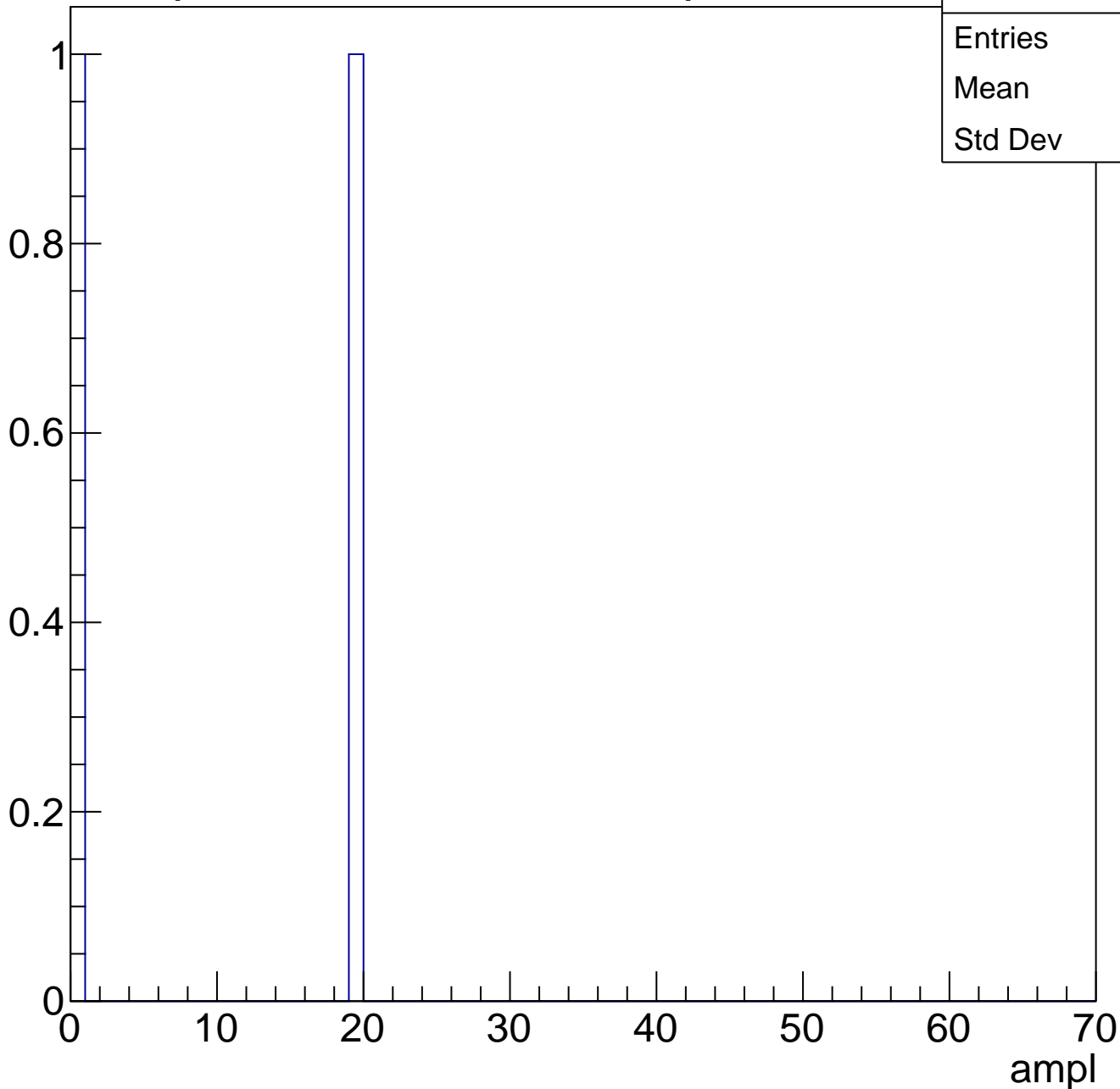
Entry



B0L002S, U7-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L002S, U7-ch121, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	30.24
Std Dev	3.986

Gaus mean : 30.7139

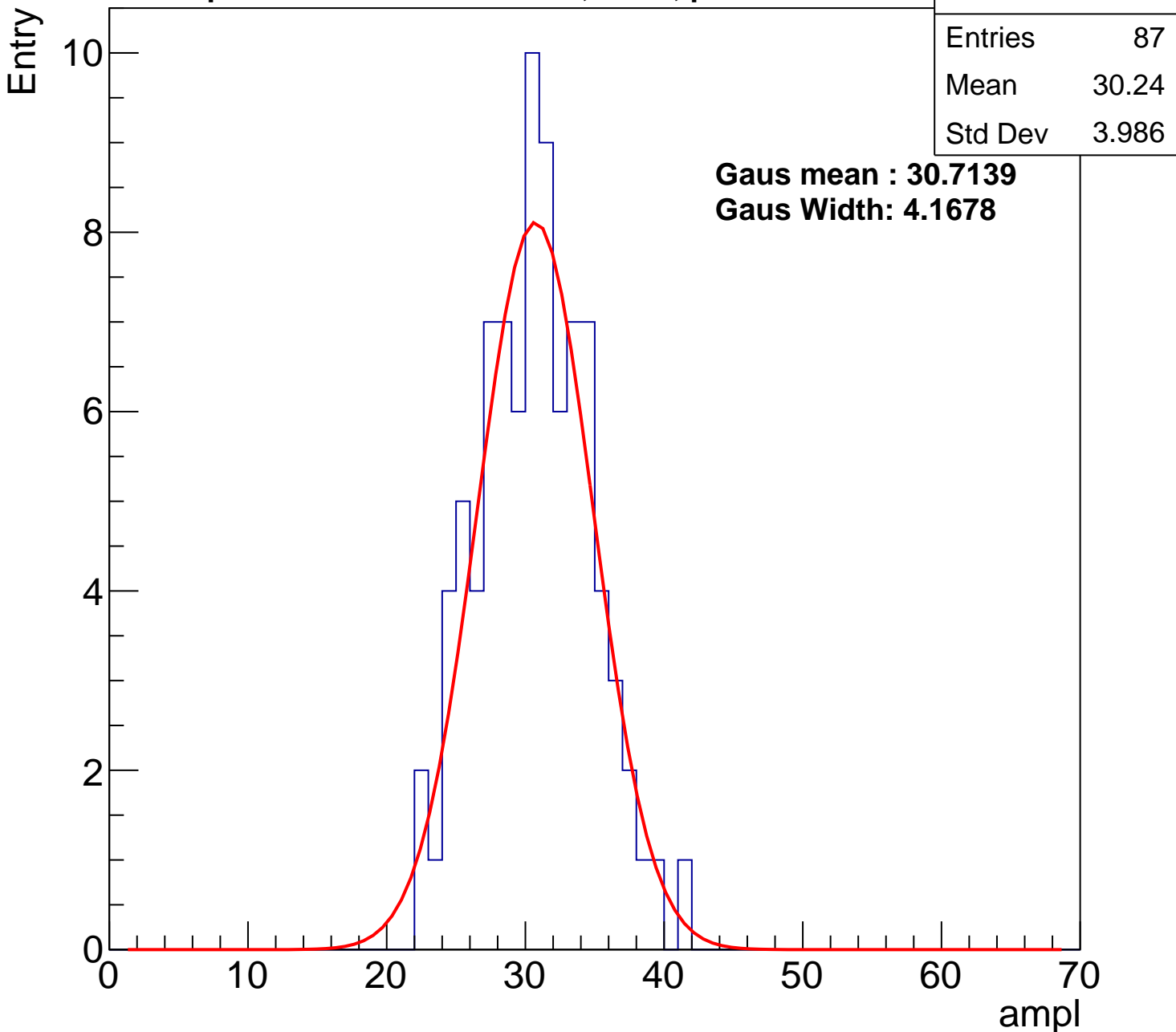
Gaus Width: 4.1678

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U7-ch121, adc1

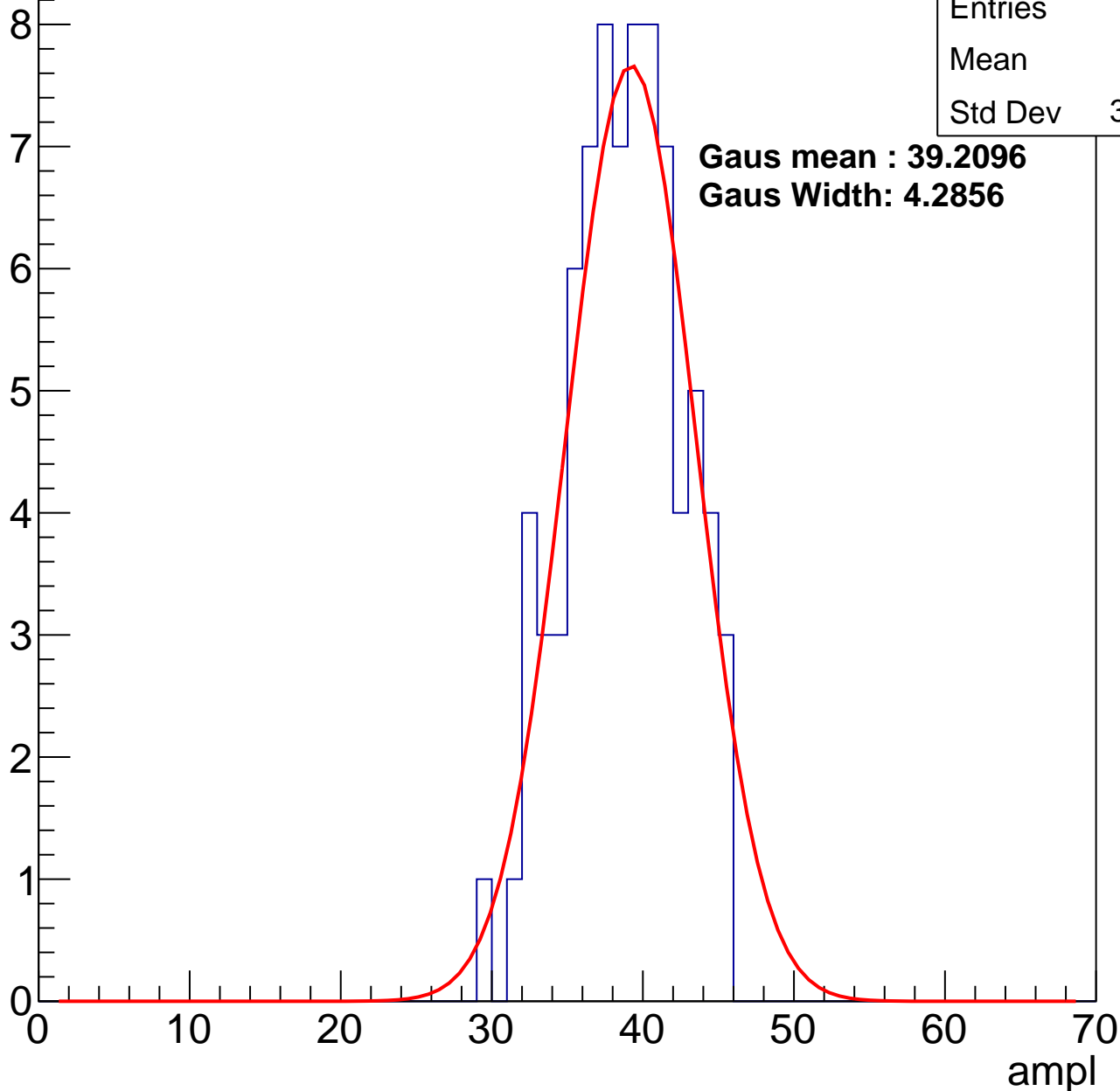
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.3
Std Dev	3.668

Gaus mean : 39.2096

Gaus Width: 4.2856



B0L002S, U7-ch121, adc2

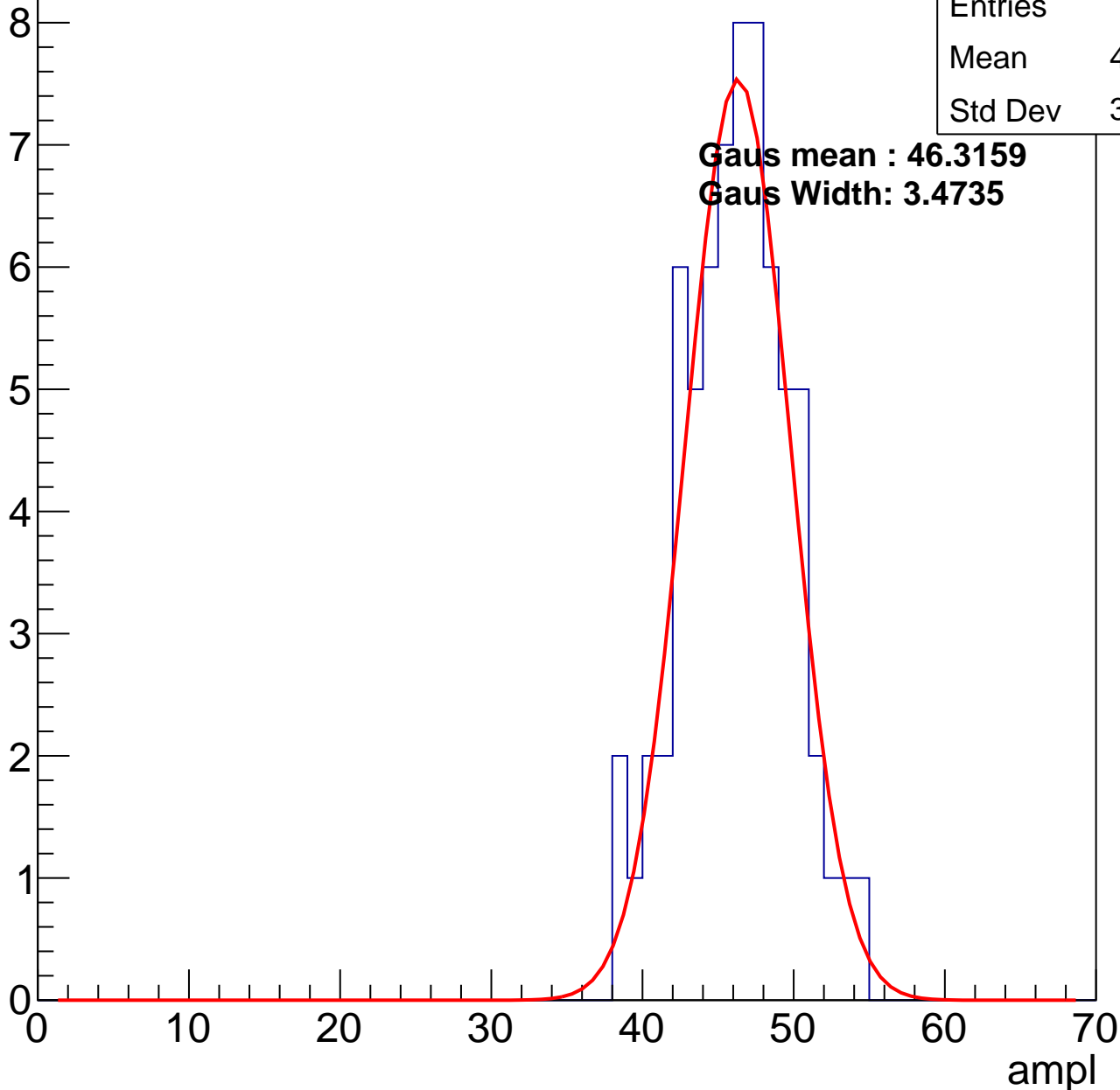
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.75
Std Dev	3.478

Gaus mean : 46.3159

Gaus Width: 3.4735

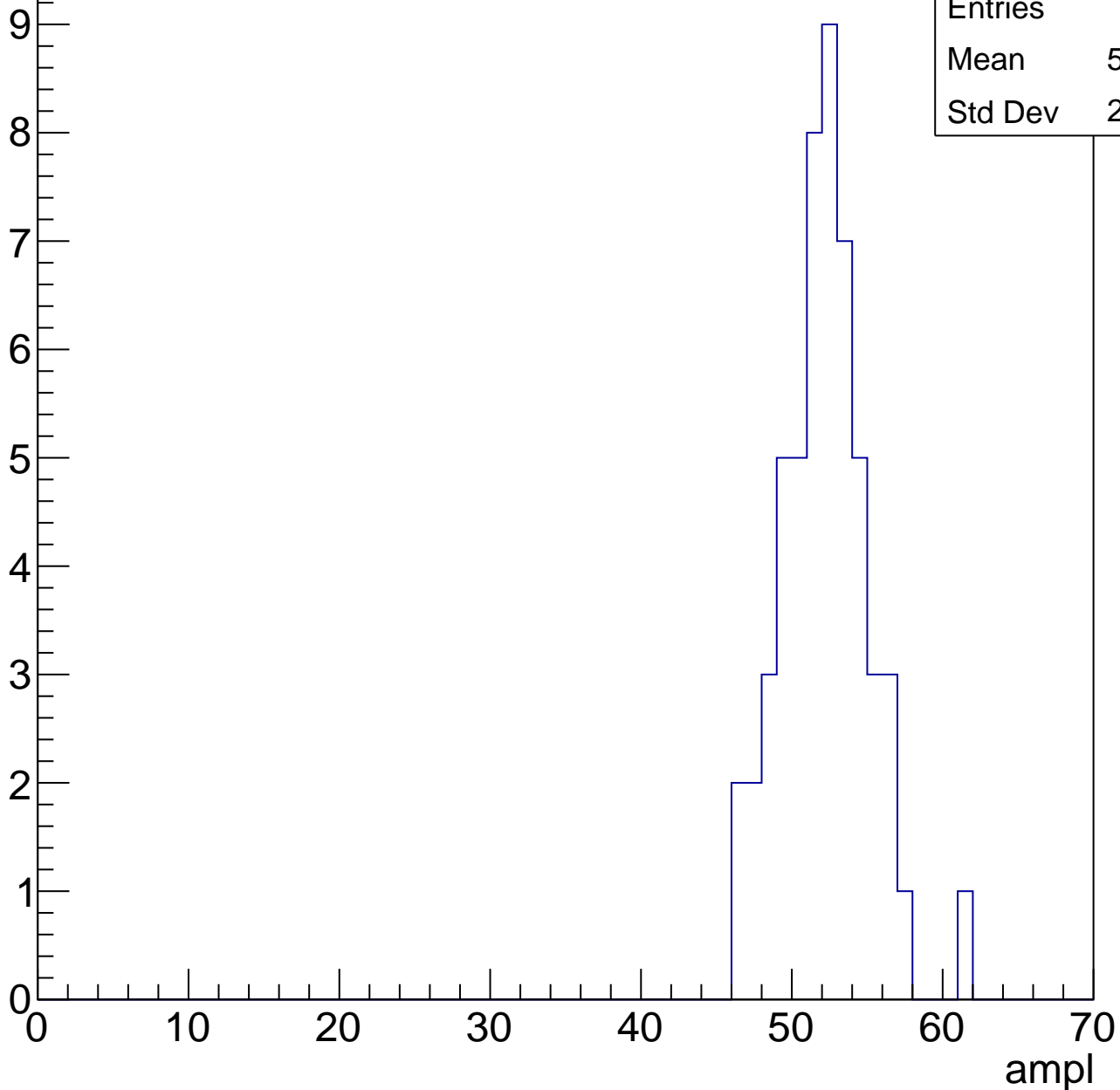


B0L002S, U7-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	51.72
Std Dev	2.877



B0L002S, U7-ch121, adc4

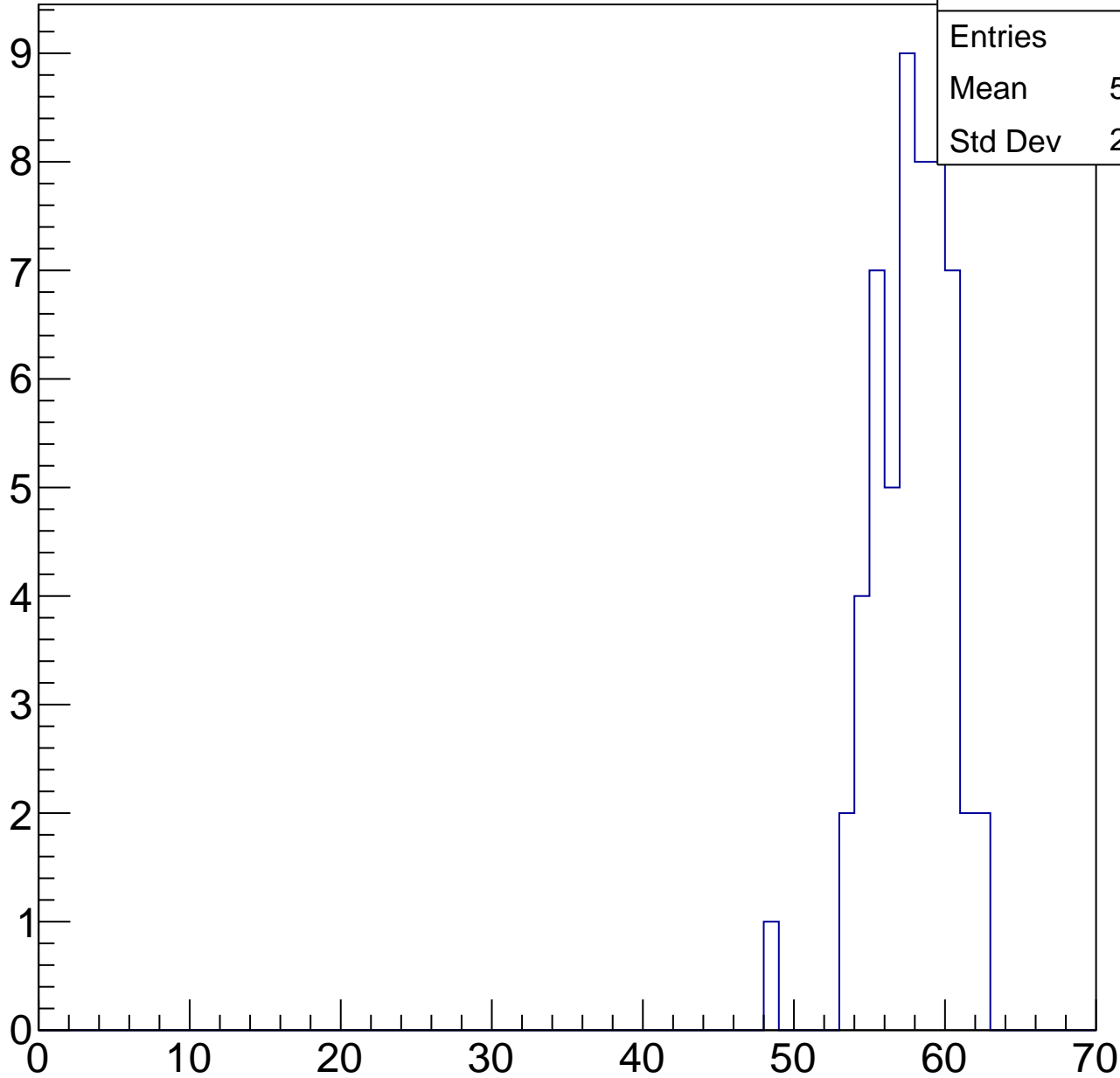
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	57.27
Std Dev	2.569

ampl

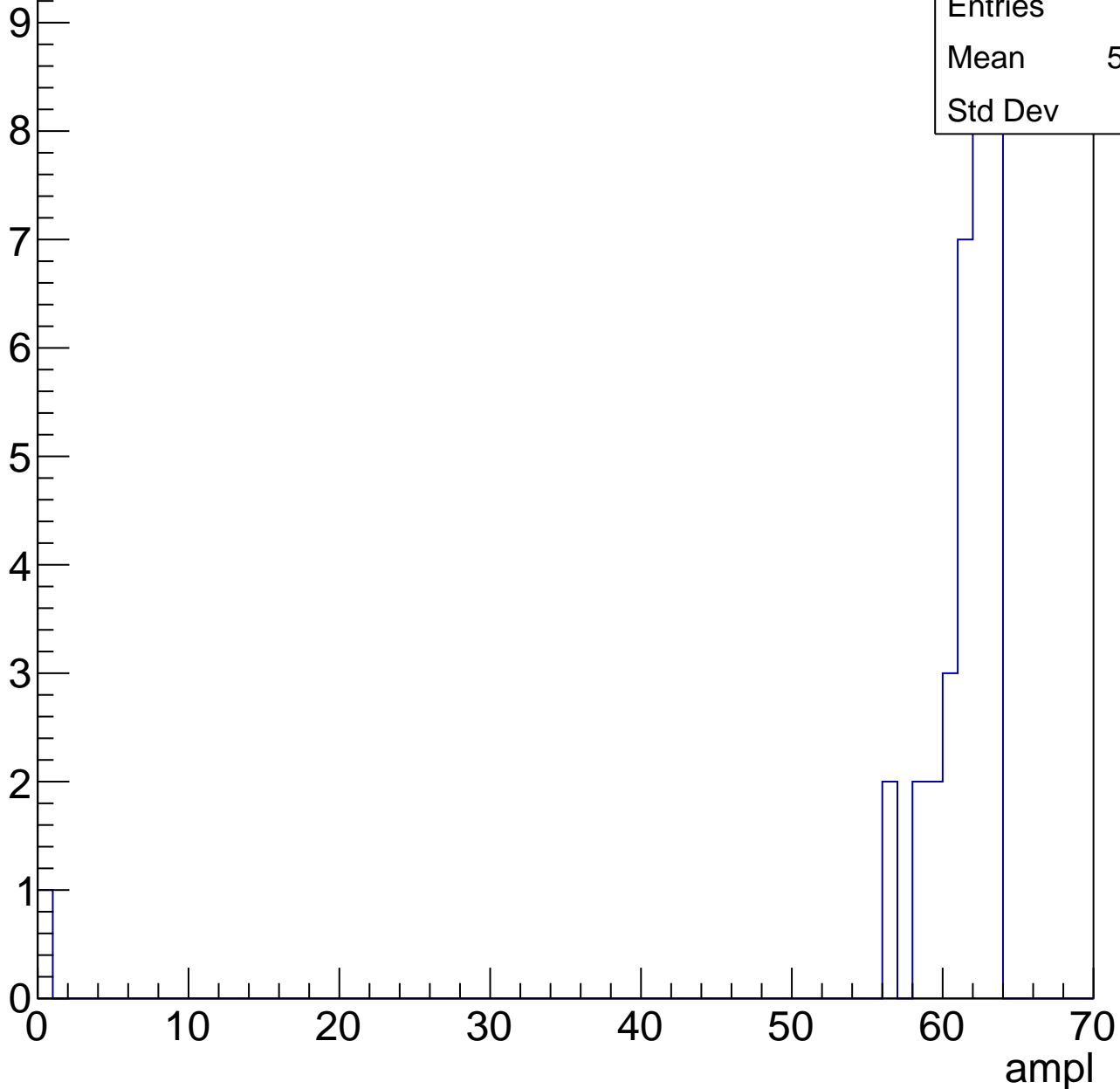


B0L002S, U7-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.29
Std Dev	10.5



B0L002S, U7-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch122, adc0

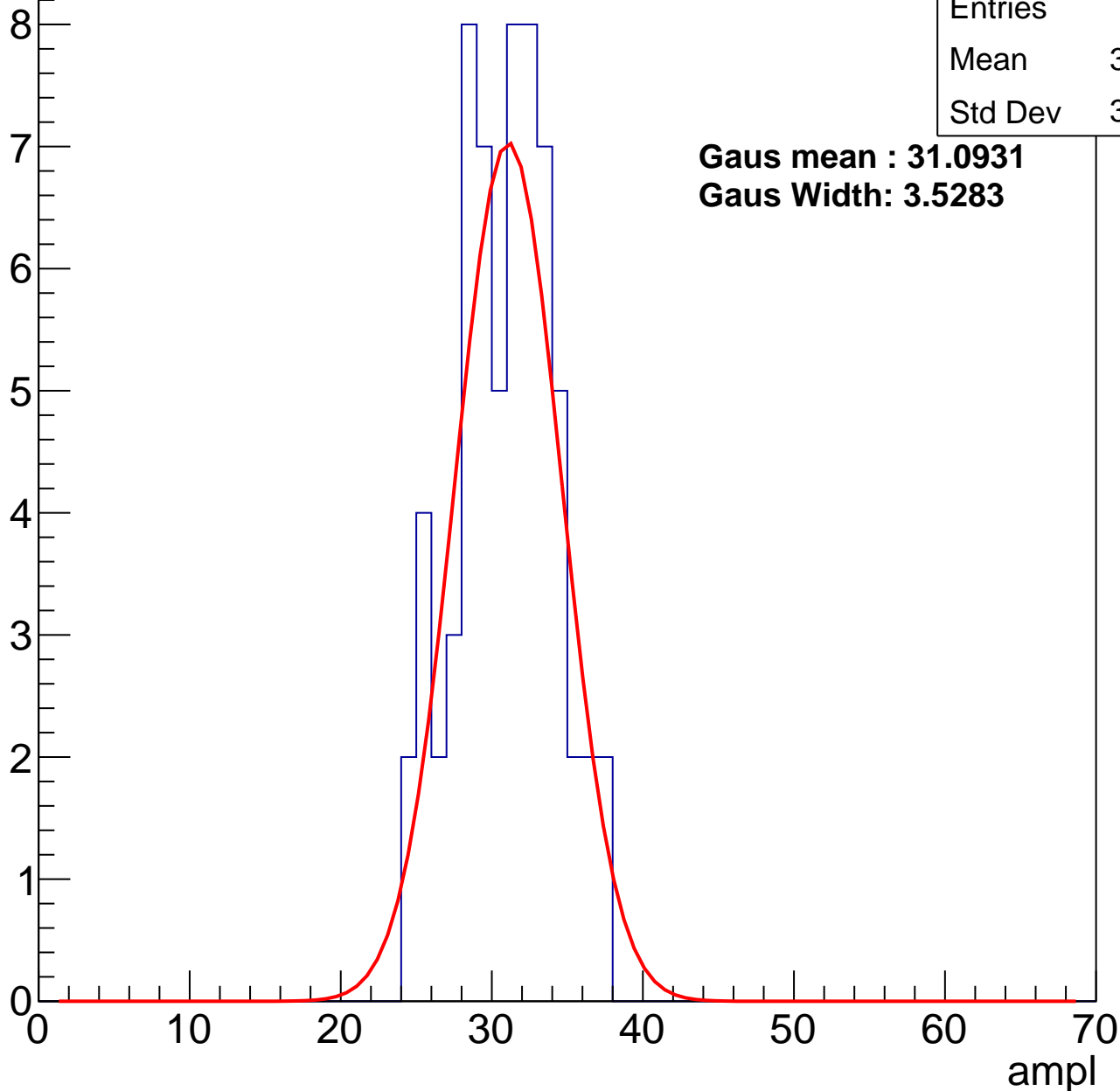
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.45
Std Dev	3.187

Gaus mean : 31.0931

Gaus Width: 3.5283



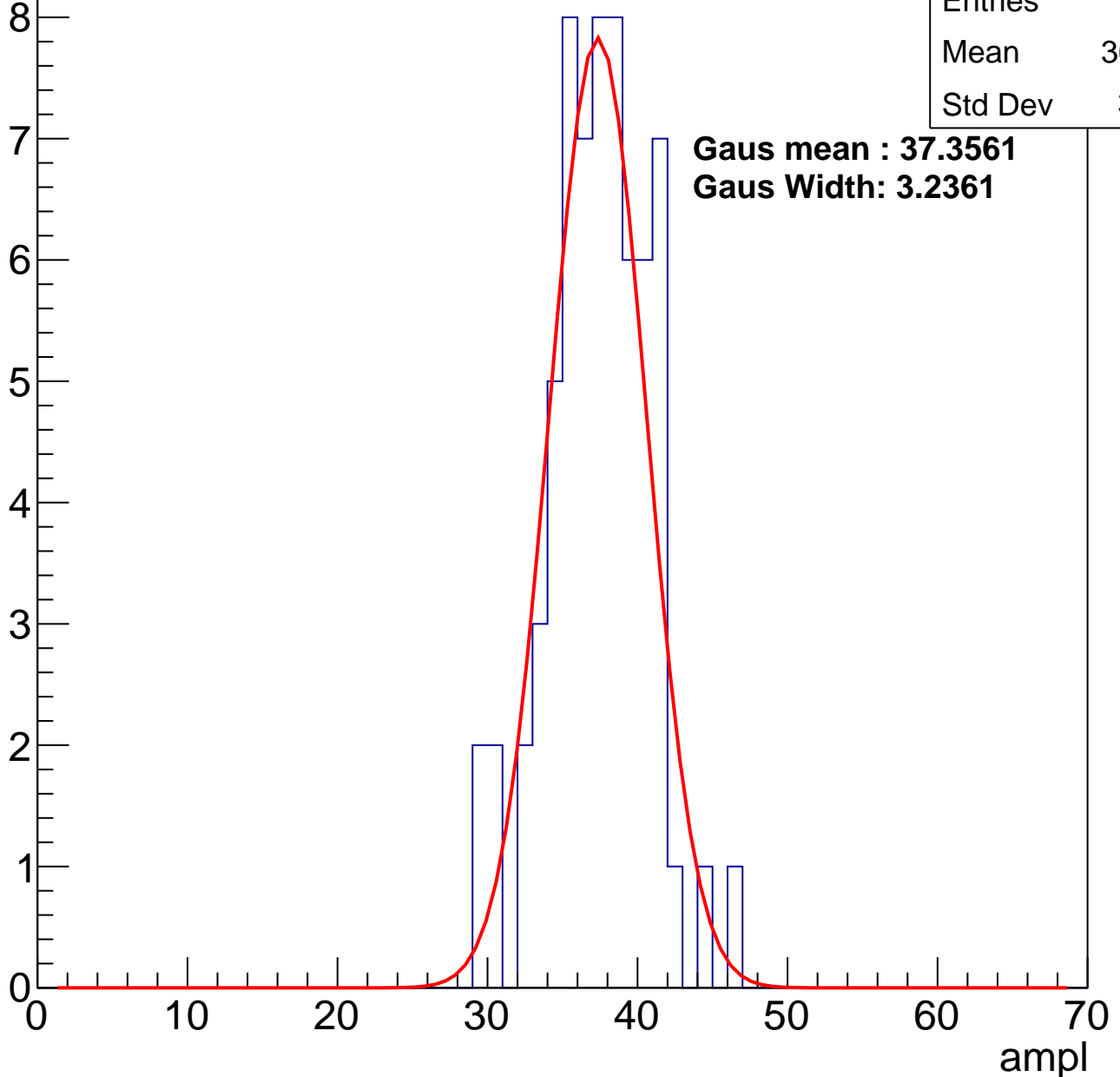
B0L002S, U7-ch122, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.96
Std Dev	3.37

Gaus mean : 37.3561
Gaus Width: 3.2361



B0L002S, U7-ch122, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	43.53
Std Dev	3.062

Gaus mean : 43.8699

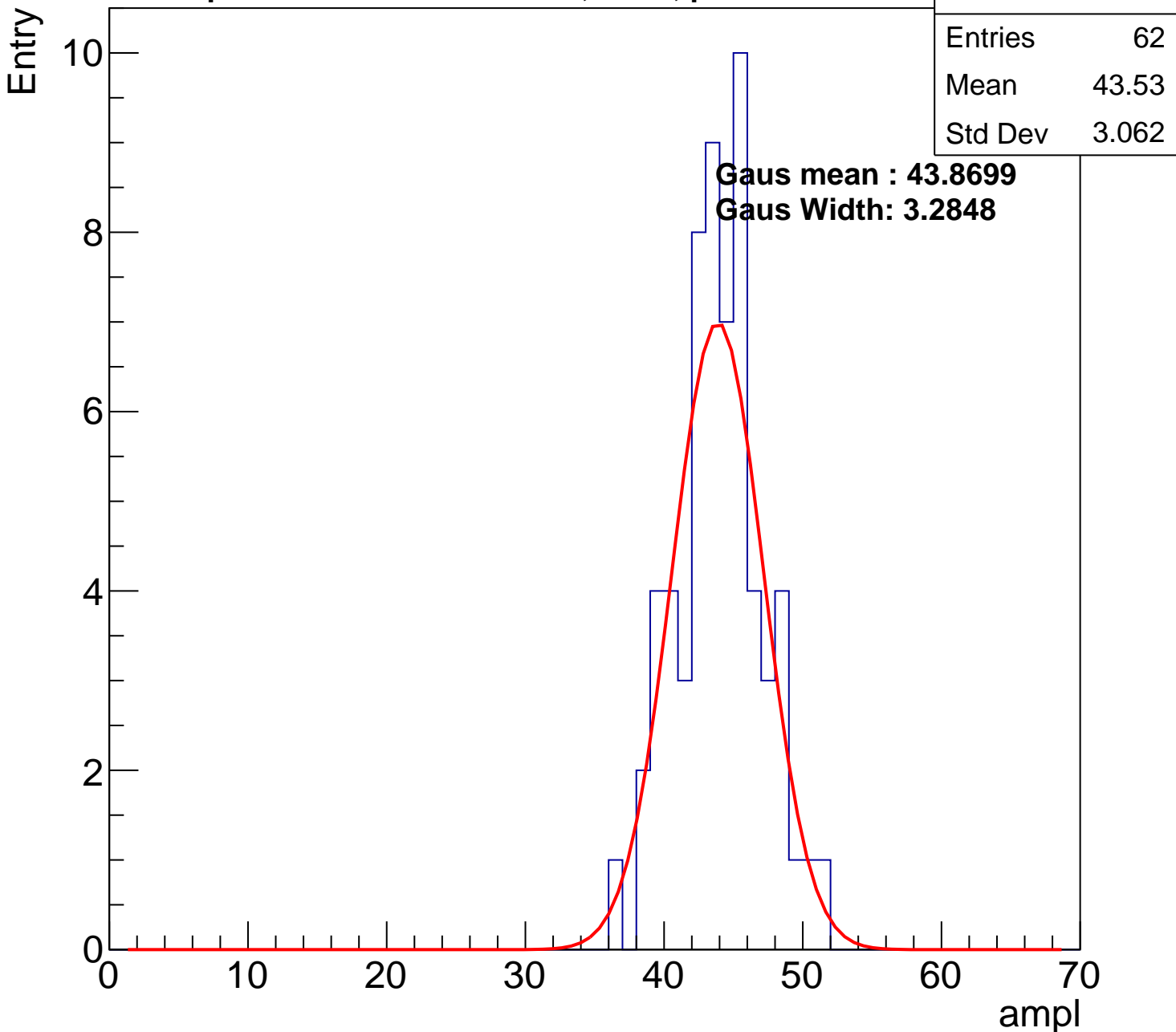
Gaus Width: 3.2848

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

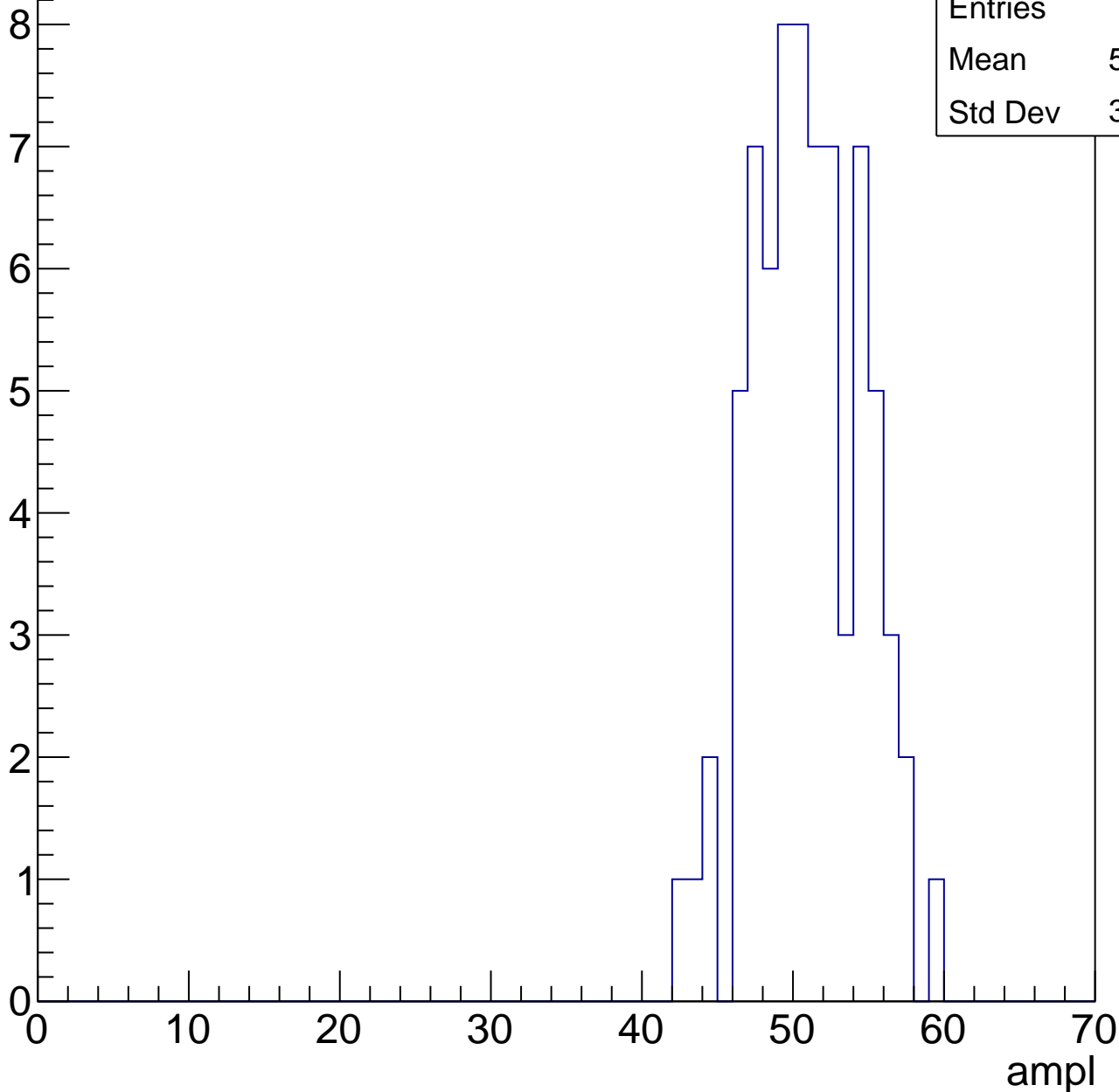


B0L002S, U7-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	50.49
Std Dev	3.566

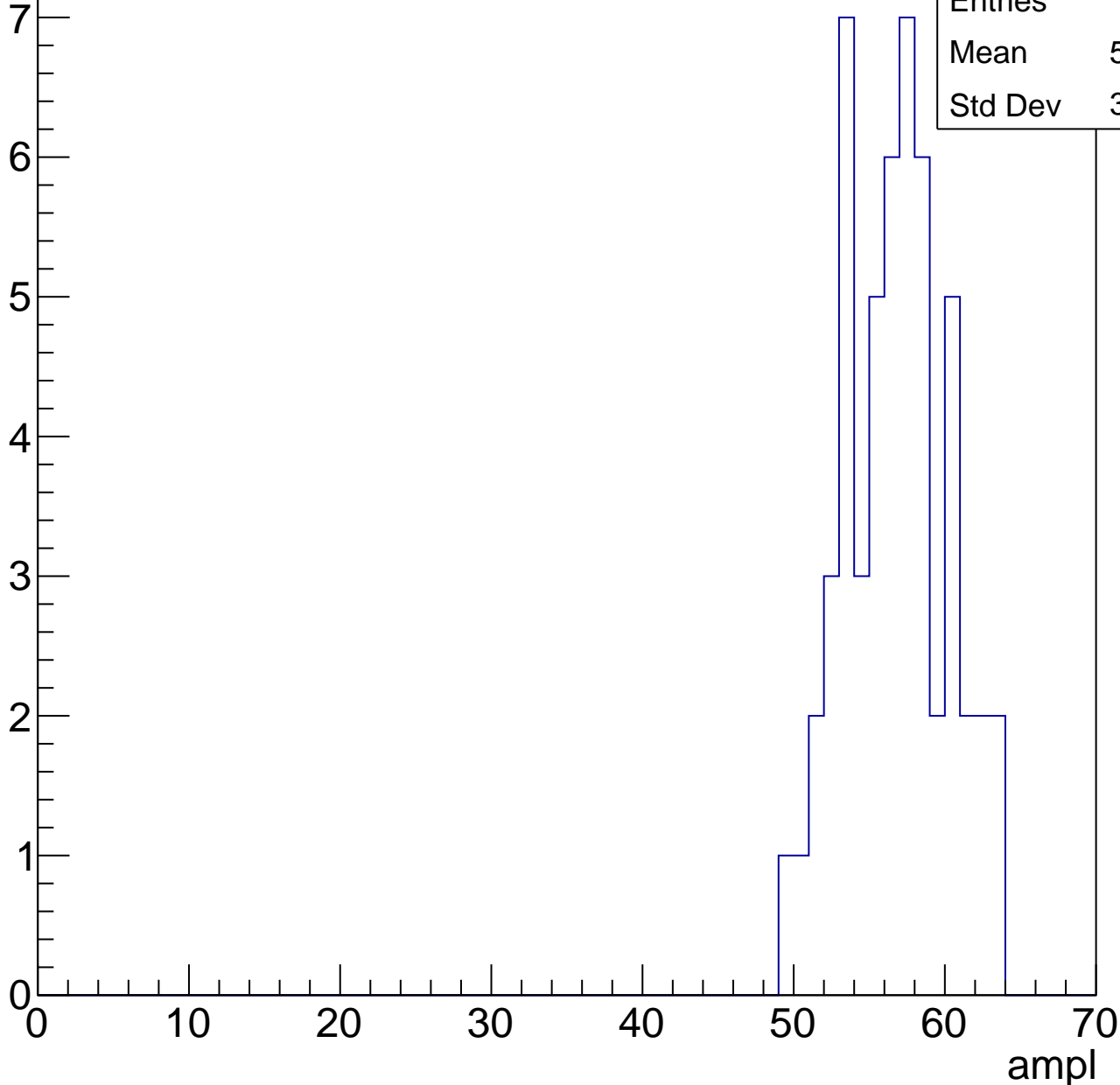


B0L002S, U7-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

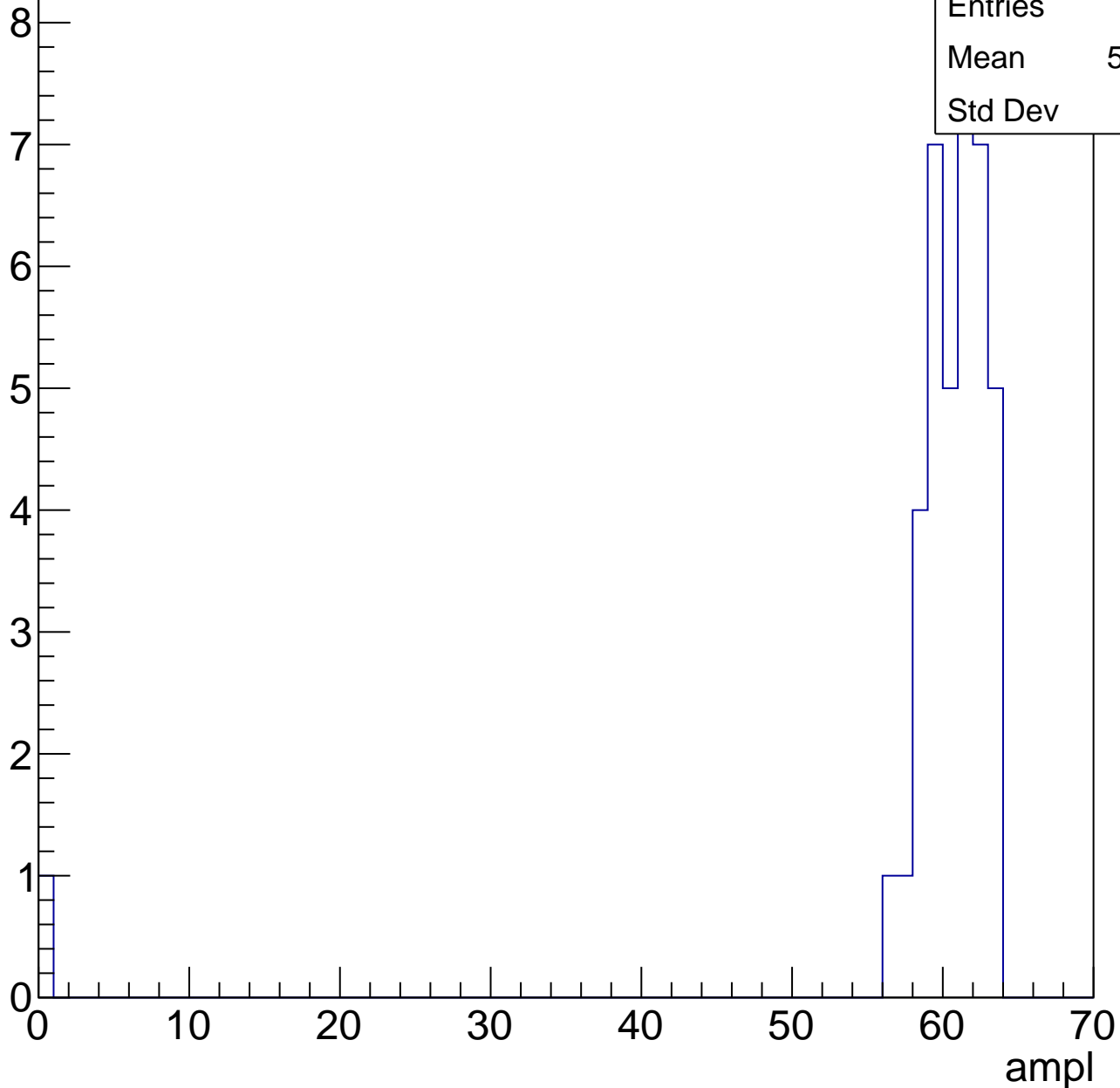
Entries	54
Mean	56.26
Std Dev	3.357



B0L002S, U7-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U7-ch123, adc0

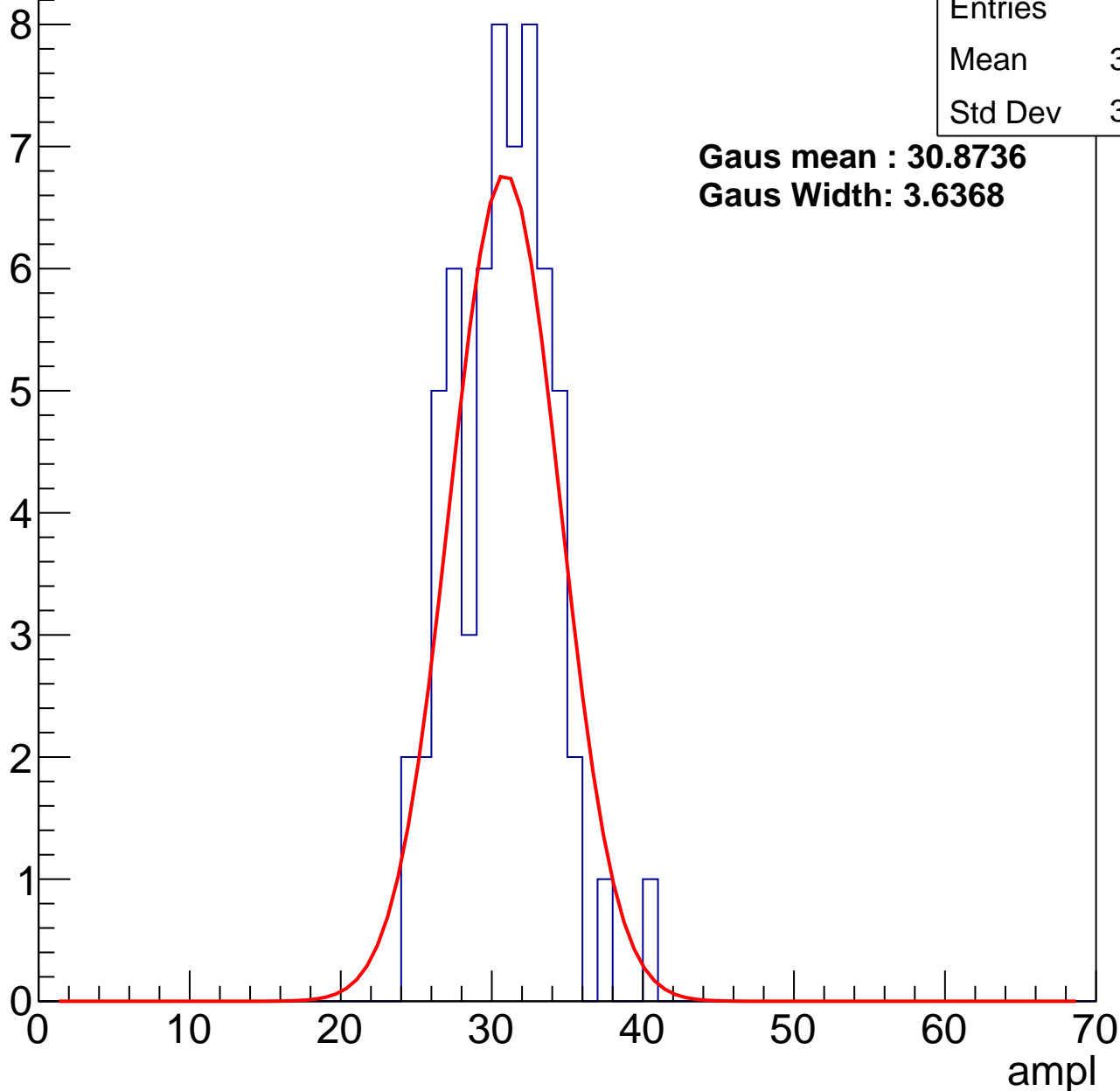
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.26
Std Dev	3.218

Gaus mean : 30.8736

Gaus Width: 3.6368



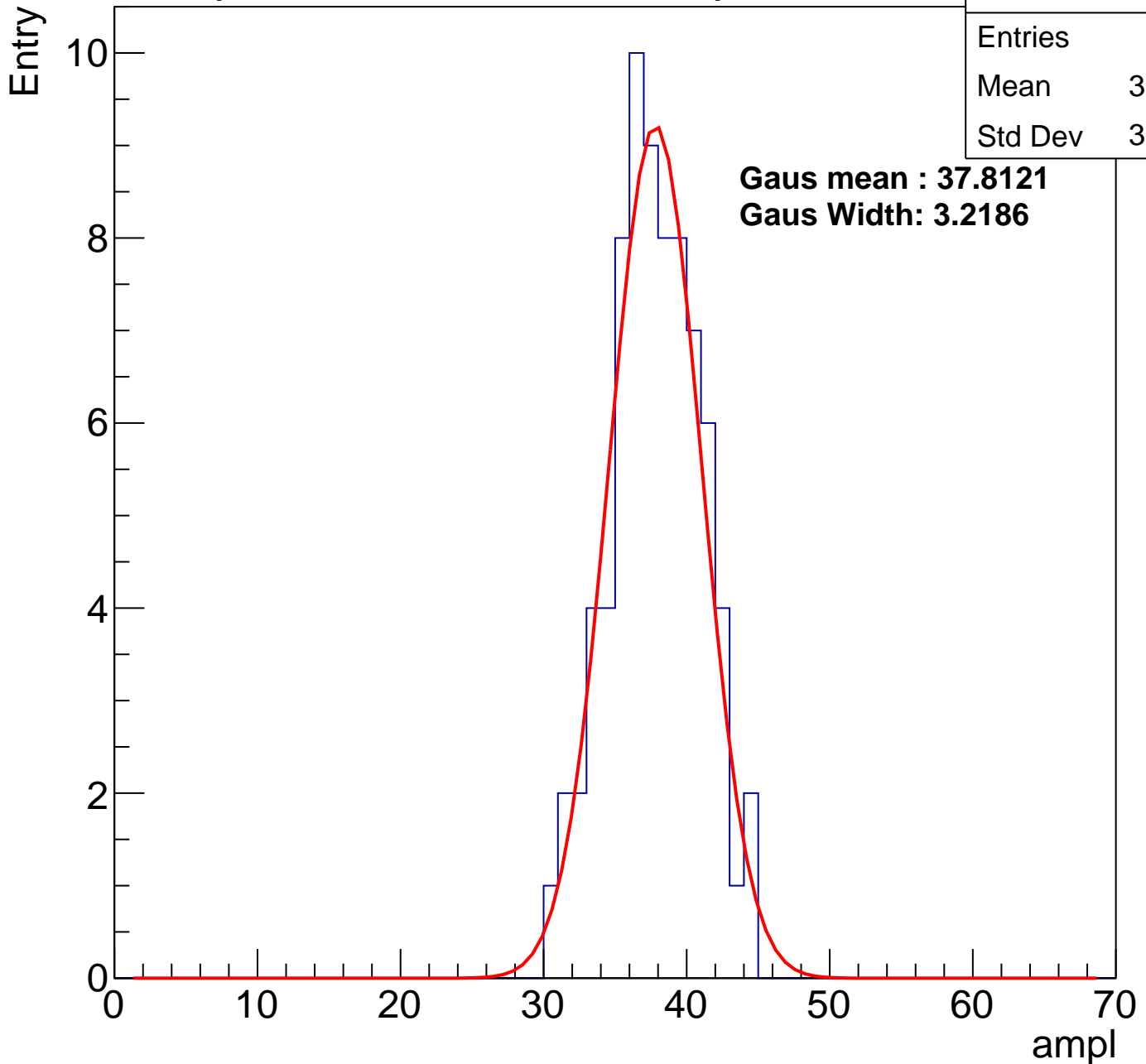
B0L002S, U7-ch123, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	37.34
Std Dev	3.114

Gaus mean : 37.8121

Gaus Width: 3.2186



B0L002S, U7-ch123, adc2

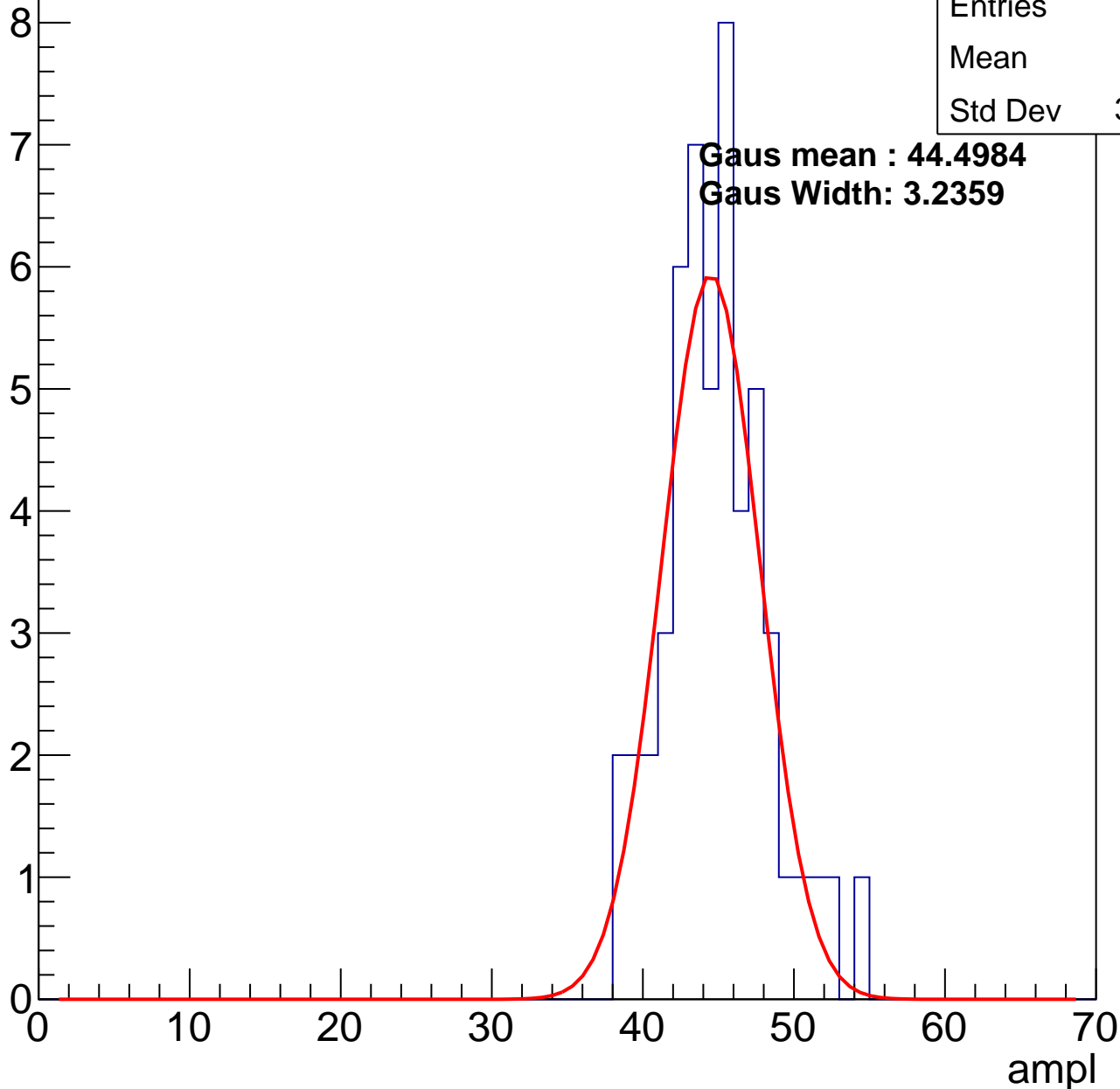
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44.4
Std Dev	3.381

Gaus mean : 44.4984

Gaus Width: 3.2359

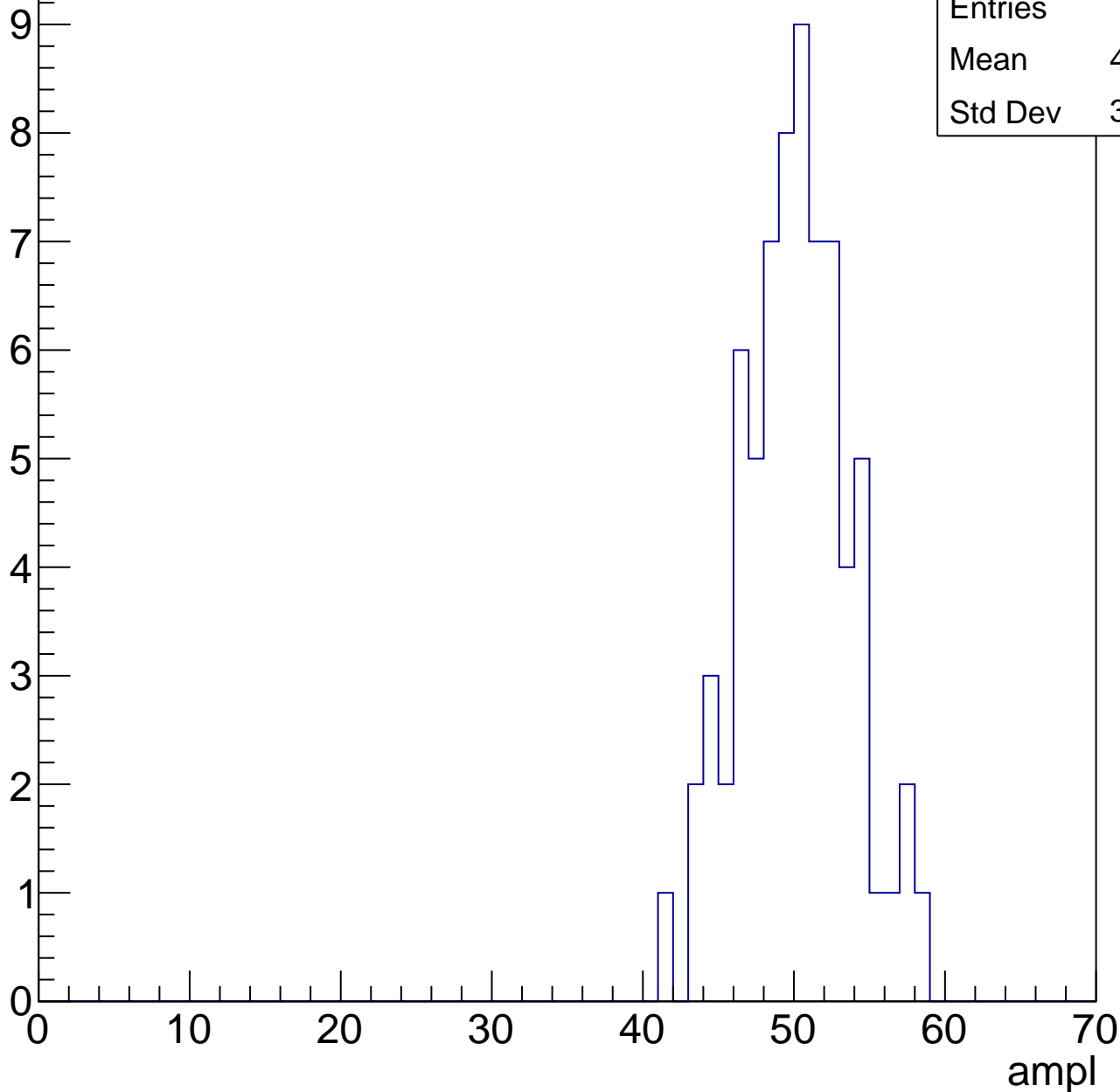


B0L002S, U7-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	49.63
Std Dev	3.518

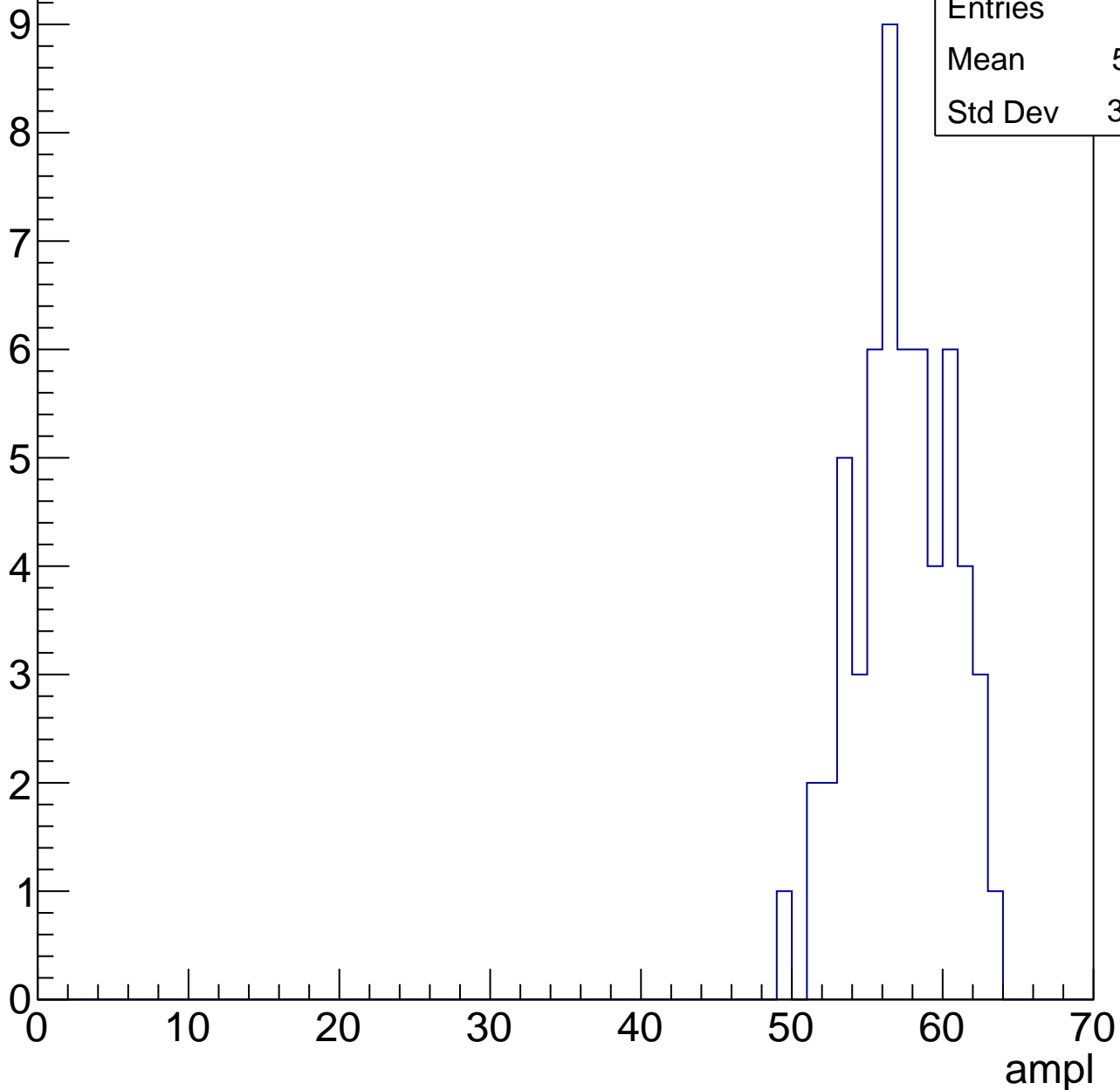


B0L002S, U7-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.81
Std Dev	3.148

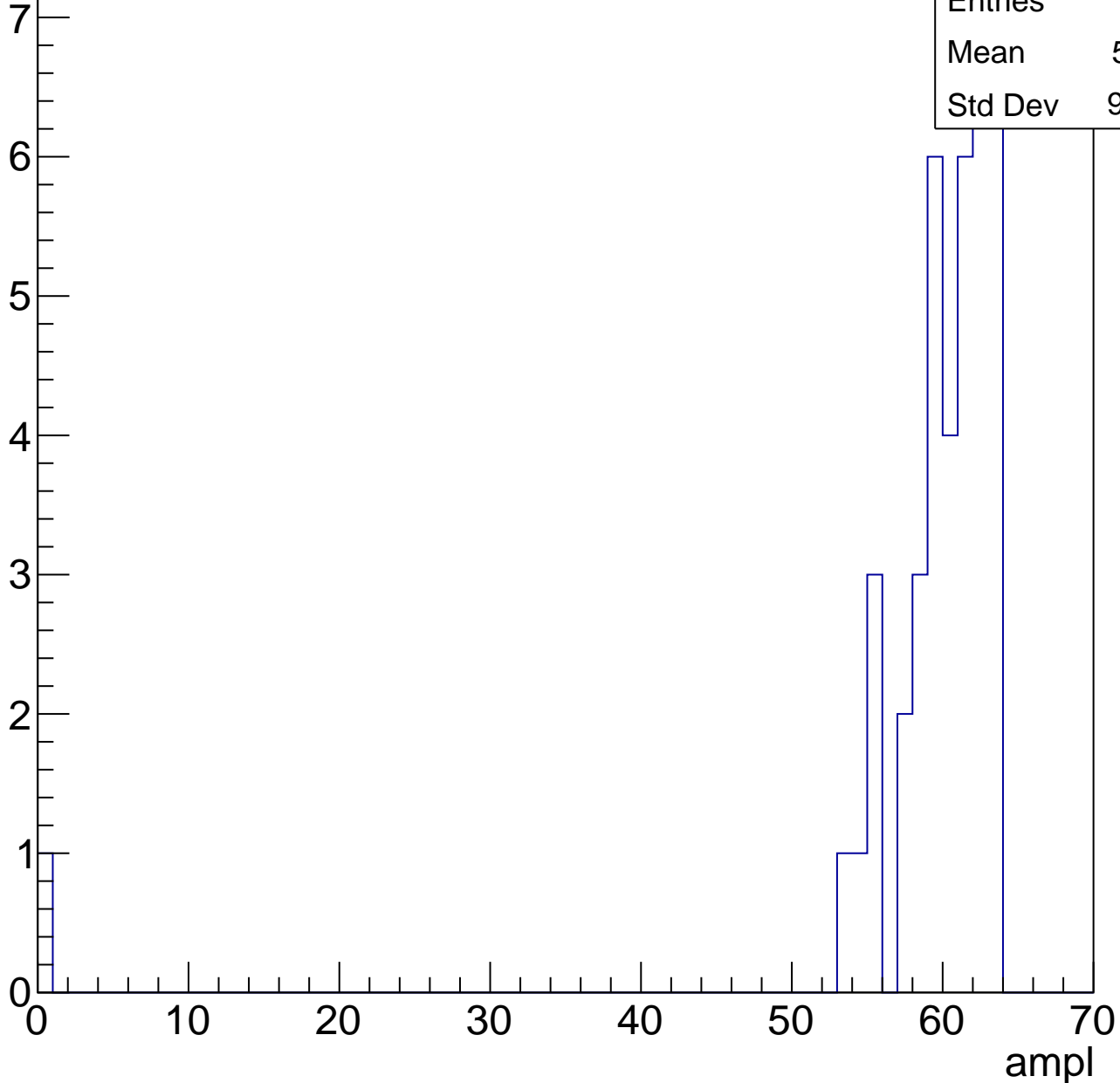


B0L002S, U7-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.41
Std Dev	9.614



B0L002S, U7-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	63
Std Dev	0

B0L002S, U7-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch124, adc0

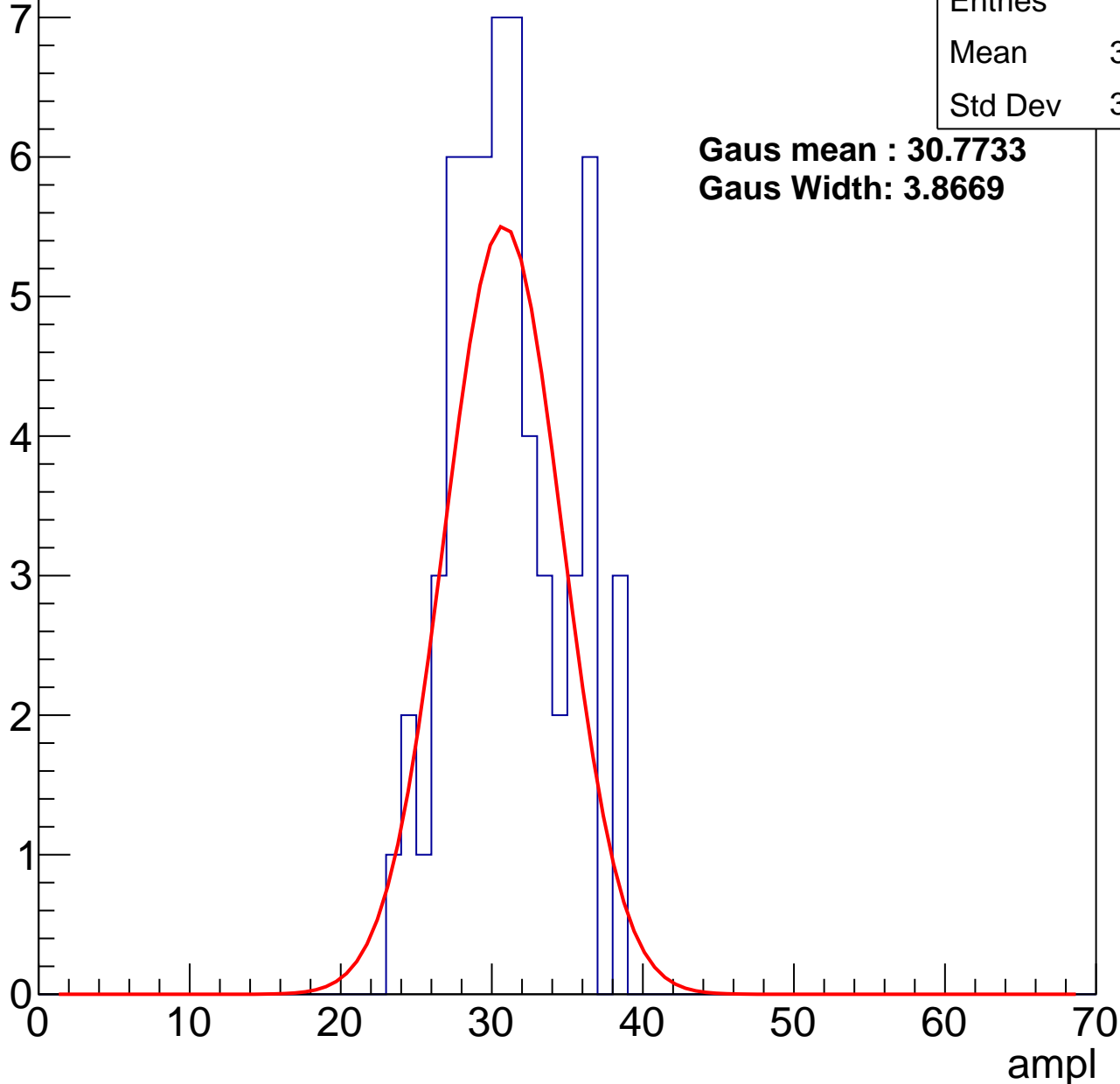
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	30.58
Std Dev	3.703

Gaus mean : 30.7733

Gaus Width: 3.8669



B0L002S, U7-ch124, adc1

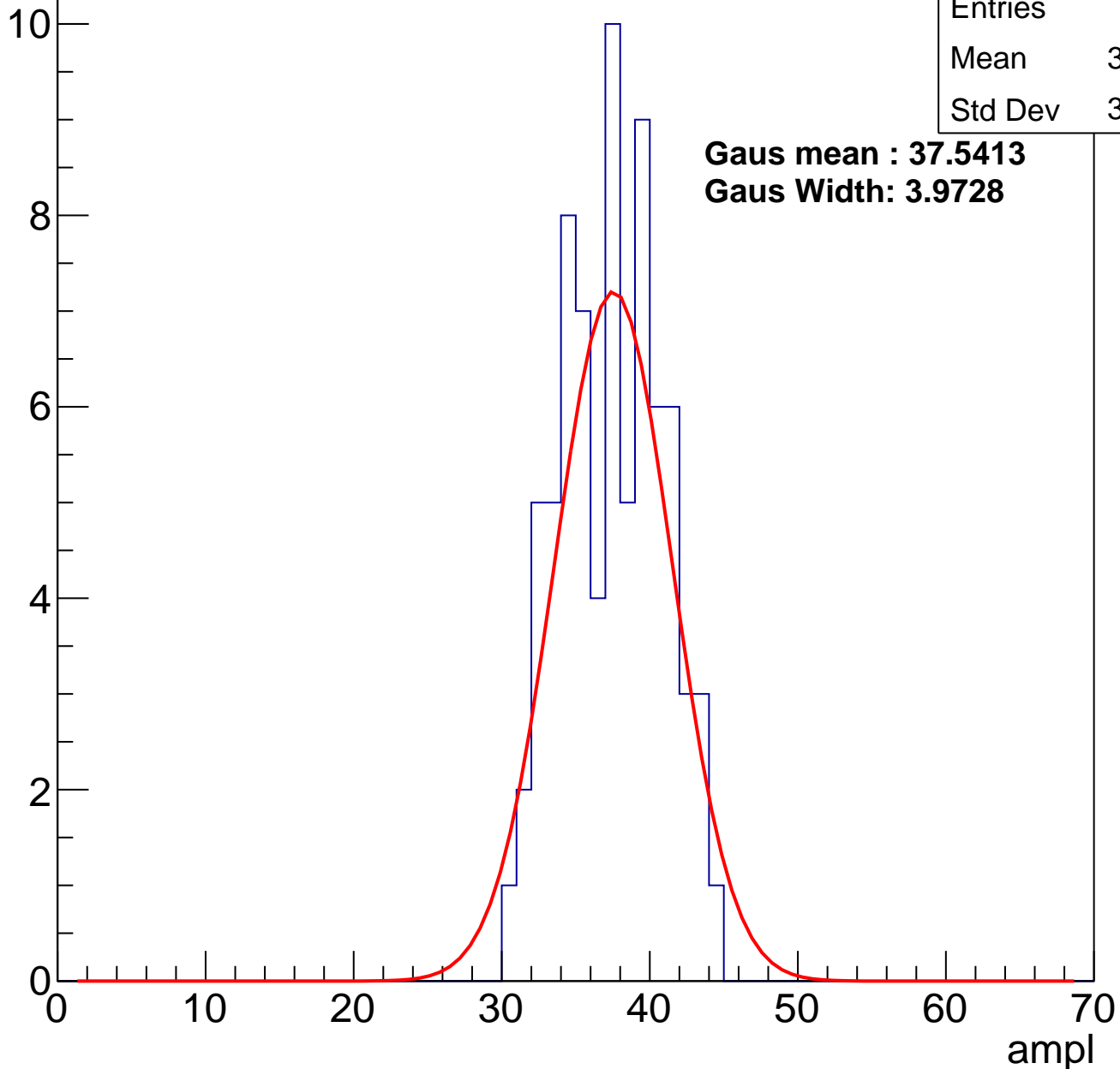
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.99
Std Dev	3.372

Gaus mean : 37.5413

Gaus Width: 3.9728

Entry



B0L002S, U7-ch124, adc2

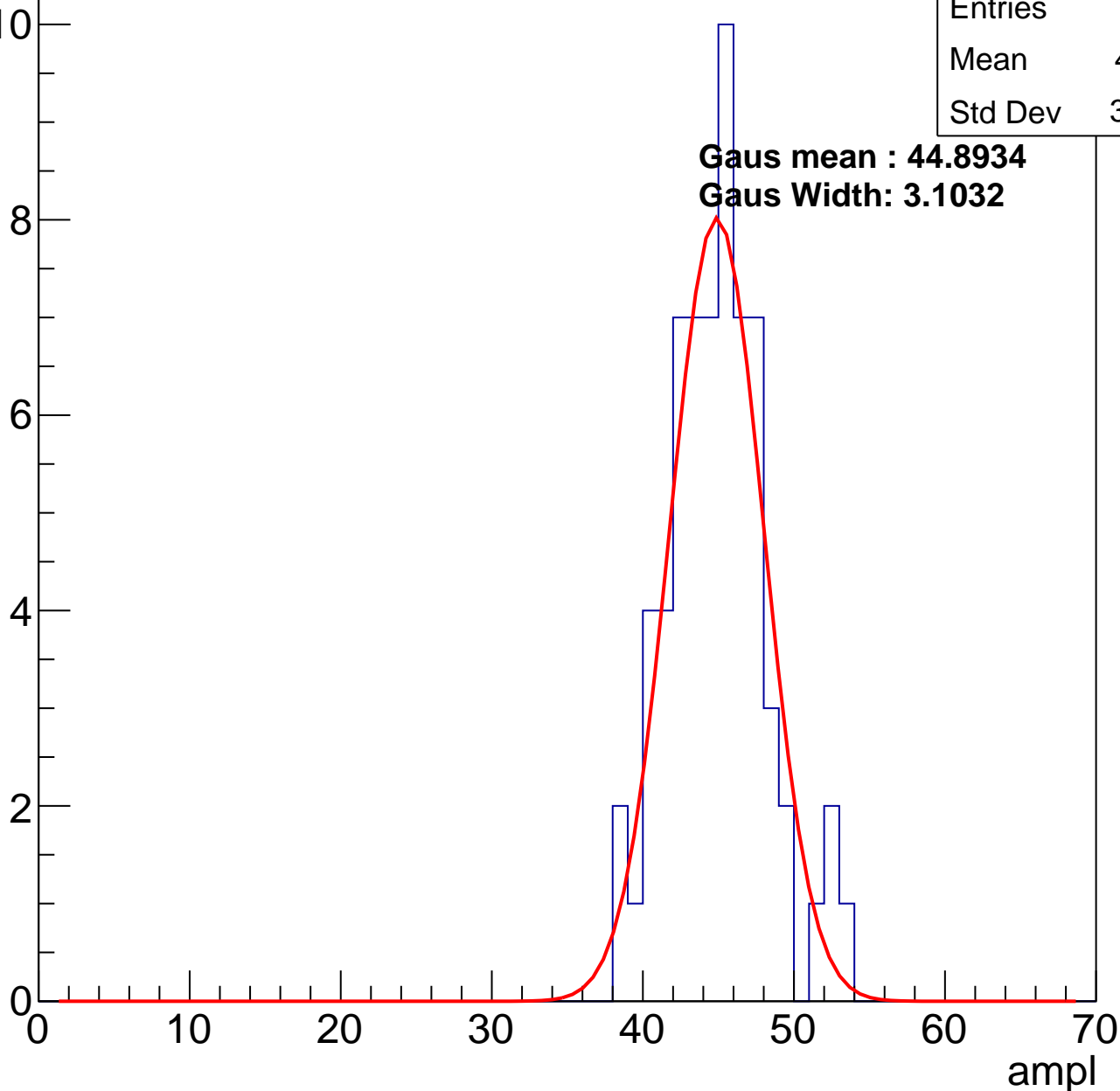
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.51
Std Dev	3.216

Gaus mean : 44.8934

Gaus Width: 3.1032

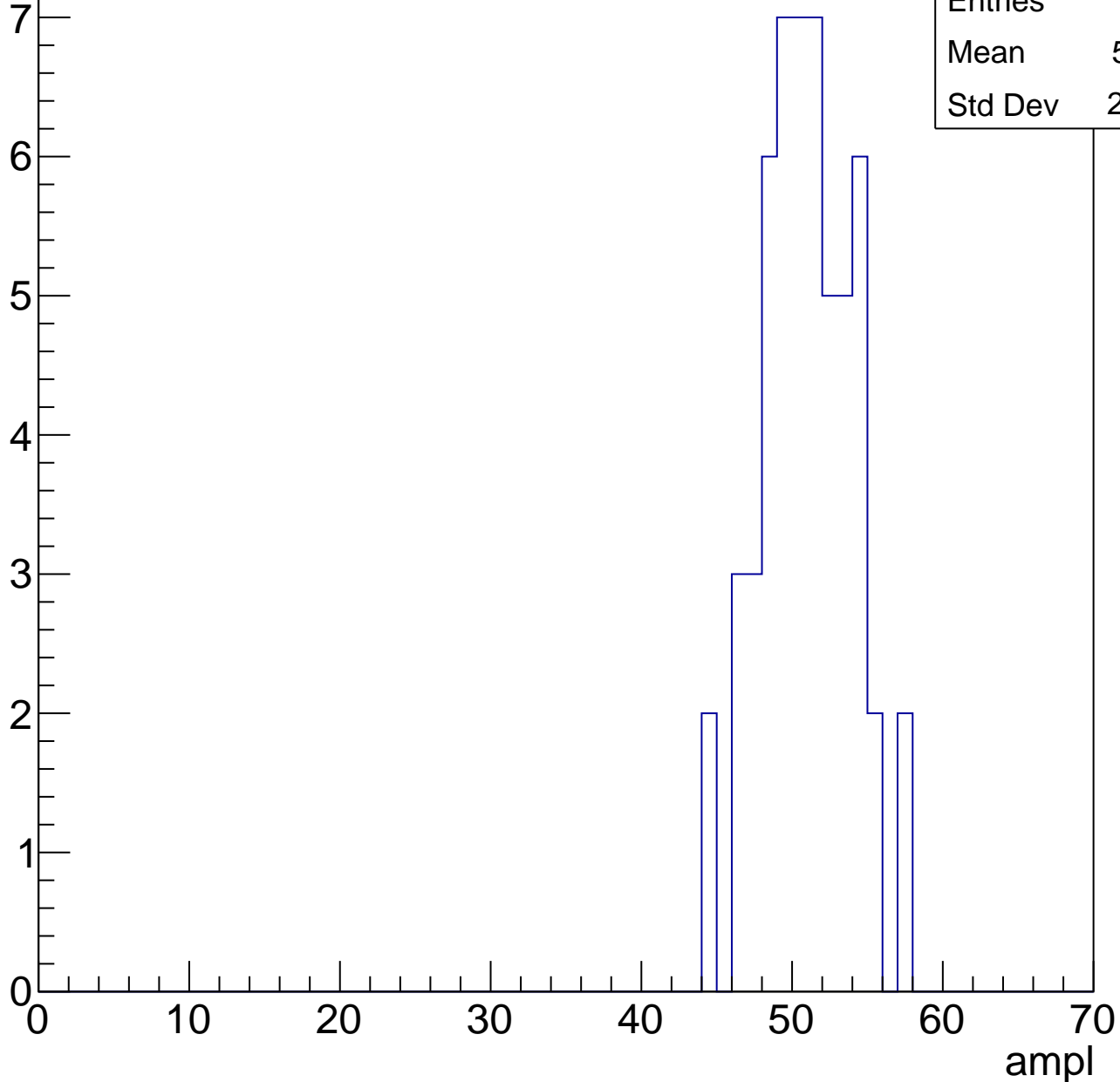


B0L002S, U7-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

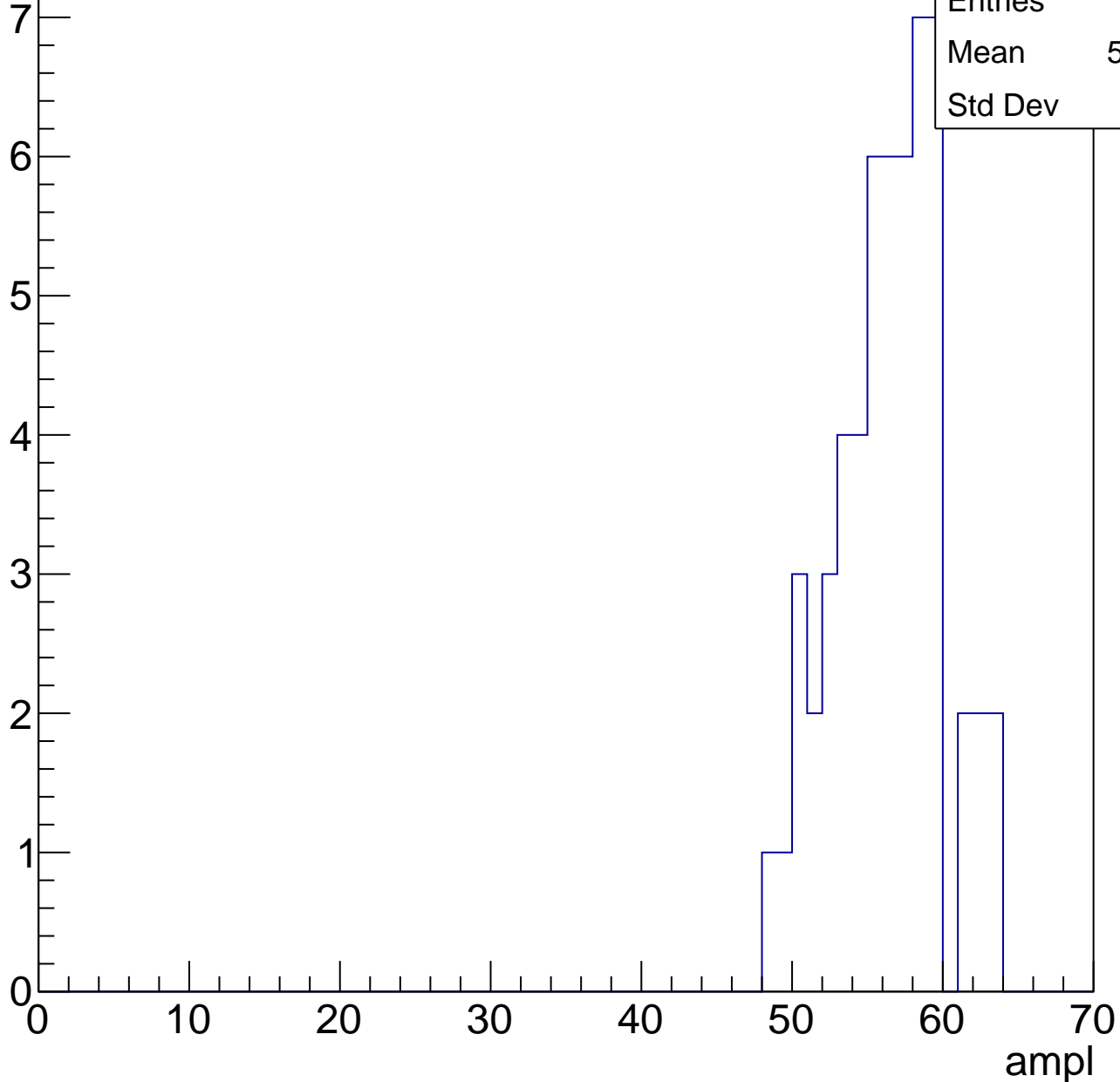
Entries	55
Mean	50.51
Std Dev	2.953



B0L002S, U7-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

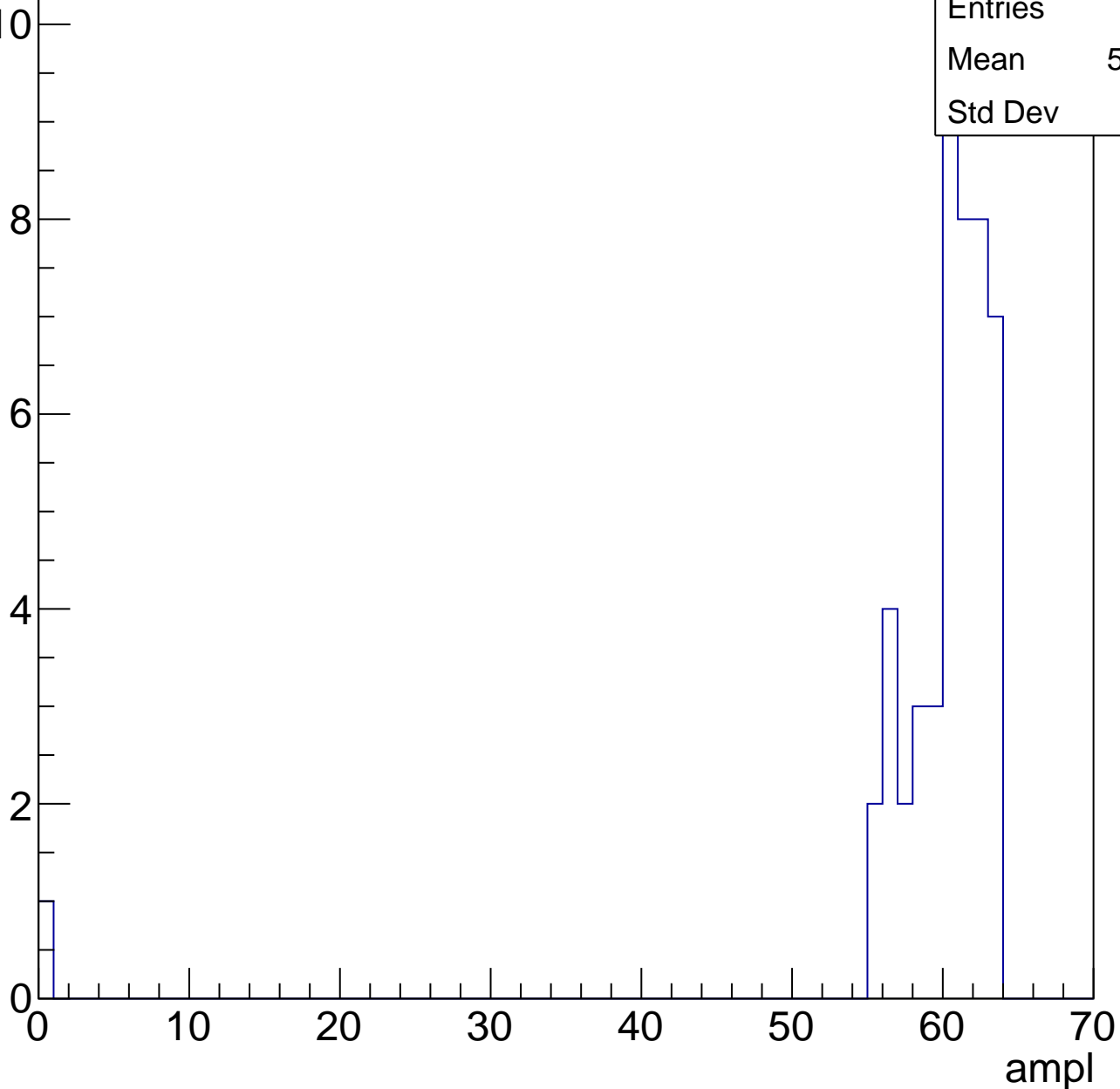


B0L002S, U7-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	58.83
Std Dev	8.88



B0L002S, U7-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch125, adc0

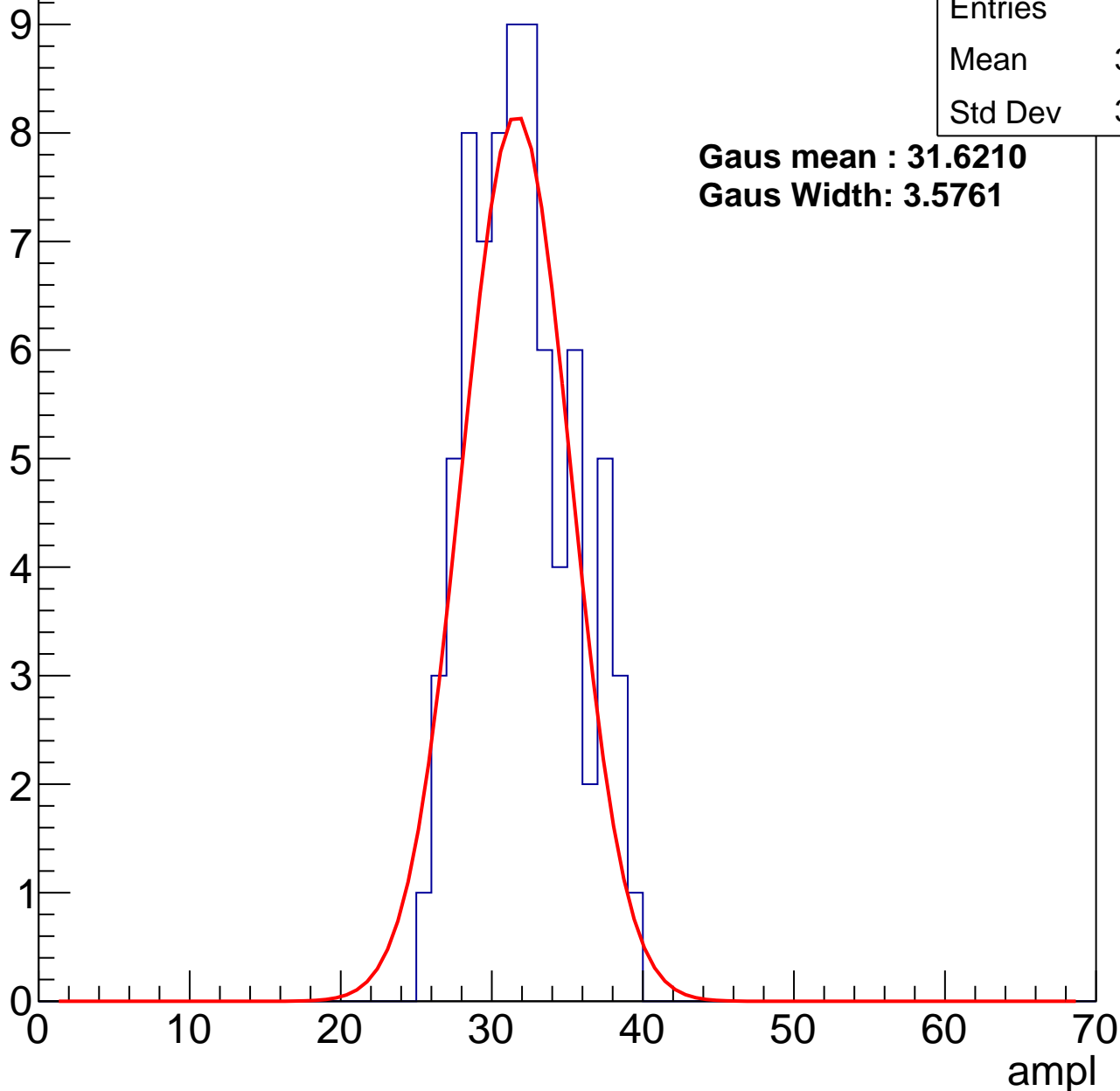
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.51
Std Dev	3.391

Gaus mean : 31.6210

Gaus Width: 3.5761



B0L002S, U7-ch125, adc1

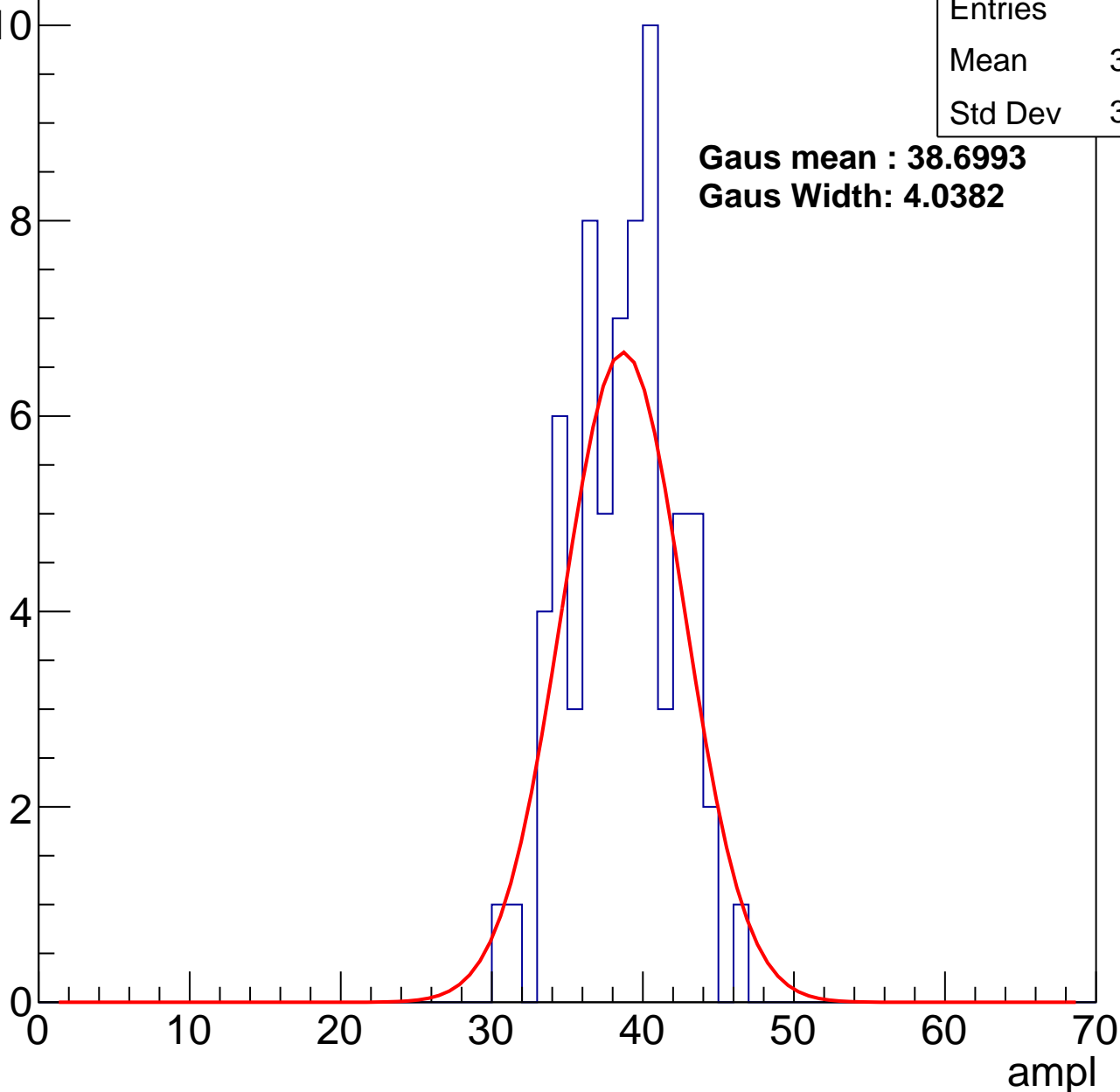
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	38.19
Std Dev	3.376

Gaus mean : 38.6993

Gaus Width: 4.0382



B0L002S, U7-ch125, adc2

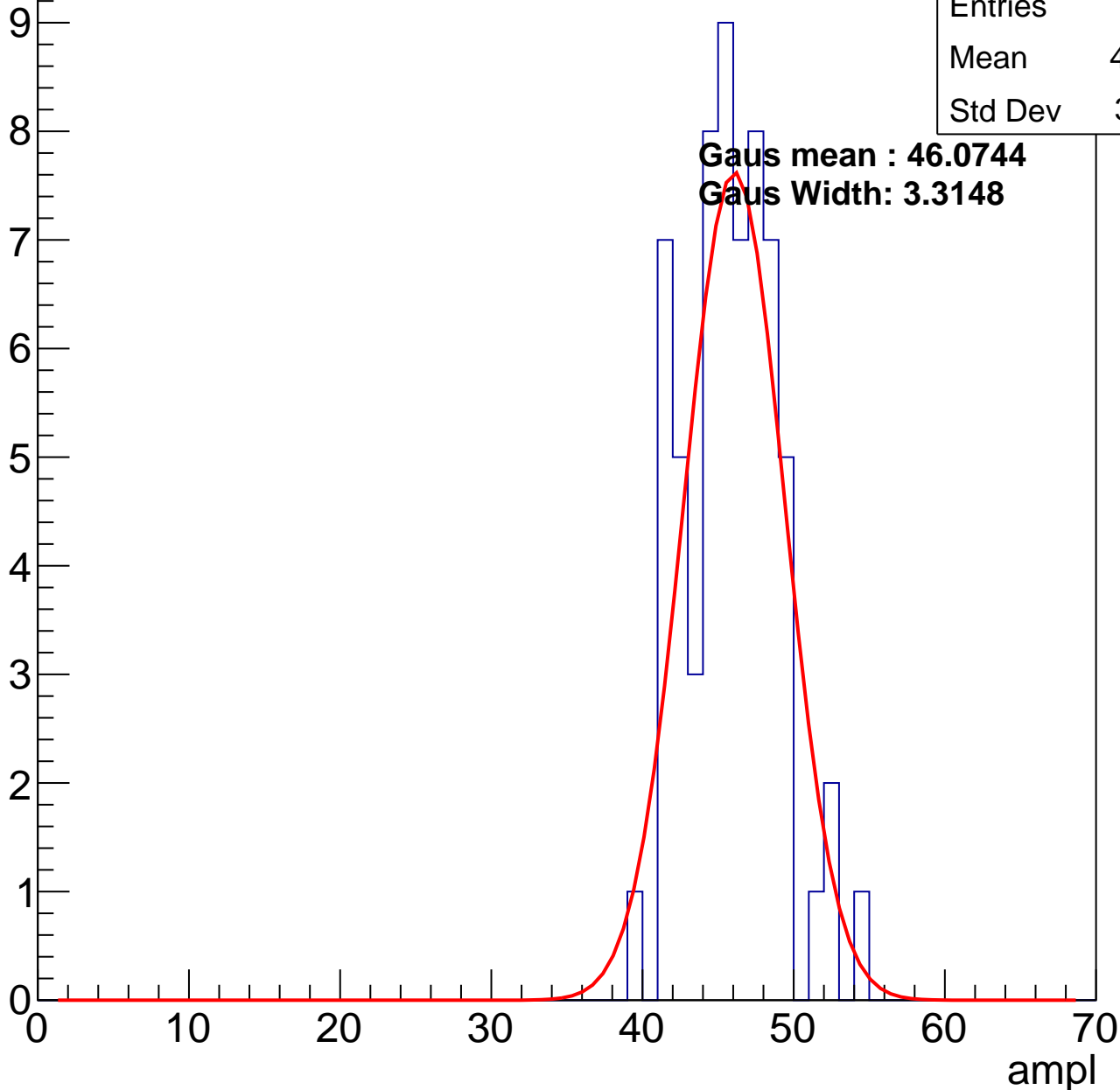
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45.47
Std Dev	3.051

Gaus mean : 46.0744

Gaus Width: 3.3148

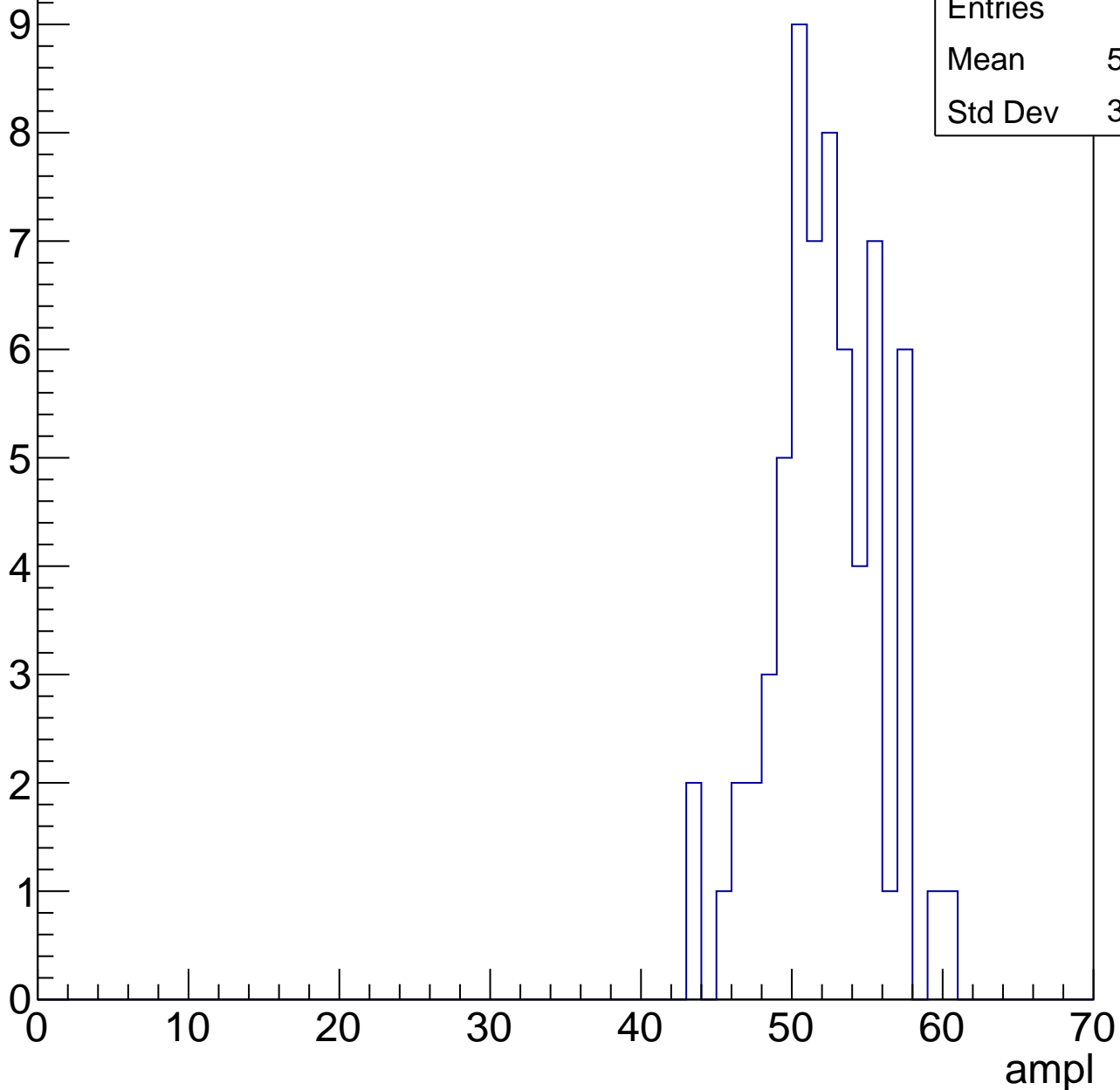


B0L002S, U7-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	51.77
Std Dev	3.572

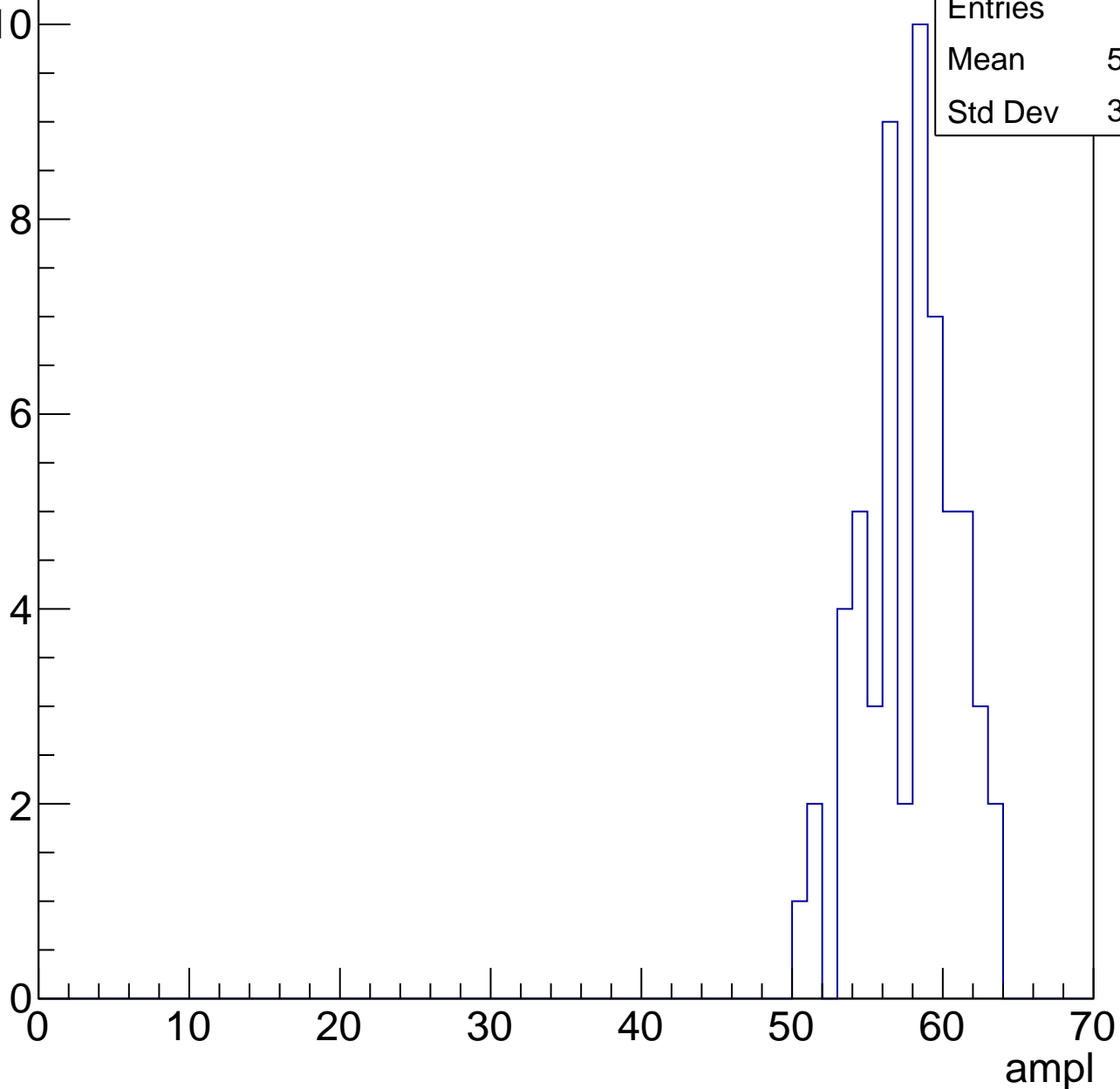


B0L002S, U7-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.36
Std Dev	3.089

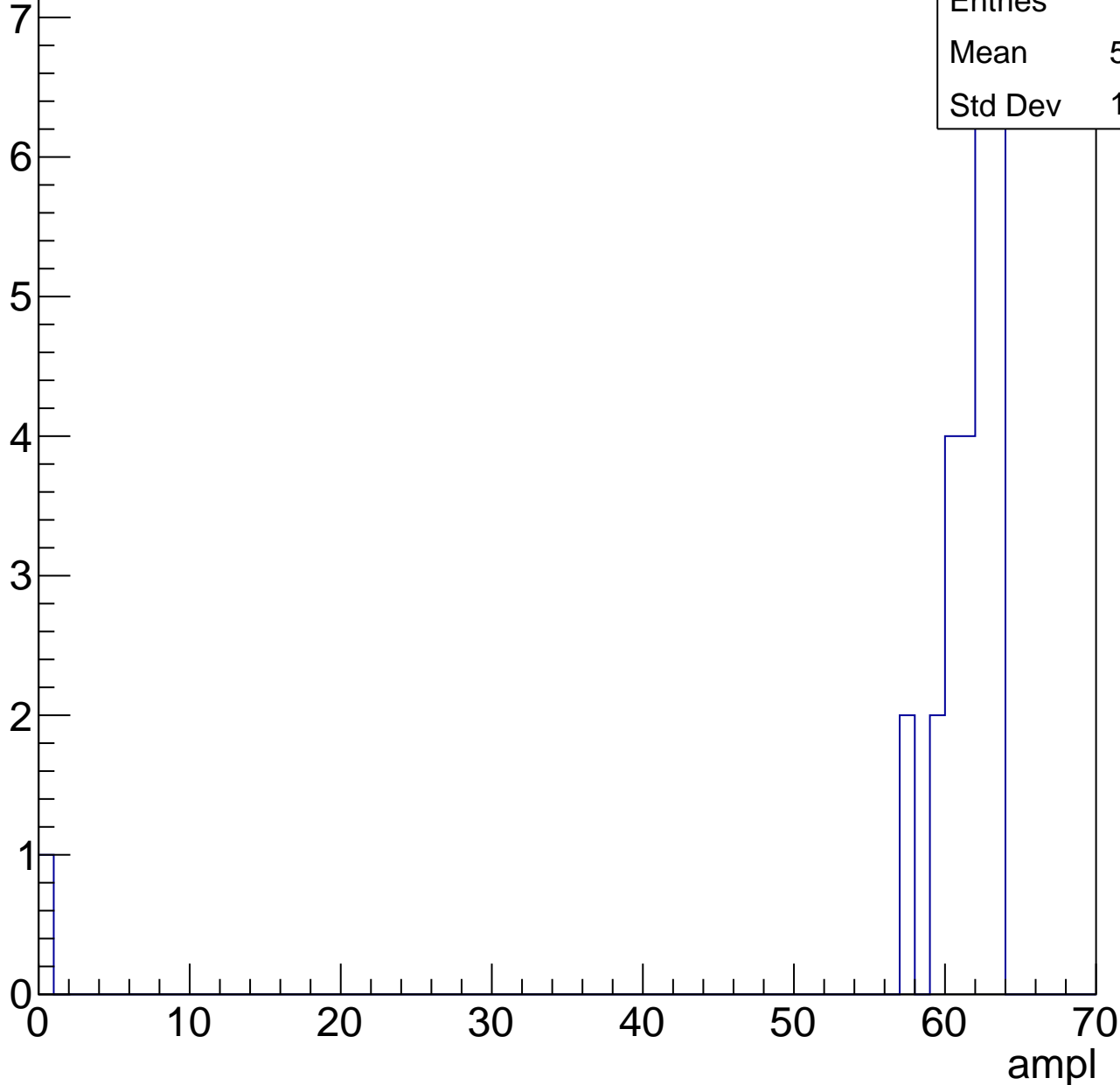


B0L002S, U7-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.93
Std Dev	11.68



B0L002S, U7-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U7-ch126, adc0

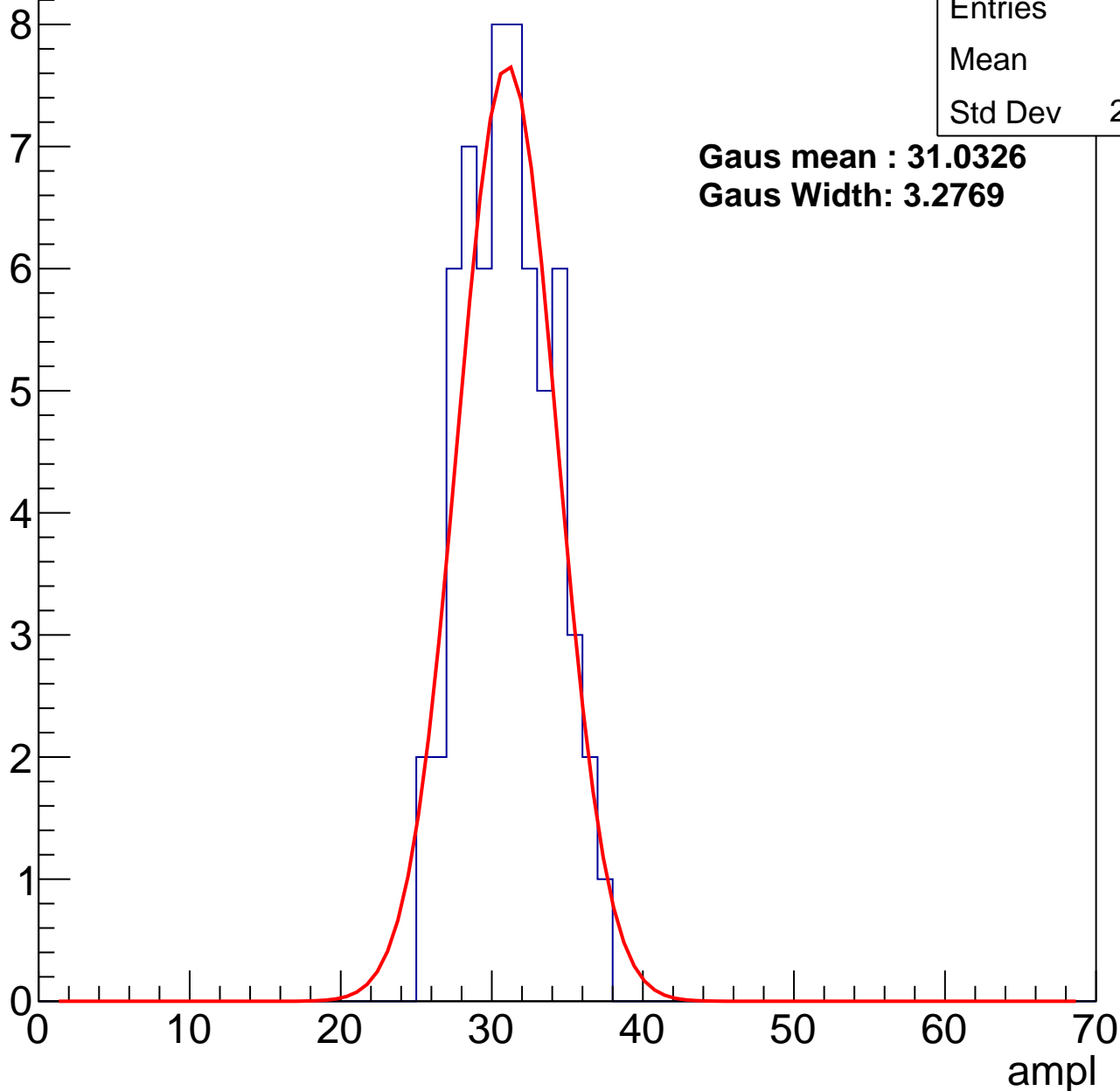
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.6
Std Dev	2.887

Gaus mean : 31.0326

Gaus Width: 3.2769



B0L002S, U7-ch126, adc1

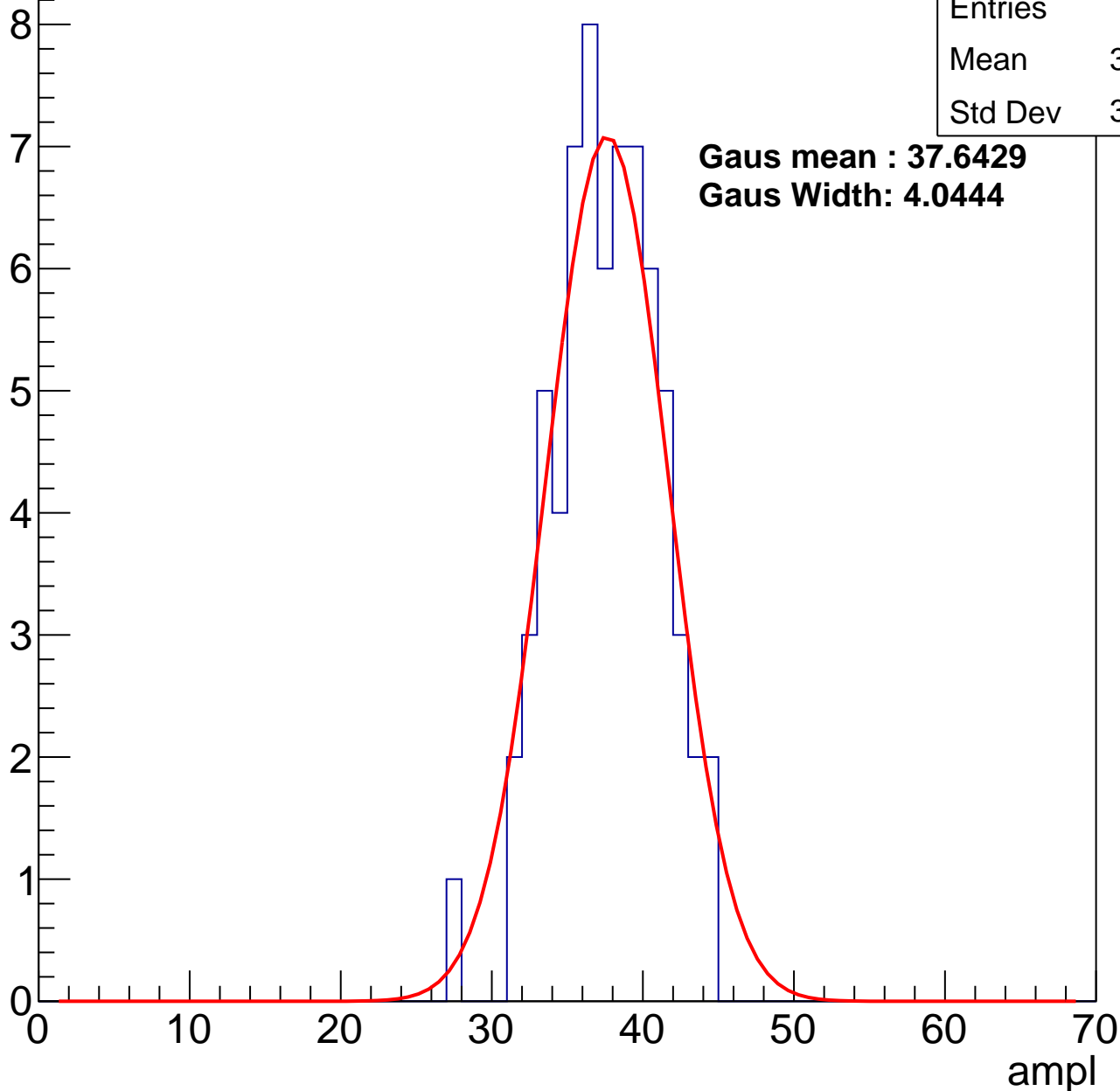
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.13
Std Dev	3.455

Gaus mean : 37.6429

Gaus Width: 4.0444



B0L002S, U7-ch126, adc2

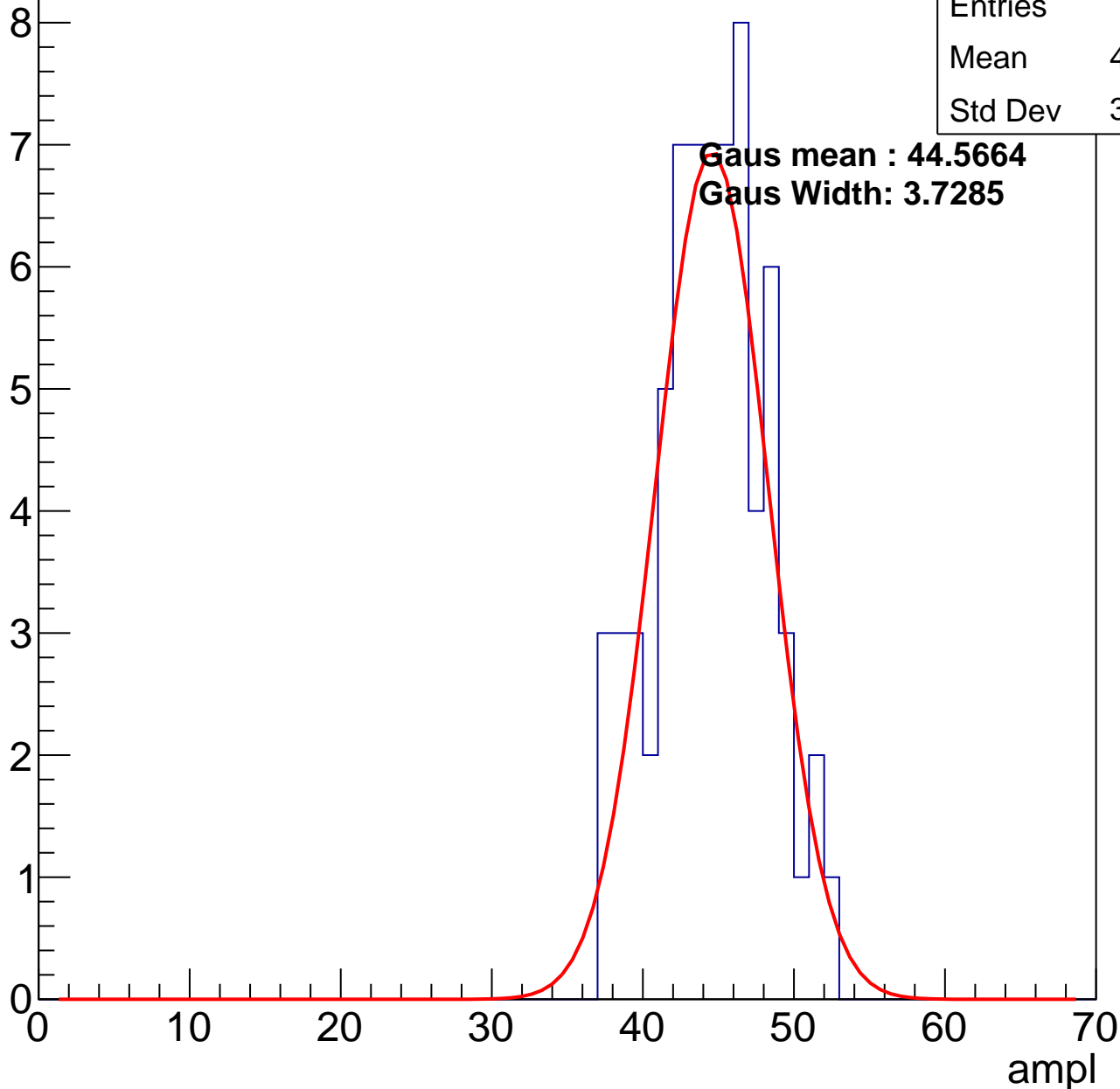
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.06
Std Dev	3.583

Gaus mean : 44.5664

Gaus Width: 3.7285

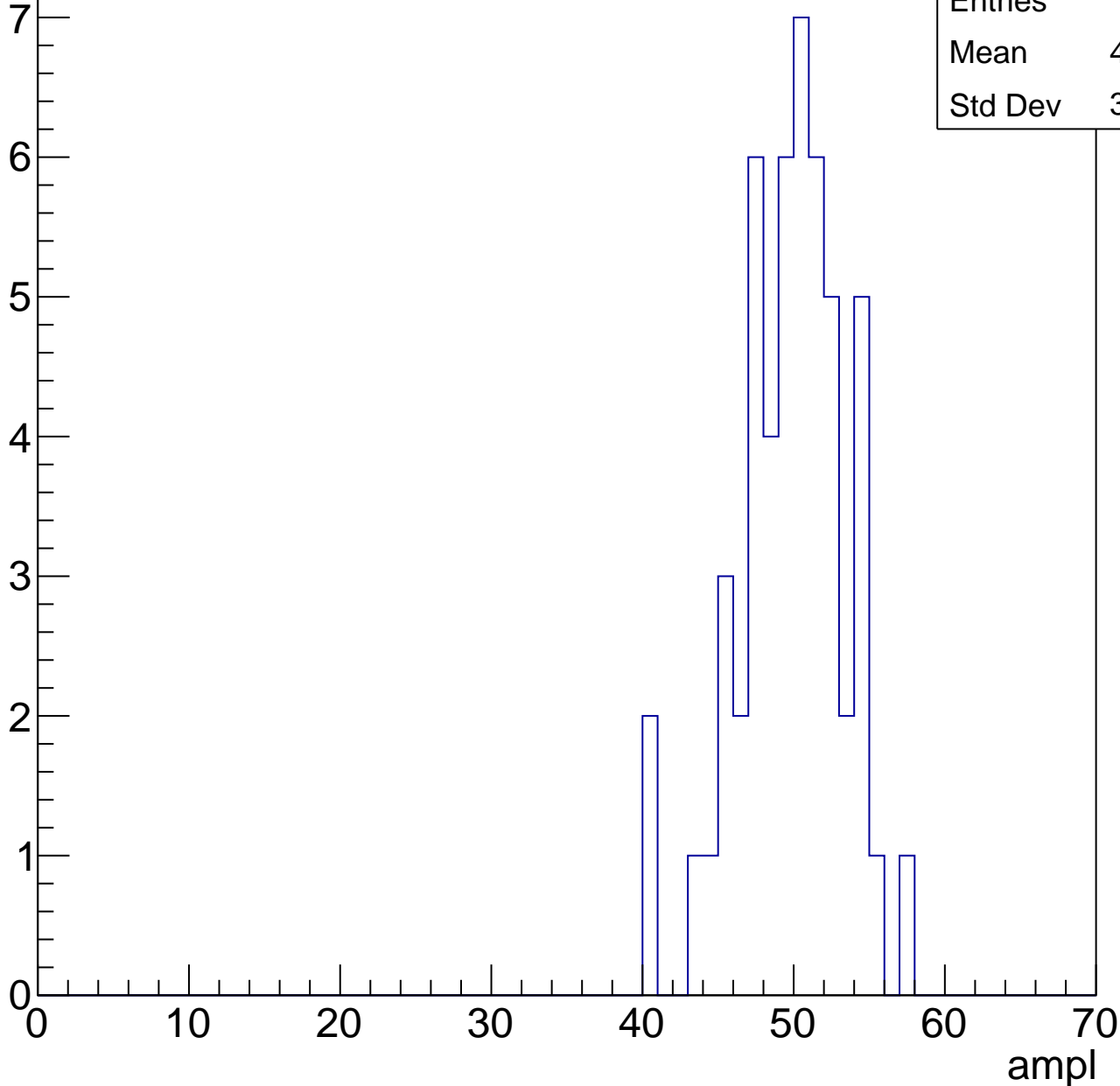


B0L002S, U7-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	49.35
Std Dev	3.519

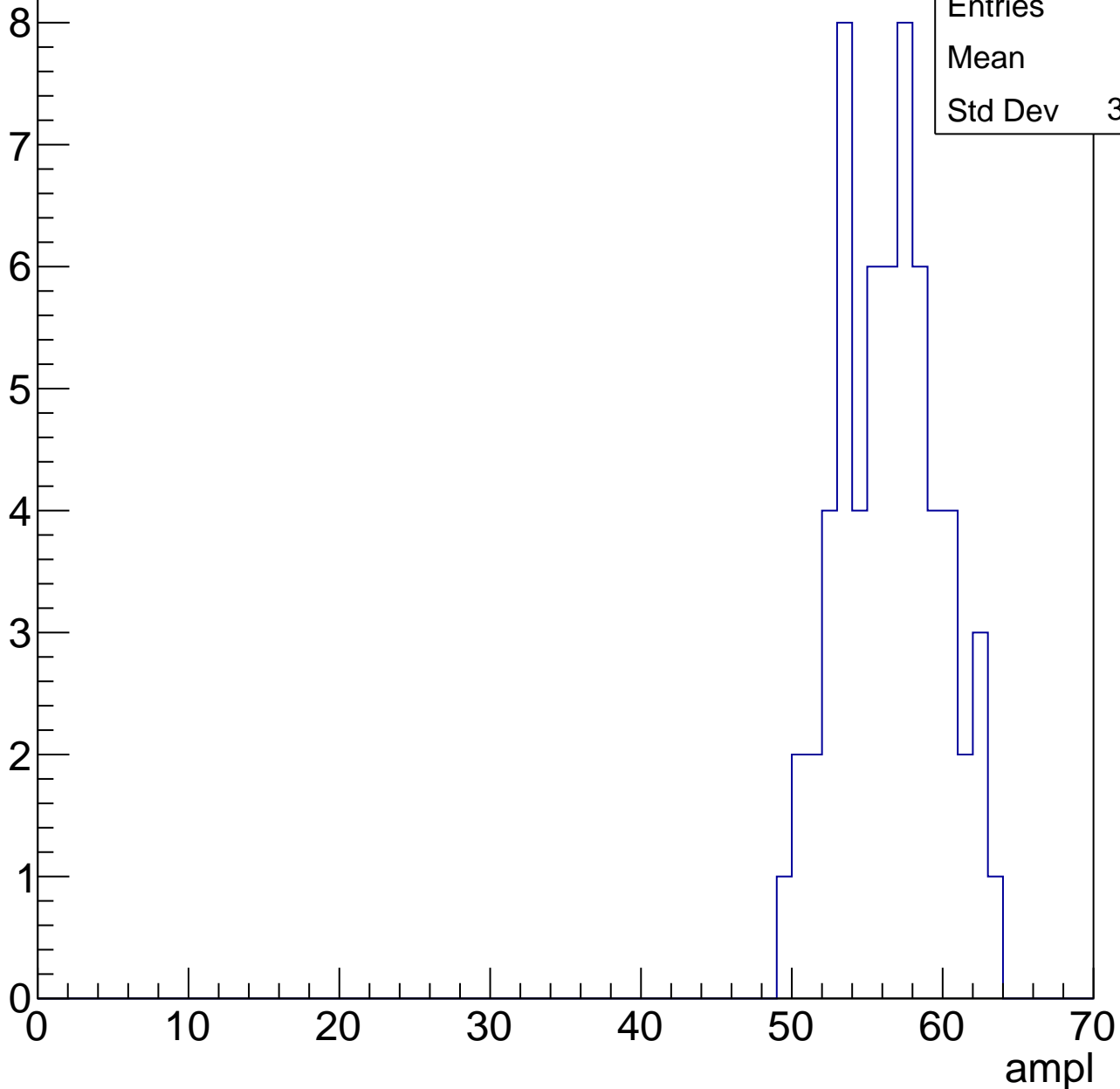


B0L002S, U7-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56
Std Dev	3.309

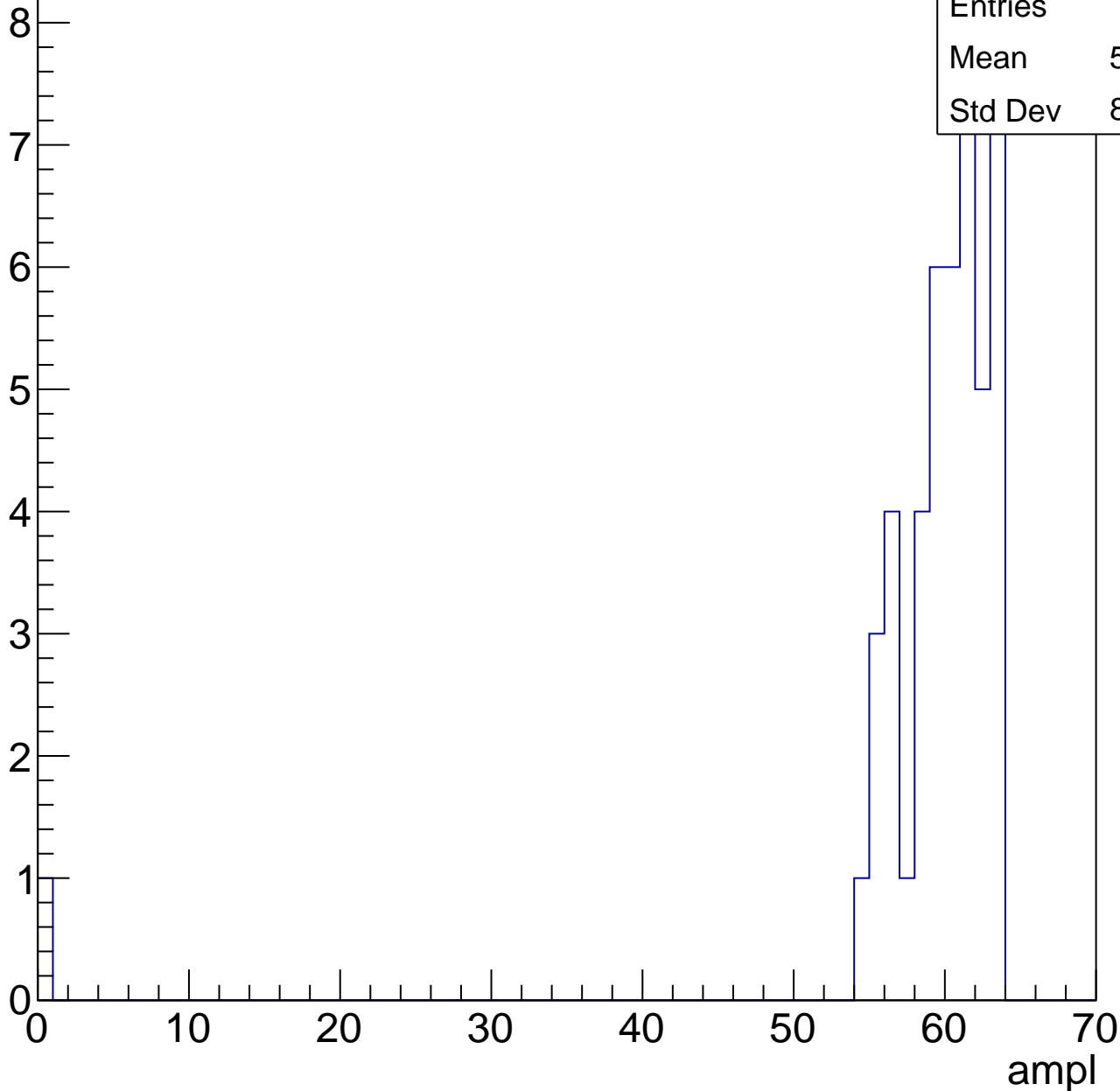


B0L002S, U7-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.47
Std Dev	8.987



B0L002S, U7-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U7-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L002S, U7-ch127, adc0

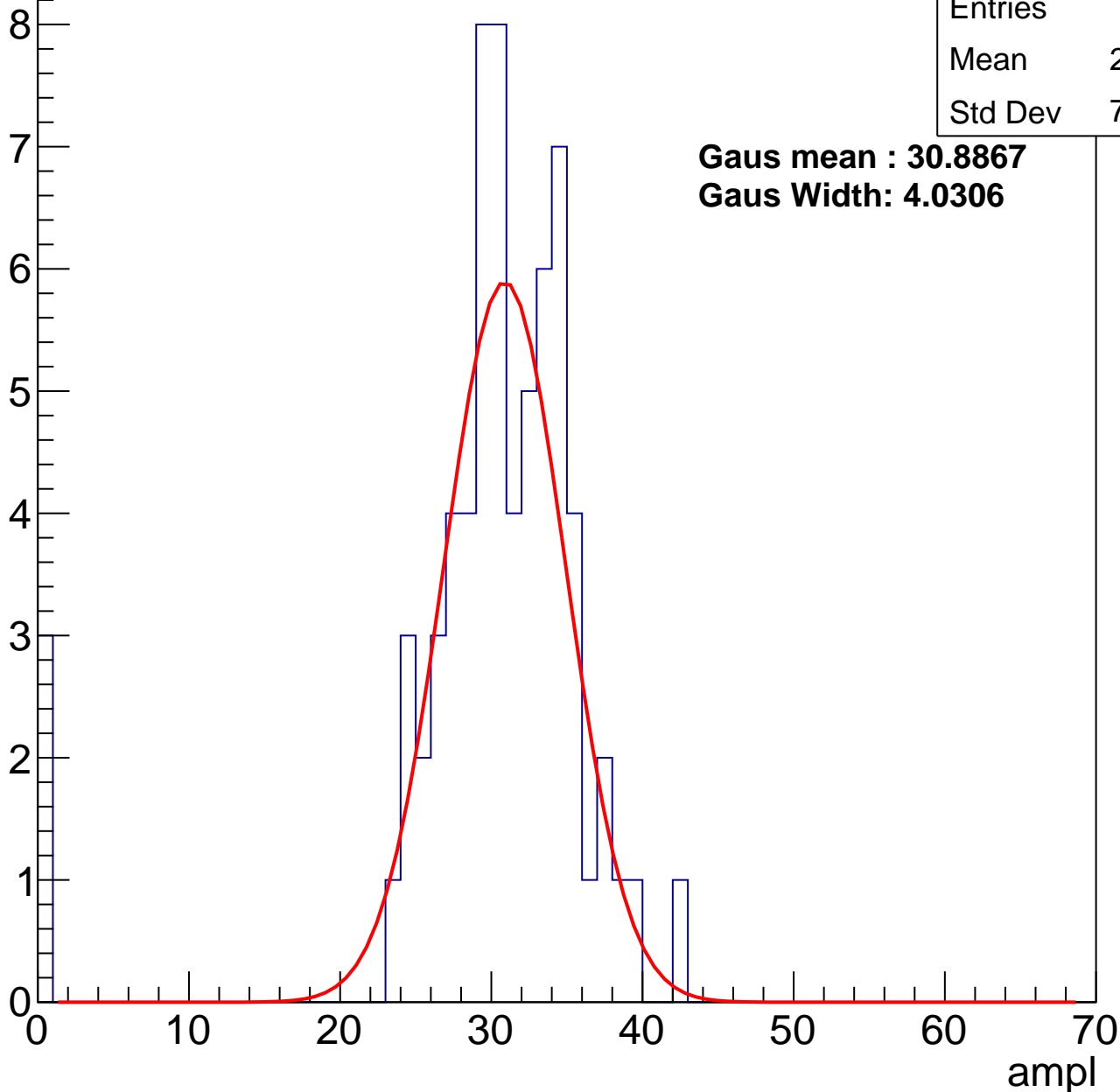
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.47
Std Dev	7.385

Gaus mean : 30.8867

Gaus Width: 4.0306



B0L002S, U7-ch127, adc1

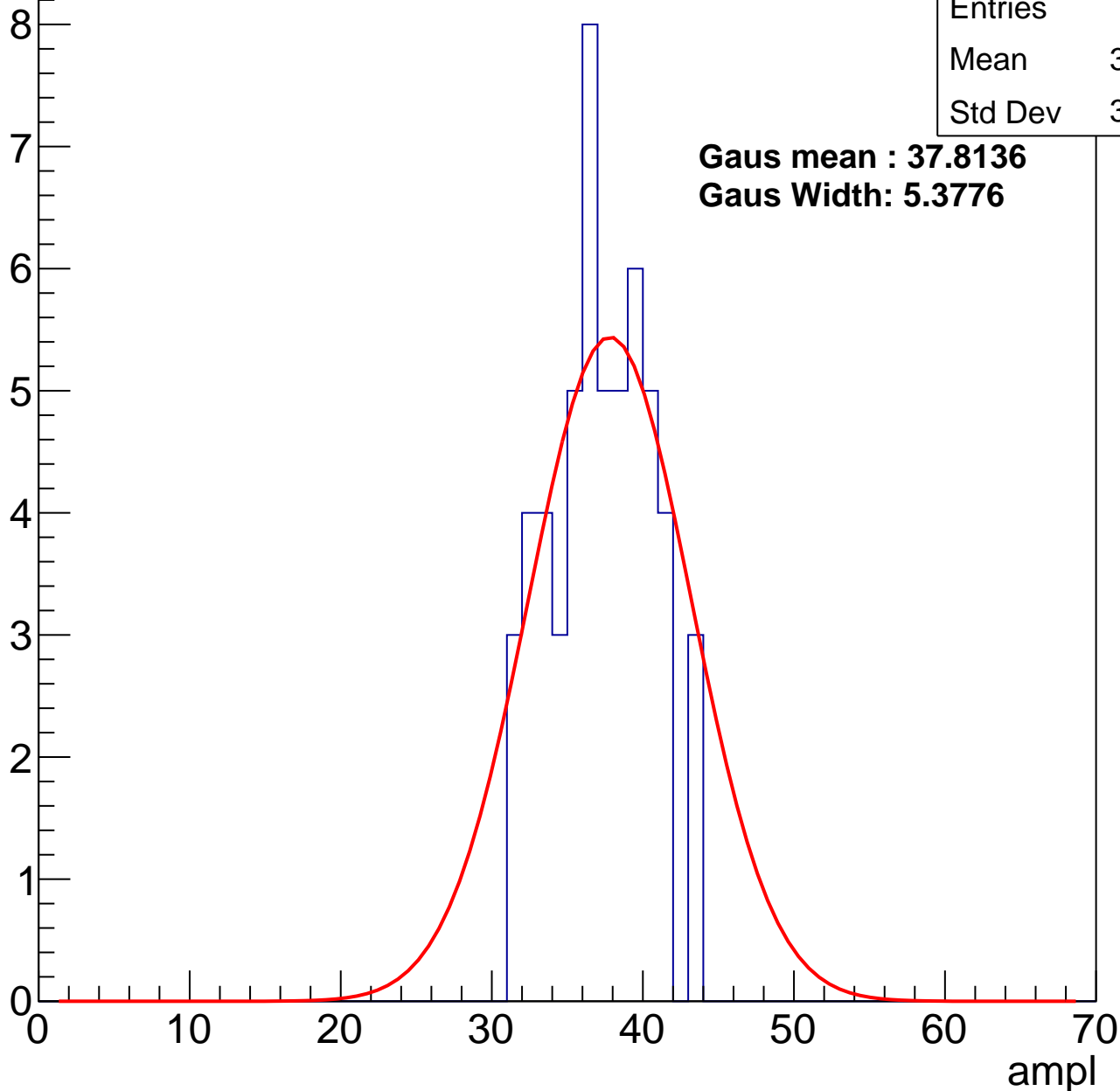
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.73
Std Dev	3.216

Gaus mean : 37.8136

Gaus Width: 5.3776



B0L002S, U7-ch127, adc2

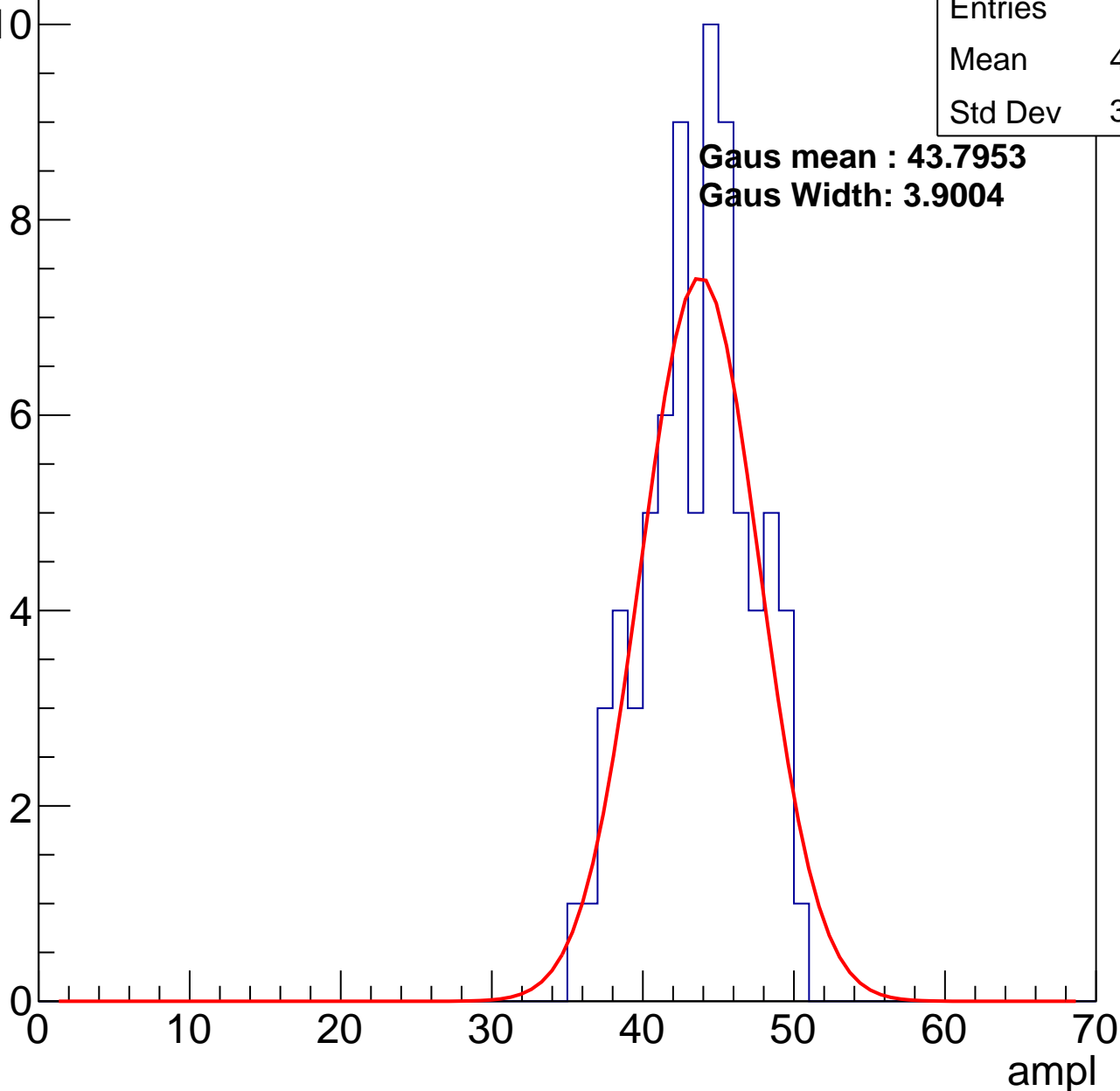
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.19
Std Dev	3.497

Gaus mean : 43.7953

Gaus Width: 3.9004



B0L002S, U7-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

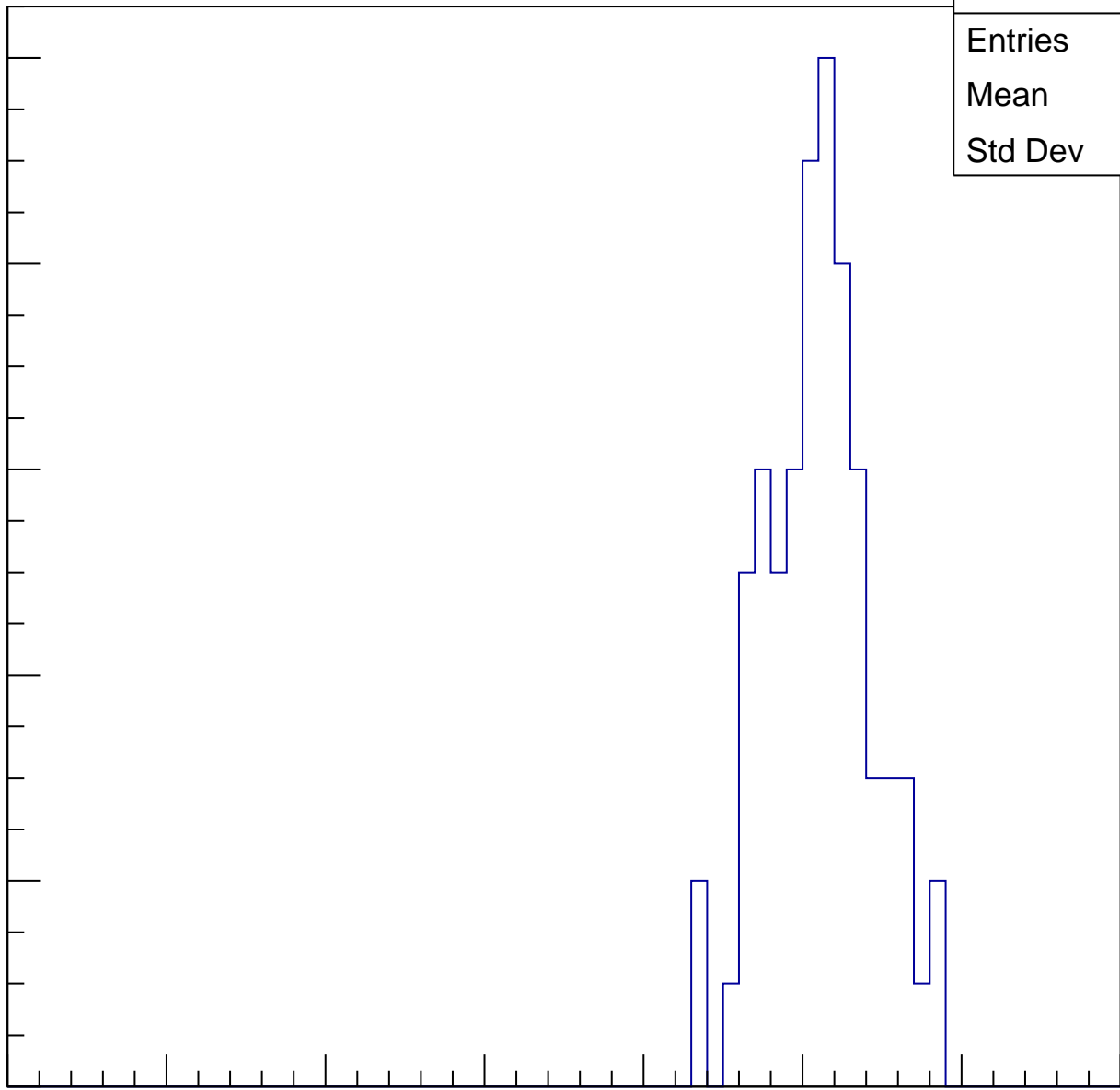
Entries	70
Mean	50.56
Std Dev	3.306

Entry

10
8
6
4
2
0

ampl

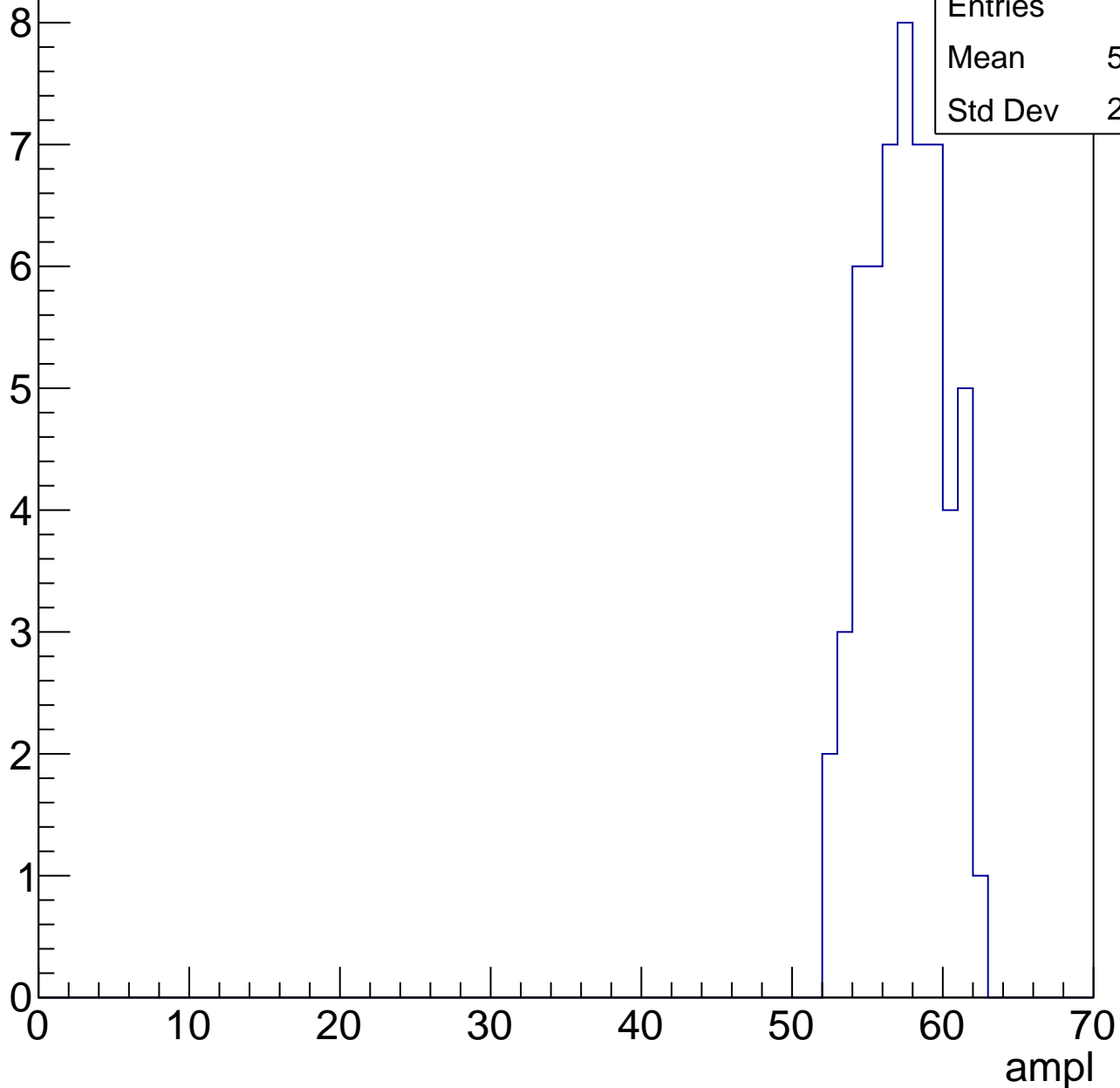
0 10 20 30 40 50 60 70



B0L002S, U7-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

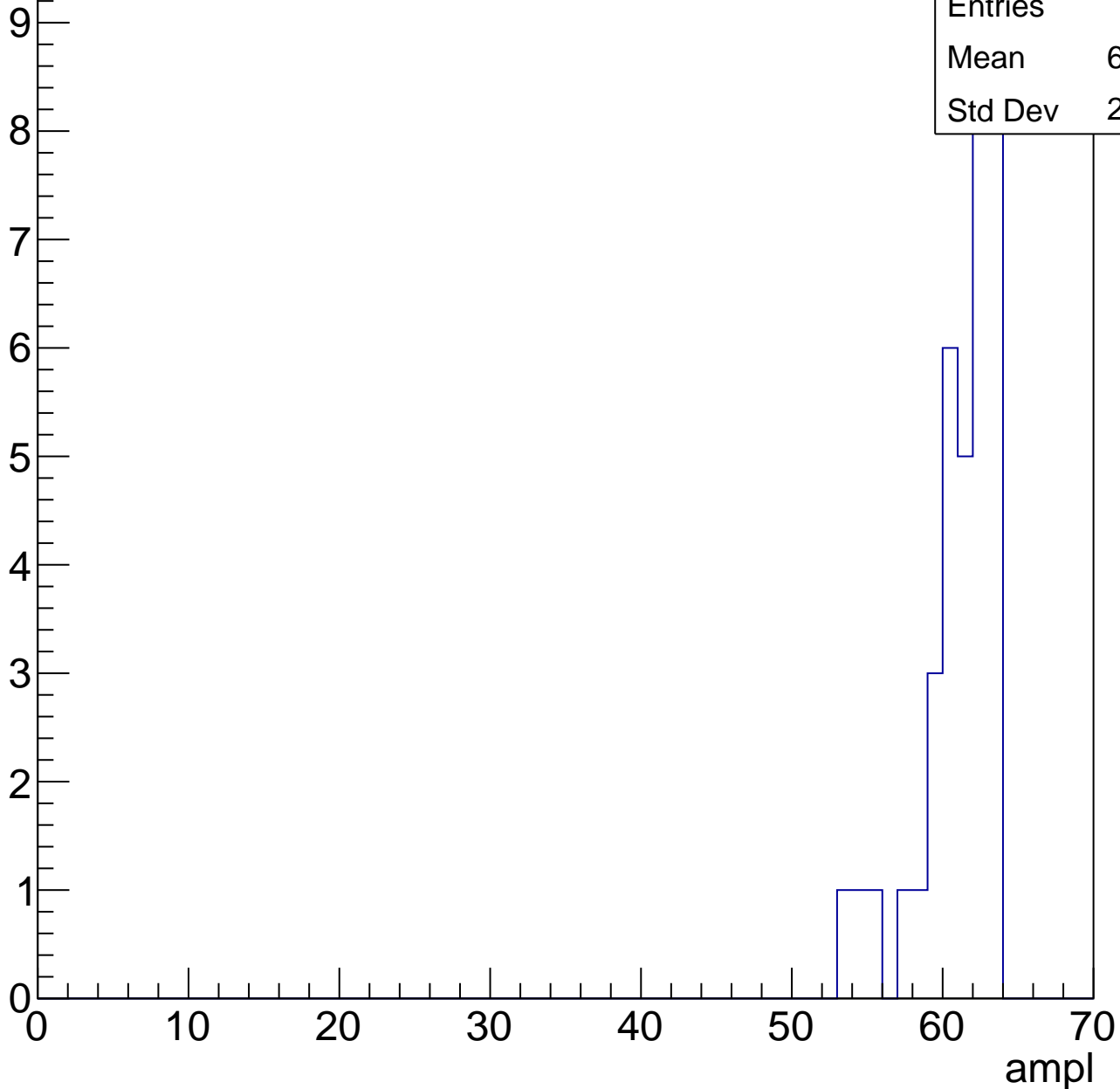


B0L002S, U7-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.65
Std Dev	2.506



B0L002S, U7-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

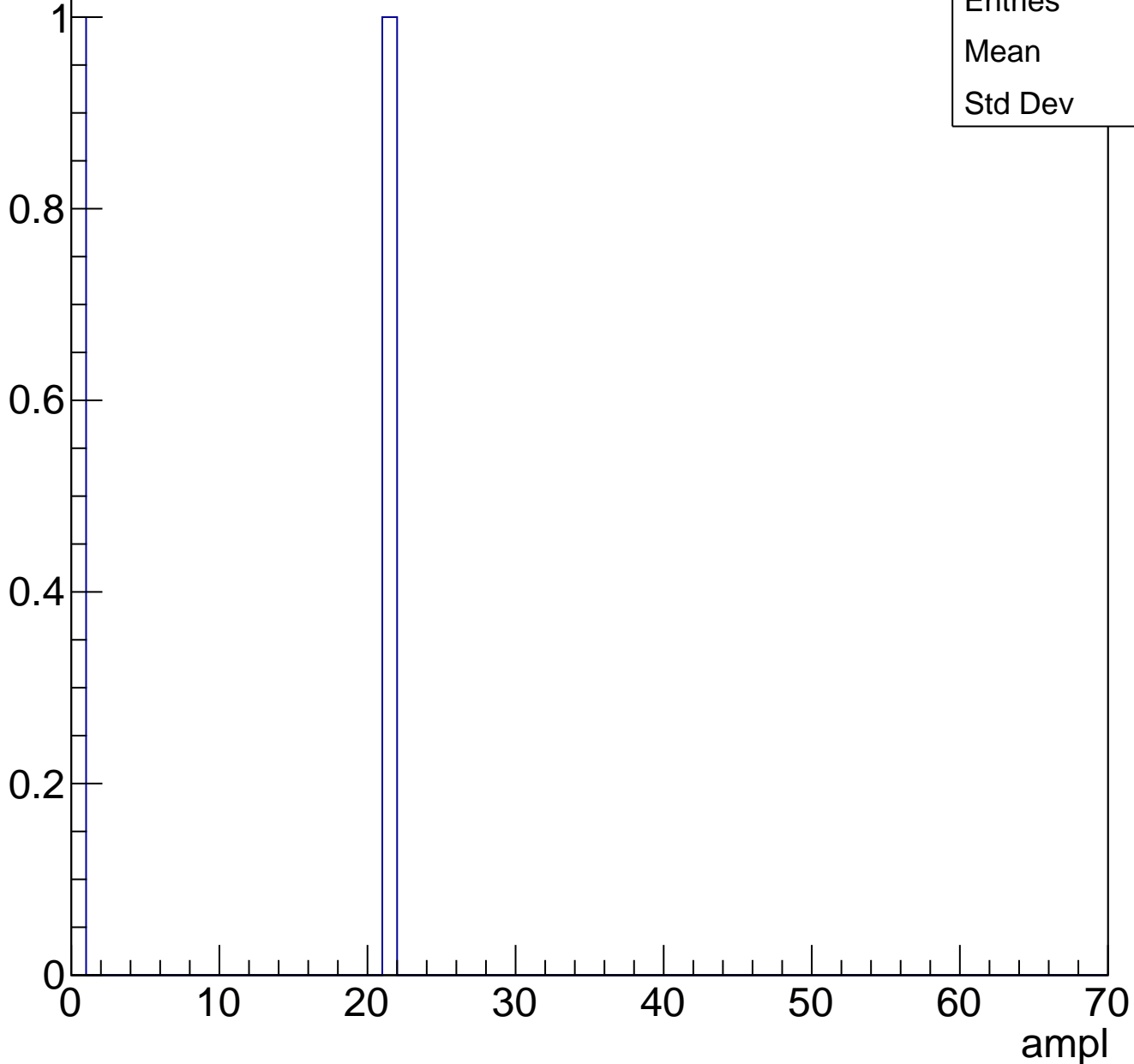
Entry



B0L002S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5