

B1L100S, U9-ch0, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 93 |
| Mean | 28.46 |
| Std Dev | 6.202 |

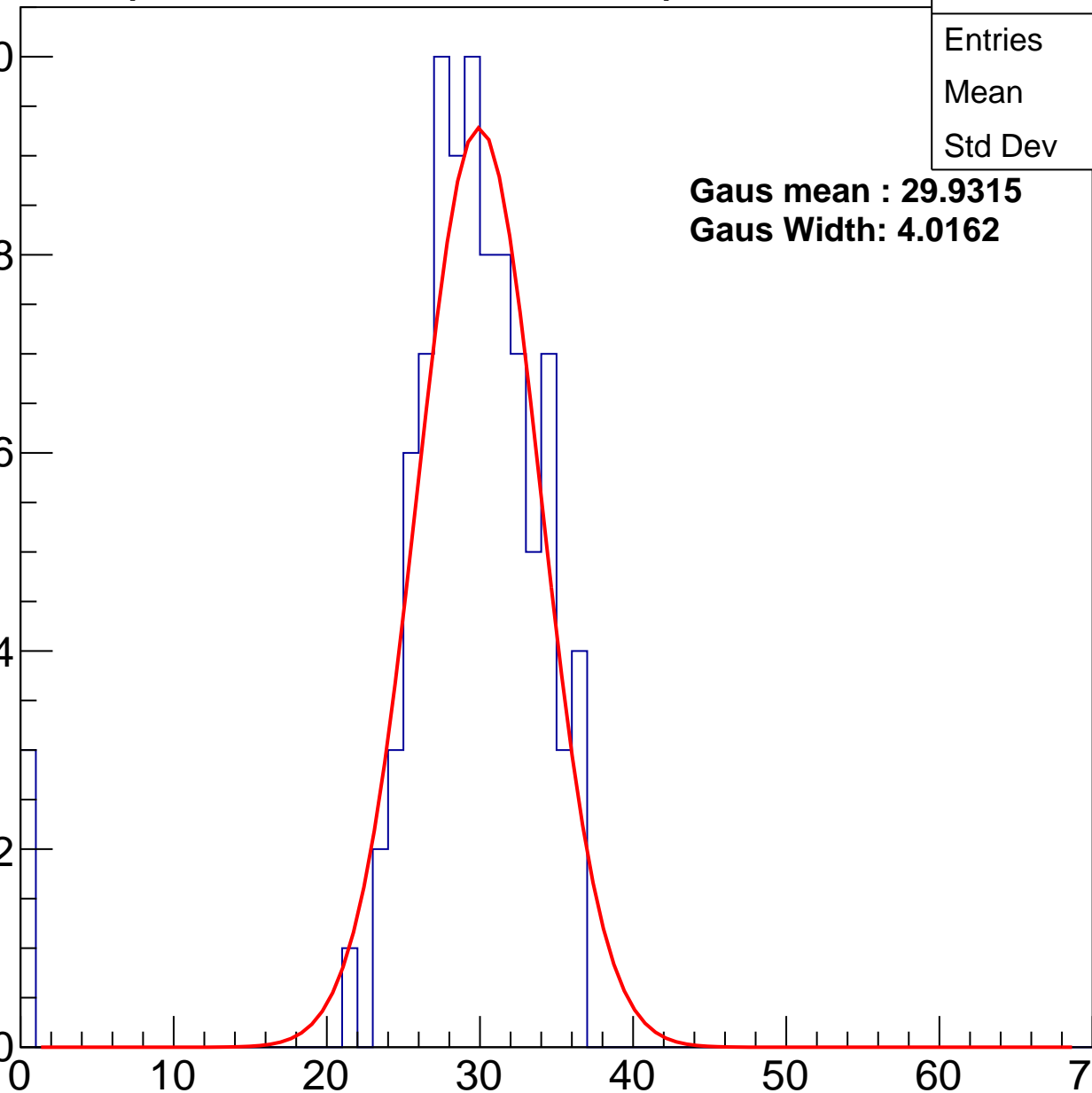
Gaus mean : 29.9315

Gaus Width: 4.0162

Entry

10
8
6
4
2
0

ampl



B1L100S, U9-ch0, adc1

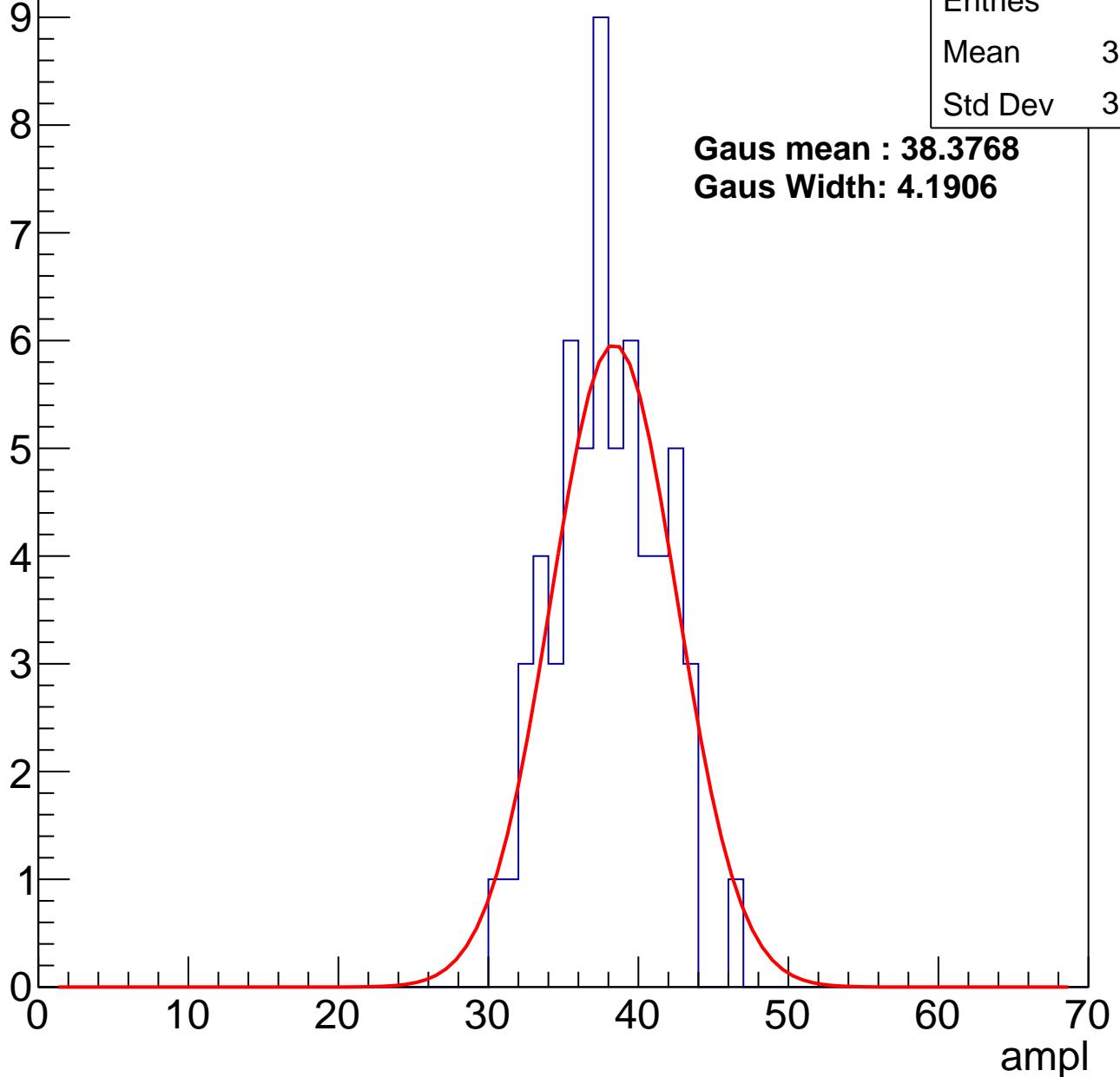
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 37.45 |
| Std Dev | 3.442 |

Gaus mean : 38.3768

Gaus Width: 4.1906



B1L100S, U9-ch0, adc2

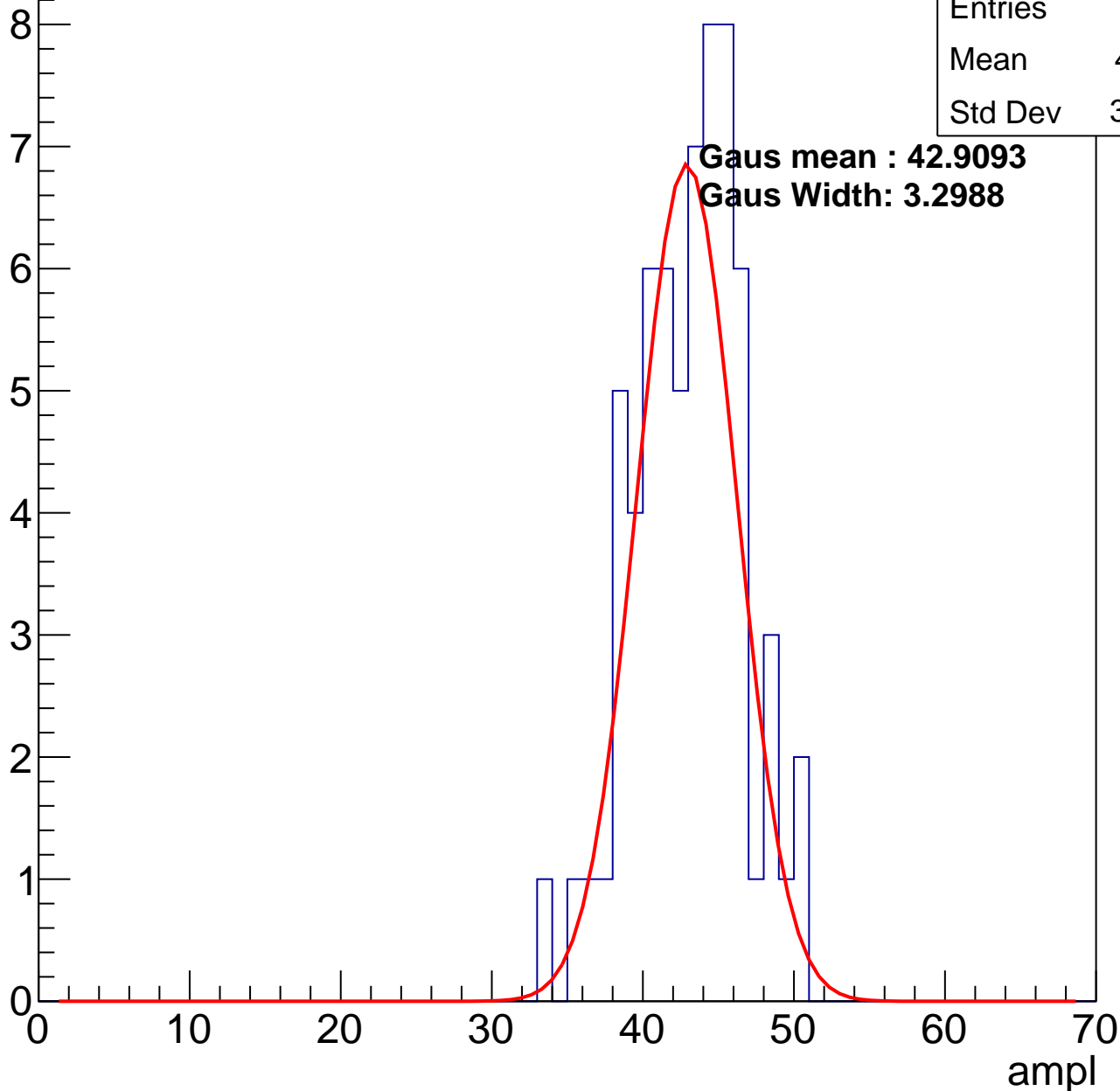
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 42.61 |
| Std Dev | 3.546 |

Gaus mean : 42.9093

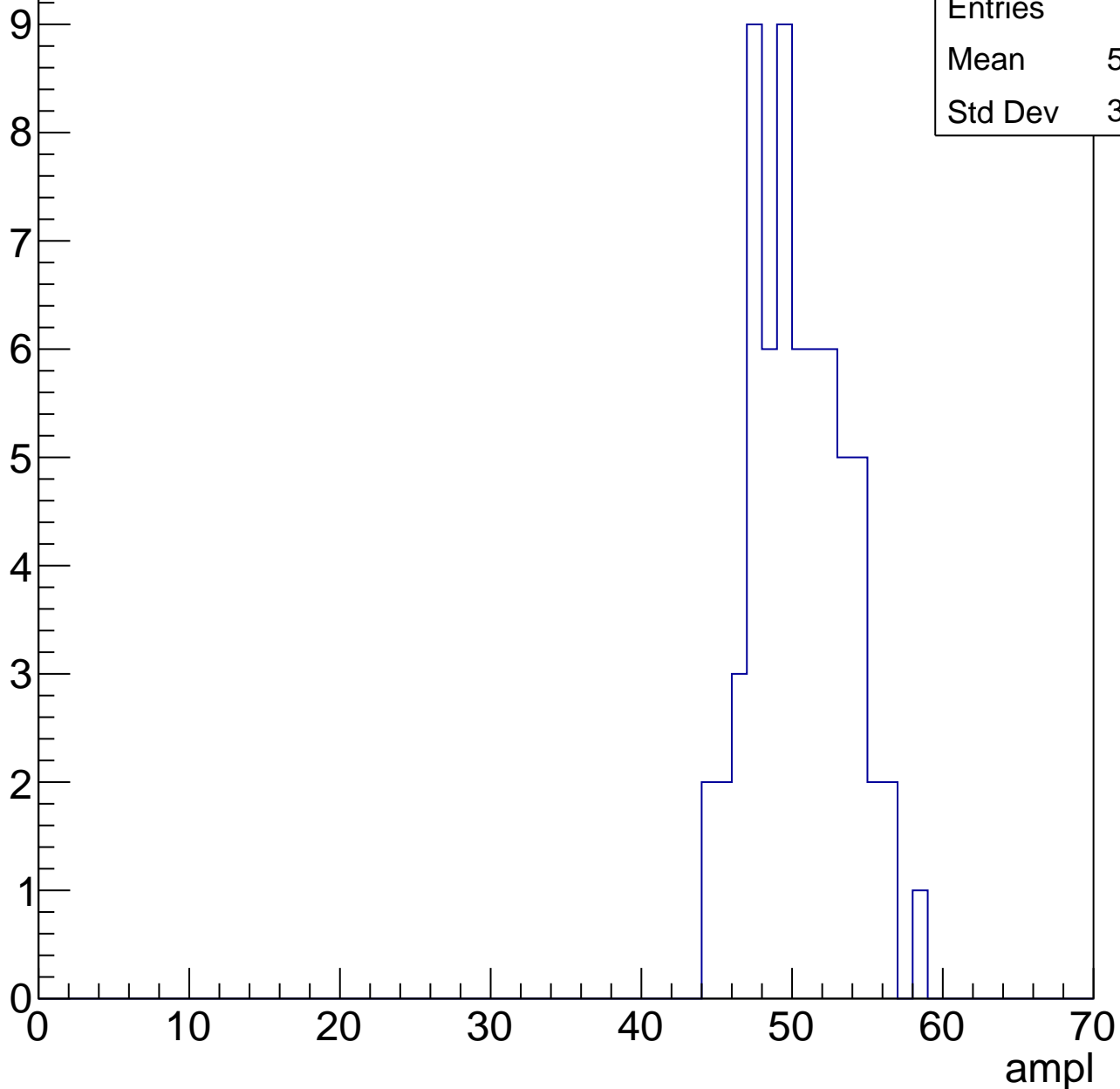
Gaus Width: 3.2988



B1L100S, U9-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



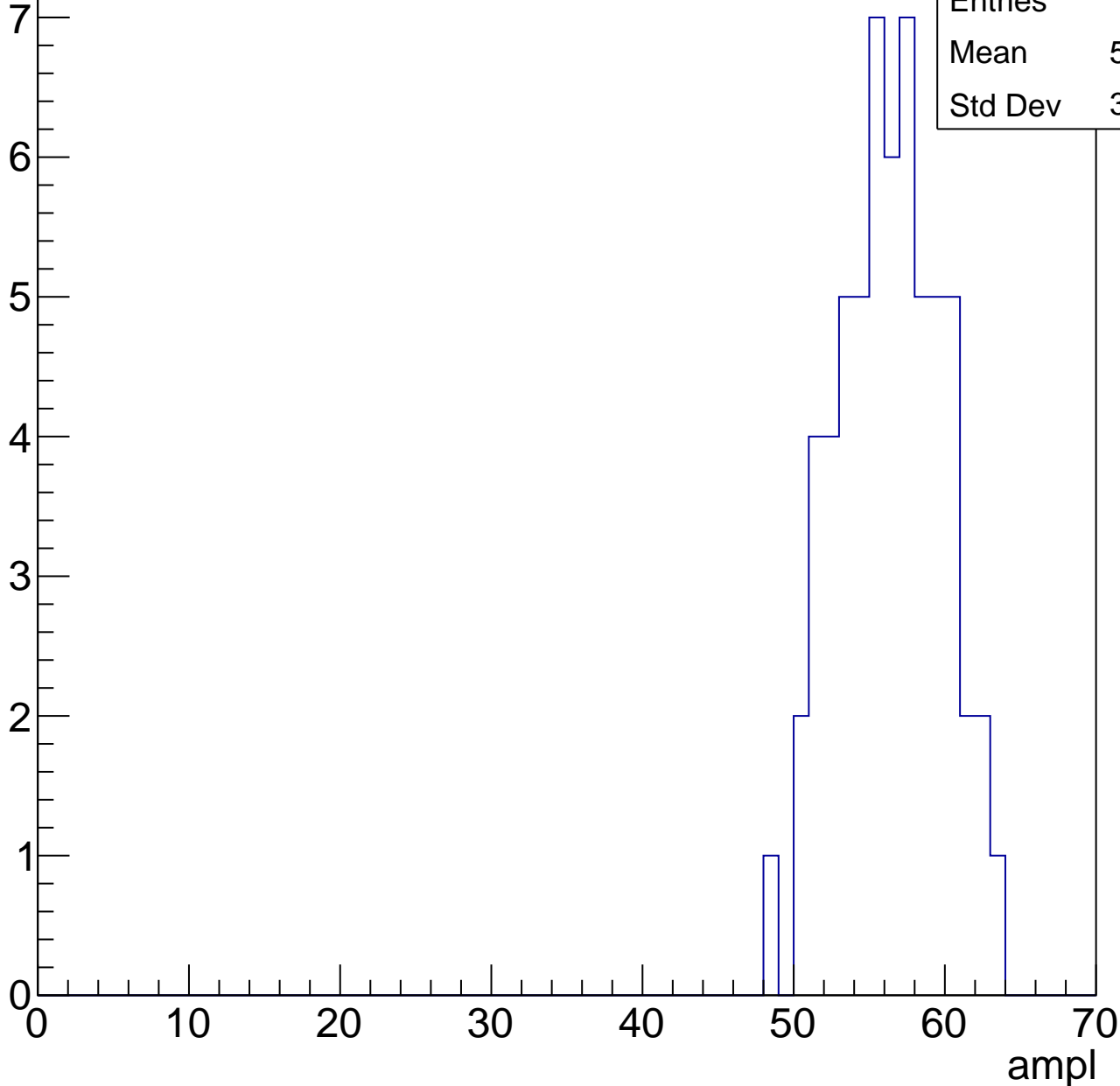
| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.02 |
| Std Dev | 3.125 |

B1L100S, U9-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.89 |
| Std Dev | 3.373 |

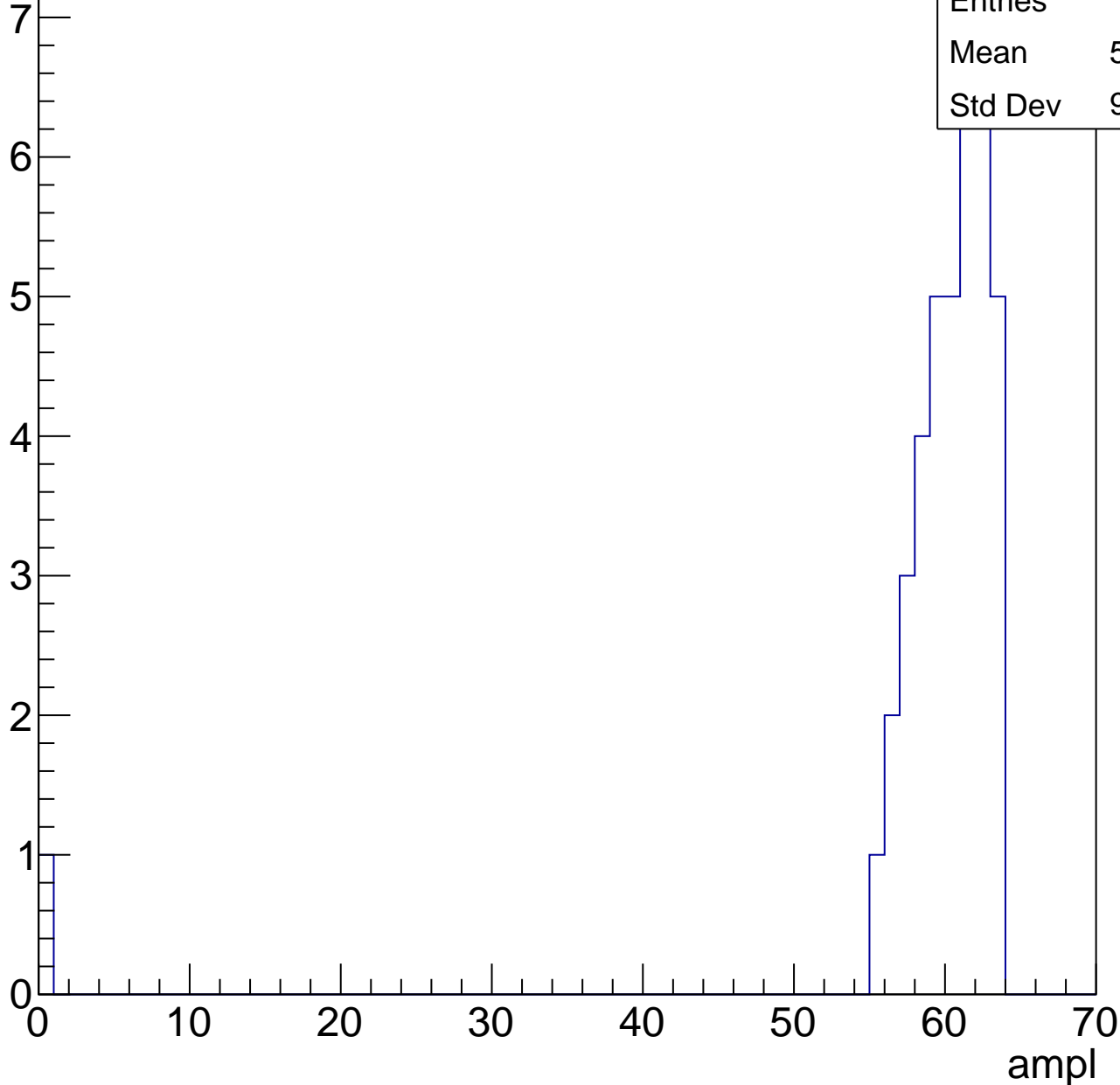


B1L100S, U9-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.52 |
| Std Dev | 9.615 |



B1L100S, U9-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

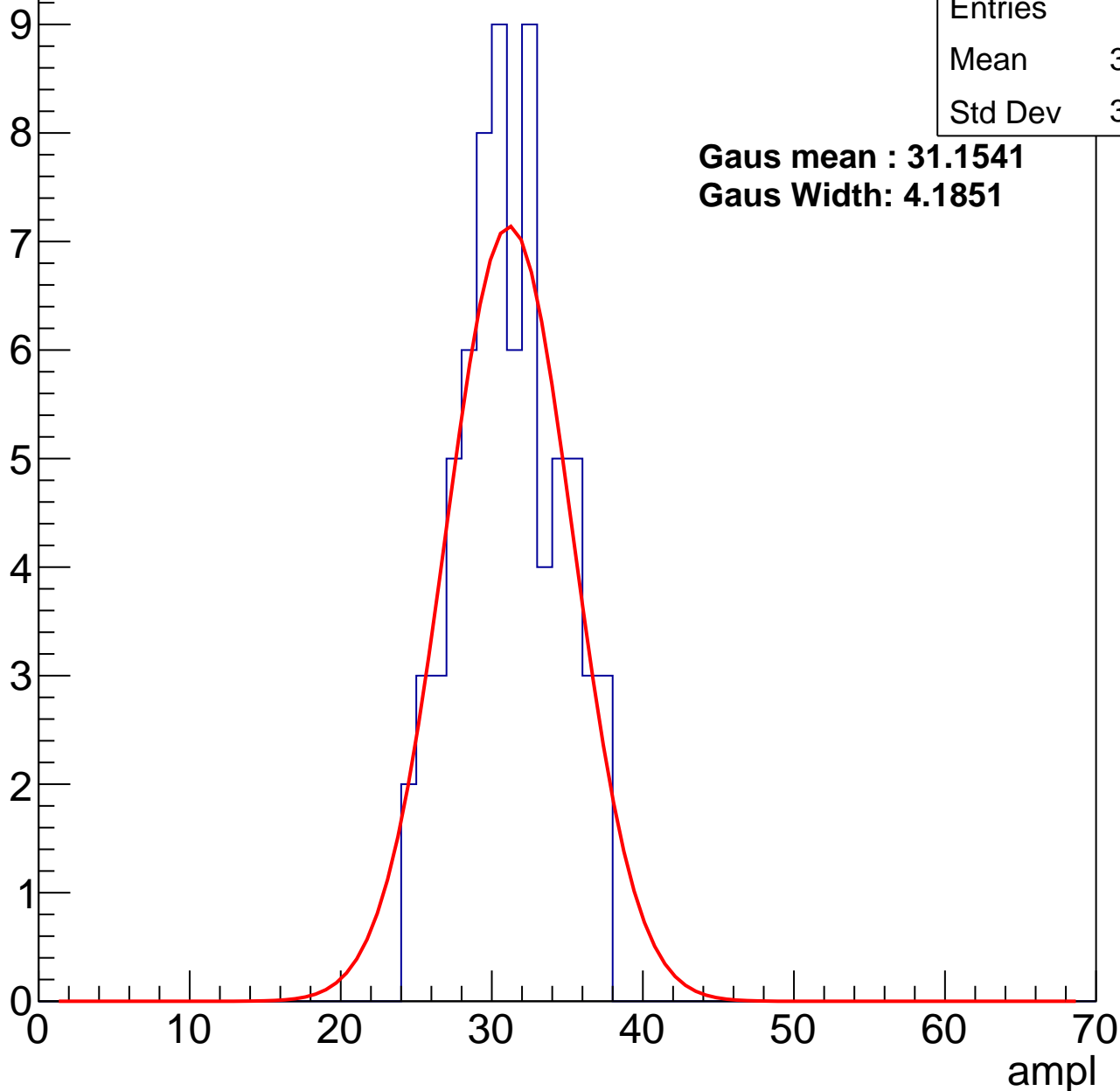


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch1, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch1, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

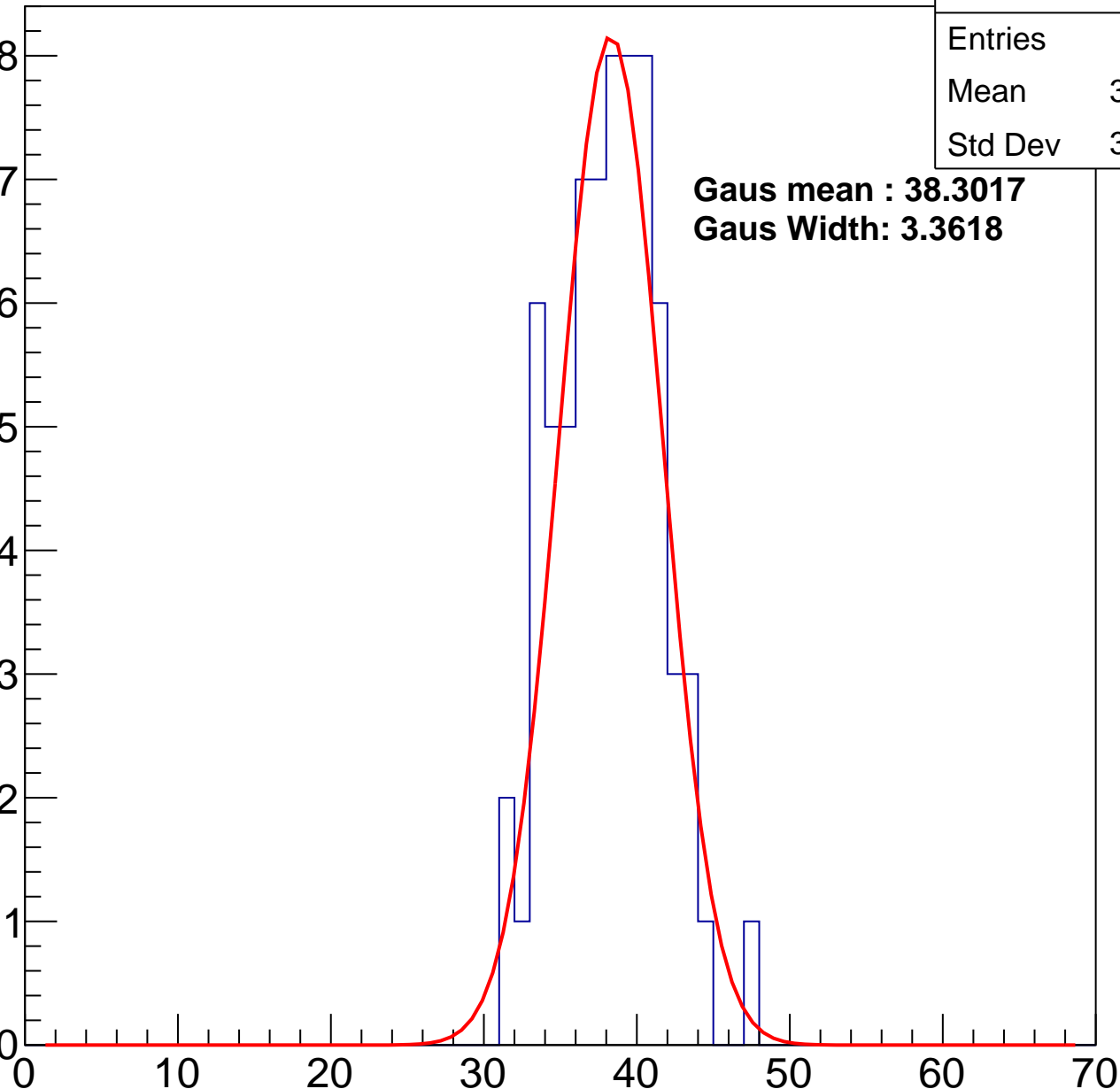
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.69 |
| Std Dev | 3.296 |

Gaus mean : 38.3017
Gaus Width: 3.3618

0
1
2
3
4
5
6
7
8

ampl



B1L100S, U9-ch1, adc2

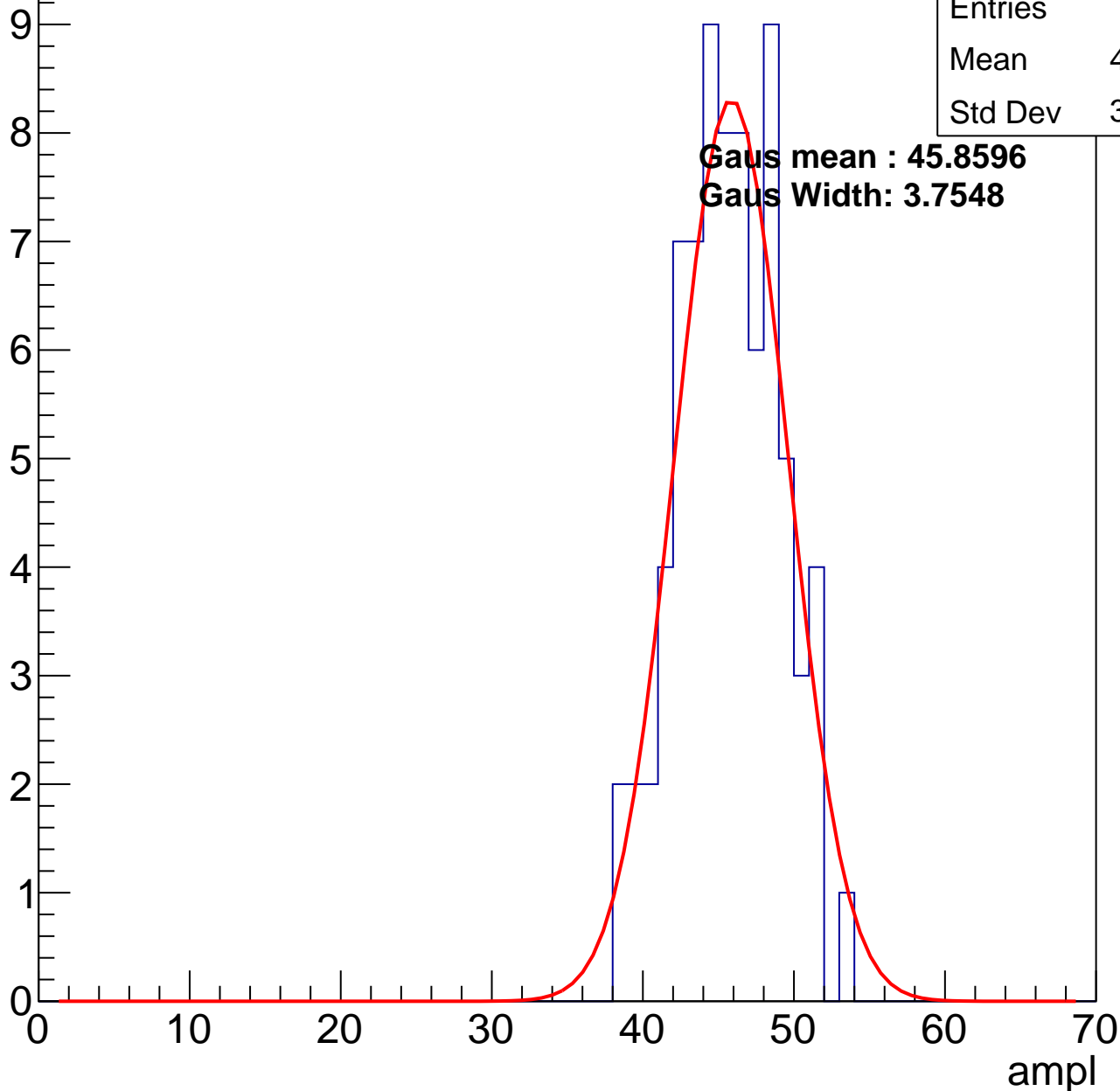
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 45.23 |
| Std Dev | 3.334 |

Gaus mean : 45.8596

Gaus Width: 3.7548

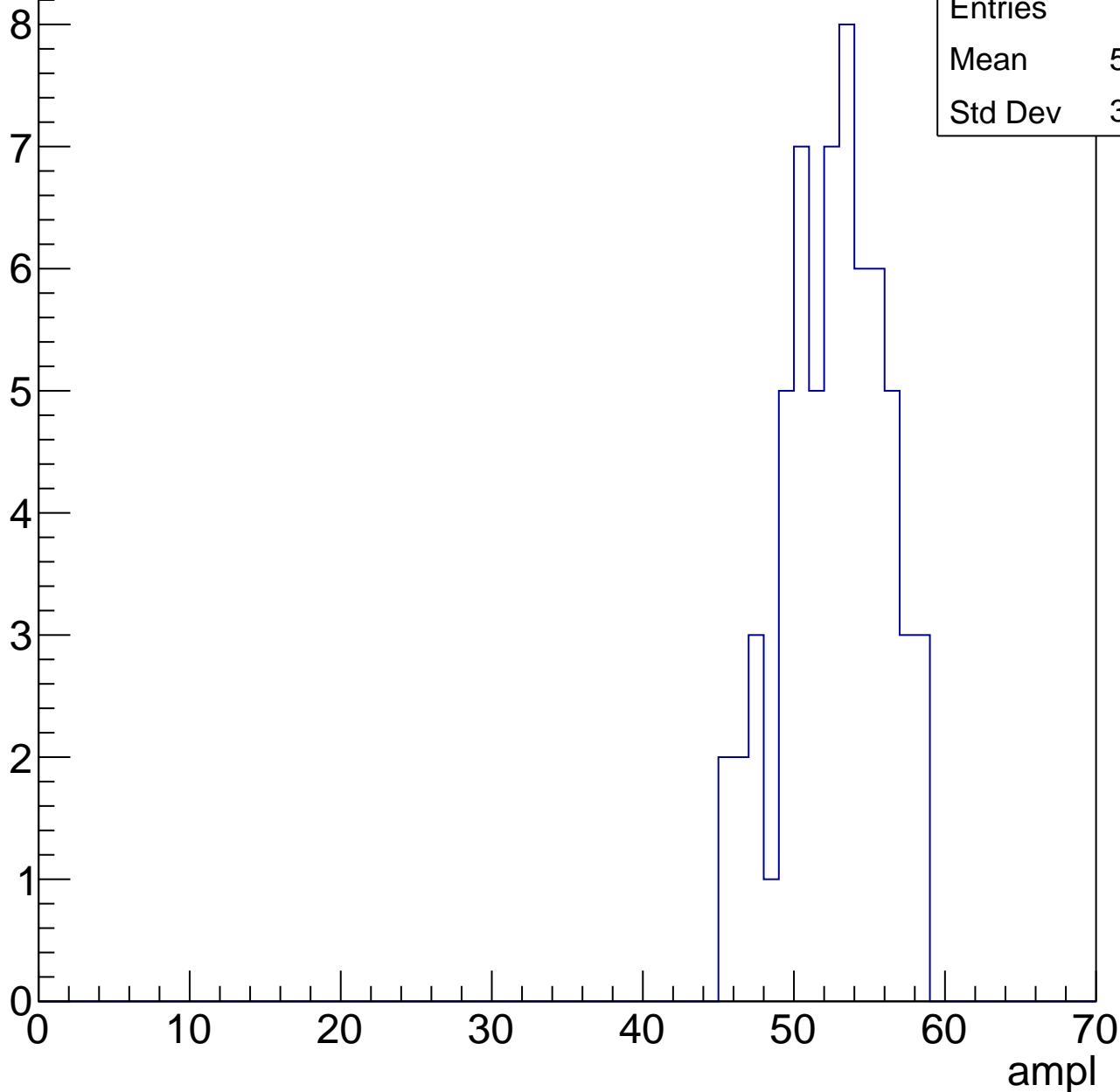


B1L100S, U9-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

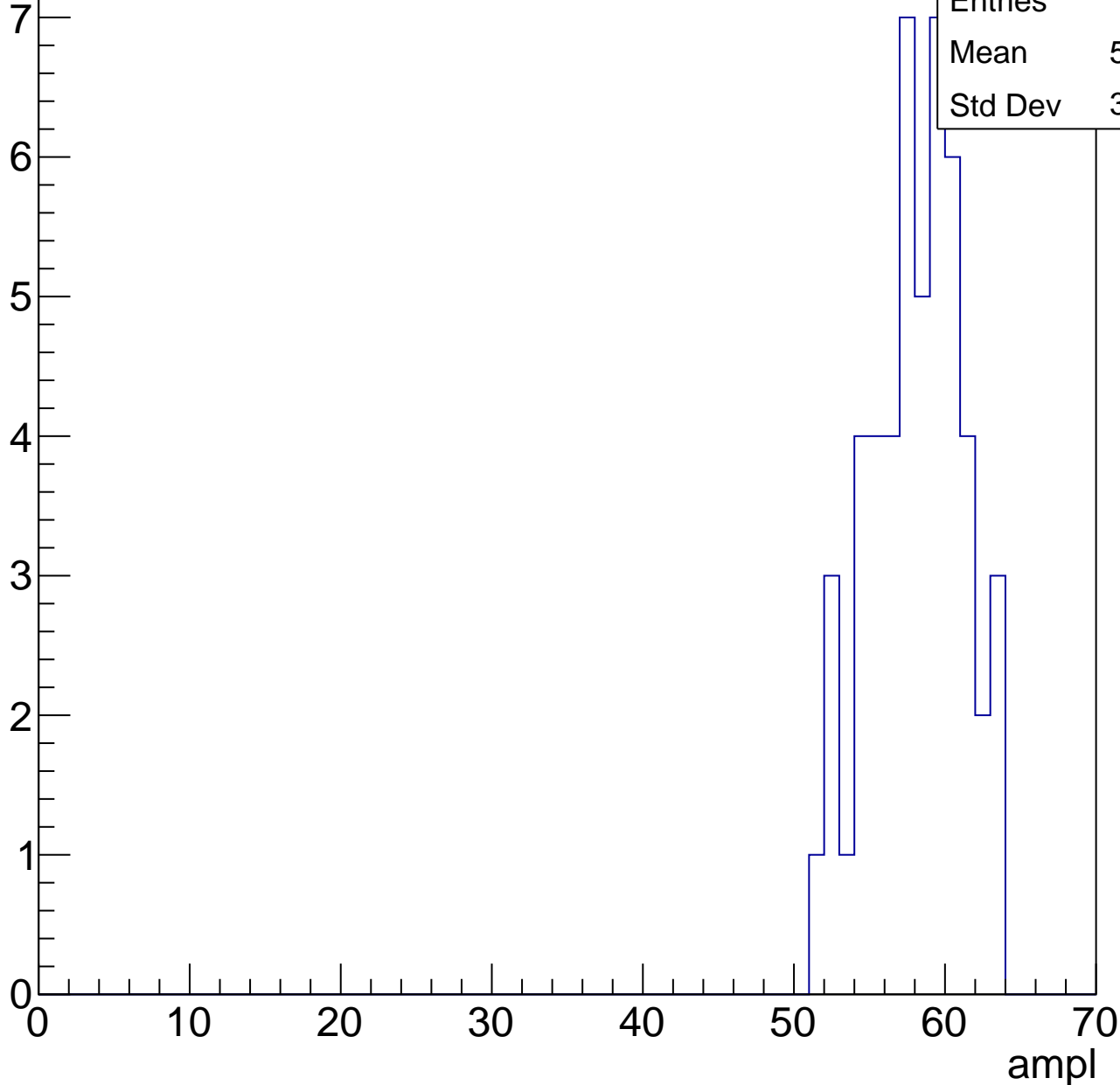
| | |
|---------|-------|
| Entries | 63 |
| Mean | 52.19 |
| Std Dev | 3.299 |



B1L100S, U9-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

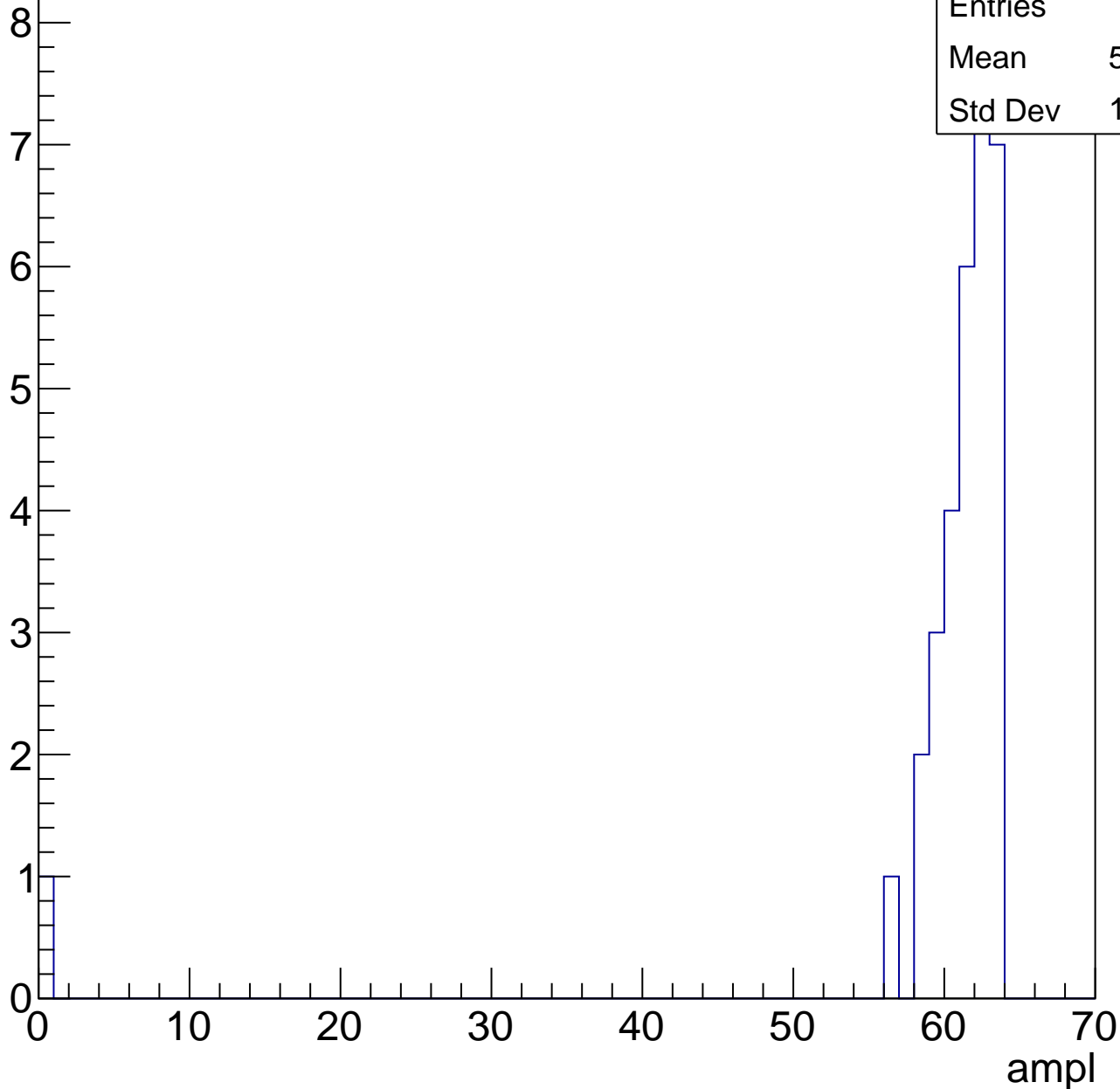


B1L100S, U9-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 59.12 |
| Std Dev | 10.76 |



B1L100S, U9-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch2, adc0

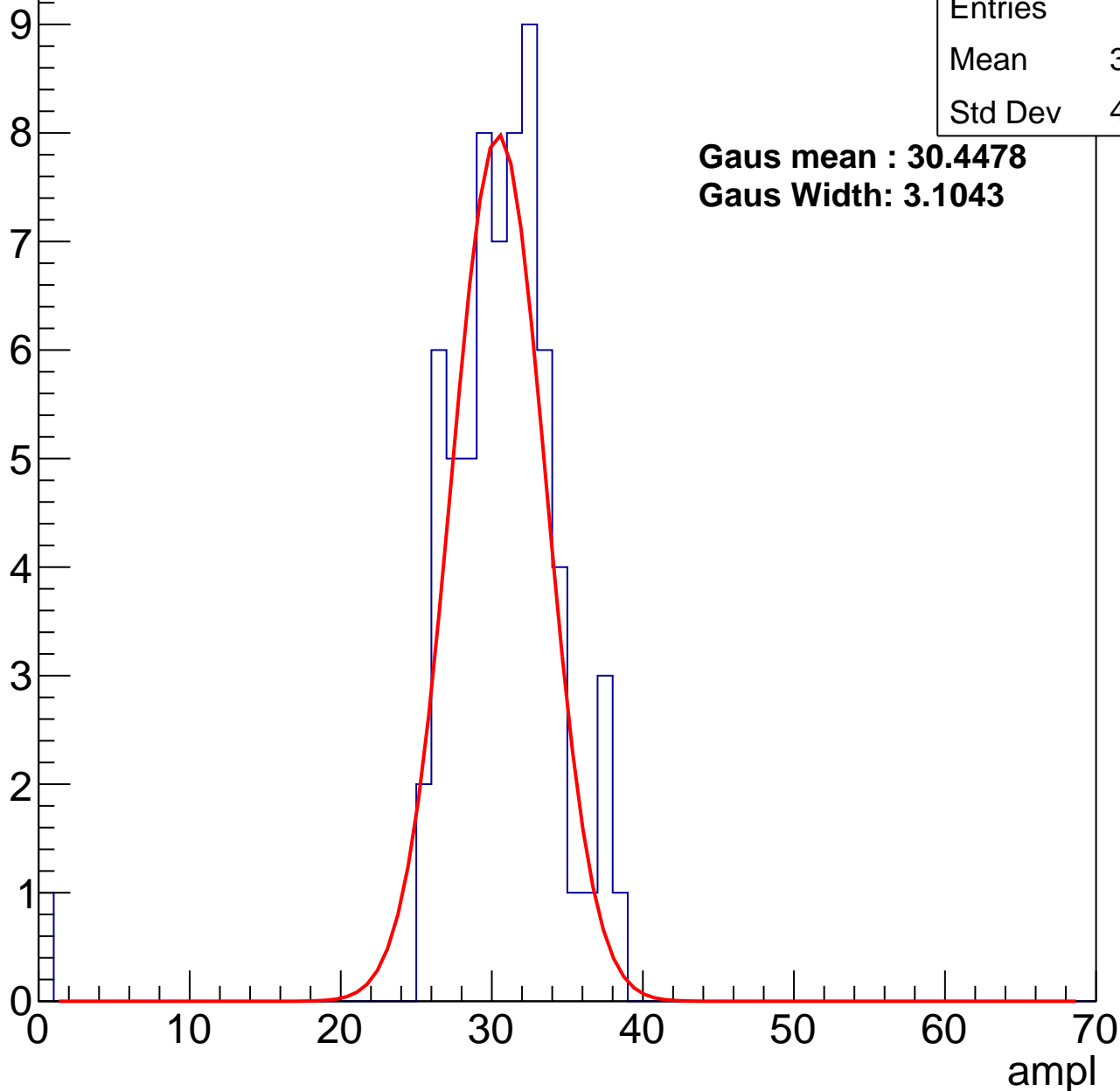
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.04 |
| Std Dev | 4.814 |

Gaus mean : 30.4478

Gaus Width: 3.1043



B1L100S, U9-ch2, adc1

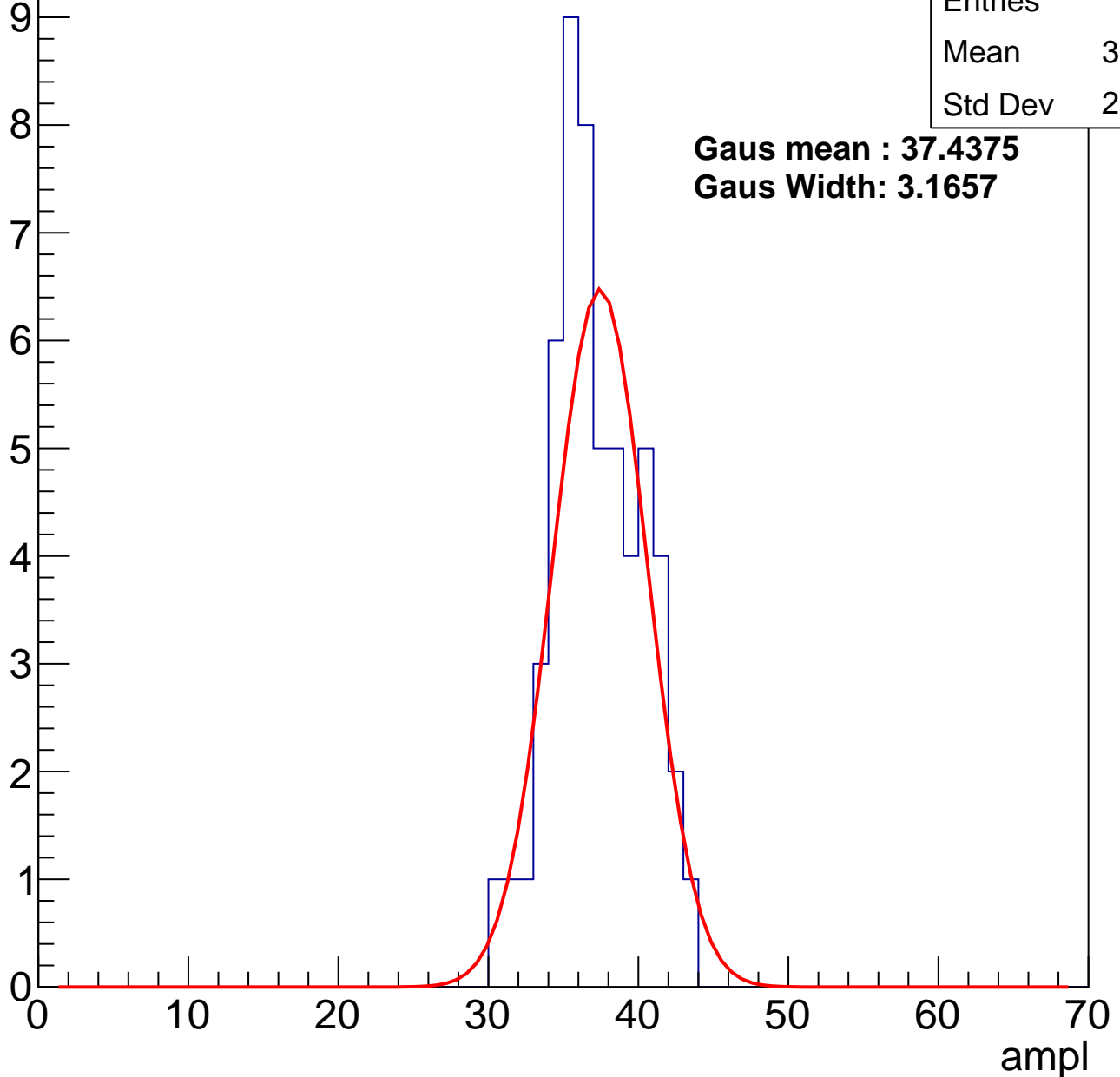
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 36.75 |
| Std Dev | 2.918 |

Gaus mean : 37.4375

Gaus Width: 3.1657



B1L100S, U9-ch2, adc2

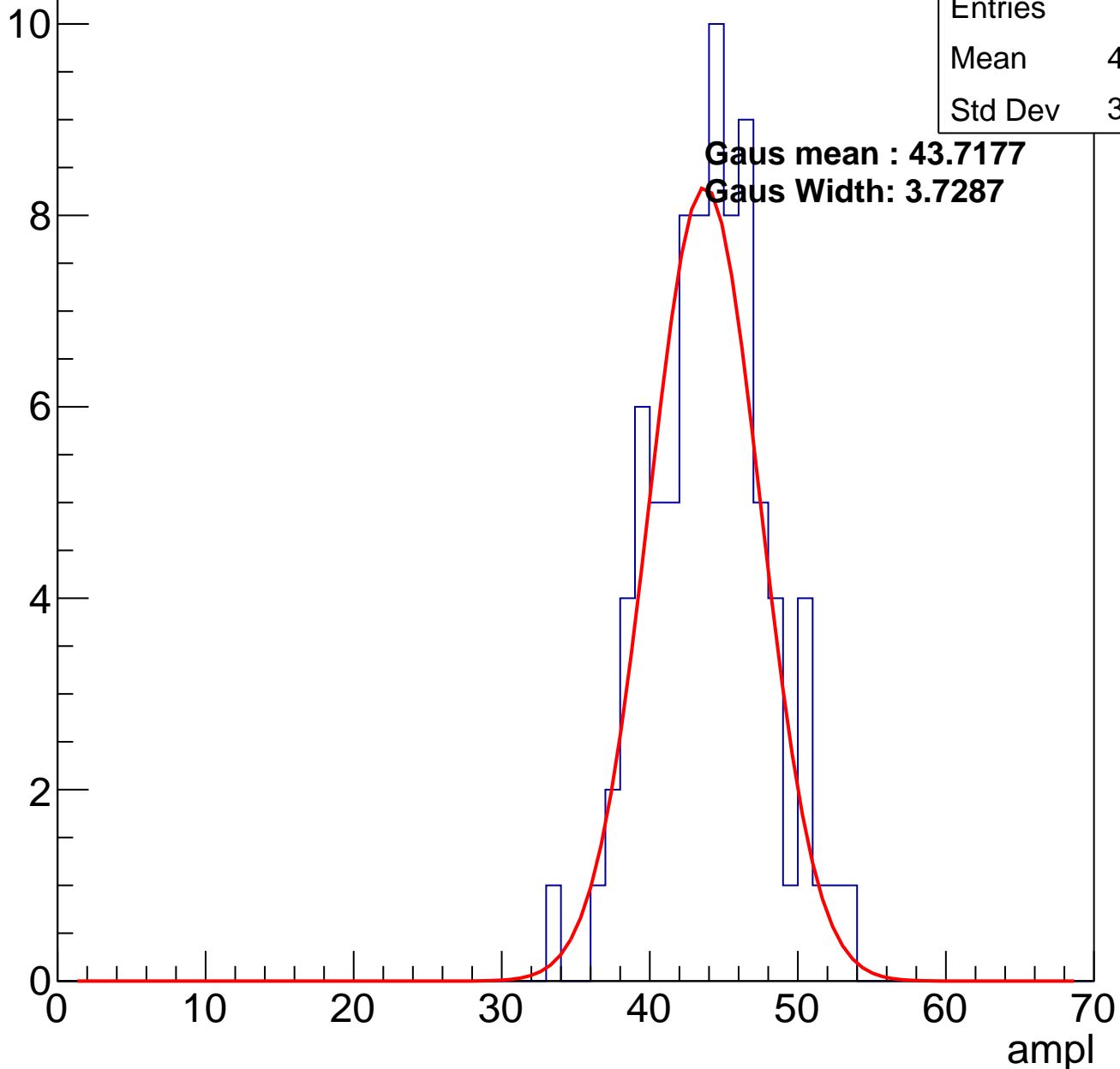
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 84 |
| Mean | 43.57 |
| Std Dev | 3.846 |

Gaus mean : 43.7177

Gaus Width: 3.7287

Entry

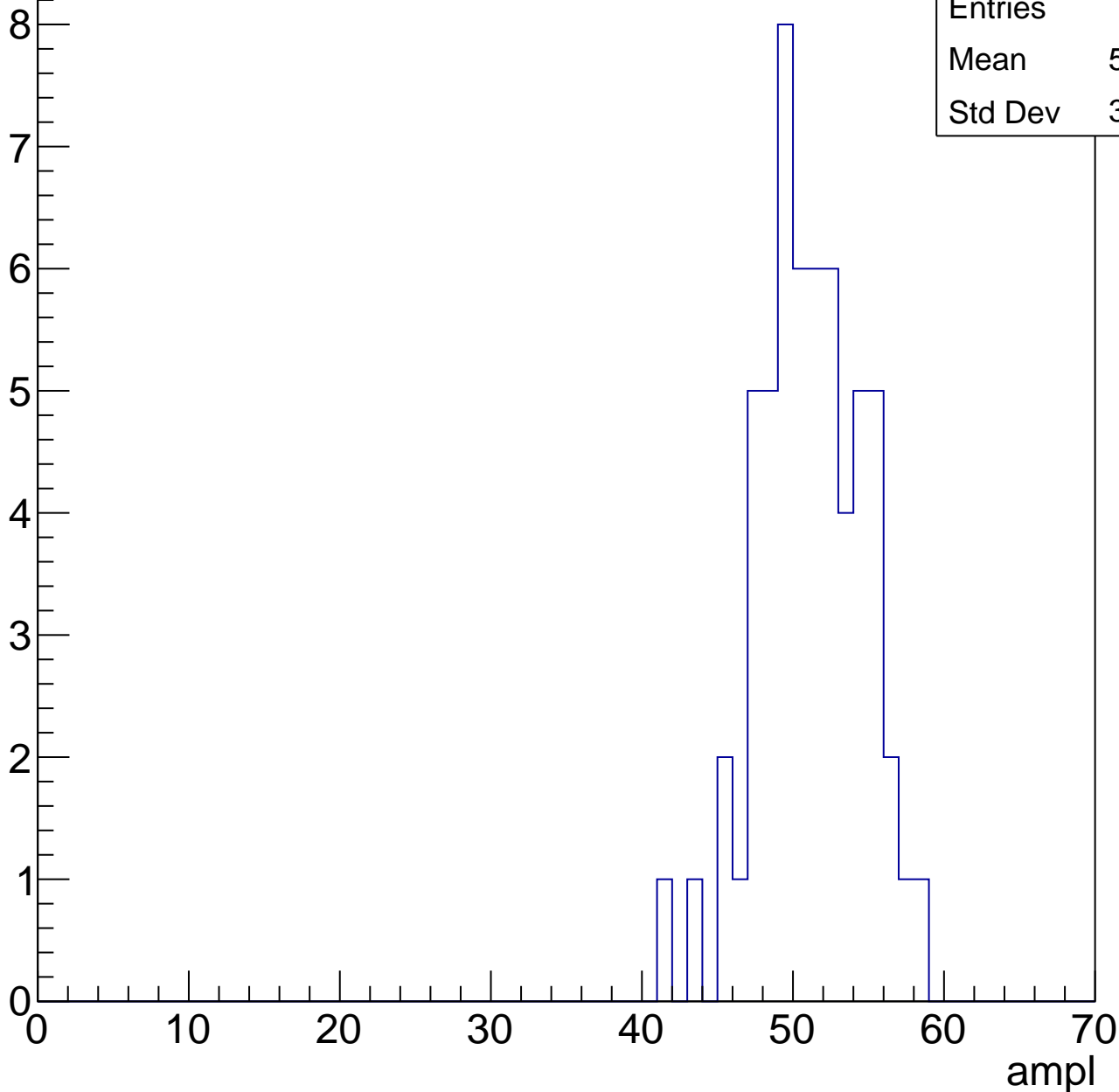


B1L100S, U9-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

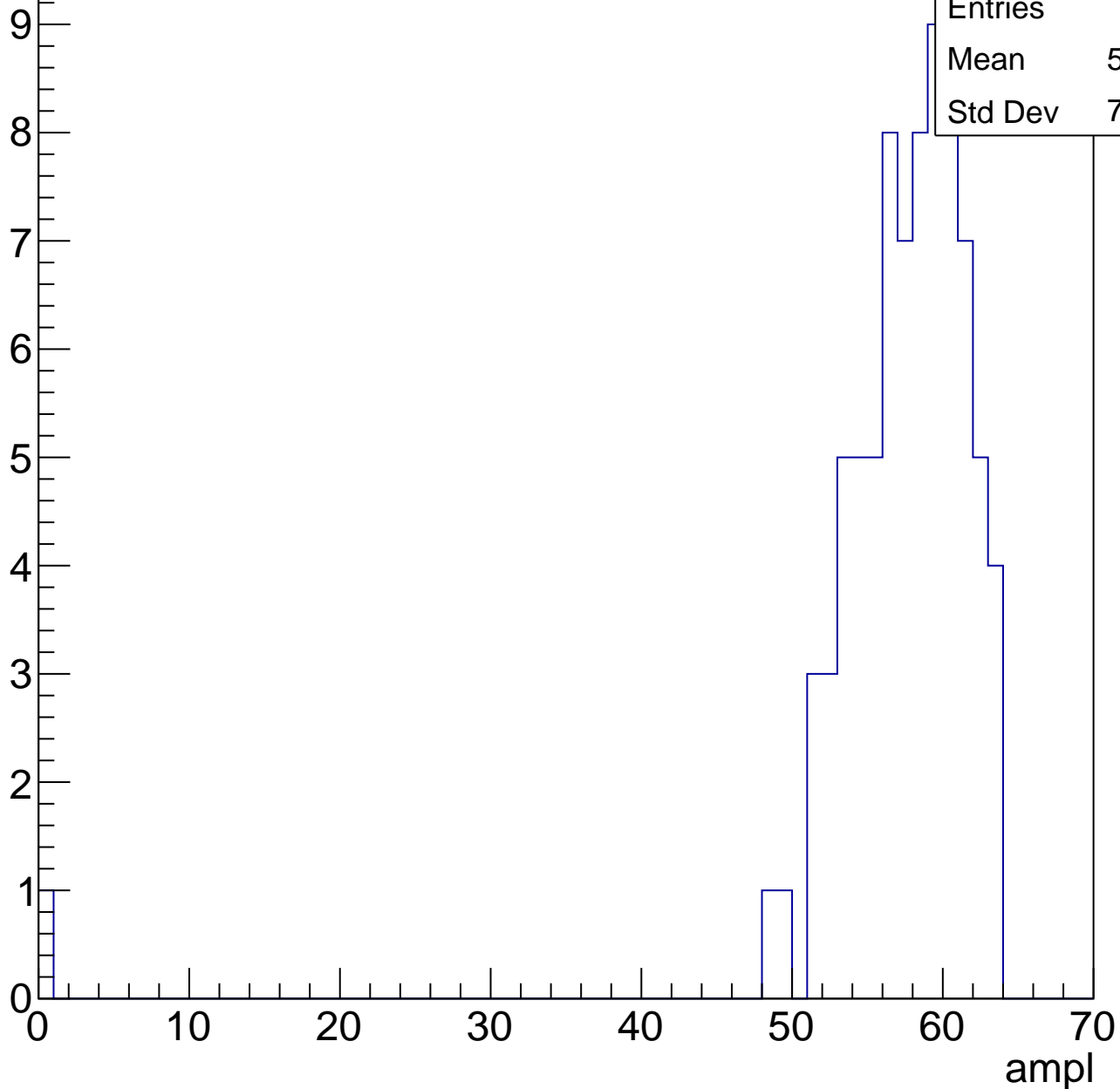
| | |
|---------|-------|
| Entries | 59 |
| Mean | 50.66 |
| Std Dev | 3.443 |



B1L100S, U9-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

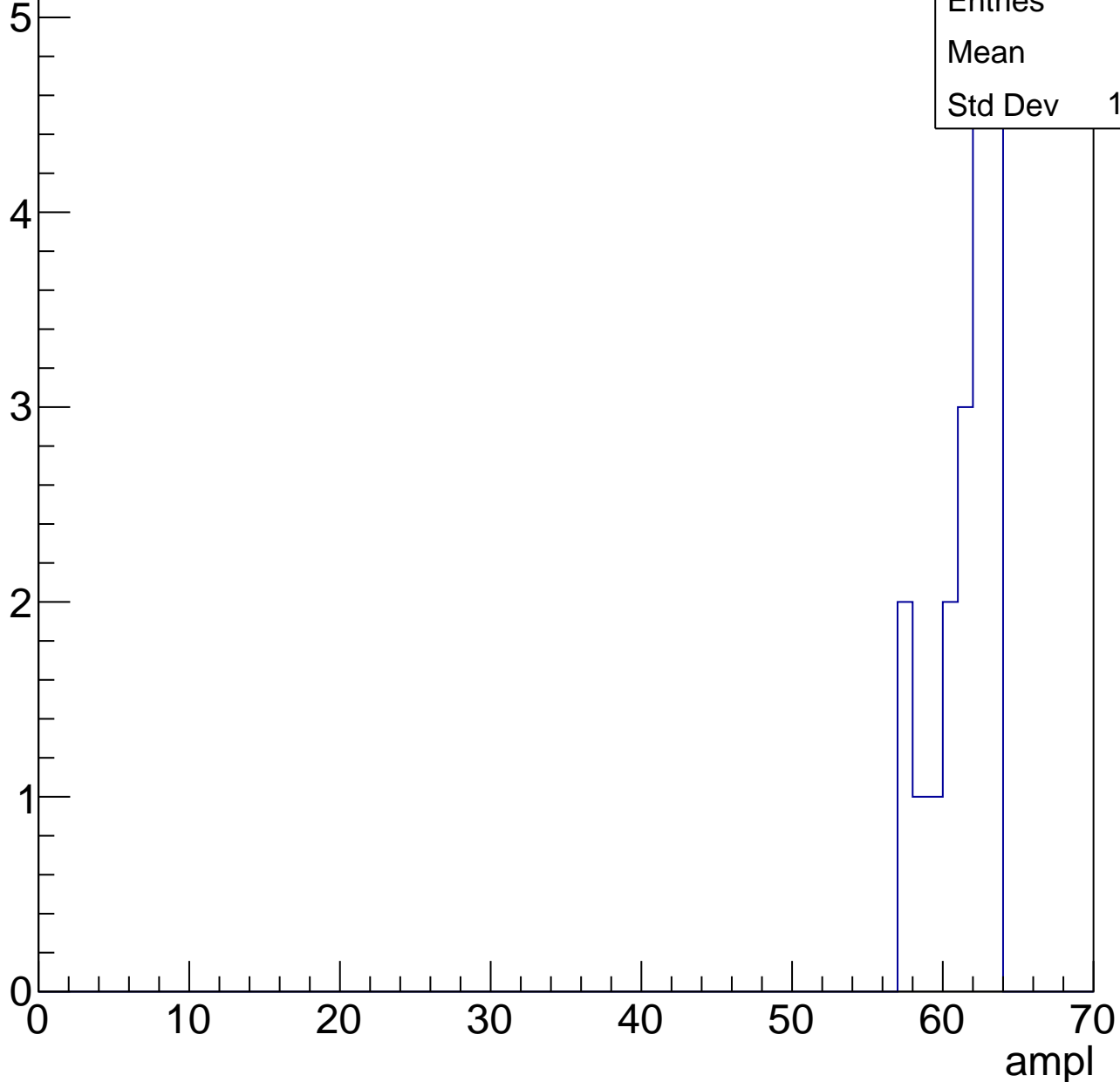
Entry



B1L100S, U9-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

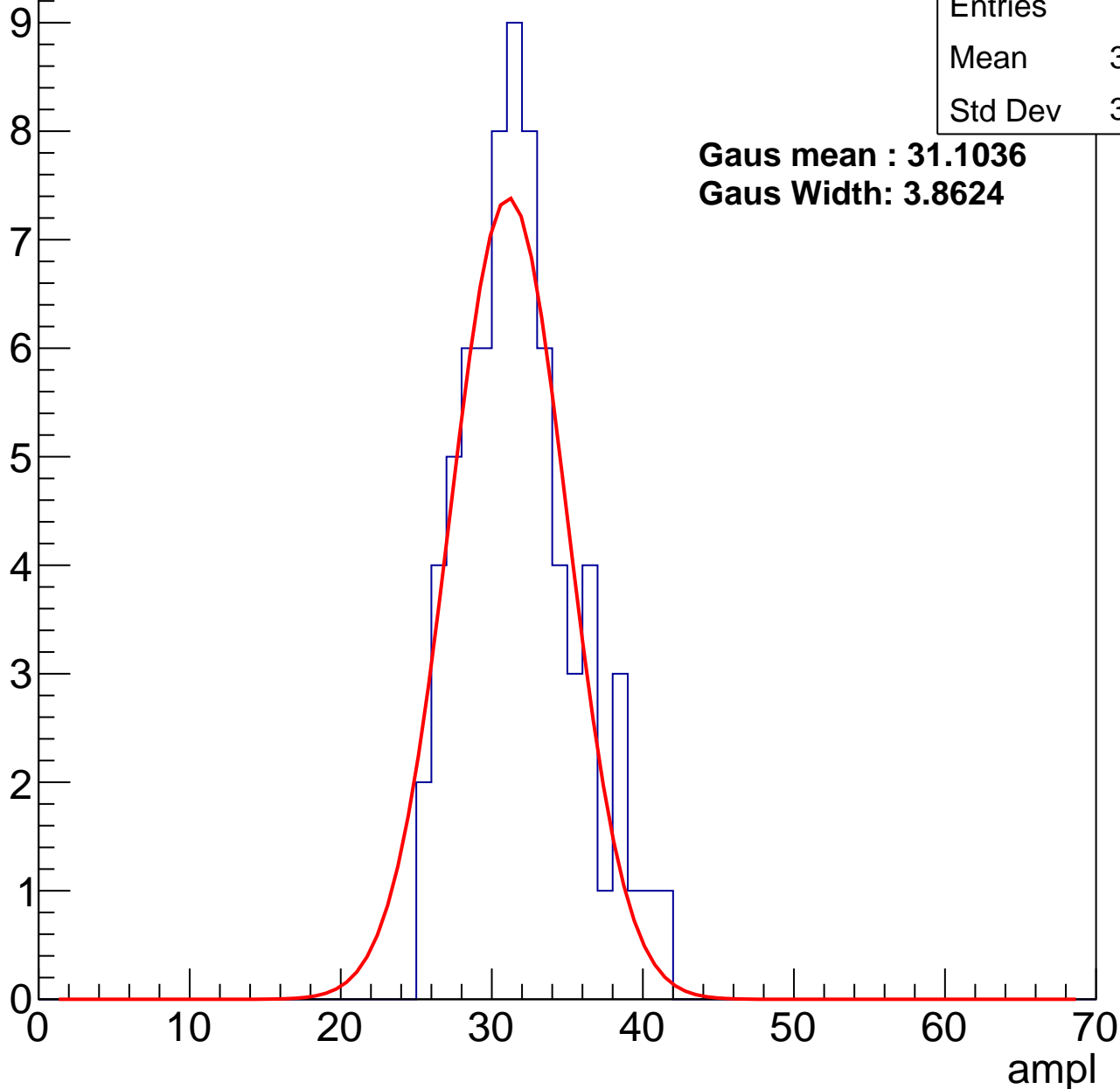
| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl

B1L100S, U9-ch3, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch3, adc1

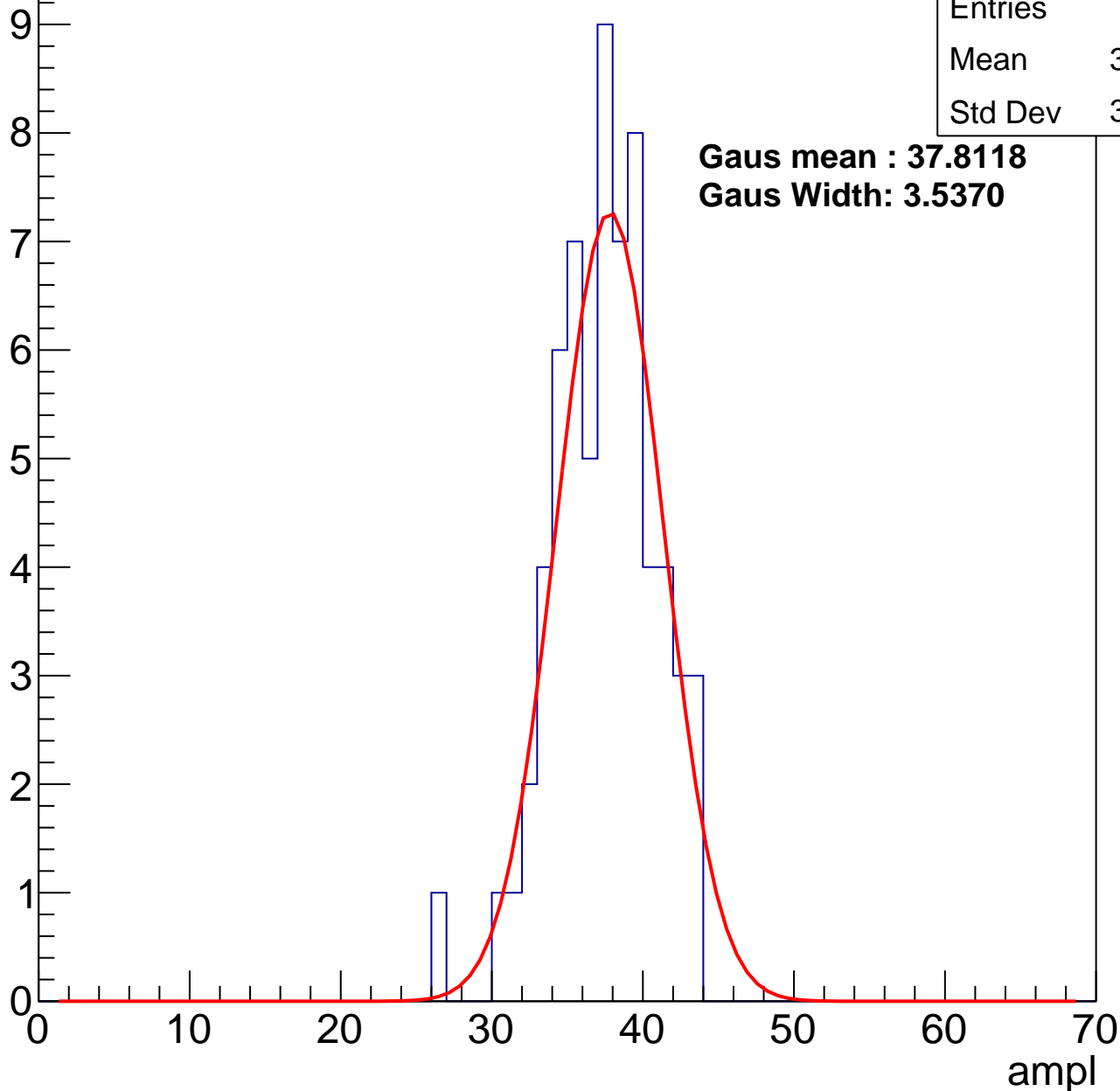
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.95 |
| Std Dev | 3.349 |

Gaus mean : 37.8118

Gaus Width: 3.5370



B1L100S, U9-ch3, adc2

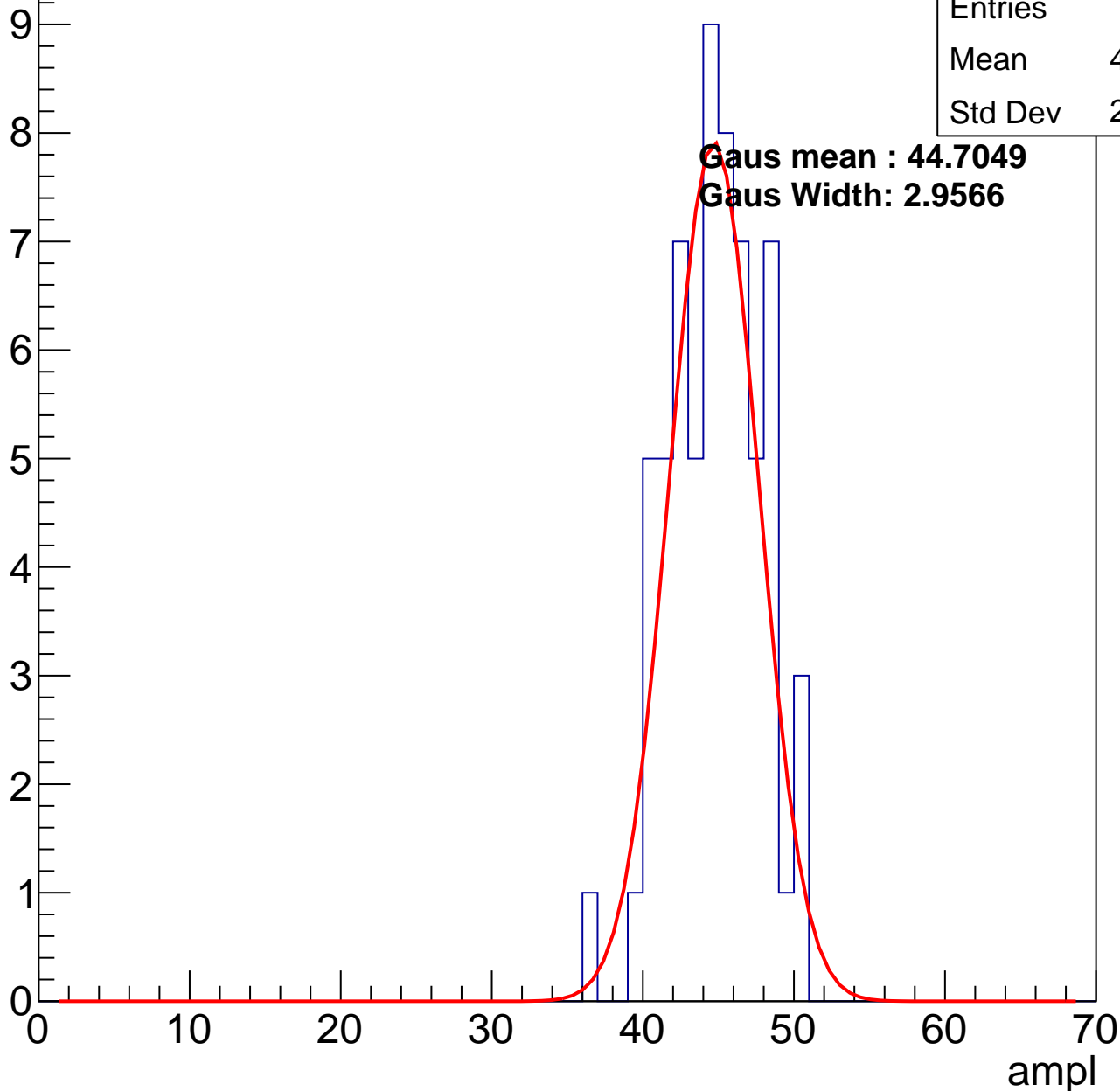
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 44.33 |
| Std Dev | 2.974 |

Gaus mean : 44.7049

Gaus Width: 2.9566

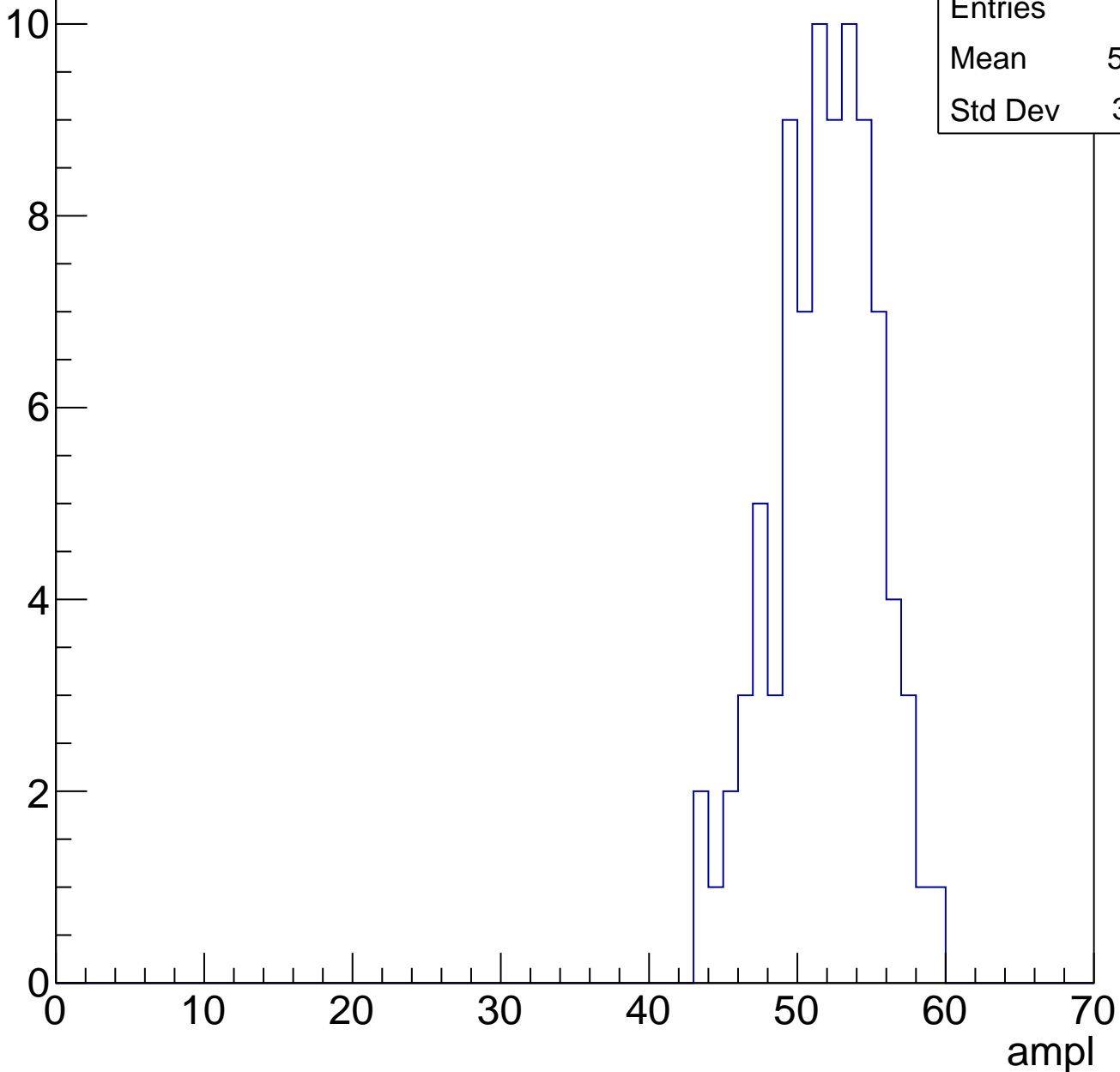


B1L100S, U9-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 86 |
| Mean | 51.38 |
| Std Dev | 3.451 |

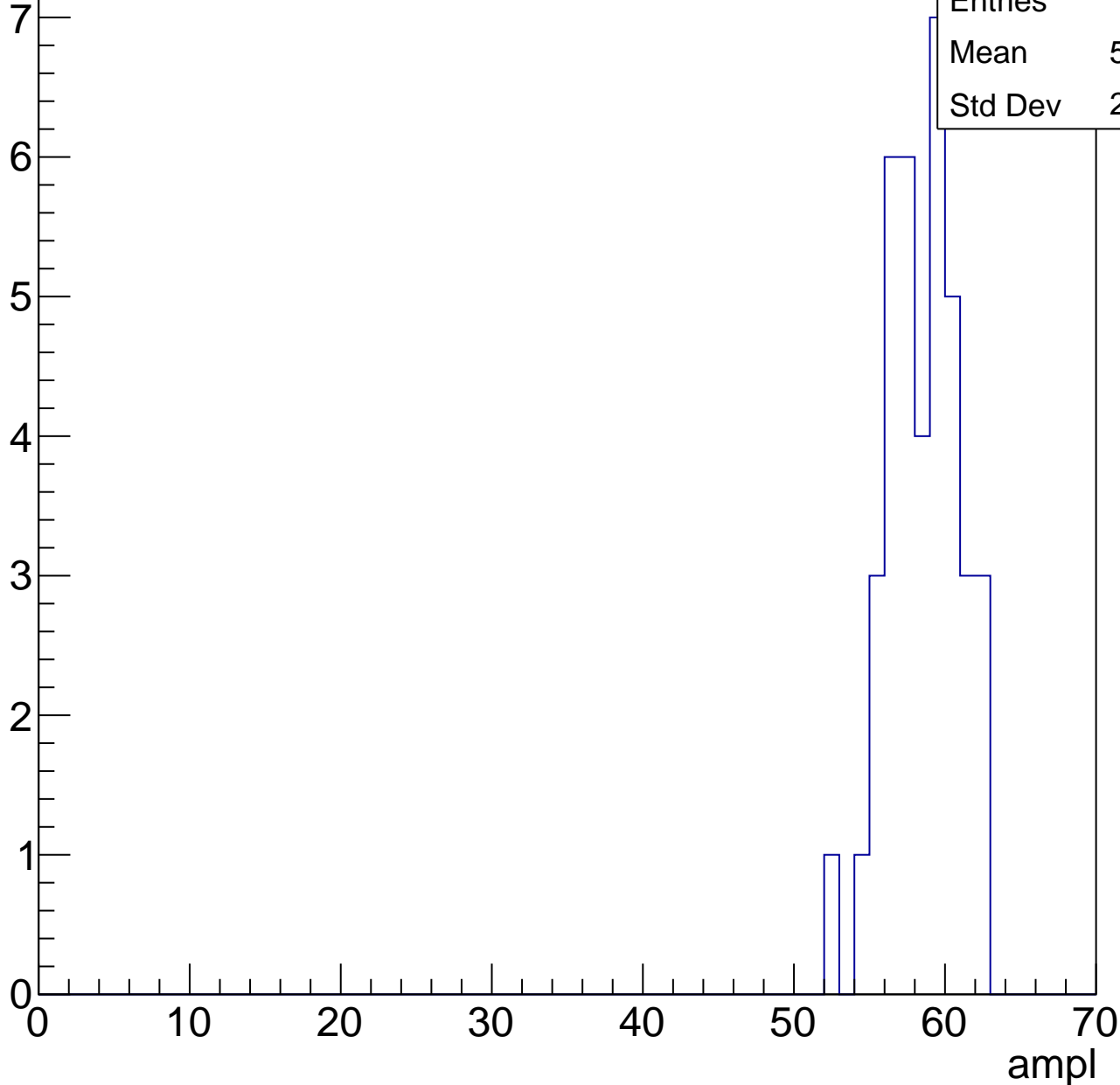
Entry



B1L100S, U9-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

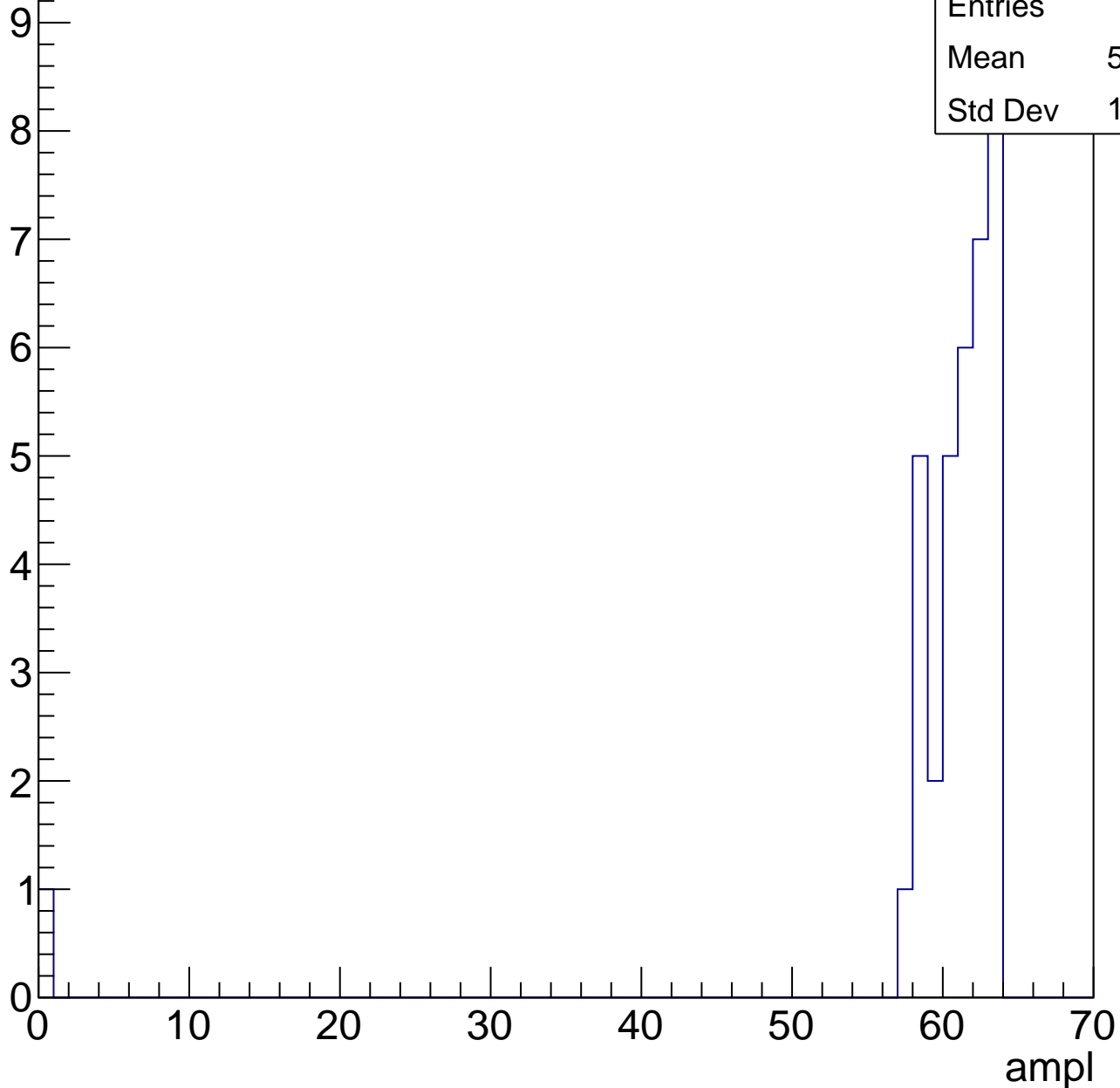
Entry



B1L100S, U9-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

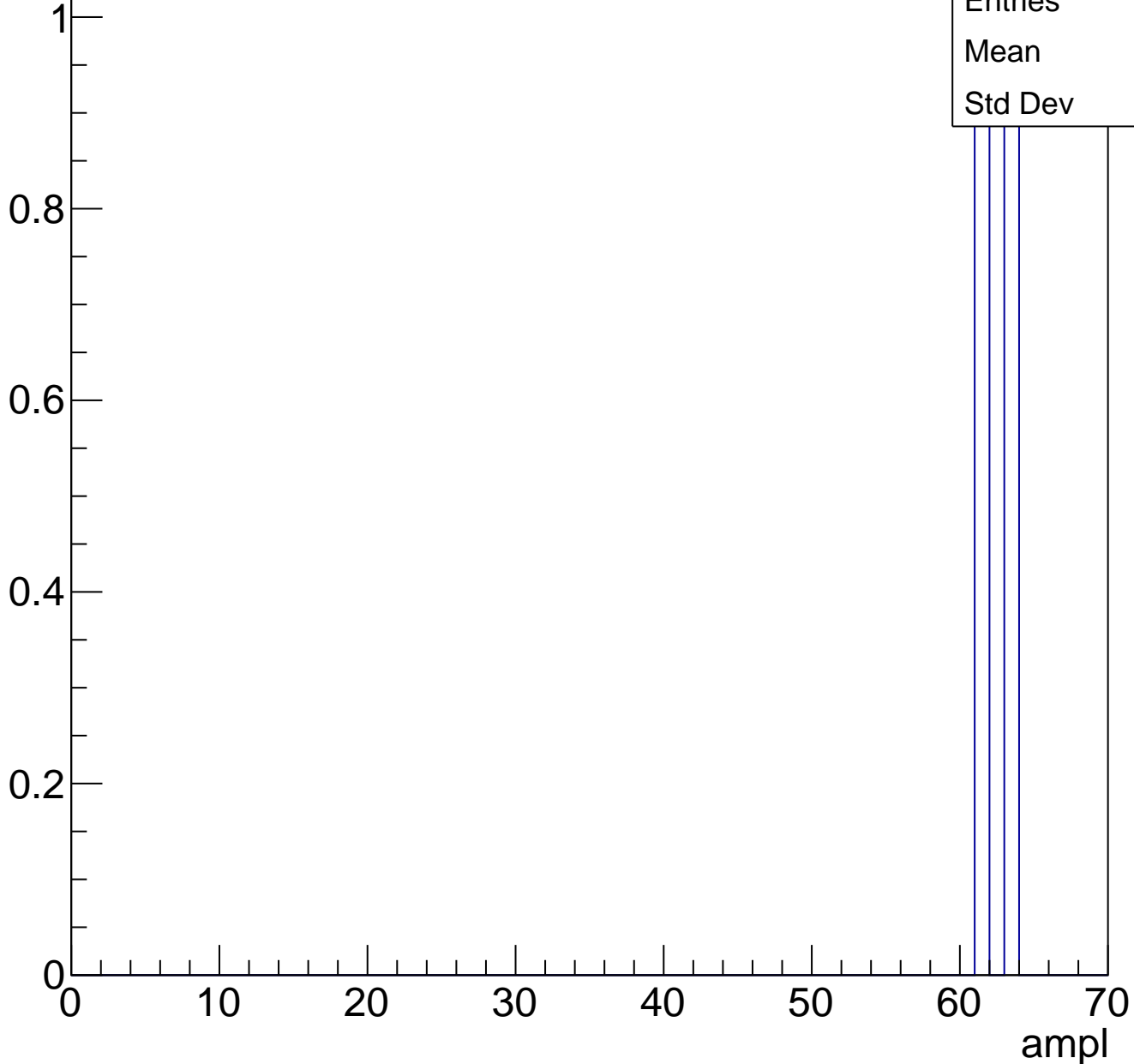
Entry



B1L100S, U9-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

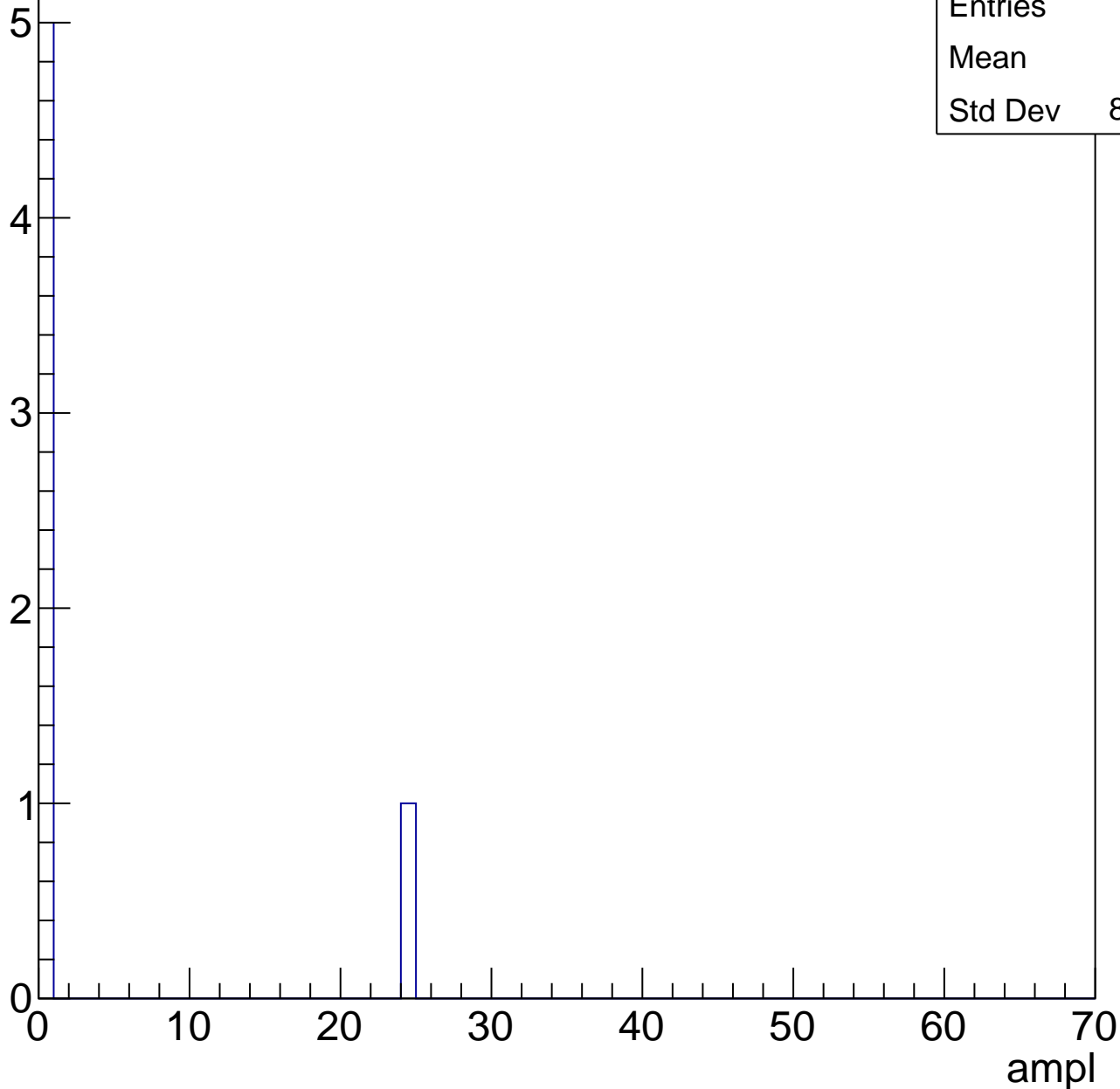


B1L100S, U9-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 6 |
| Mean | 4 |
| Std Dev | 8.944 |



B1L100S, U9-ch4, adc0

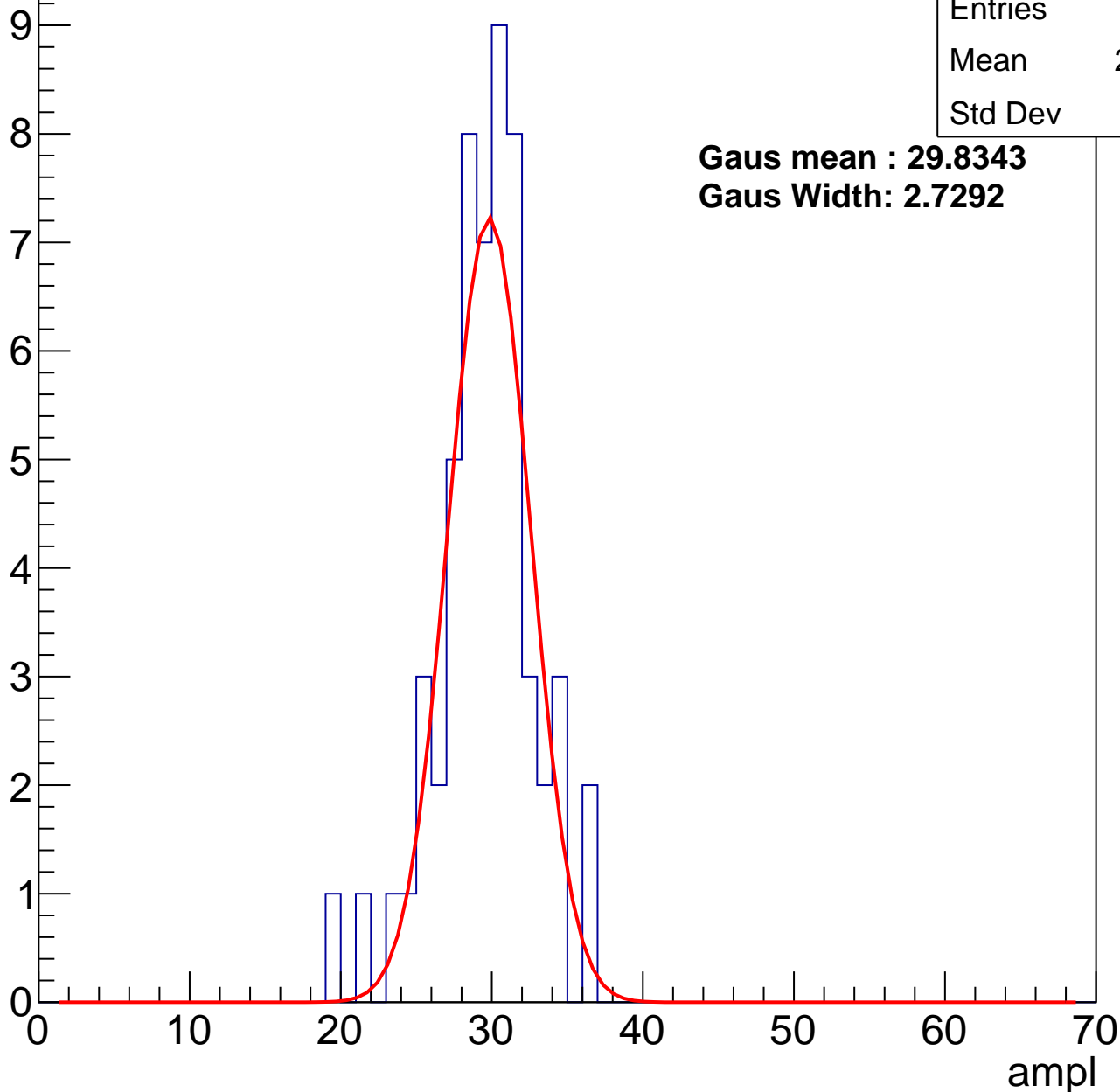
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 29.11 |
| Std Dev | 3.25 |

Gaus mean : 29.8343

Gaus Width: 2.7292



B1L100S, U9-ch4, adc1

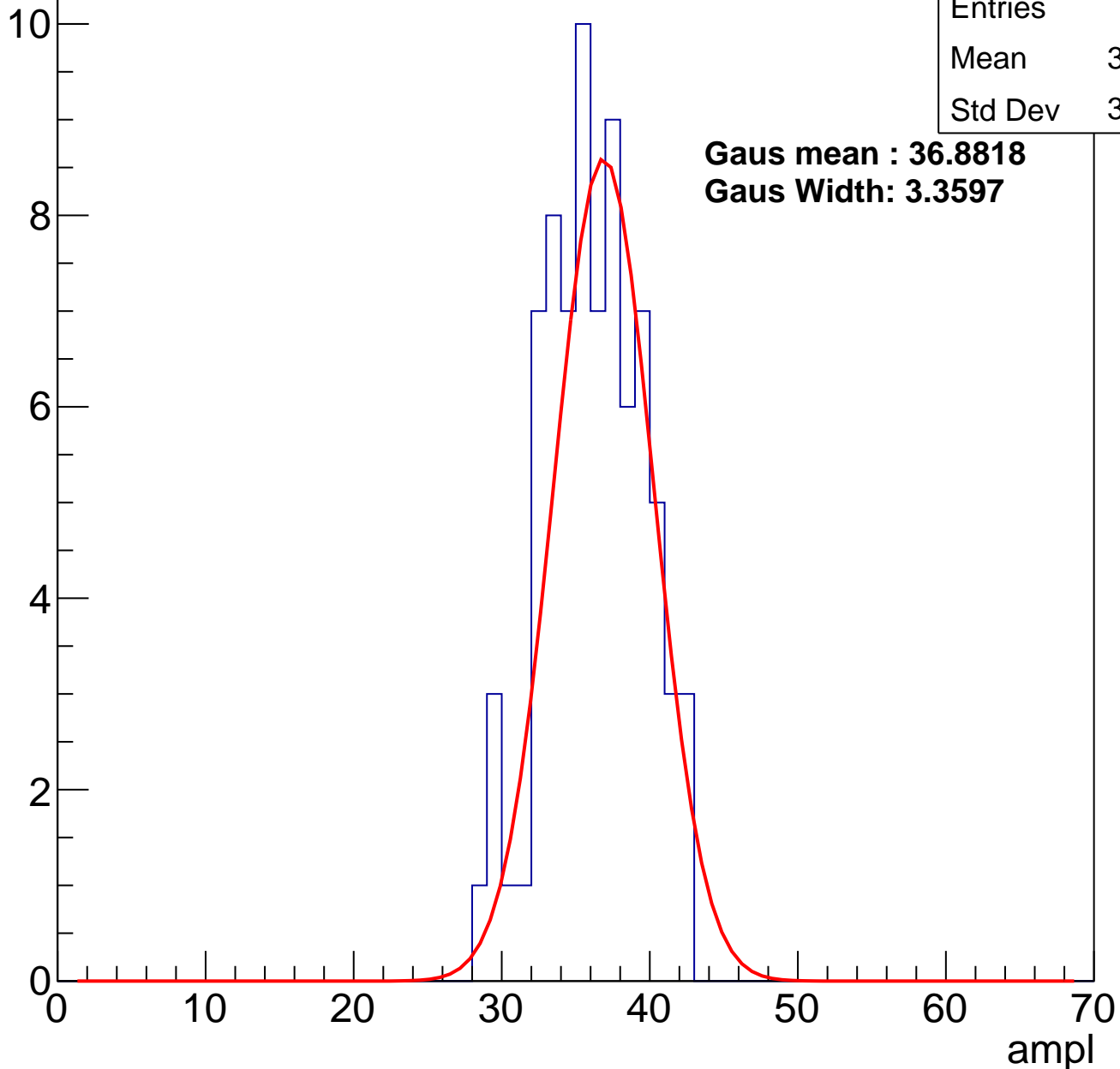
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 35.73 |
| Std Dev | 3.296 |

Gaus mean : 36.8818

Gaus Width: 3.3597

Entry



B1L100S, U9-ch4, adc2

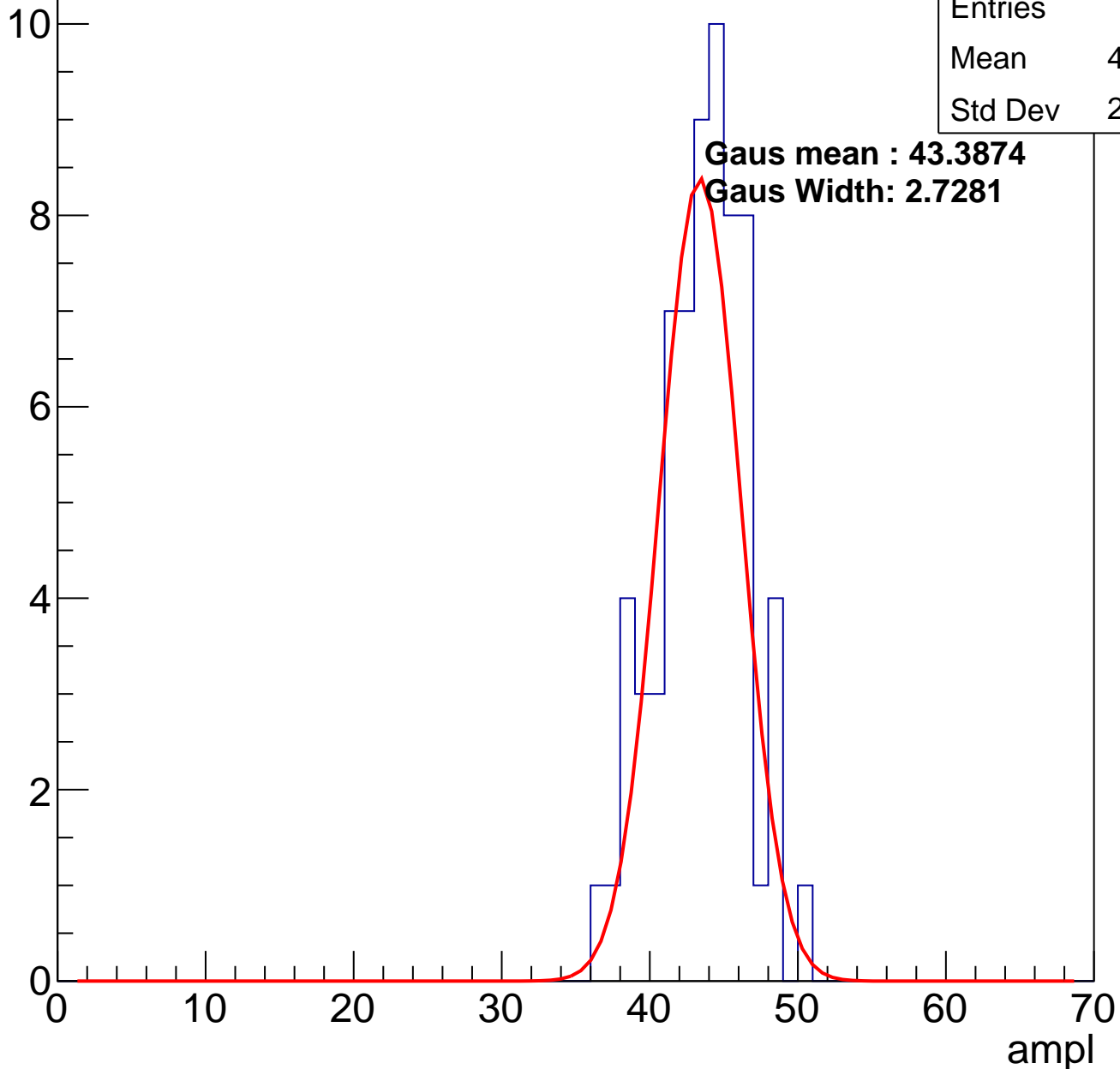
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.09 |
| Std Dev | 2.926 |

Gaus mean : 43.3874

Gaus Width: 2.7281

Entry



B1L100S, U9-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

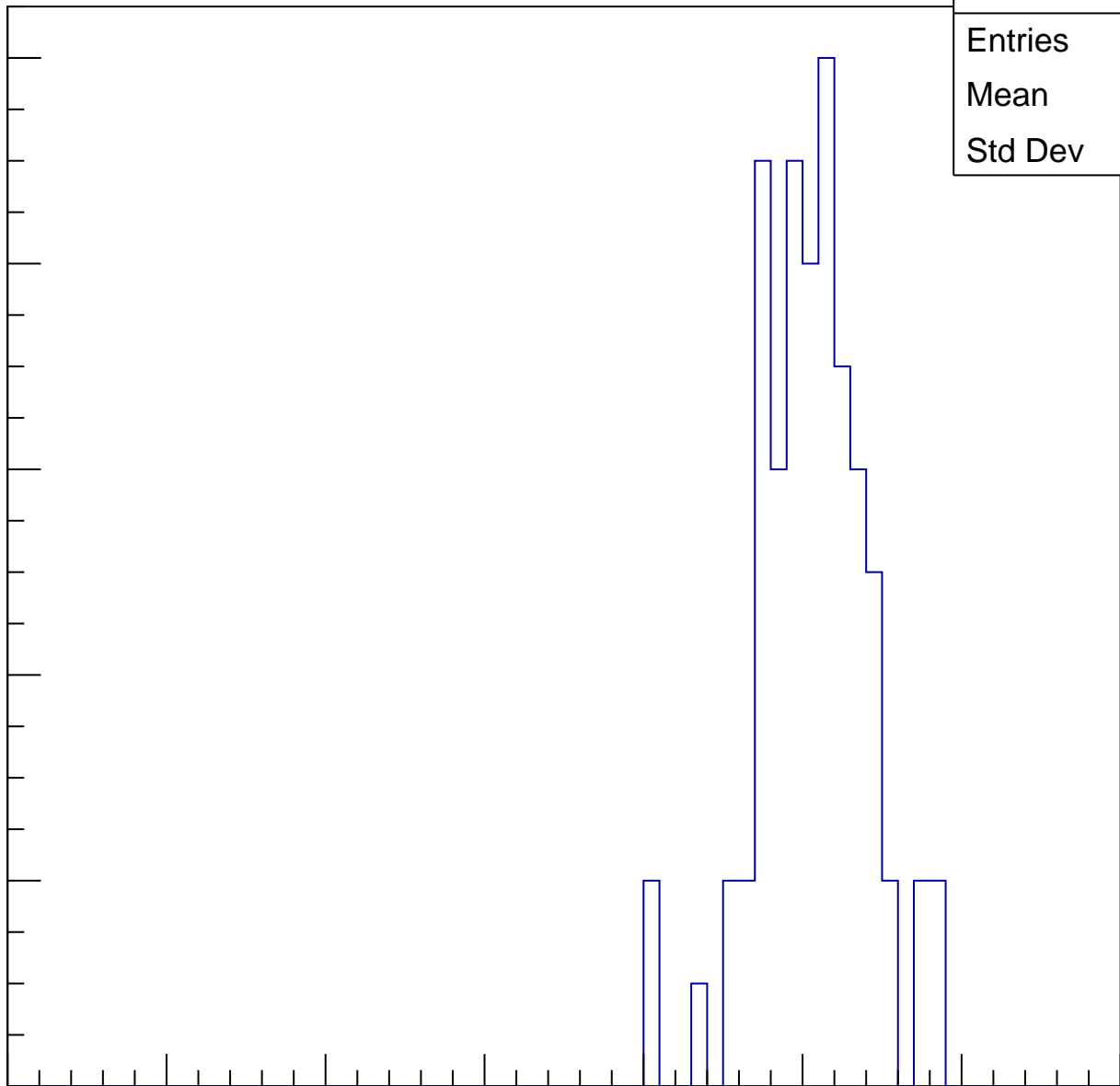
| | |
|---------|-------|
| Entries | 73 |
| Mean | 50.12 |
| Std Dev | 3.495 |

Entry

10
8
6
4
2
0

ampl

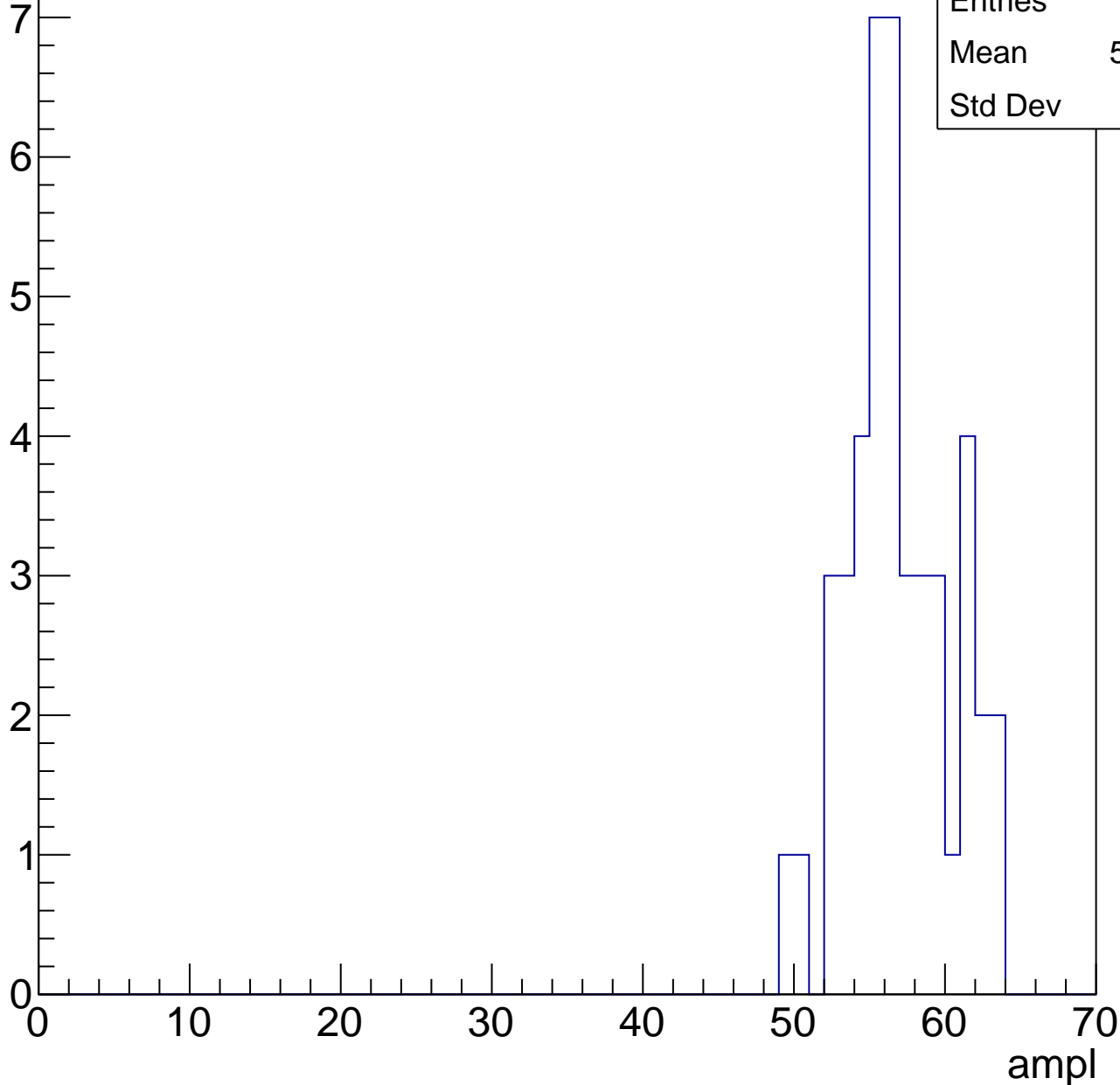
0 10 20 30 40 50 60 70



B1L100S, U9-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch4, adc5

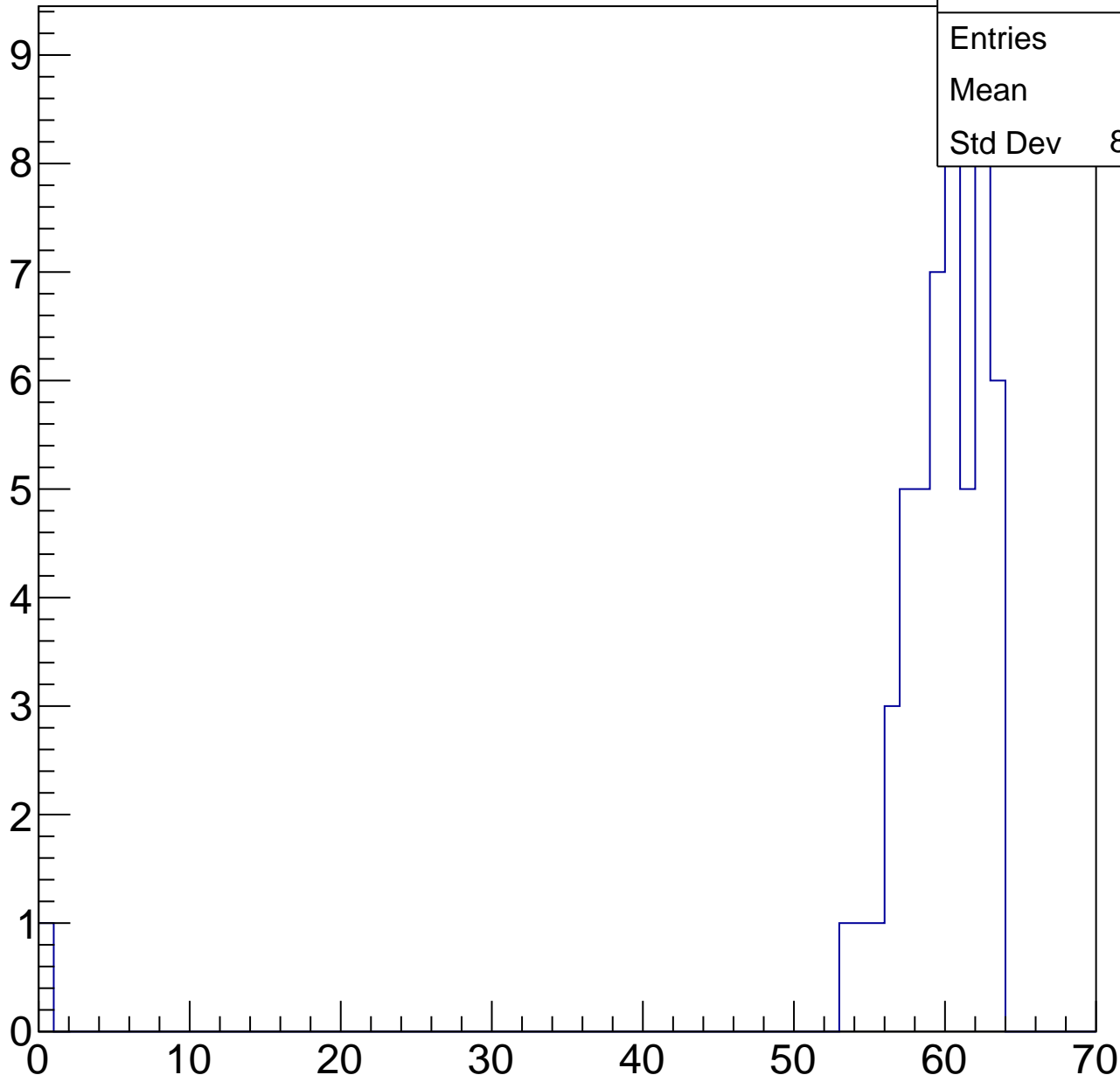
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.4 |
| Std Dev | 8.536 |

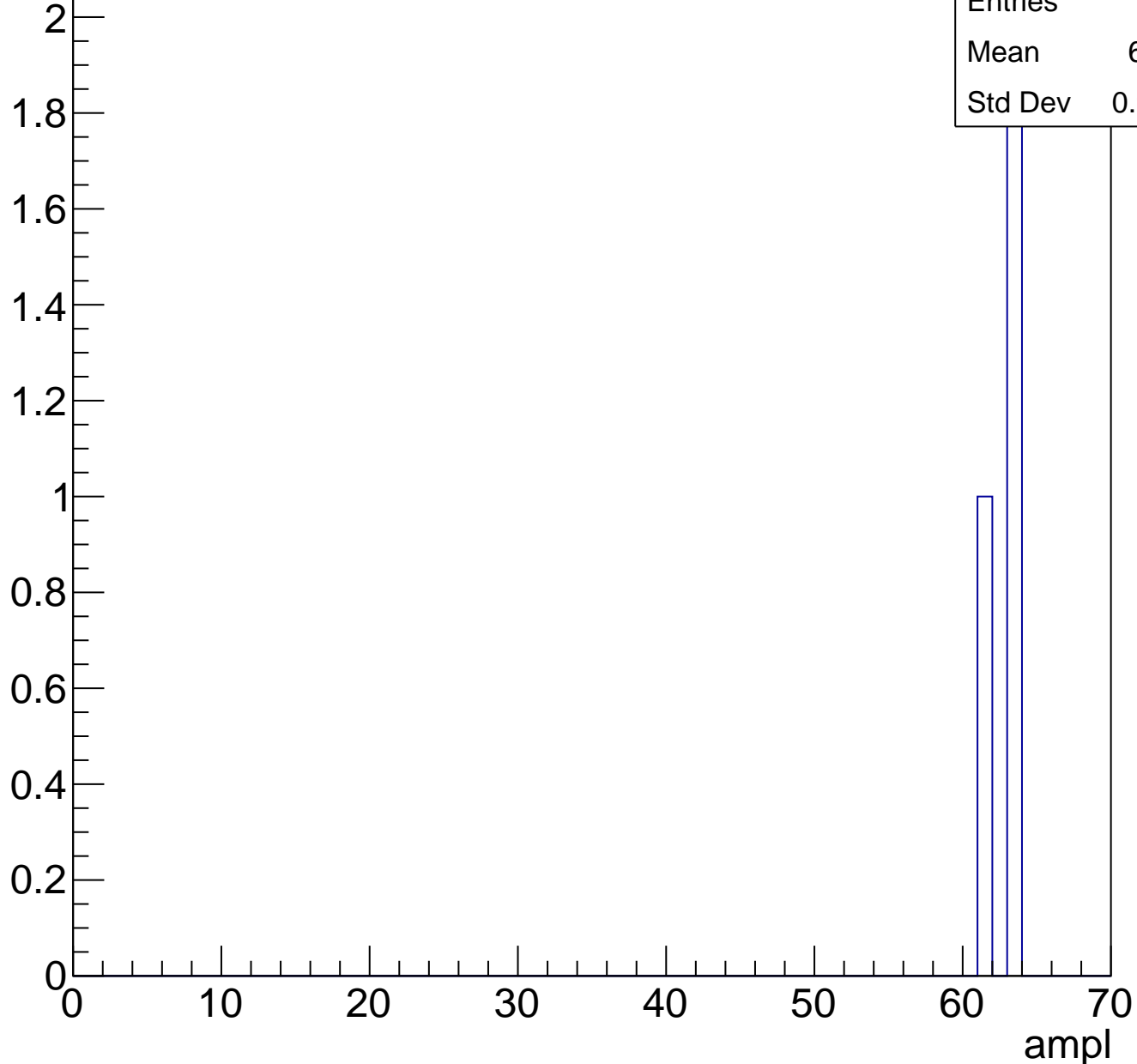
ampl



B1L100S, U9-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch5, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 30.35 |
| Std Dev | 4.697 |

Gaus mean : 31.3118

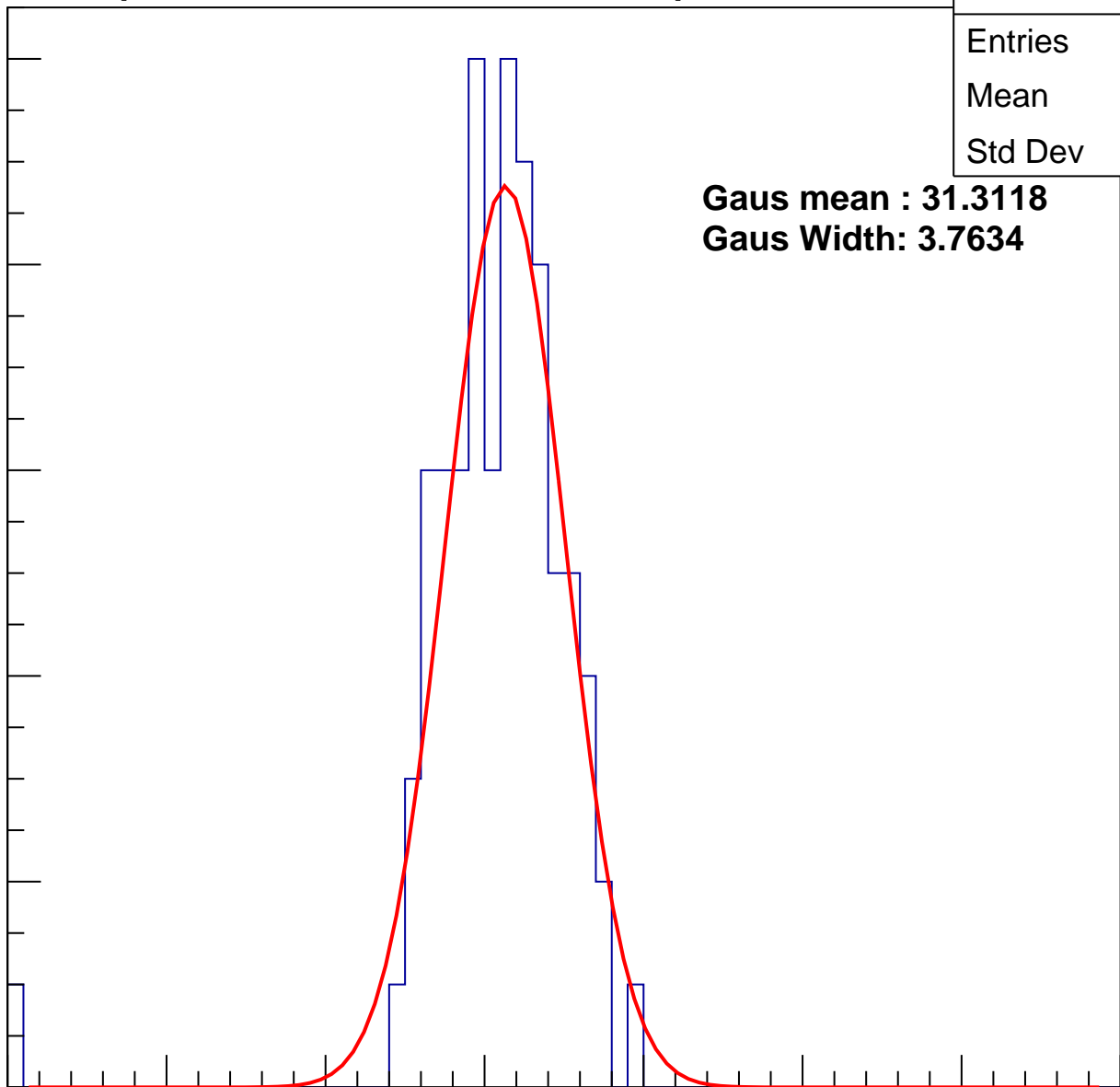
Gaus Width: 3.7634

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



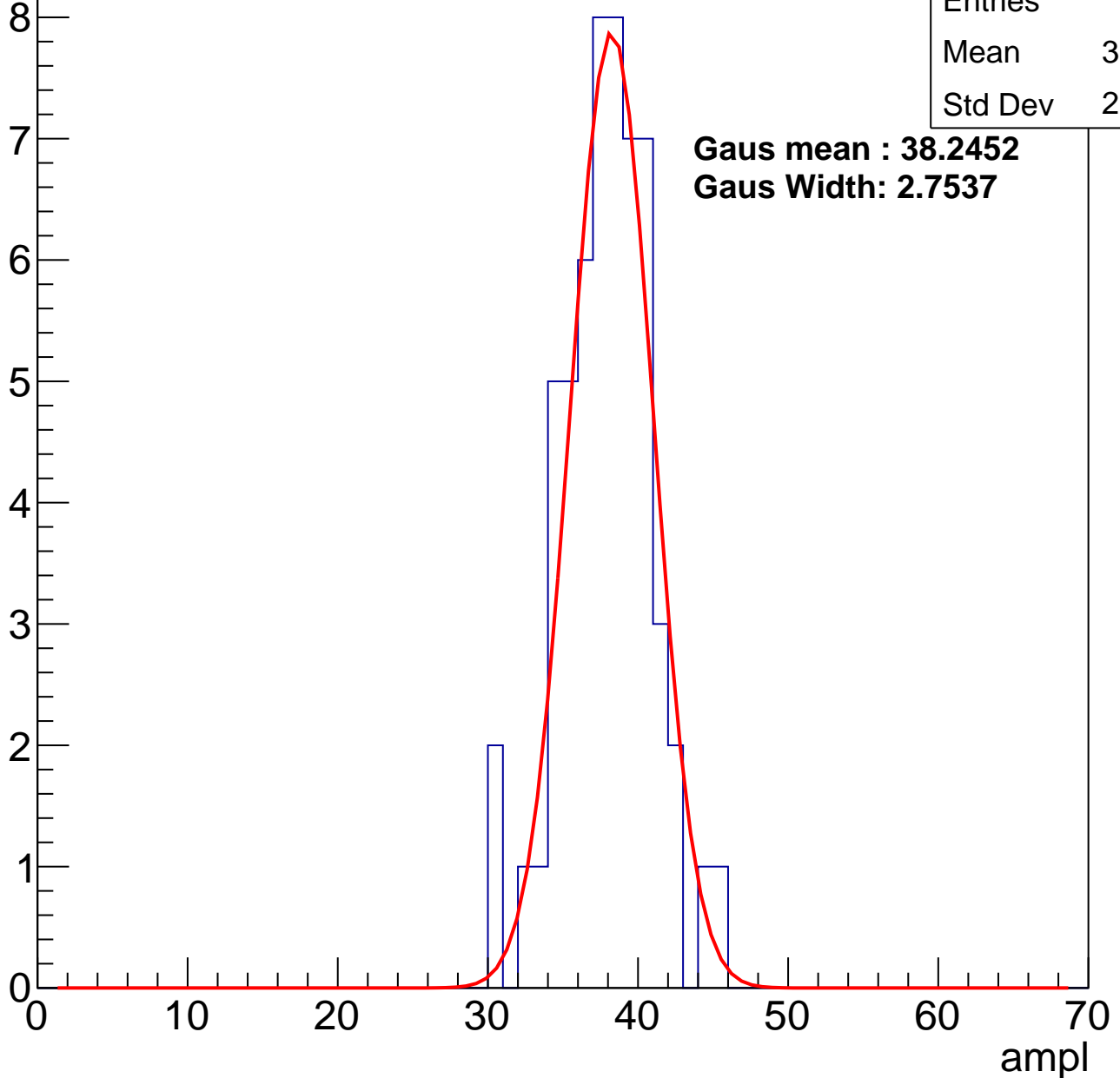
B1L100S, U9-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 37.46 |
| Std Dev | 2.986 |

Gaus mean : 38.2452
Gaus Width: 2.7537



B1L100S, U9-ch5, adc2

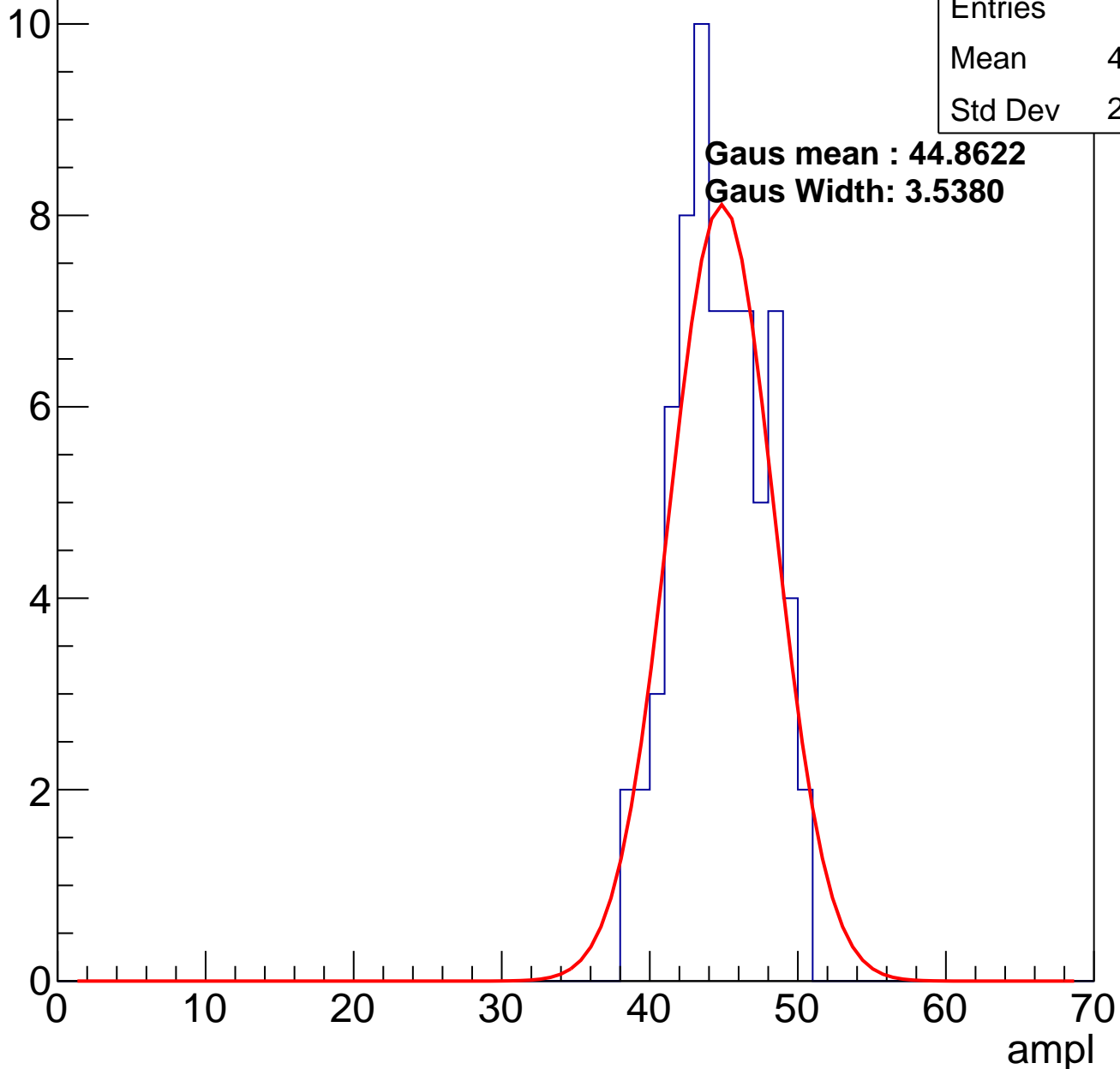
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.26 |
| Std Dev | 2.989 |

Gaus mean : 44.8622

Gaus Width: 3.5380

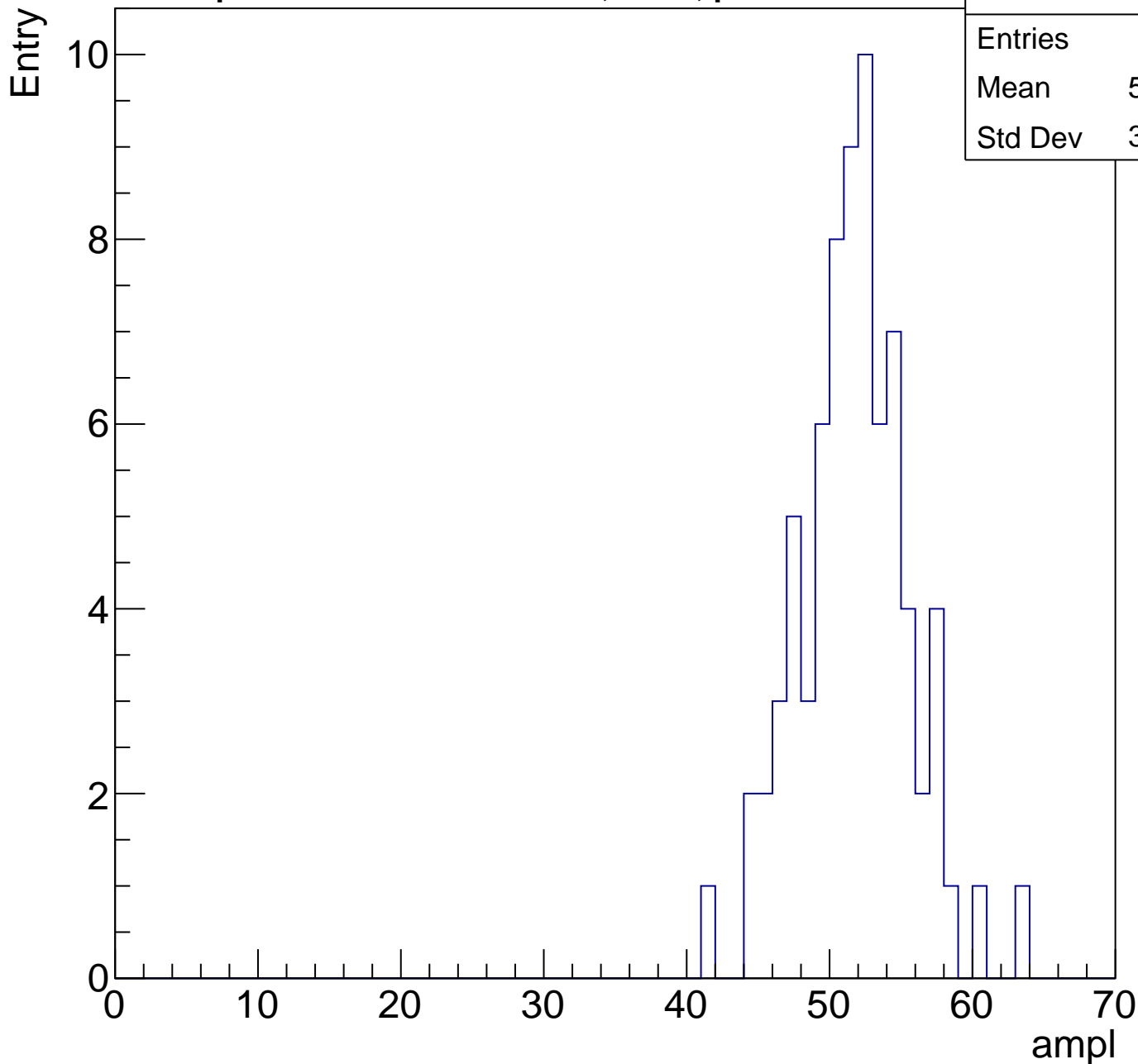
Entry



B1L100S, U9-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 51.28 |
| Std Dev | 3.842 |

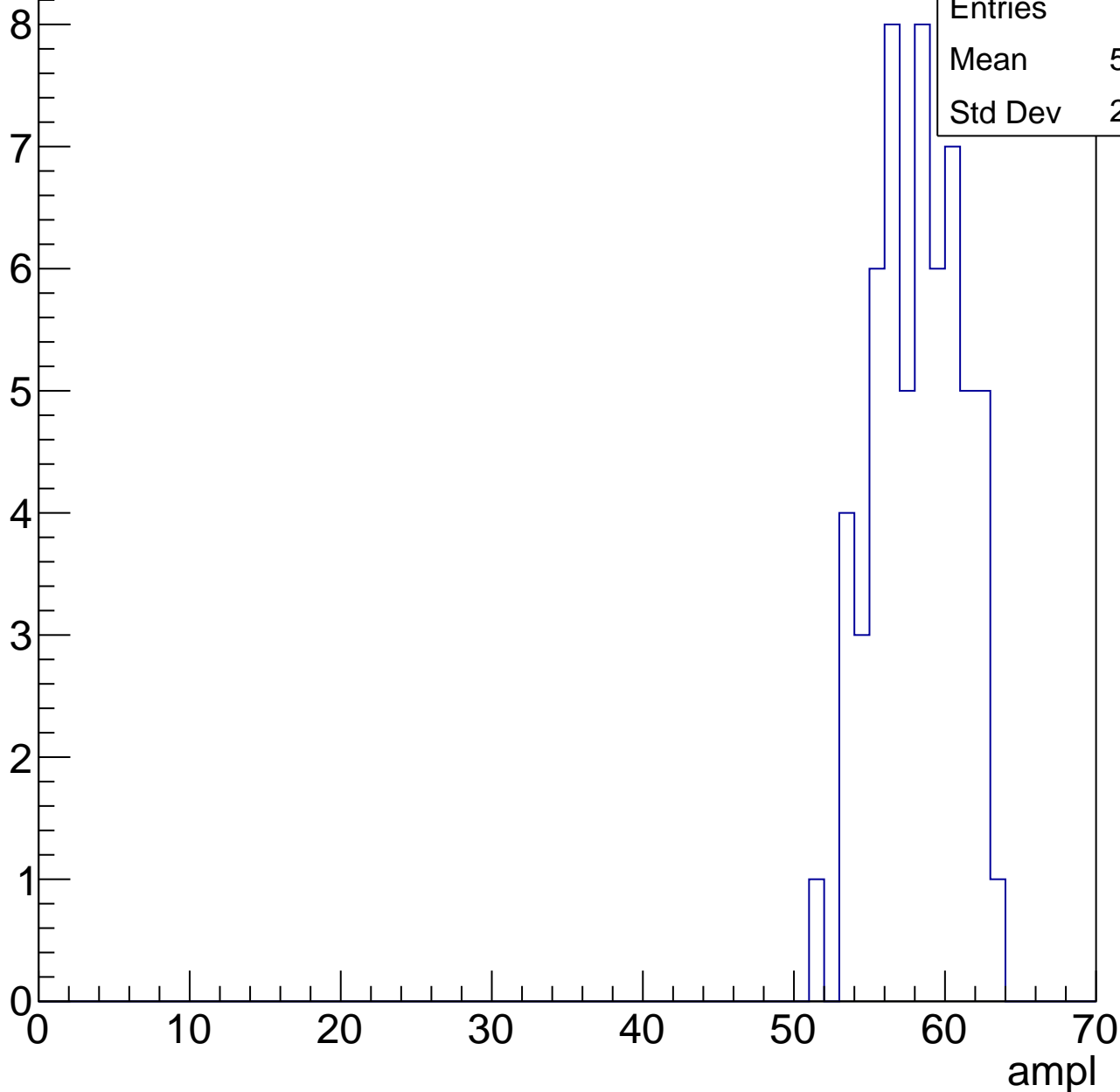


B1L100S, U9-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 57.69 |
| Std Dev | 2.812 |

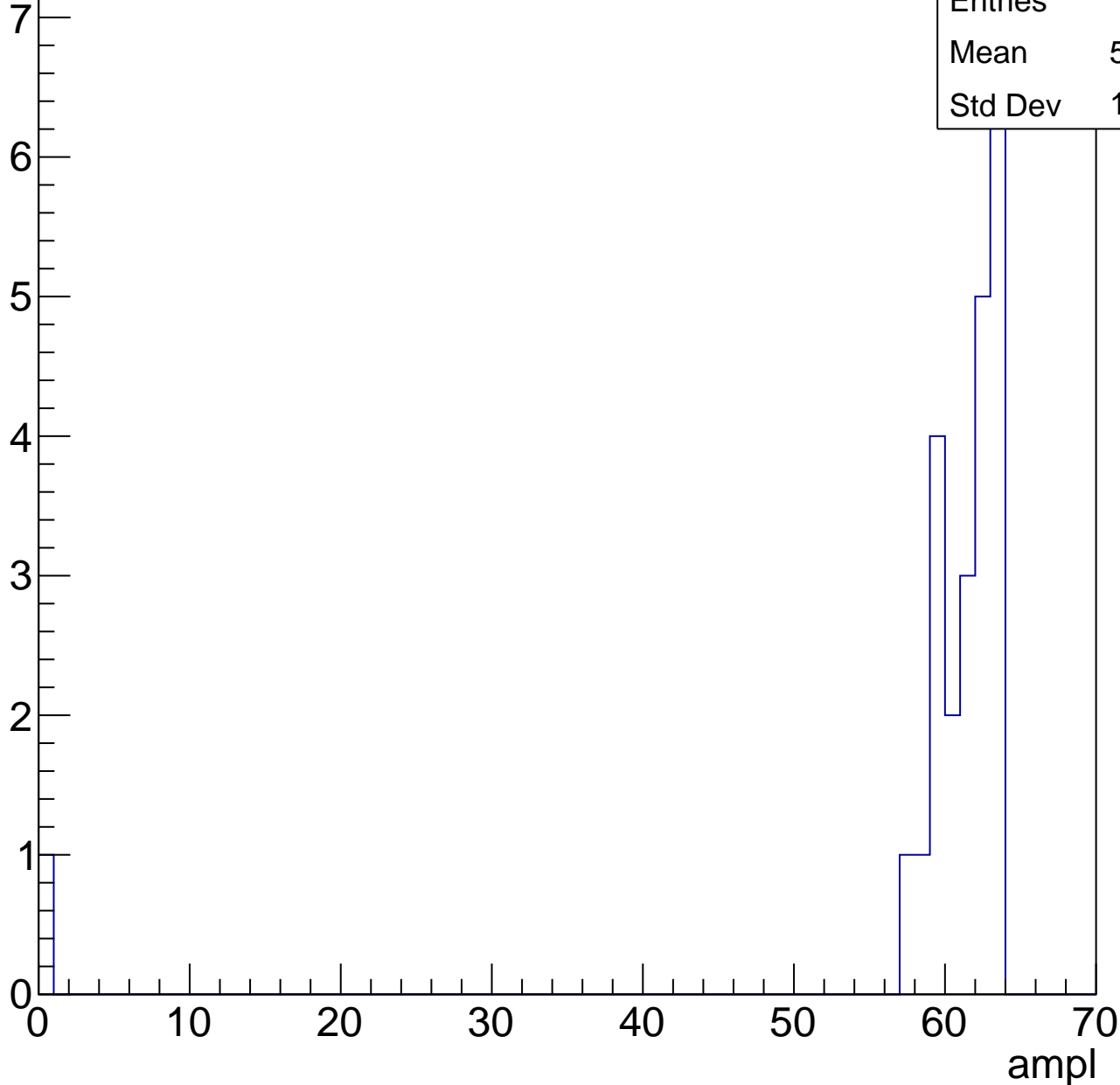


B1L100S, U9-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.54 |
| Std Dev | 12.34 |



B1L100S, U9-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

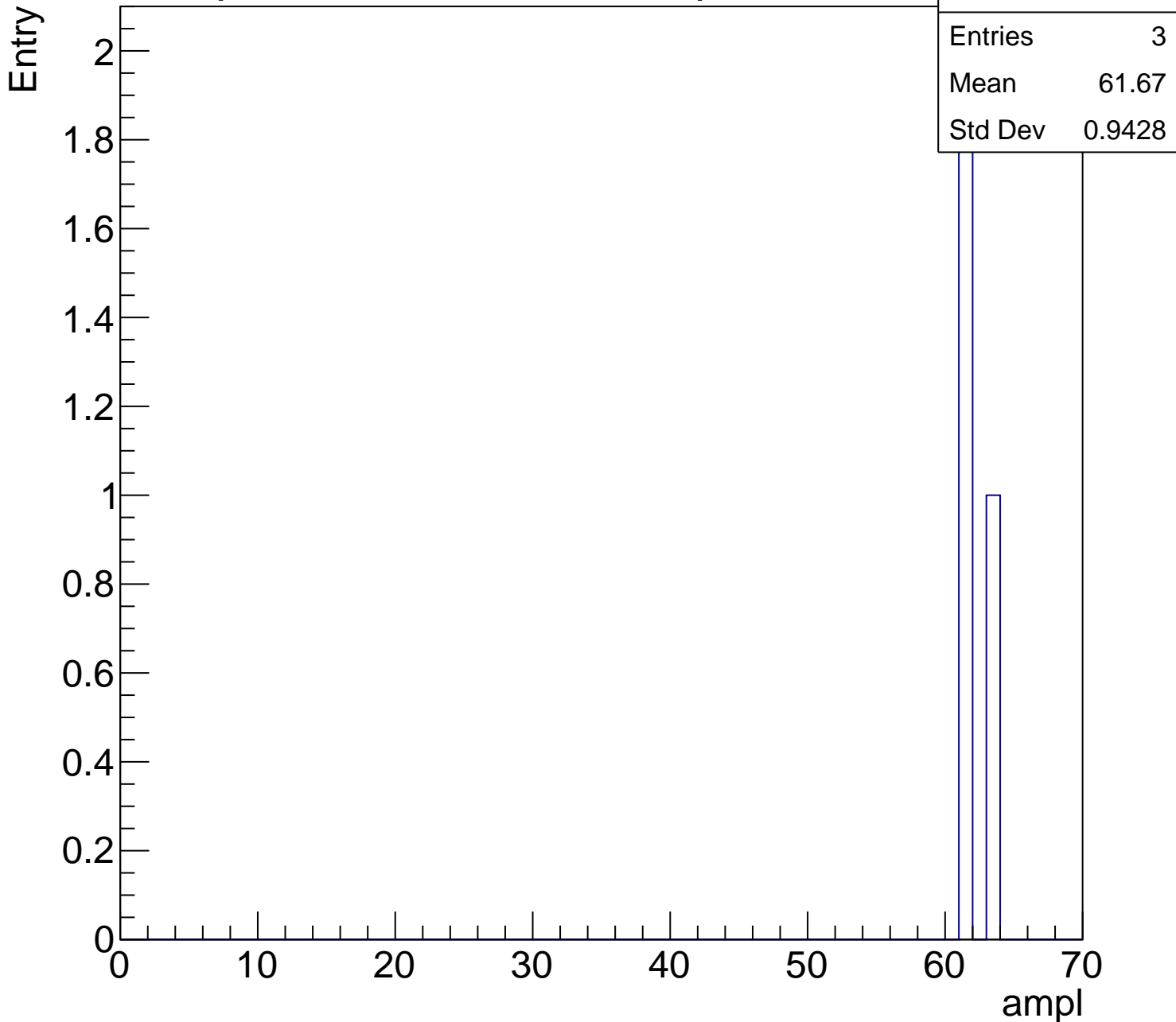
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 61.67 |
| Std Dev | 0.9428 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B1L100S, U9-ch6, adc0

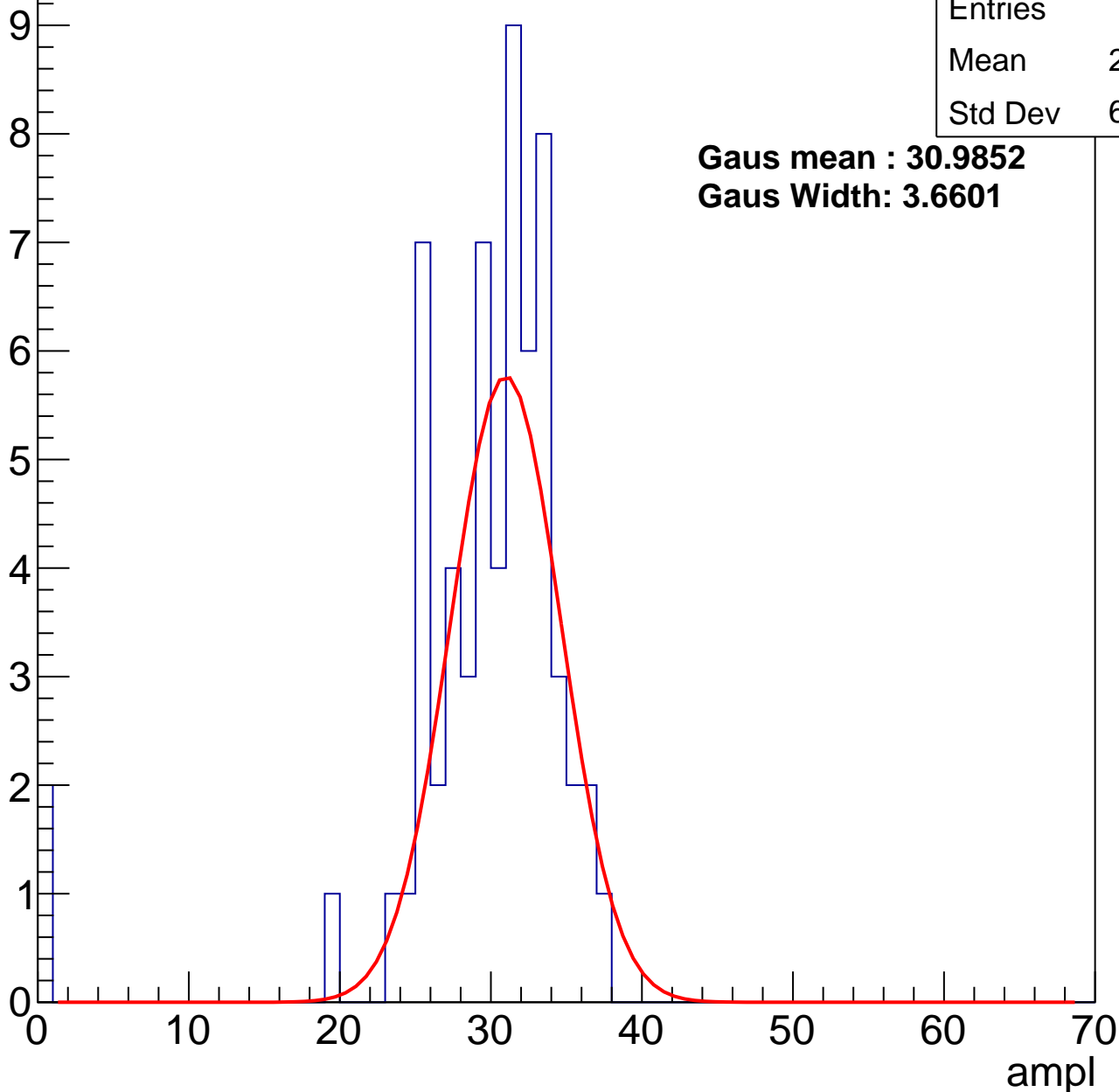
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.95 |
| Std Dev | 6.328 |

Gaus mean : 30.9852

Gaus Width: 3.6601



B1L100S, U9-ch6, adc1

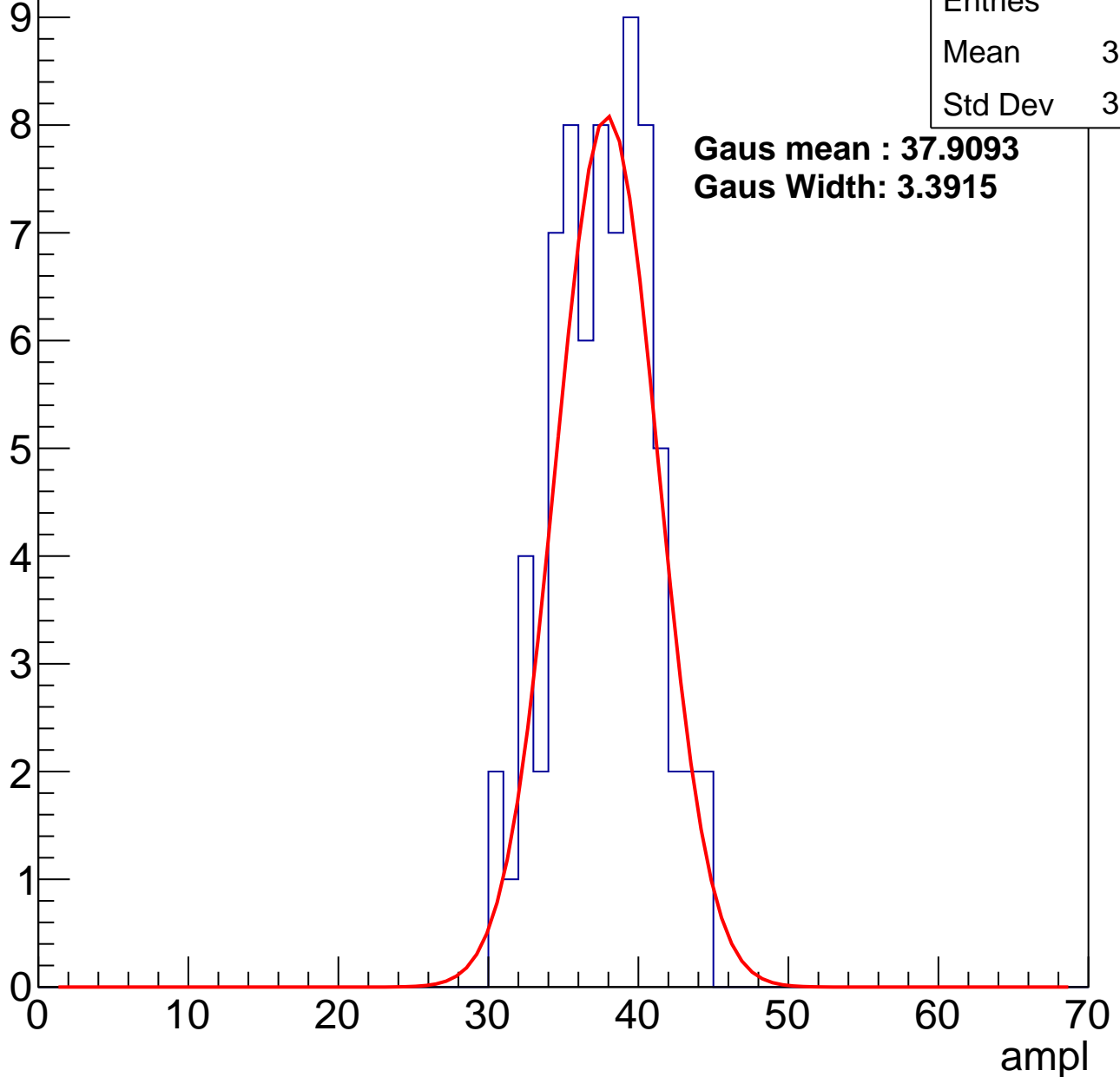
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 37.19 |
| Std Dev | 3.267 |

Gaus mean : 37.9093

Gaus Width: 3.3915



B1L100S, U9-ch6, adc2

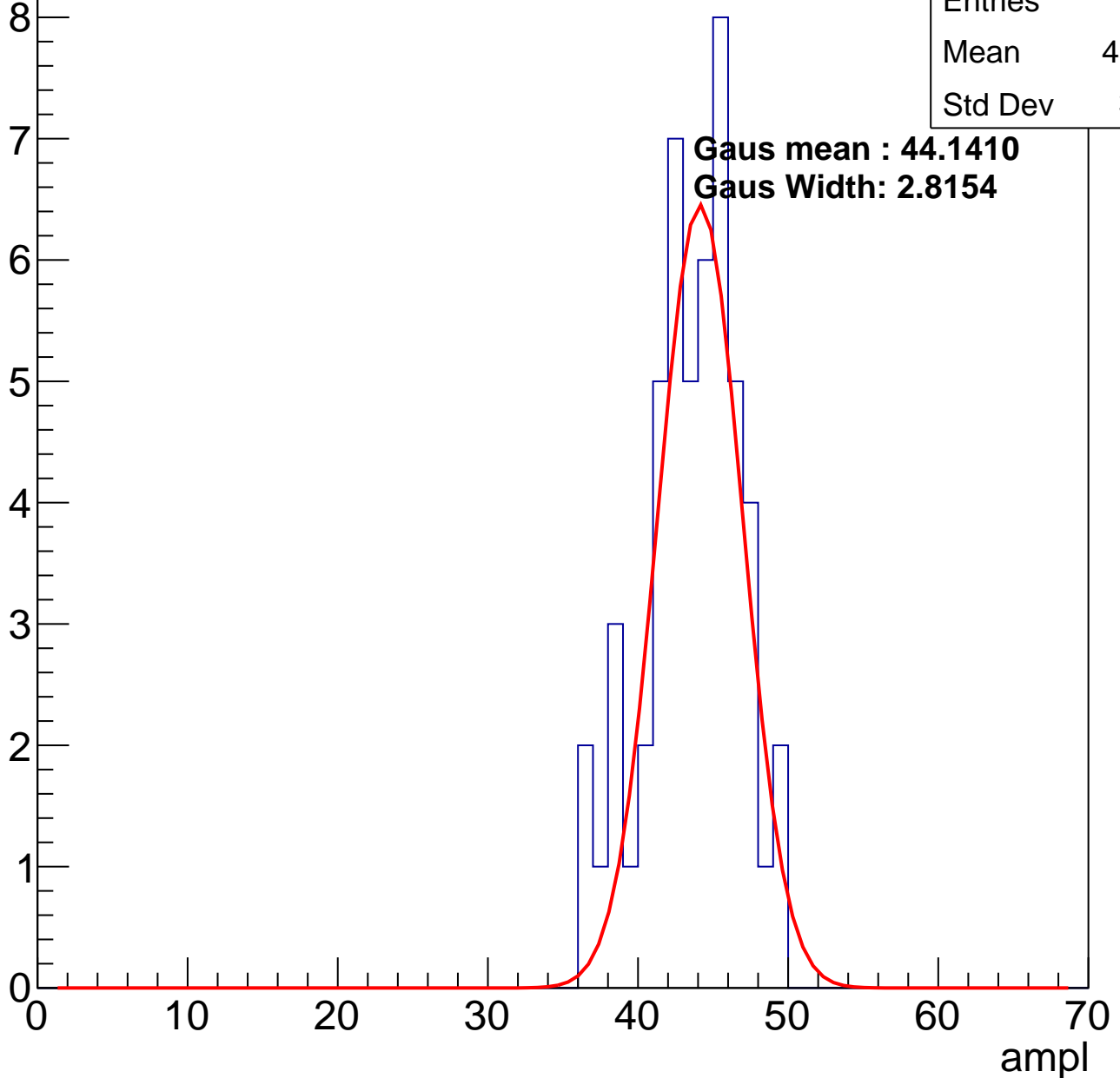
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 43.15 |
| Std Dev | 3.14 |

Gaus mean : 44.1410

Gaus Width: 2.8154

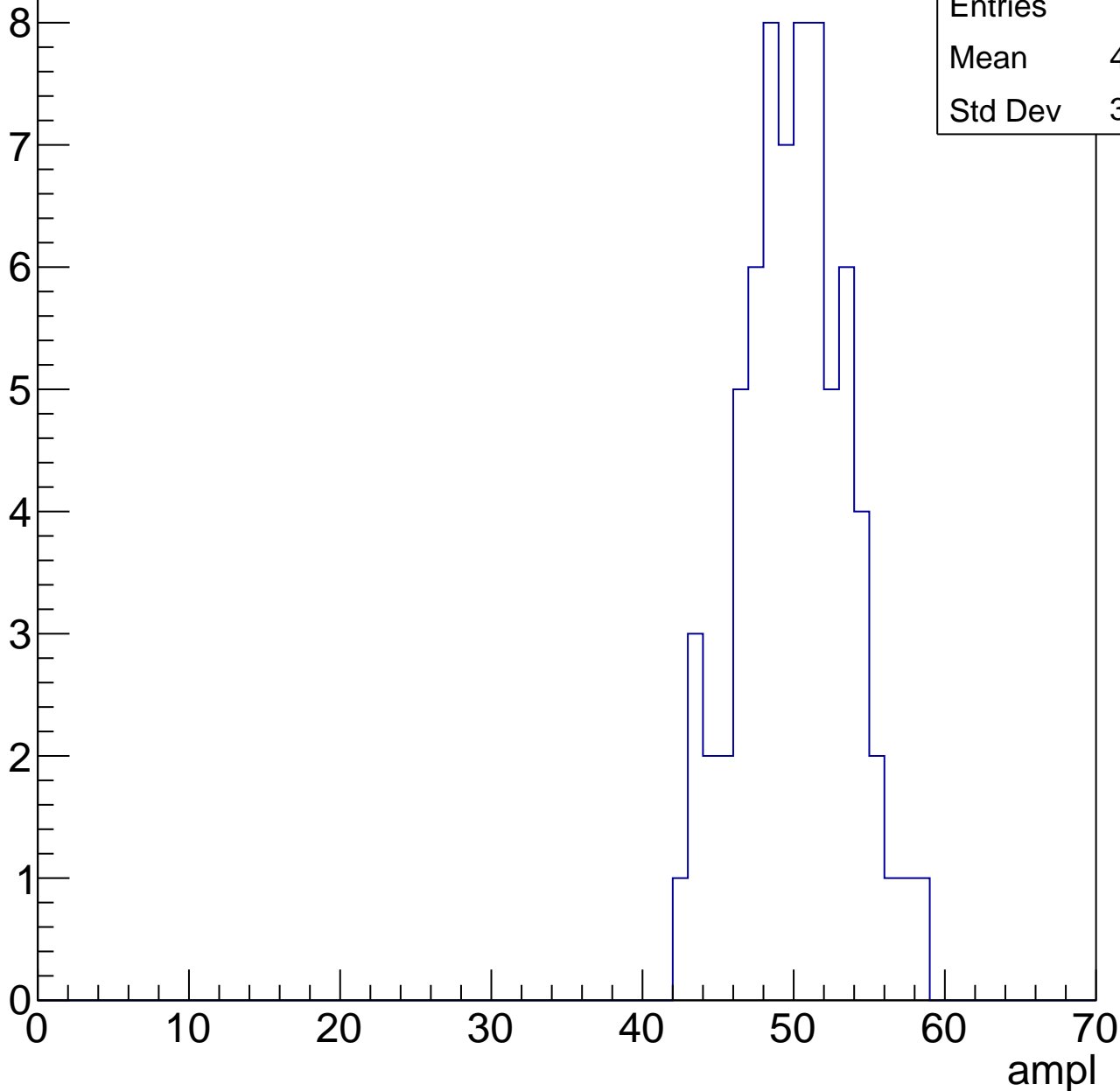


B1L100S, U9-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.59 |
| Std Dev | 3.462 |

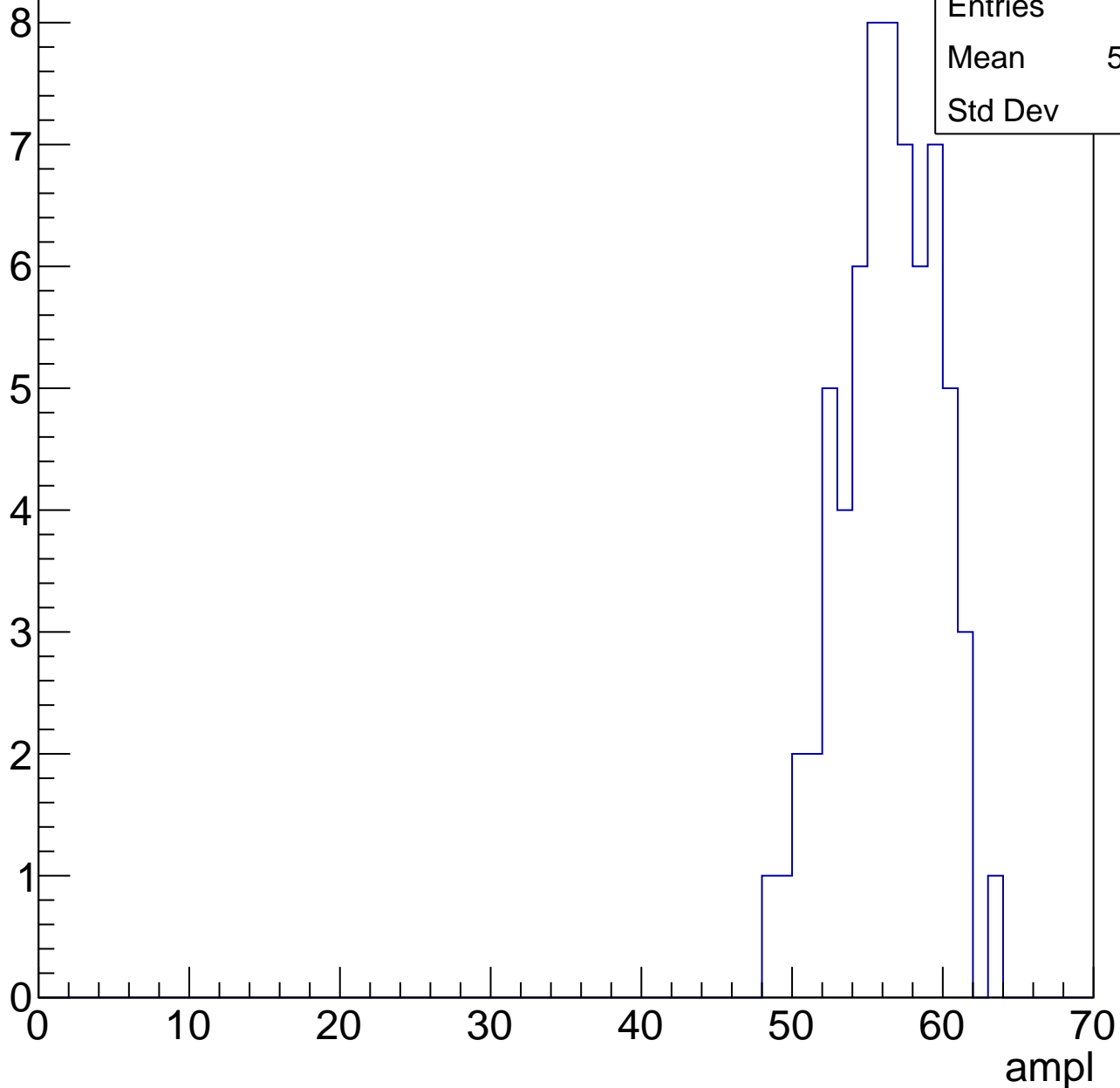


B1L100S, U9-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.89 |
| Std Dev | 3.21 |

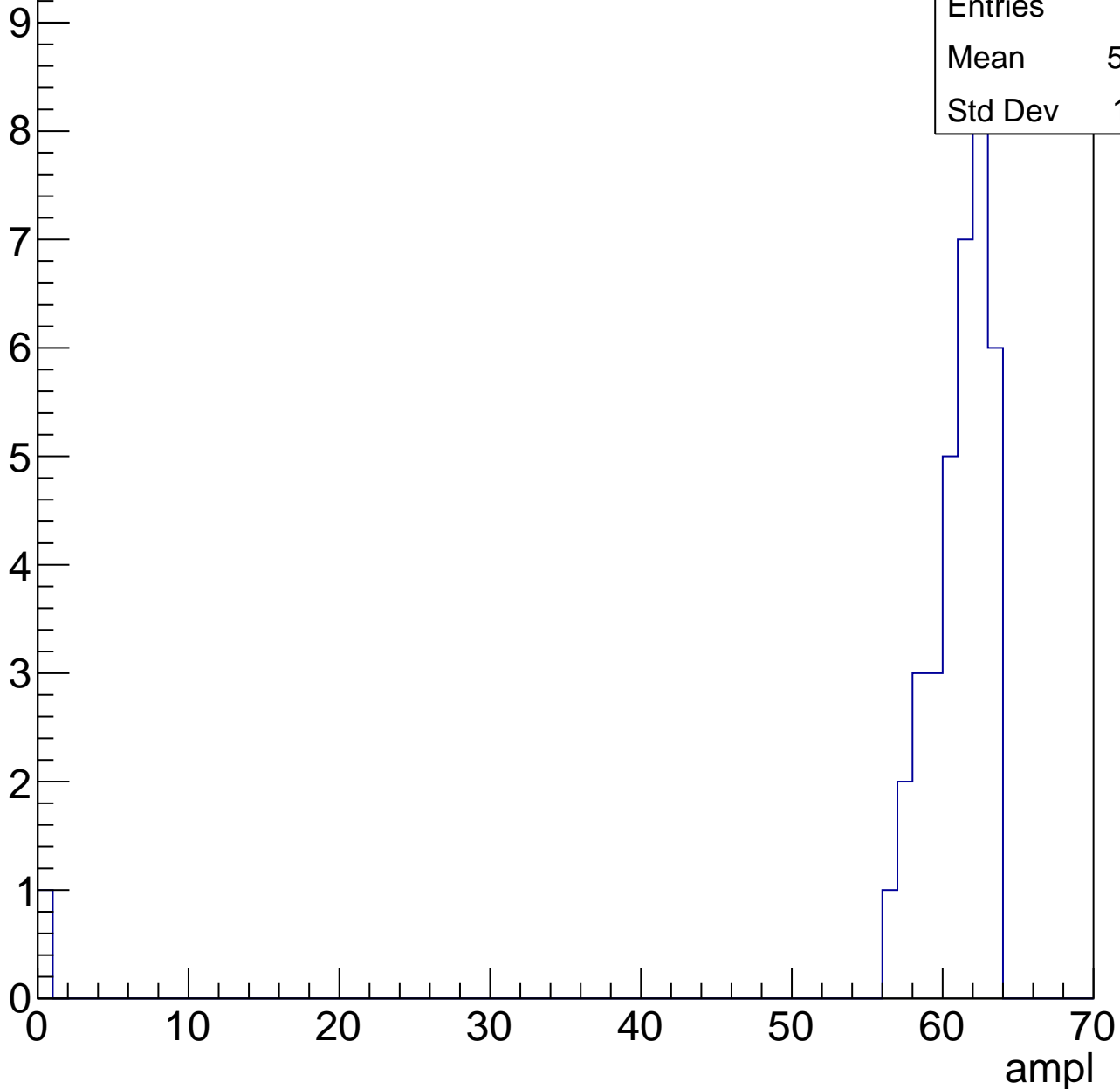


B1L100S, U9-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 59.03 |
| Std Dev | 10.01 |



B1L100S, U9-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch7, adc0

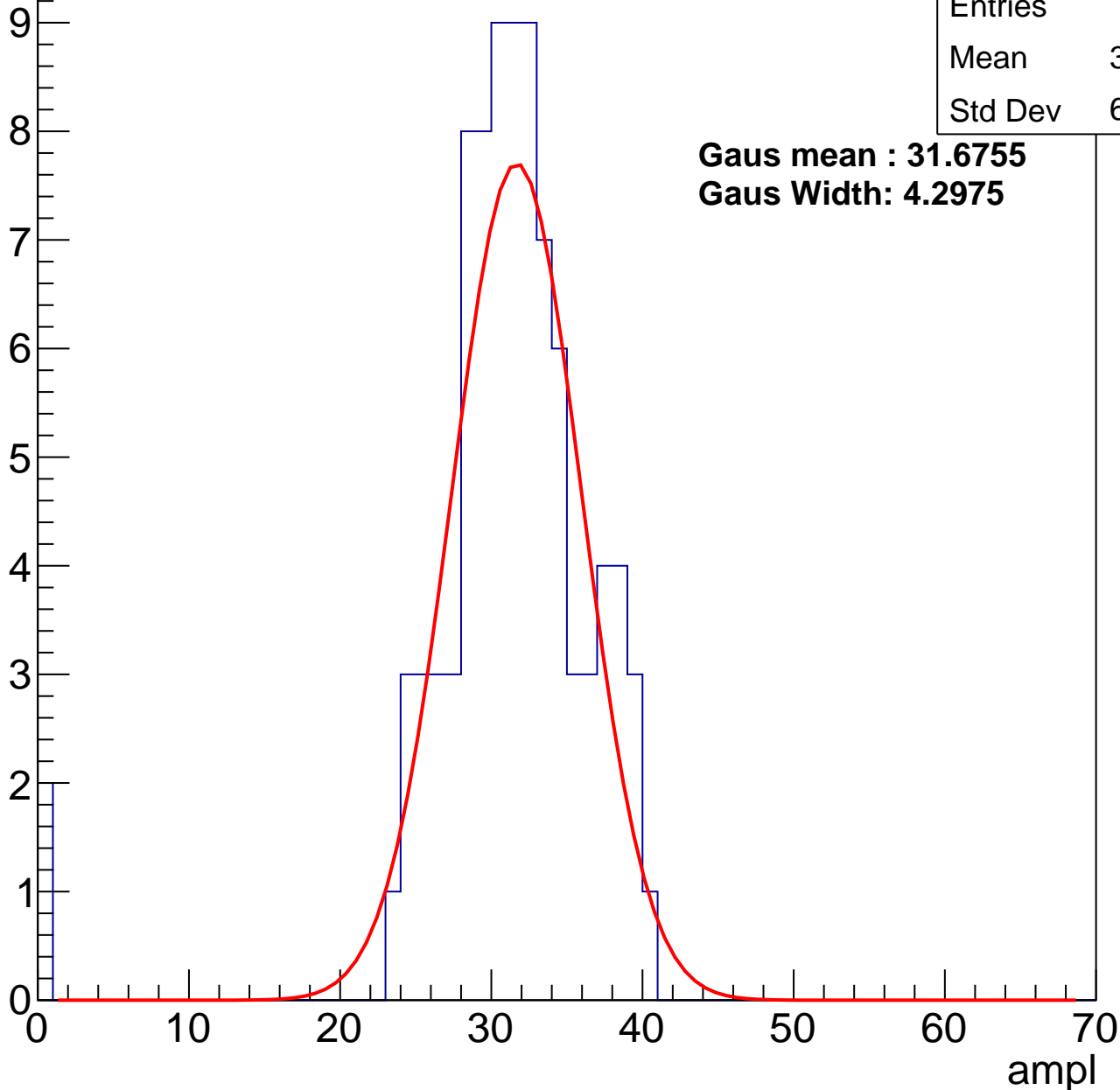
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 30.64 |
| Std Dev | 6.082 |

Gaus mean : 31.6755

Gaus Width: 4.2975



B1L100S, U9-ch7, adc1

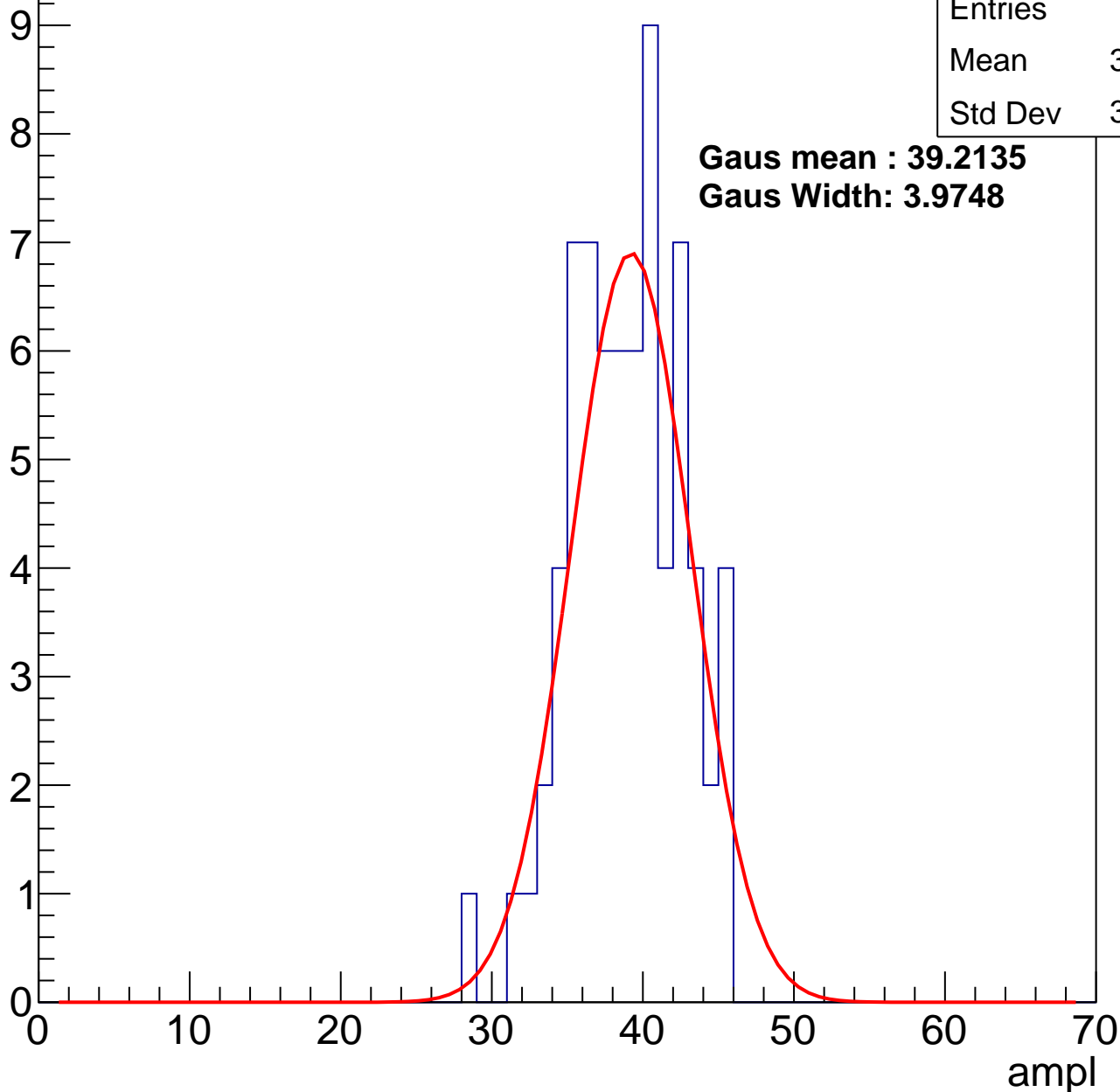
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 38.48 |
| Std Dev | 3.634 |

Gaus mean : 39.2135

Gaus Width: 3.9748



B1L100S, U9-ch7, adc2

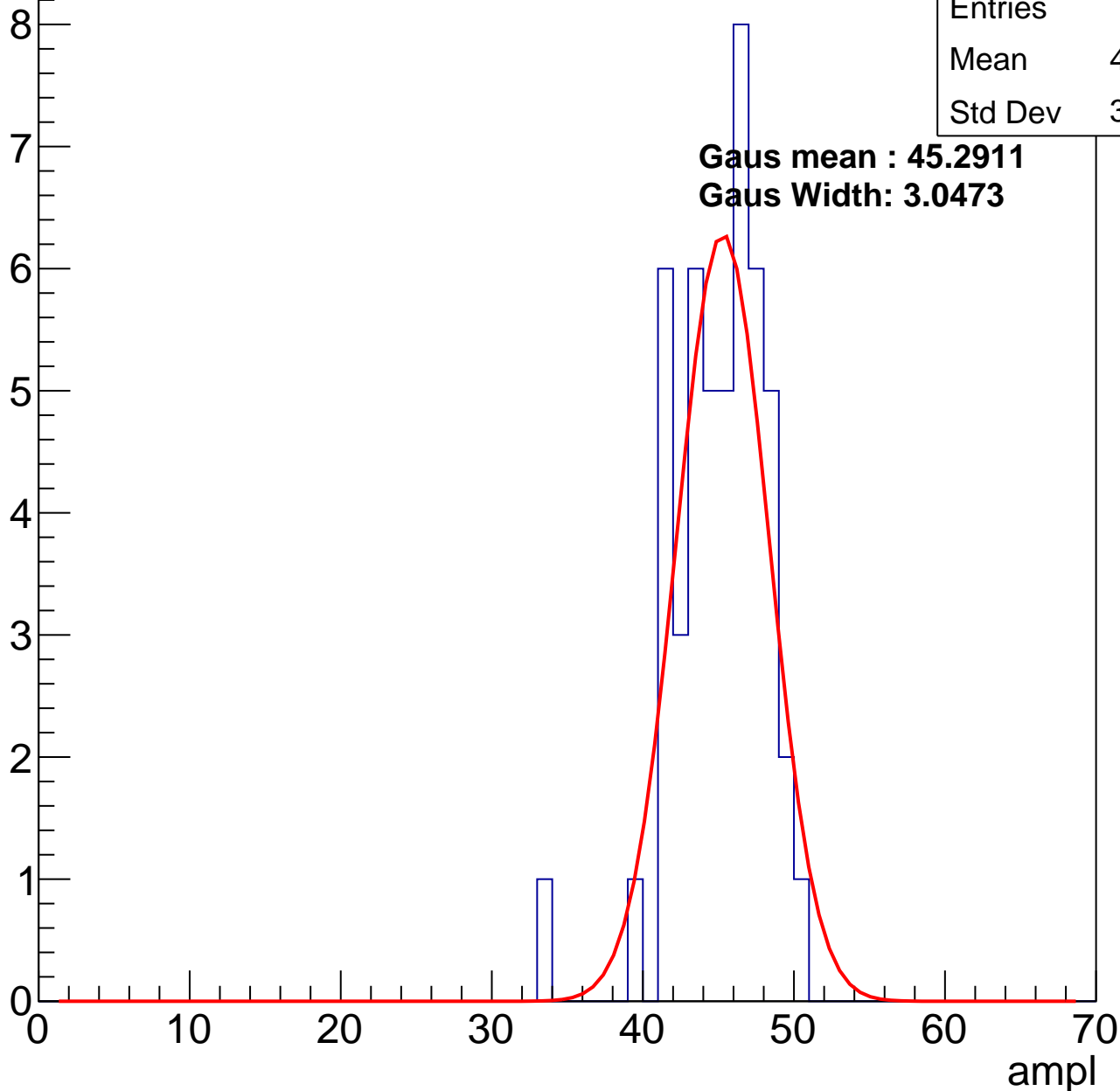
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 44.59 |
| Std Dev | 3.057 |

Gaus mean : 45.2911

Gaus Width: 3.0473

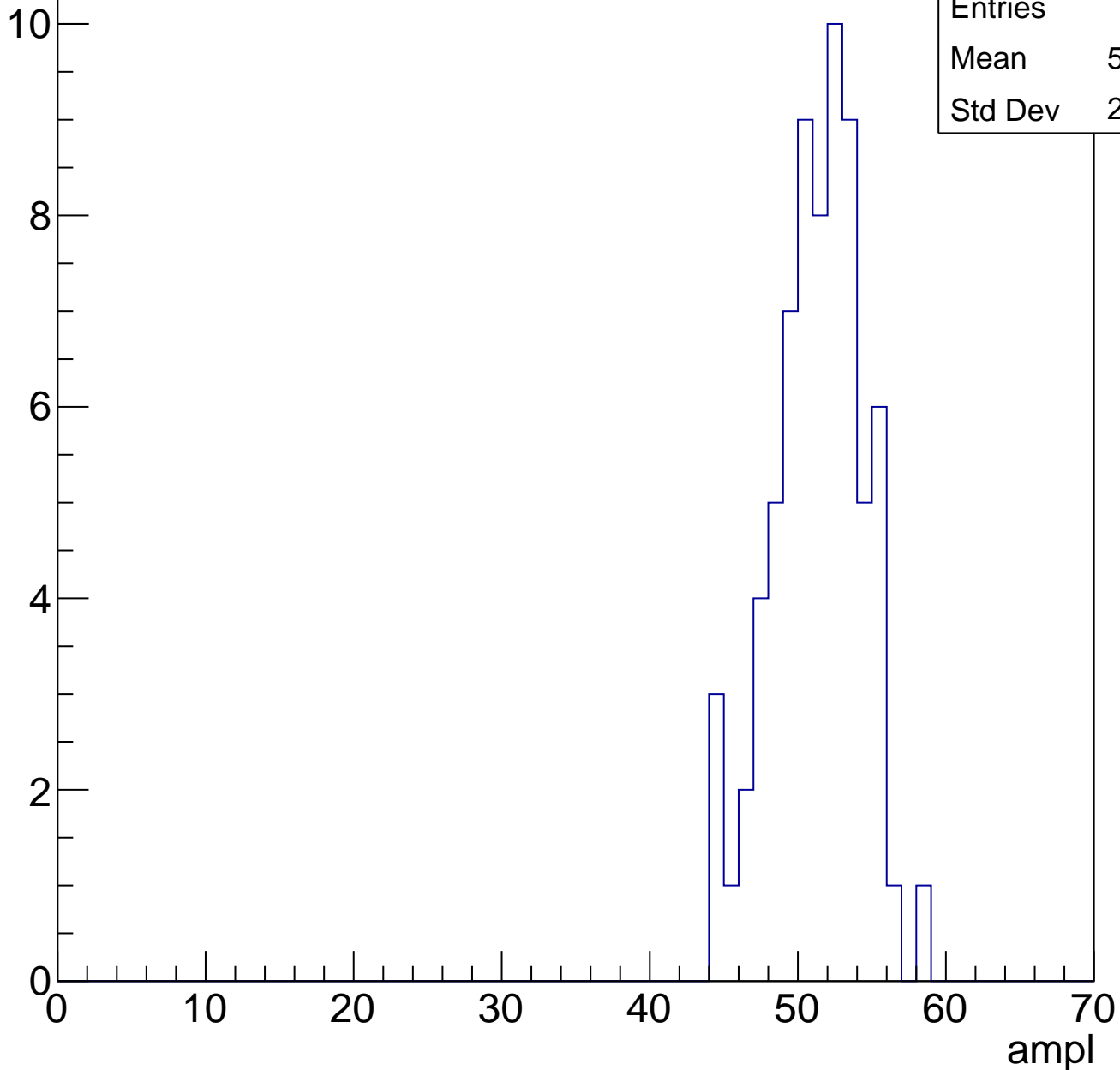


B1L100S, U9-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 50.83 |
| Std Dev | 2.998 |

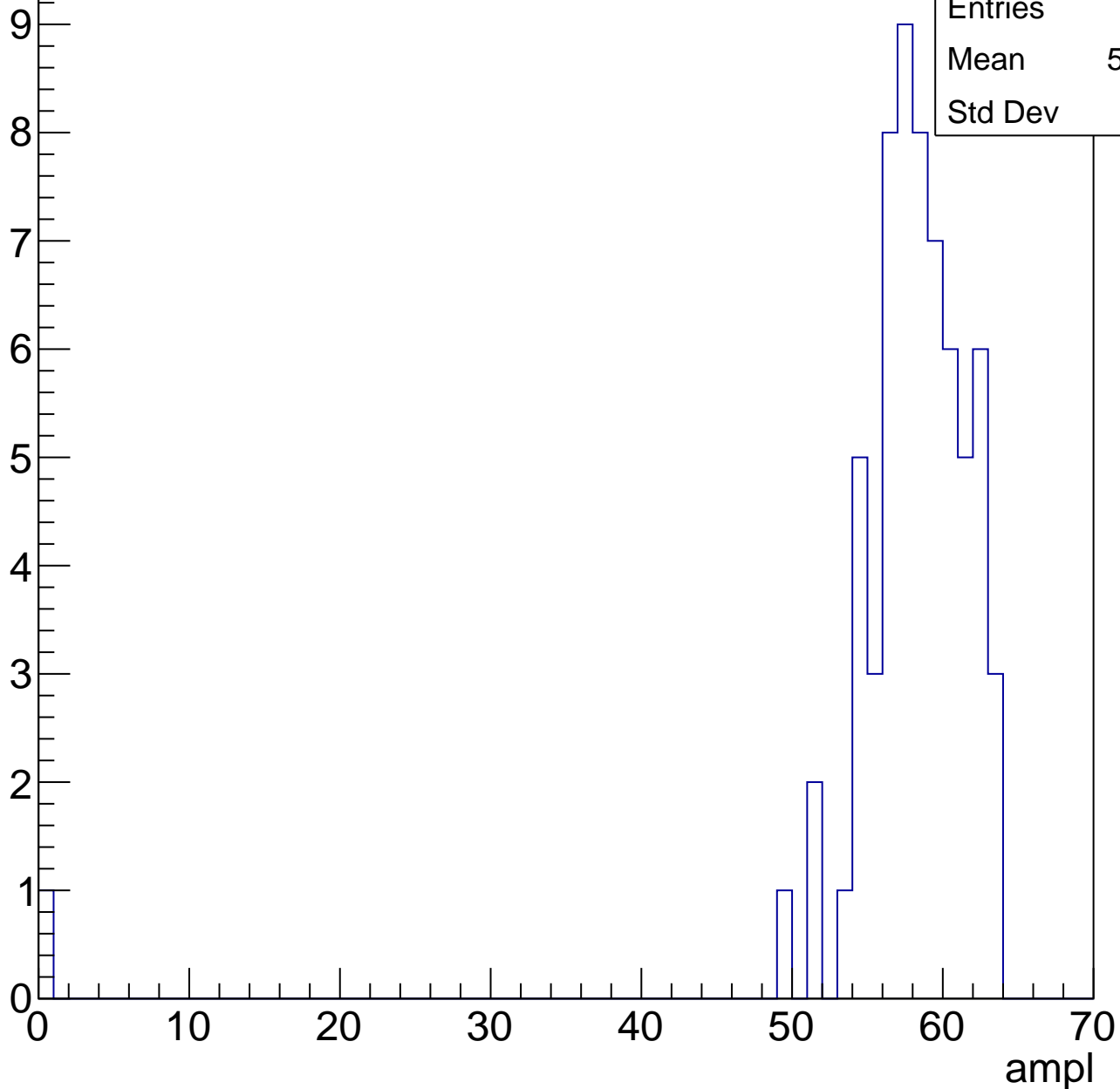
Entry



B1L100S, U9-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

24

Mean

60.58

Std Dev

2.272

0

ampl

0

10

20

30

40

50

60

70

B1L100S, U9-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

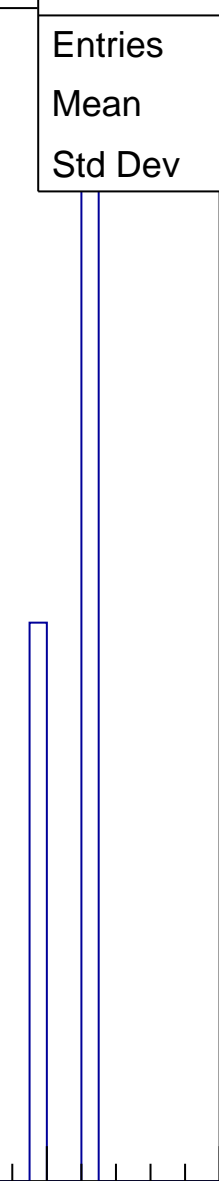
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 3 |
| Mean | 61 |
| Std Dev | 1.414 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch8, adc0

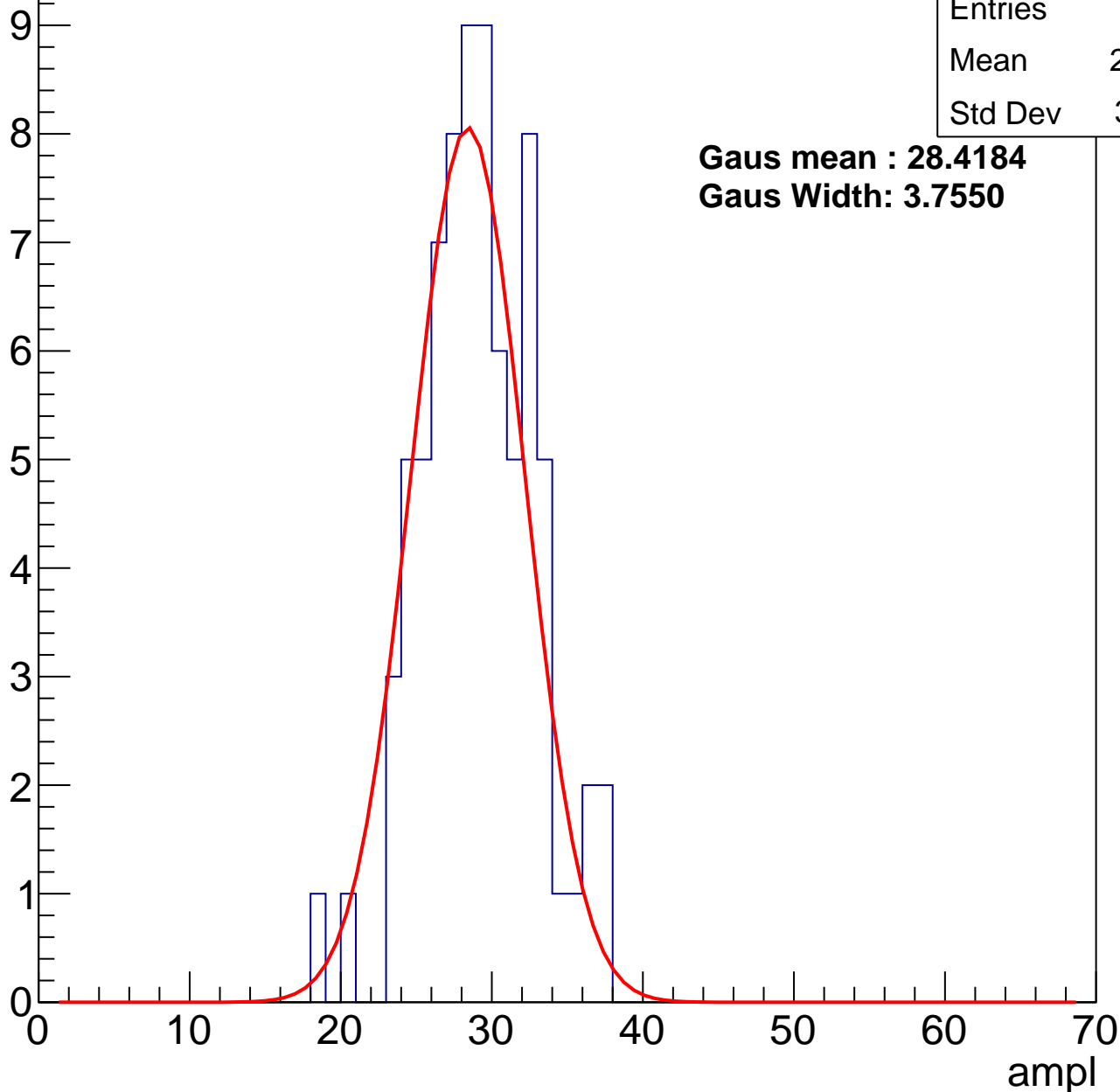
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 28.64 |
| Std Dev | 3.711 |

Gaus mean : 28.4184

Gaus Width: 3.7550



B1L100S, U9-ch8, adc1

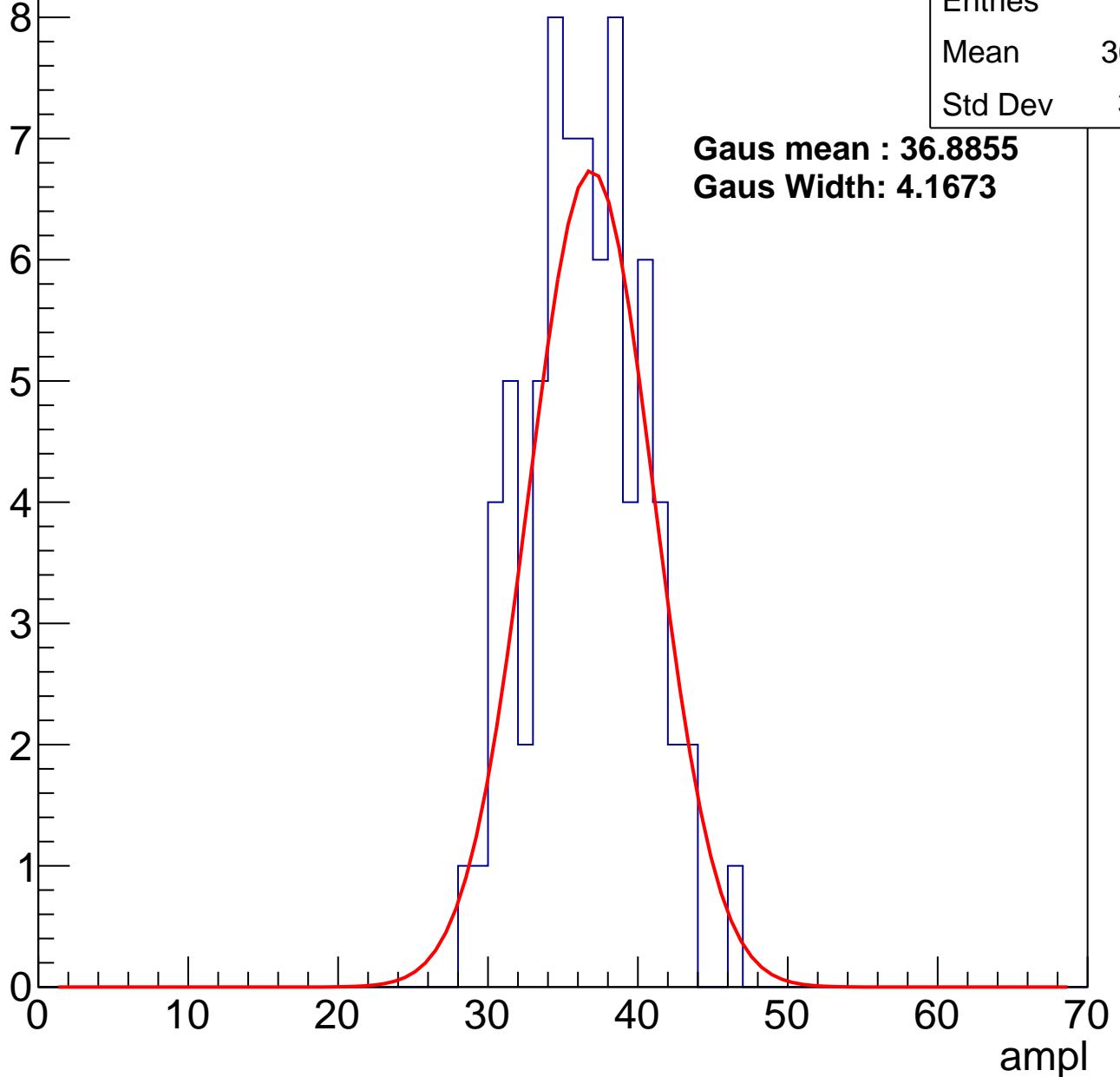
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 36.05 |
| Std Dev | 3.77 |

Gaus mean : 36.8855

Gaus Width: 4.1673



B1L100S, U9-ch8, adc2

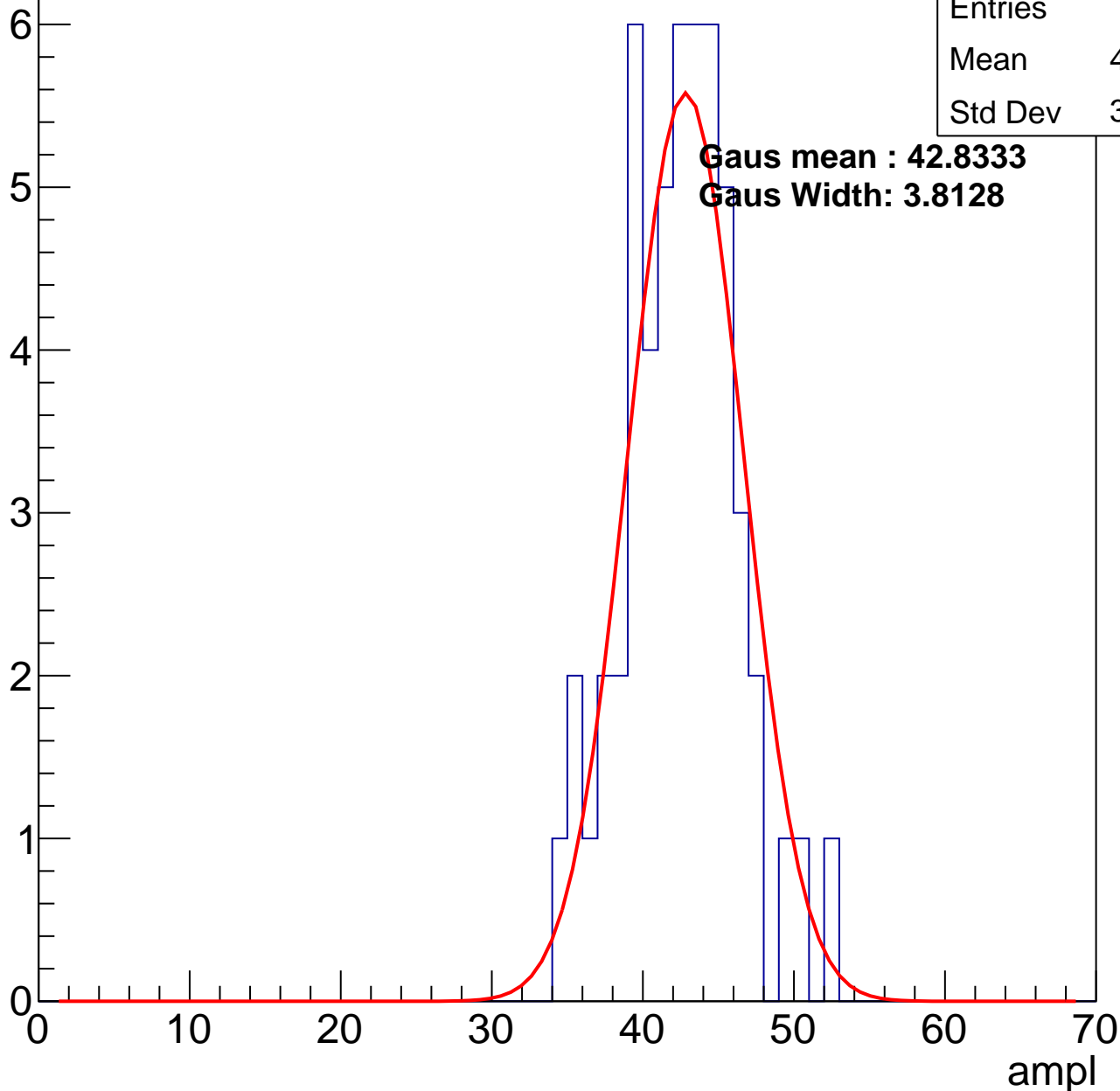
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 42.06 |
| Std Dev | 3.704 |

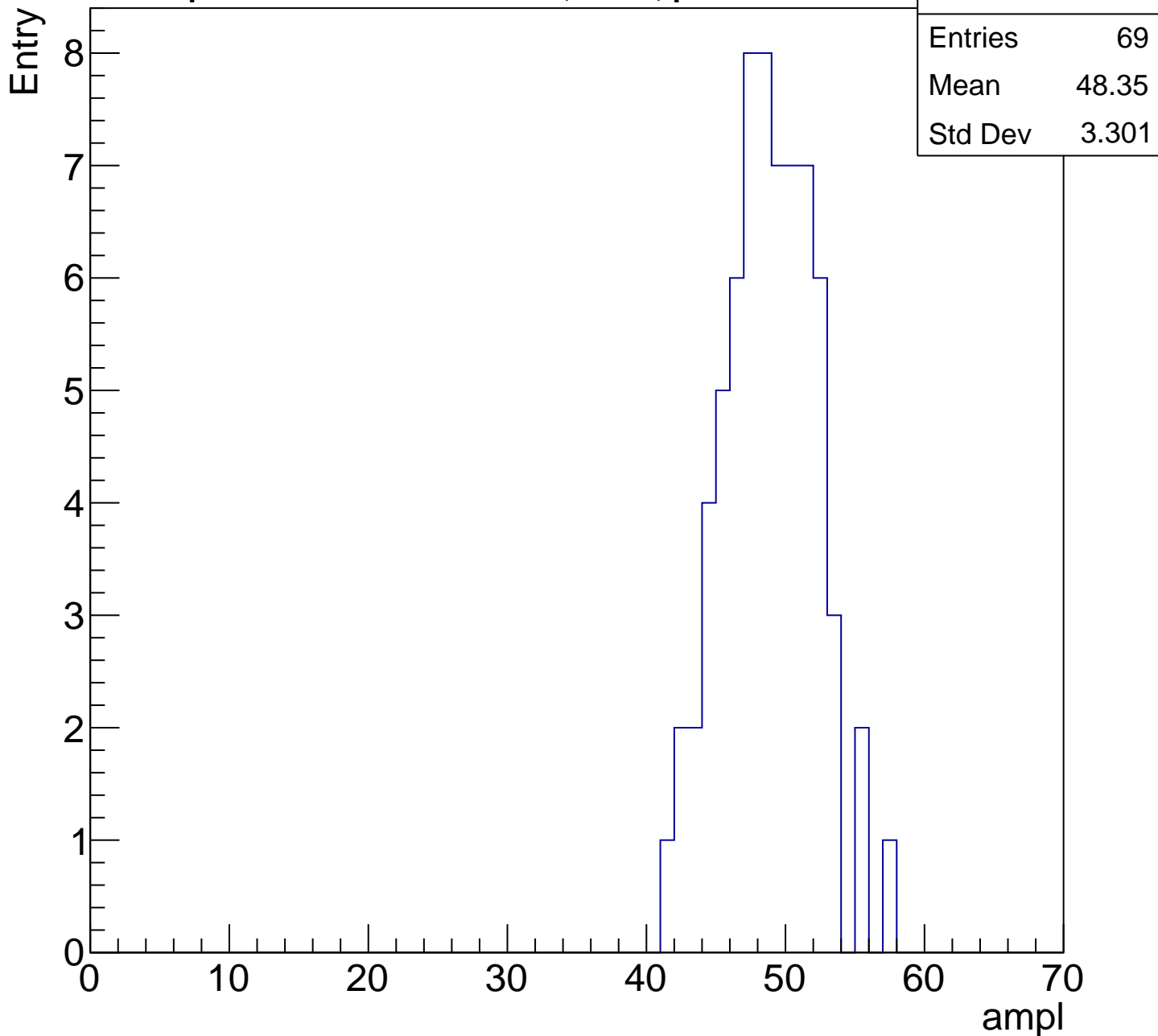
Gaus mean : 42.8333

Gaus Width: 3.8128



B1L100S, U9-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

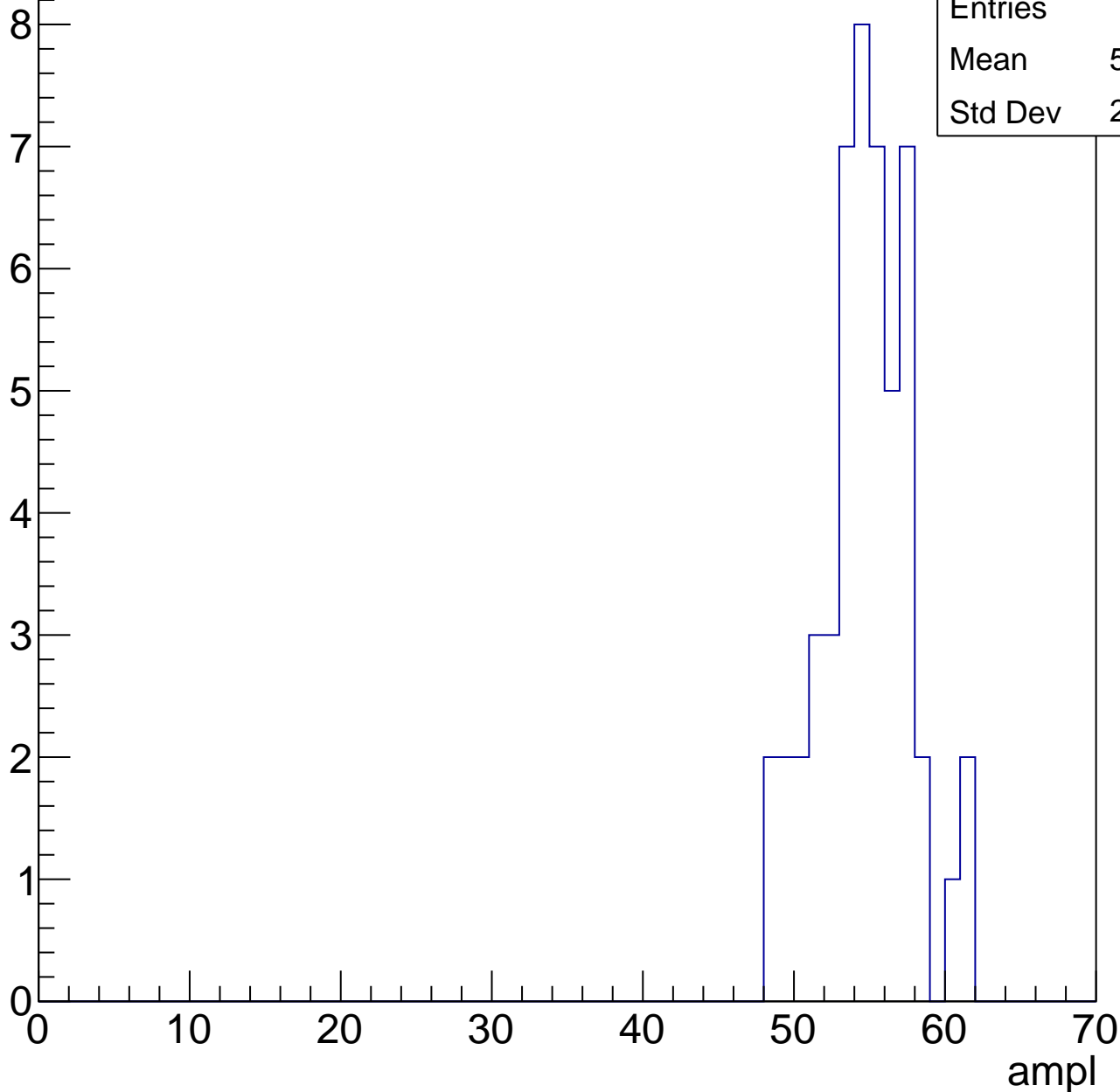


B1L100S, U9-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

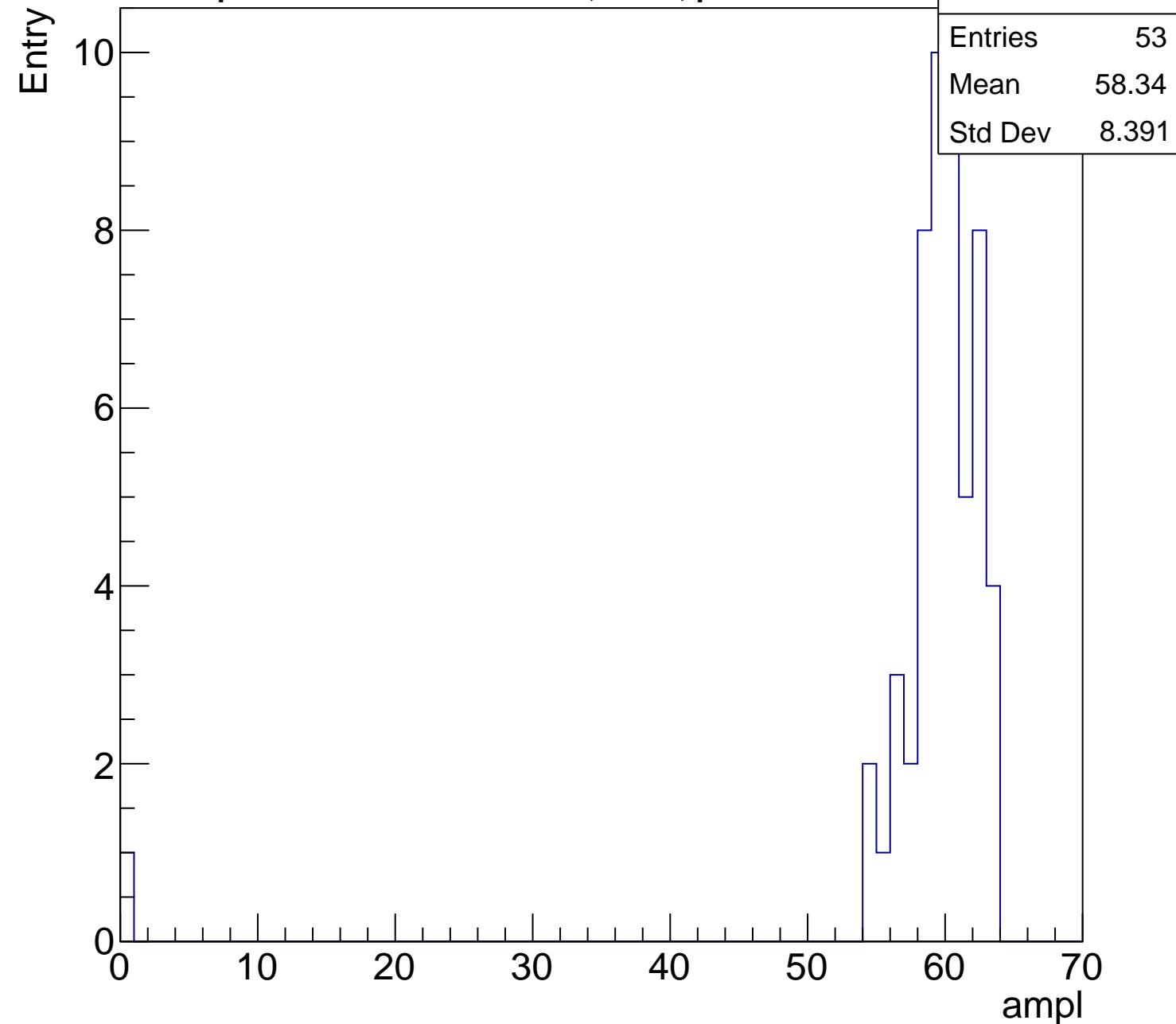
| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.27 |
| Std Dev | 2.978 |



B1L100S, U9-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.34 |
| Std Dev | 8.391 |

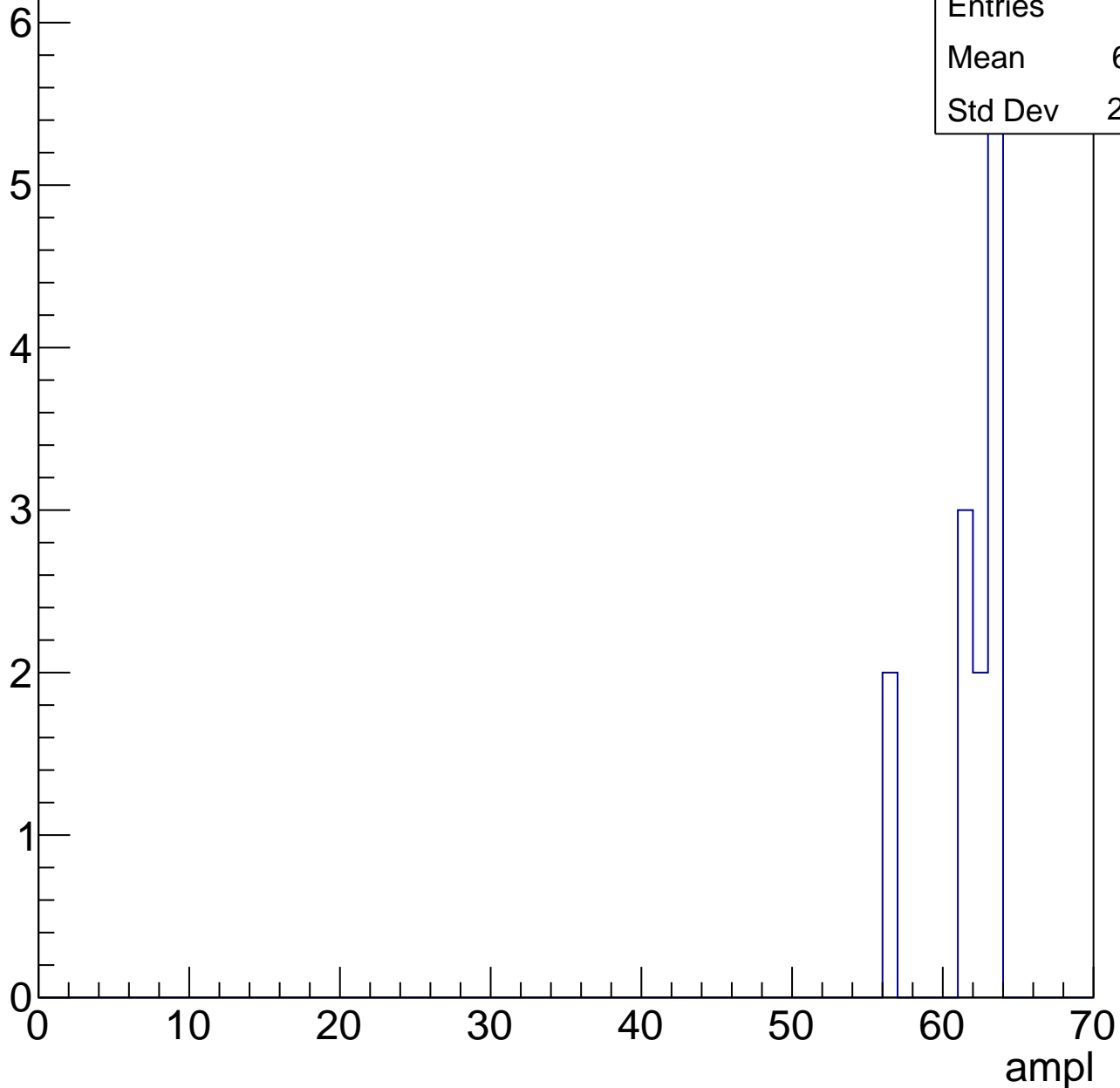


B1L100S, U9-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.31 |
| Std Dev | 2.398 |



B1L100S, U9-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

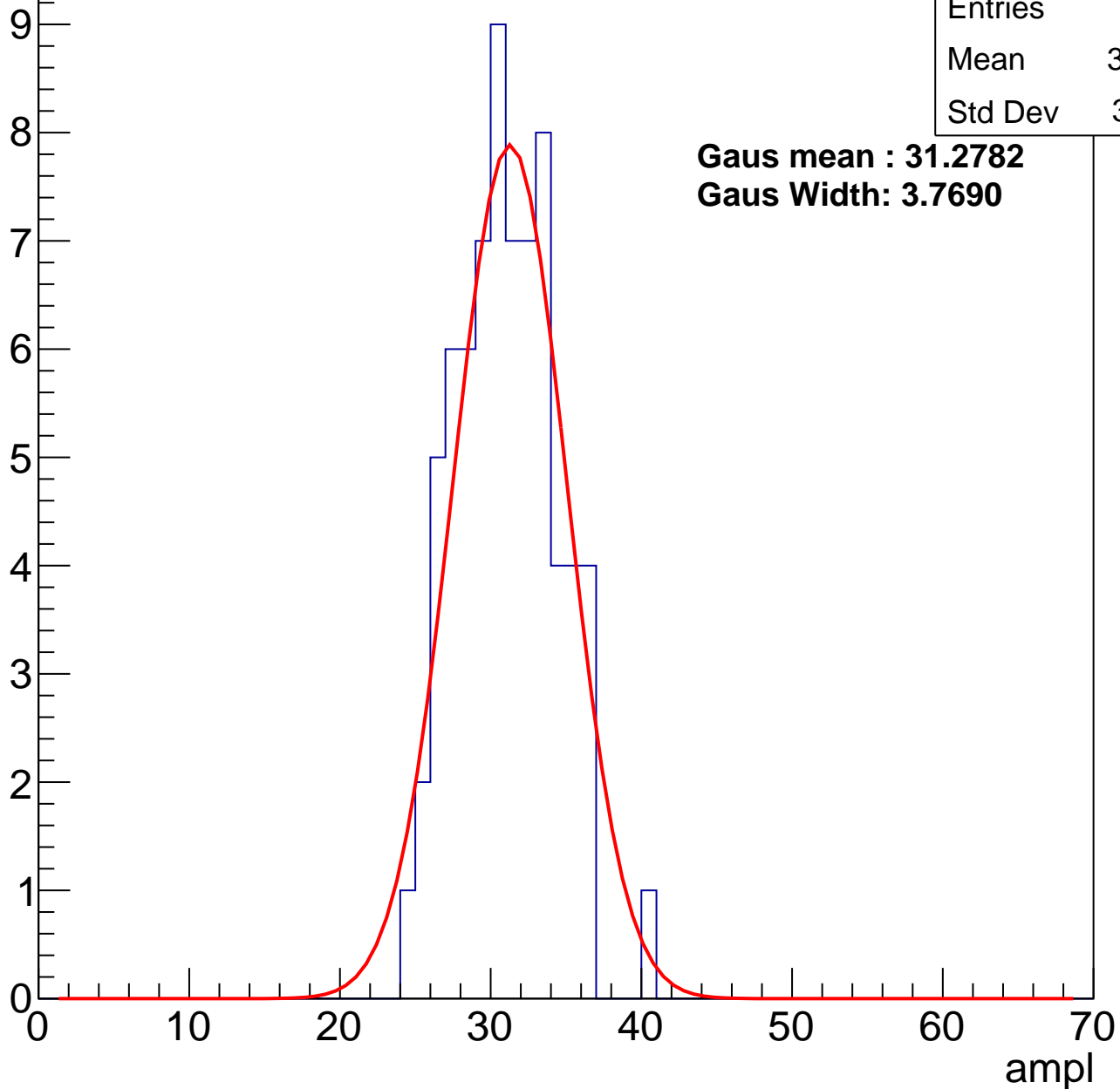
Entry



B1L100S, U9-ch9, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

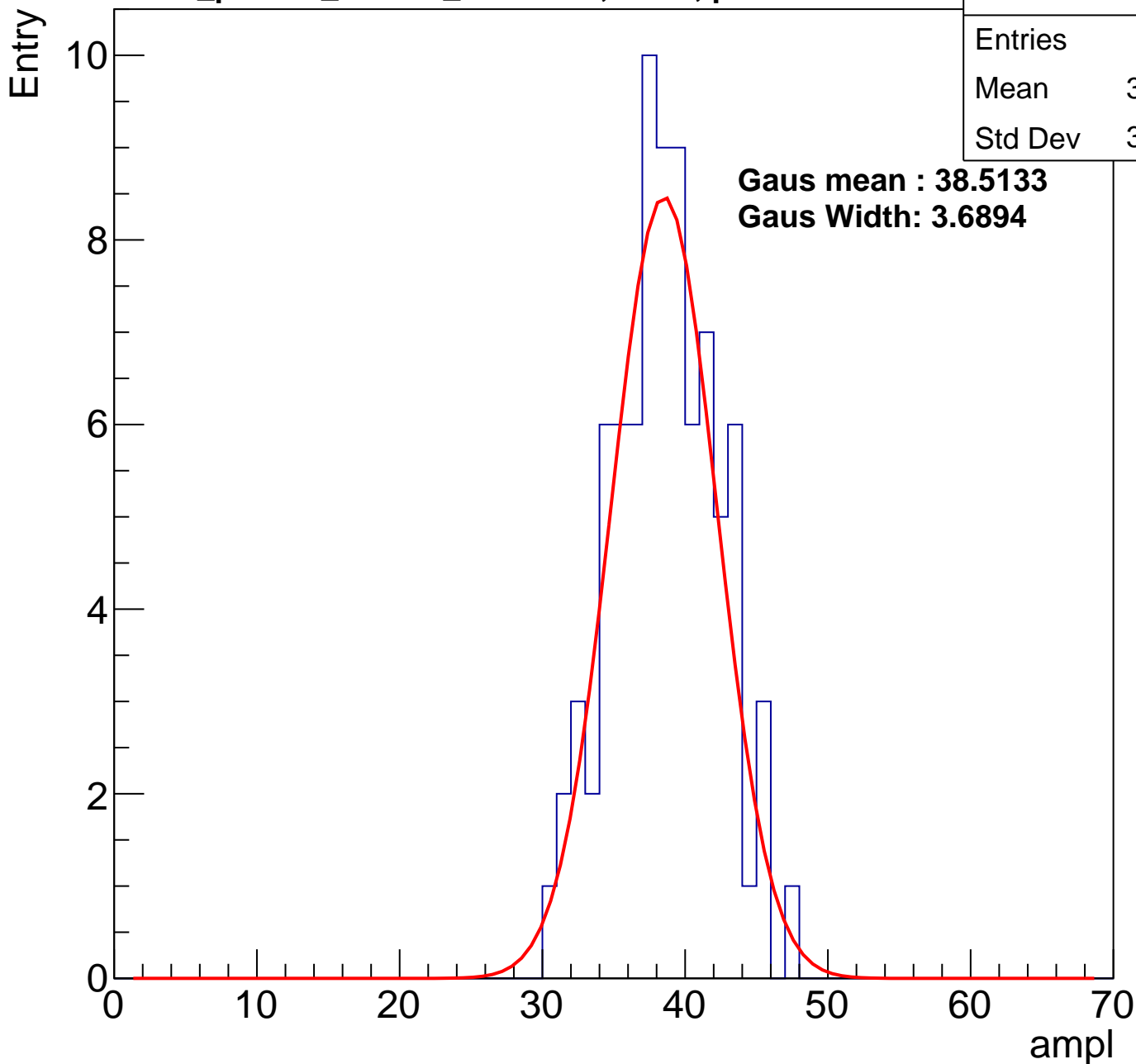


B1L100S, U9-ch9, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 38.17 |
| Std Dev | 3.627 |

Gaus mean : 38.5133
Gaus Width: 3.6894



B1L100S, U9-ch9, adc2

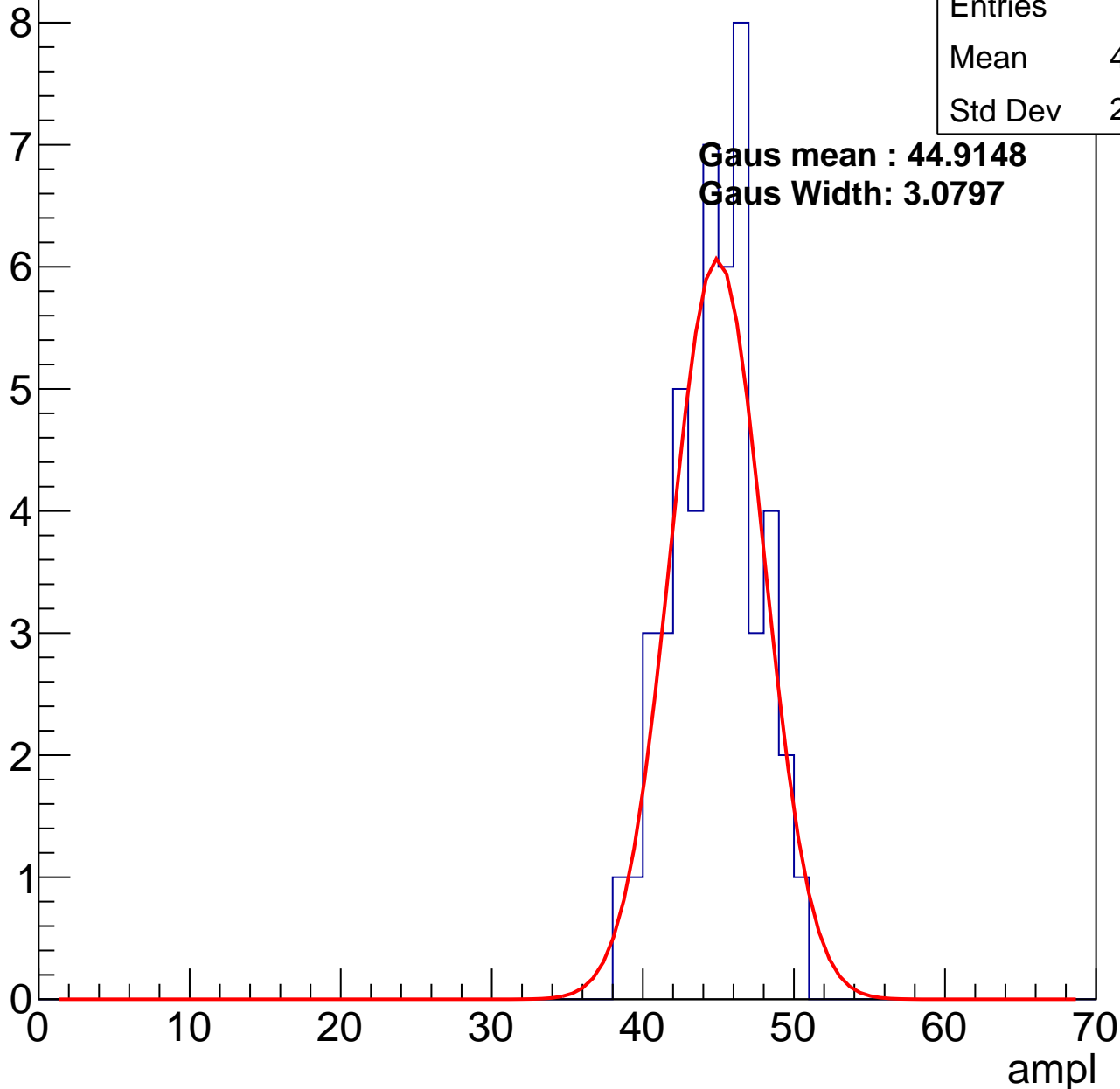
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 44.35 |
| Std Dev | 2.773 |

Gaus mean : 44.9148

Gaus Width: 3.0797



B1L100S, U9-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

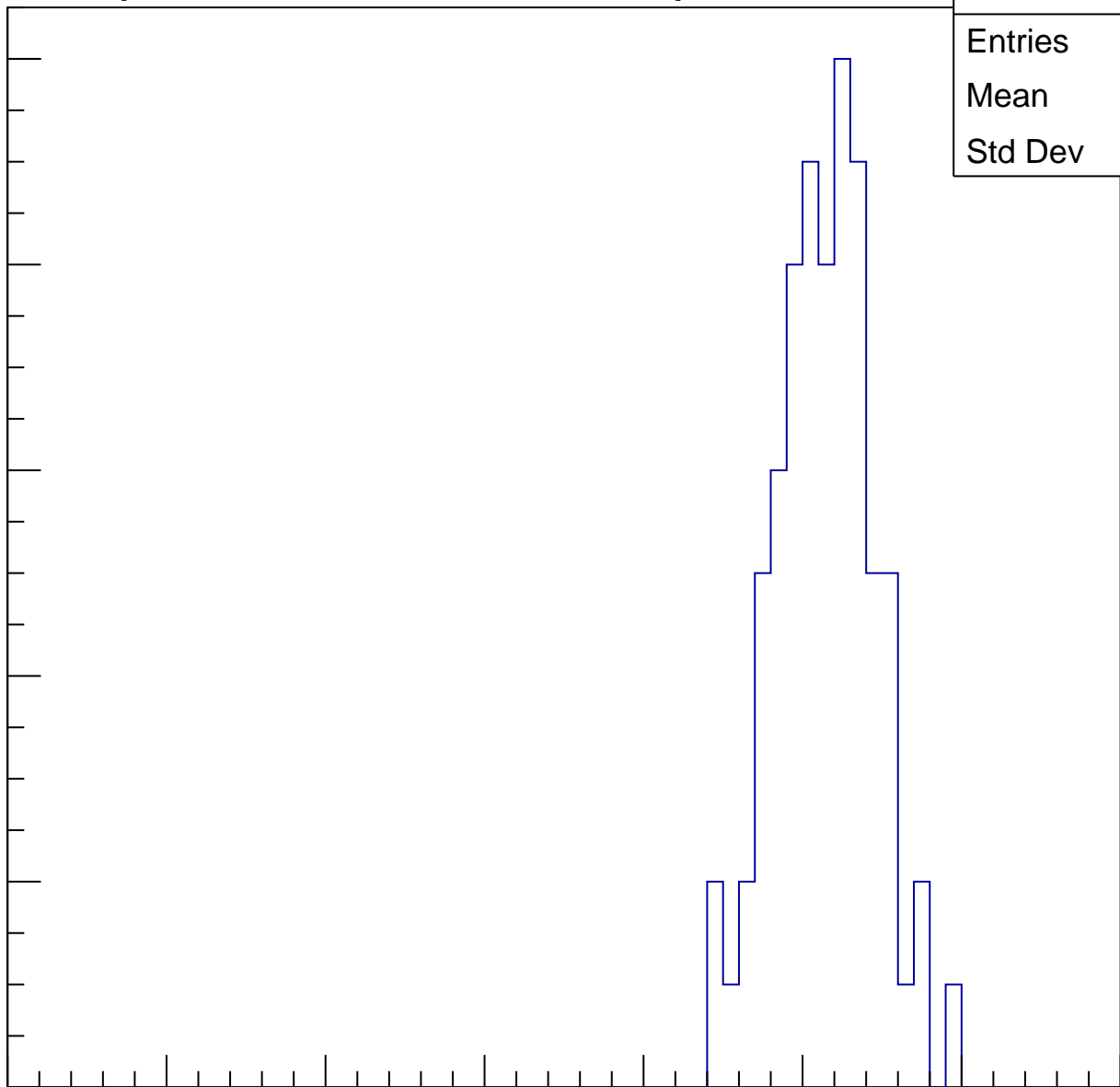
| | |
|---------|-------|
| Entries | 74 |
| Mean | 50.93 |
| Std Dev | 3.055 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

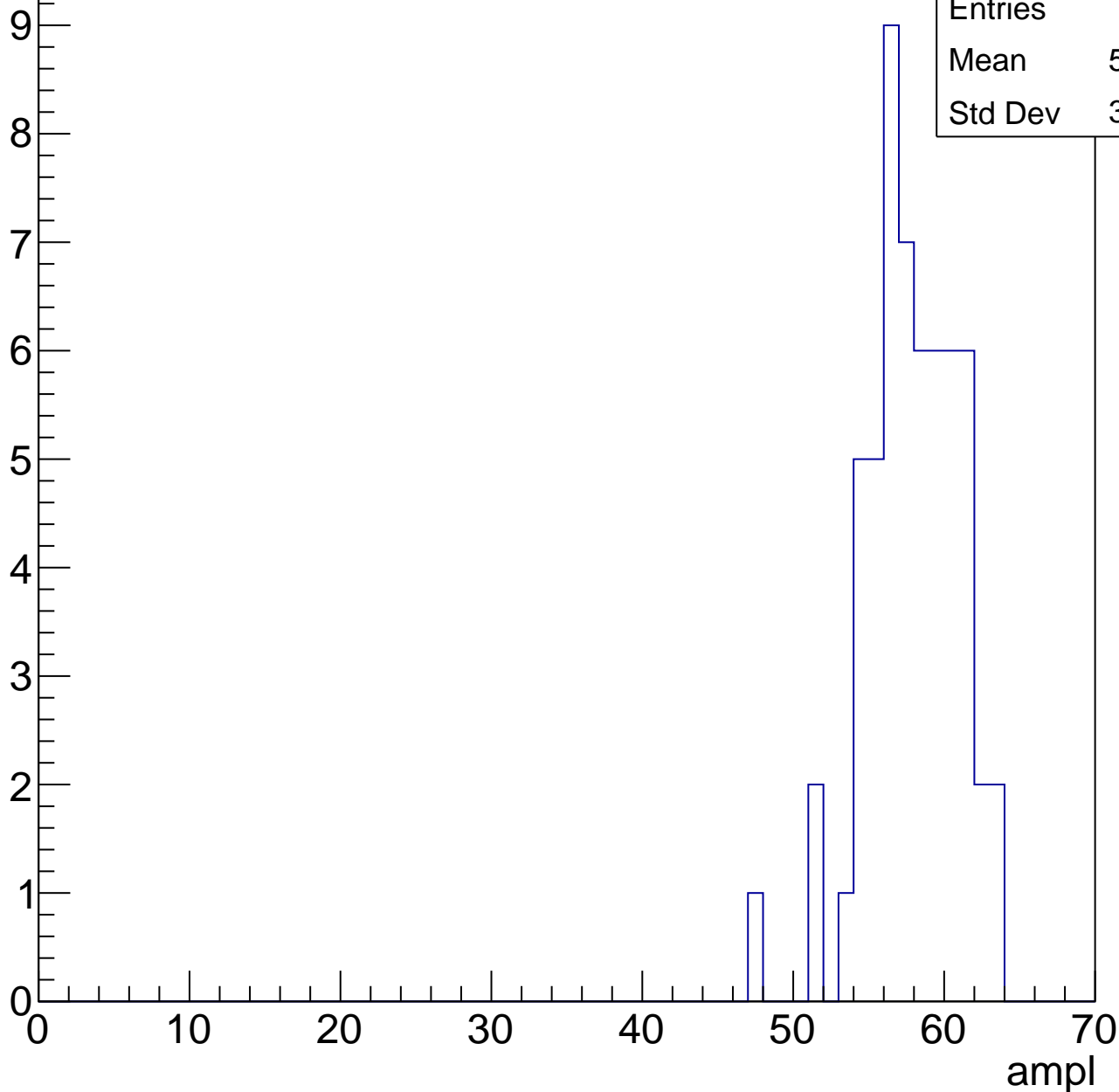


B1L100S, U9-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 57.38 |
| Std Dev | 3.095 |

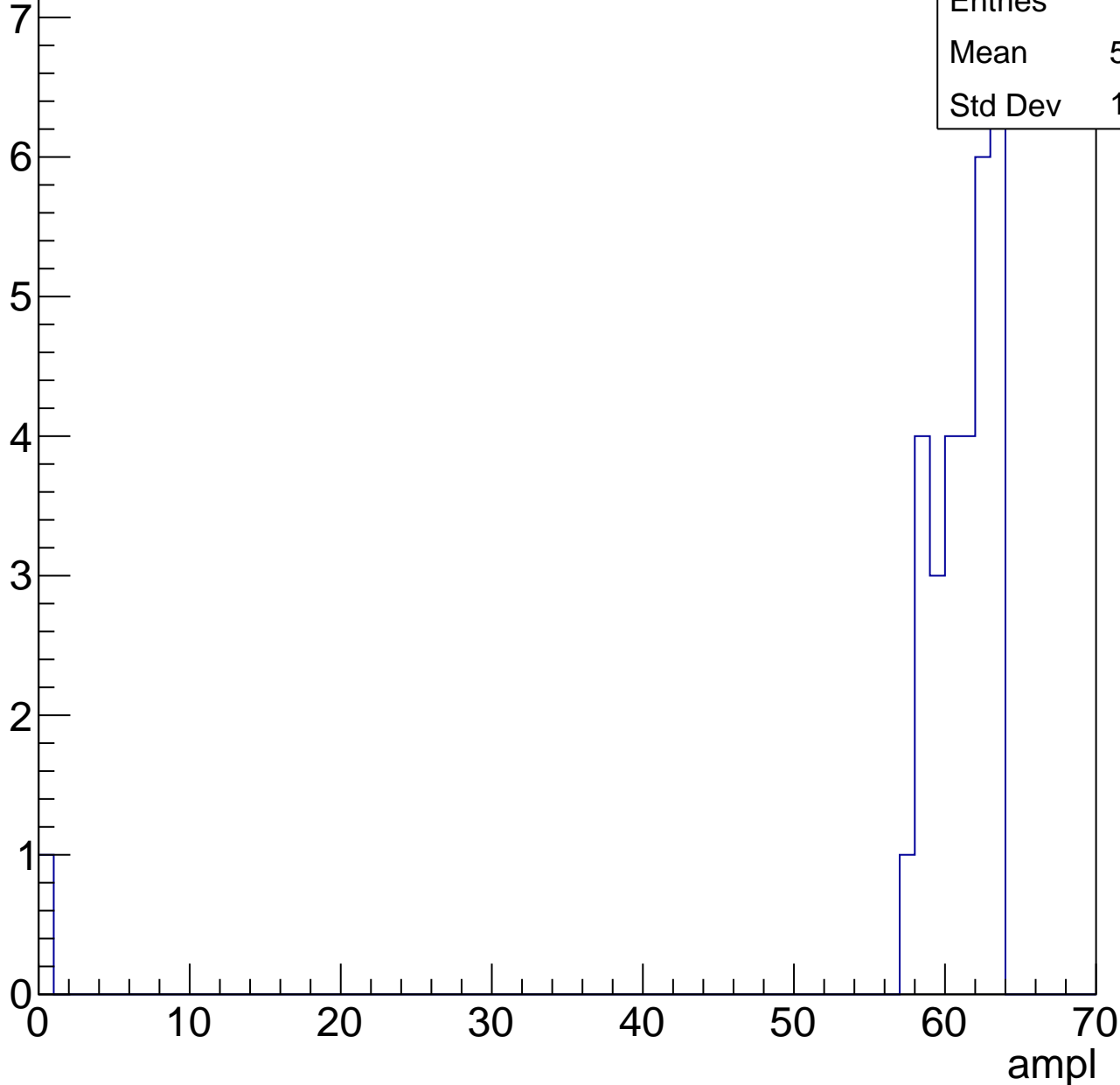


B1L100S, U9-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 30 |
| Mean | 58.77 |
| Std Dev | 11.07 |



B1L100S, U9-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.33 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L100S, U9-ch10, adc0

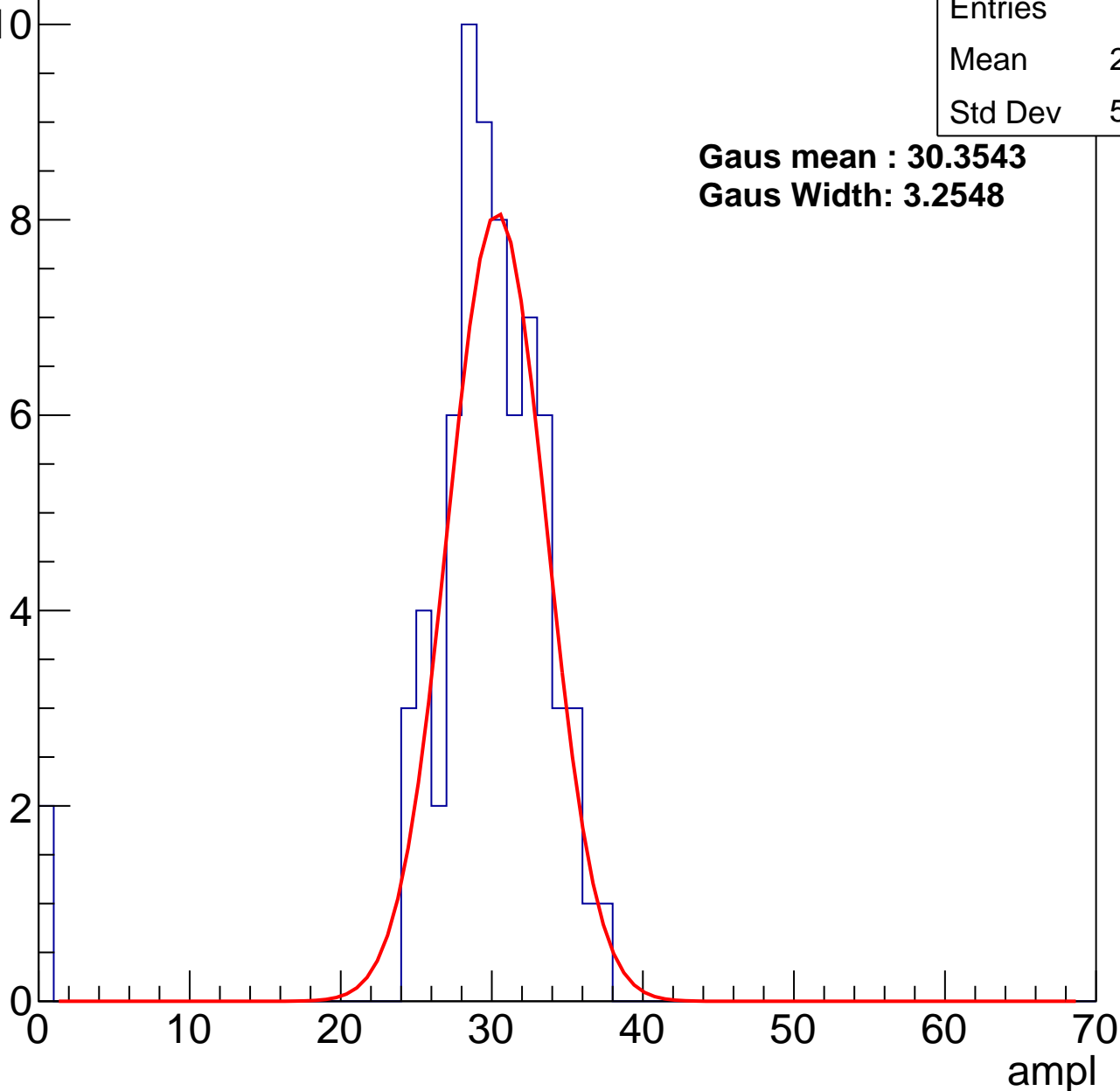
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.94 |
| Std Dev | 5.765 |

Gaus mean : 30.3543

Gaus Width: 3.2548



B1L100S, U9-ch10, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.22 |
| Std Dev | 2.78 |

Gaus mean : 36.8904

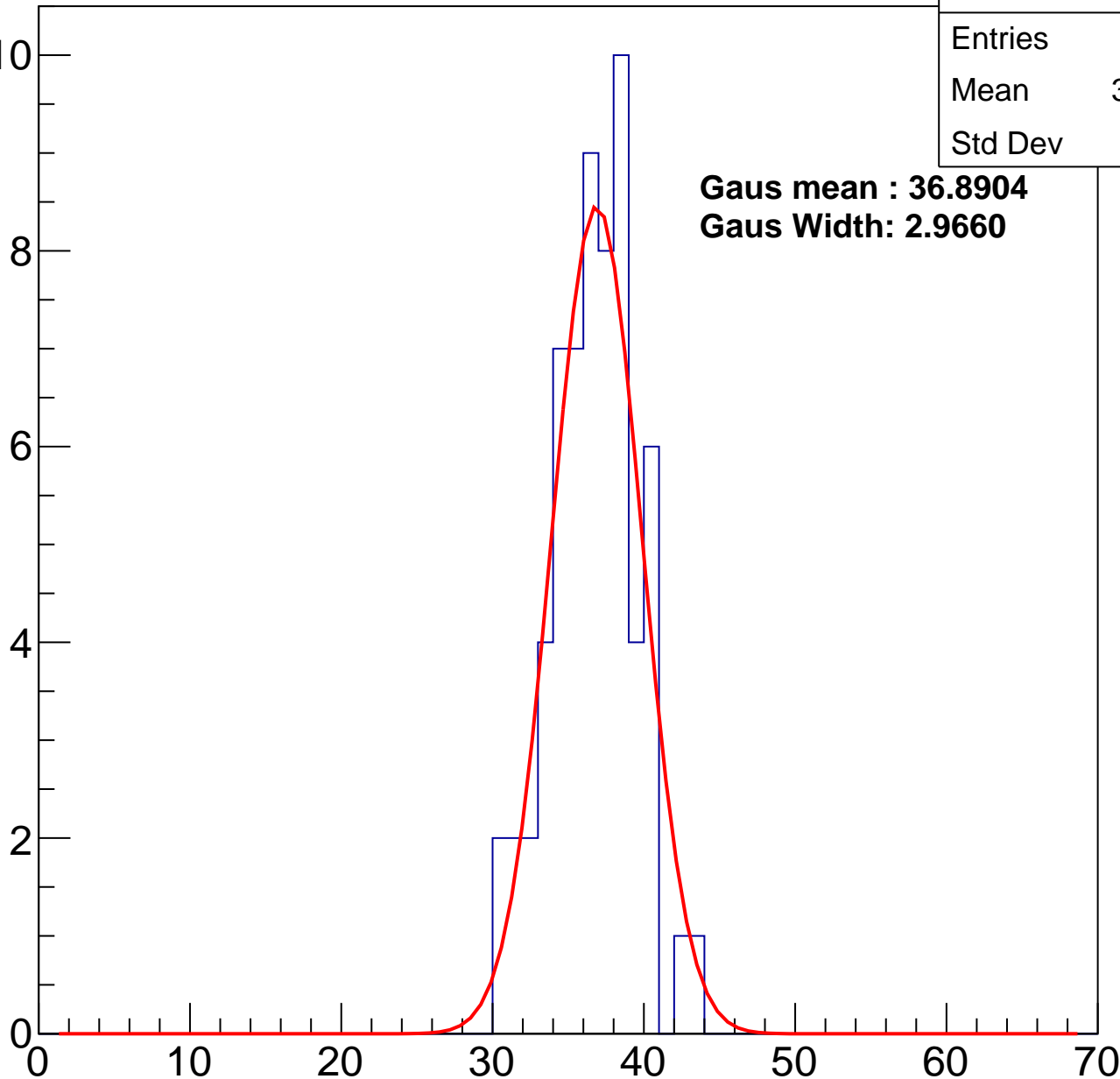
Gaus Width: 2.9660

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch10, adc2

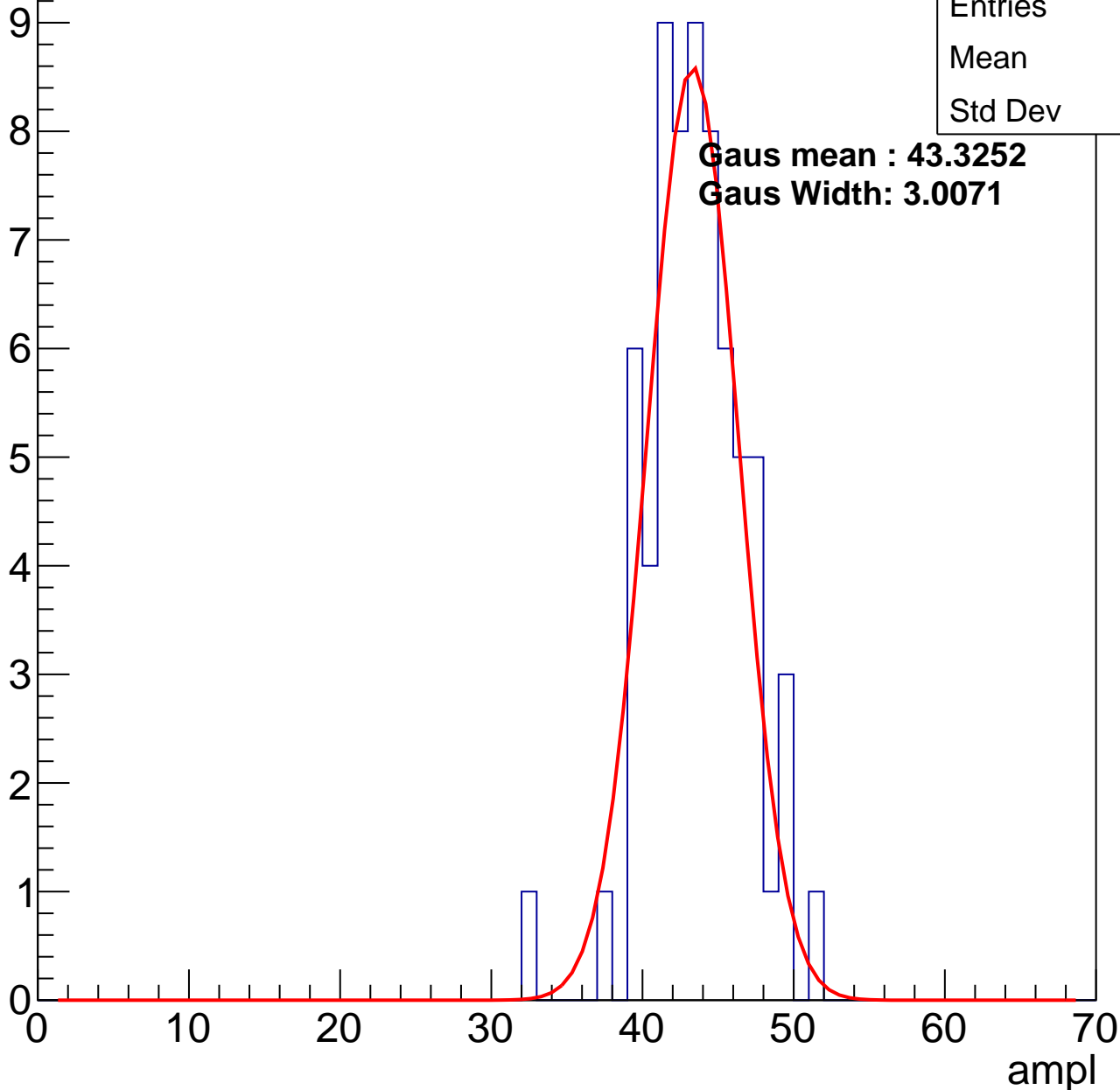
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 67 |
| Mean | 43.1 |
| Std Dev | 3.2 |

Gaus mean : 43.3252

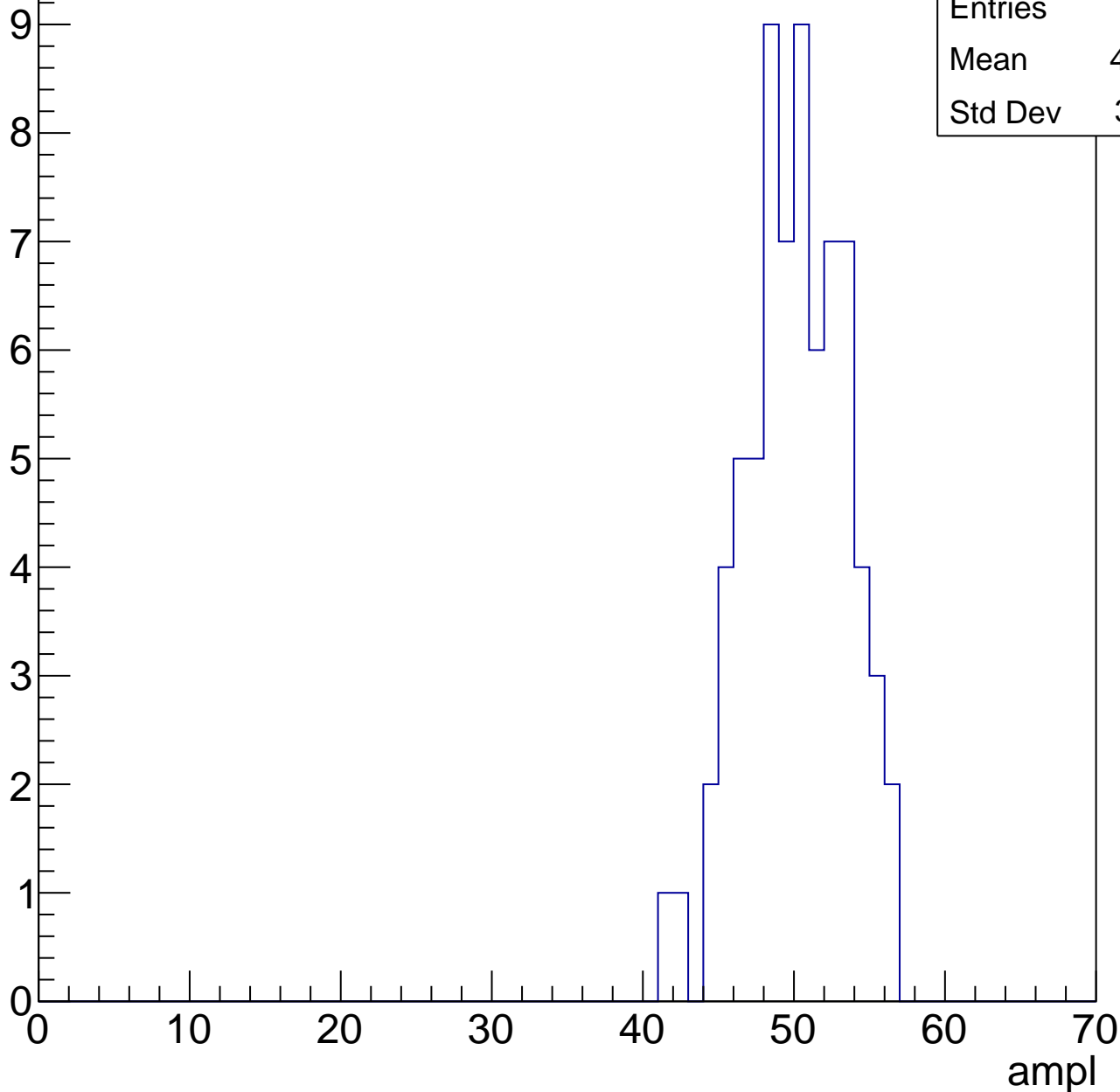
Gaus Width: 3.0071



B1L100S, U9-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

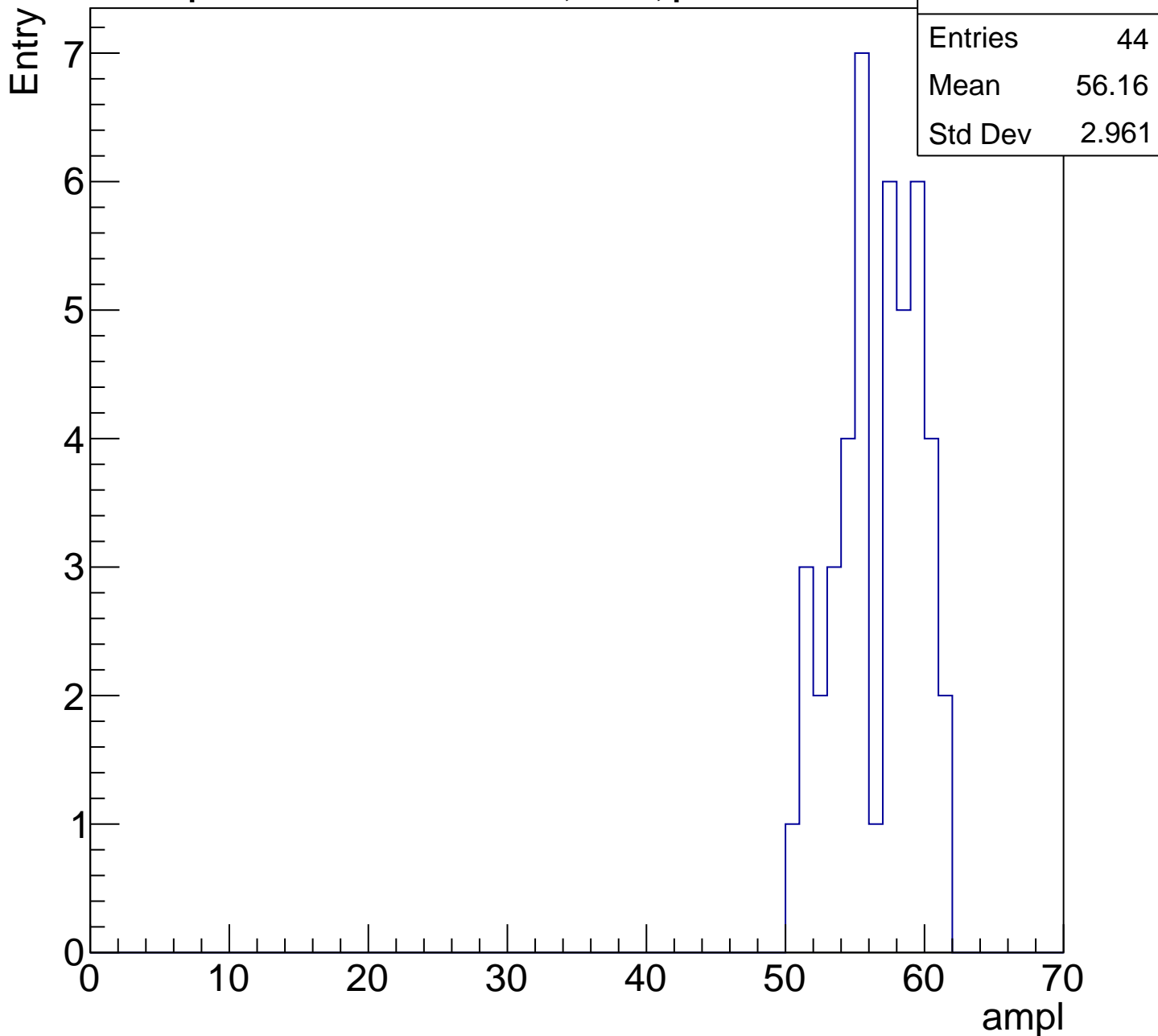
Entry



| | |
|---------|-------|
| Entries | 72 |
| Mean | 49.65 |
| Std Dev | 3.301 |

B1L100S, U9-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

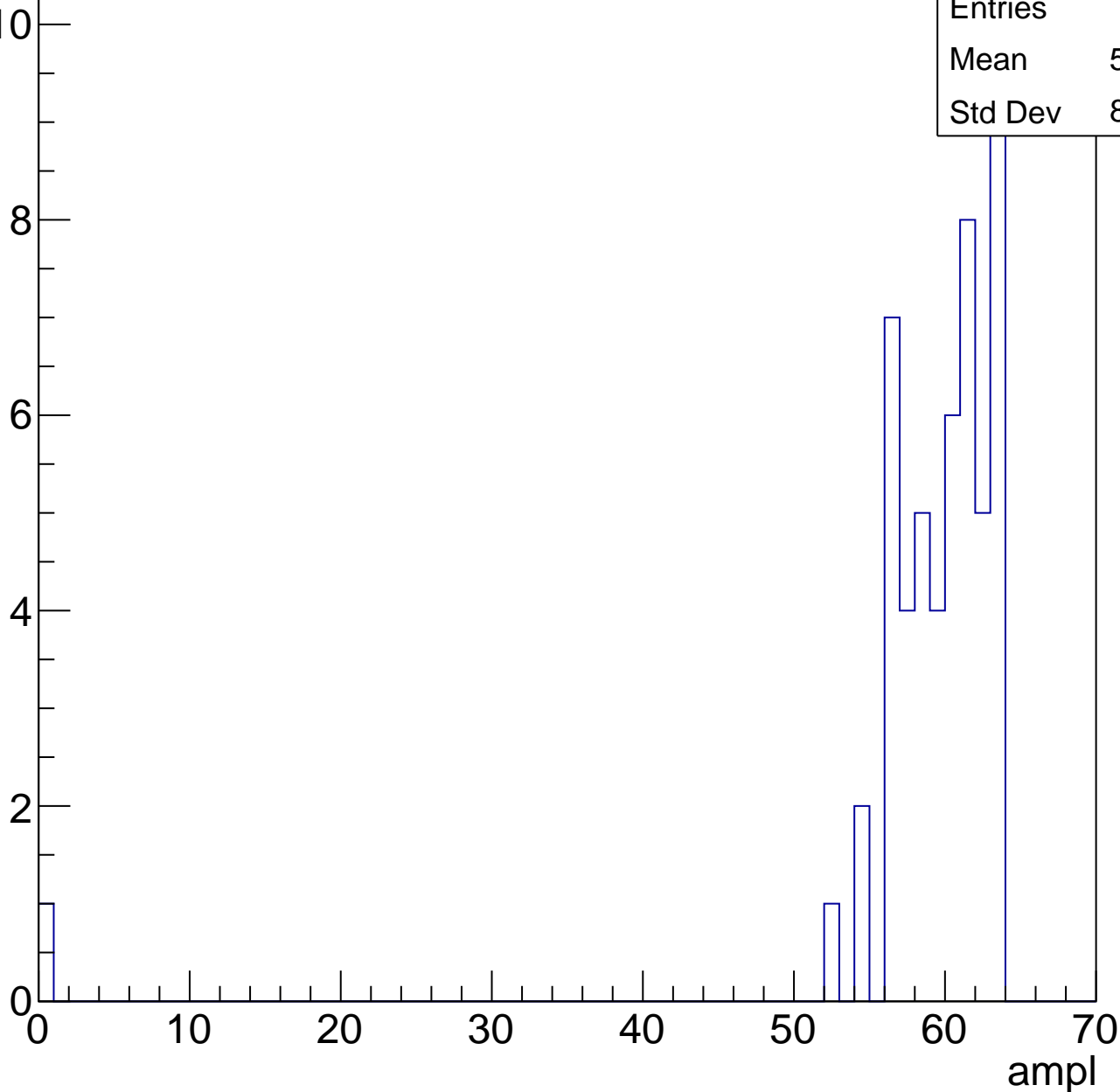


B1L100S, U9-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

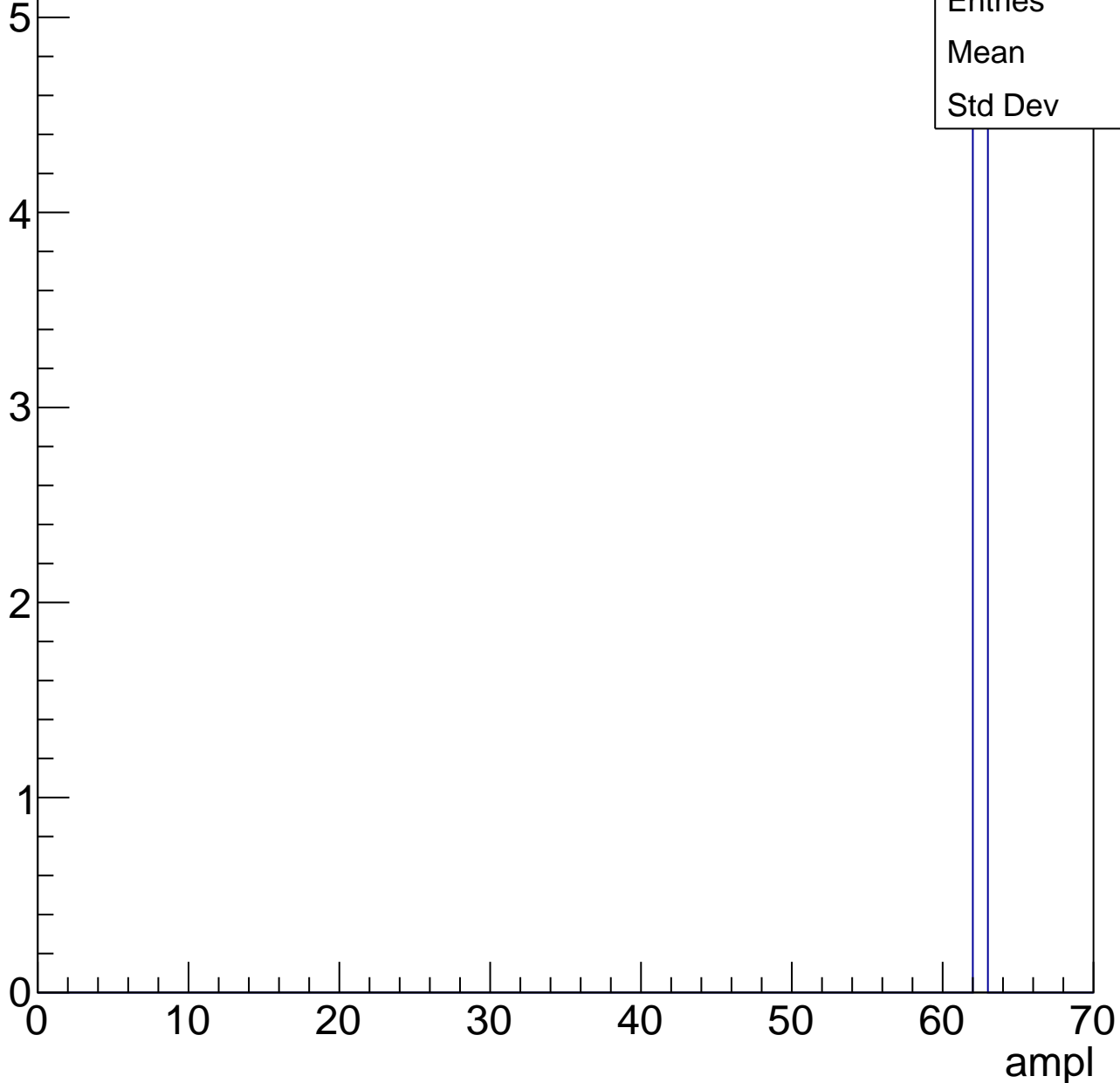
| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.38 |
| Std Dev | 8.563 |



B1L100S, U9-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch11, adc0

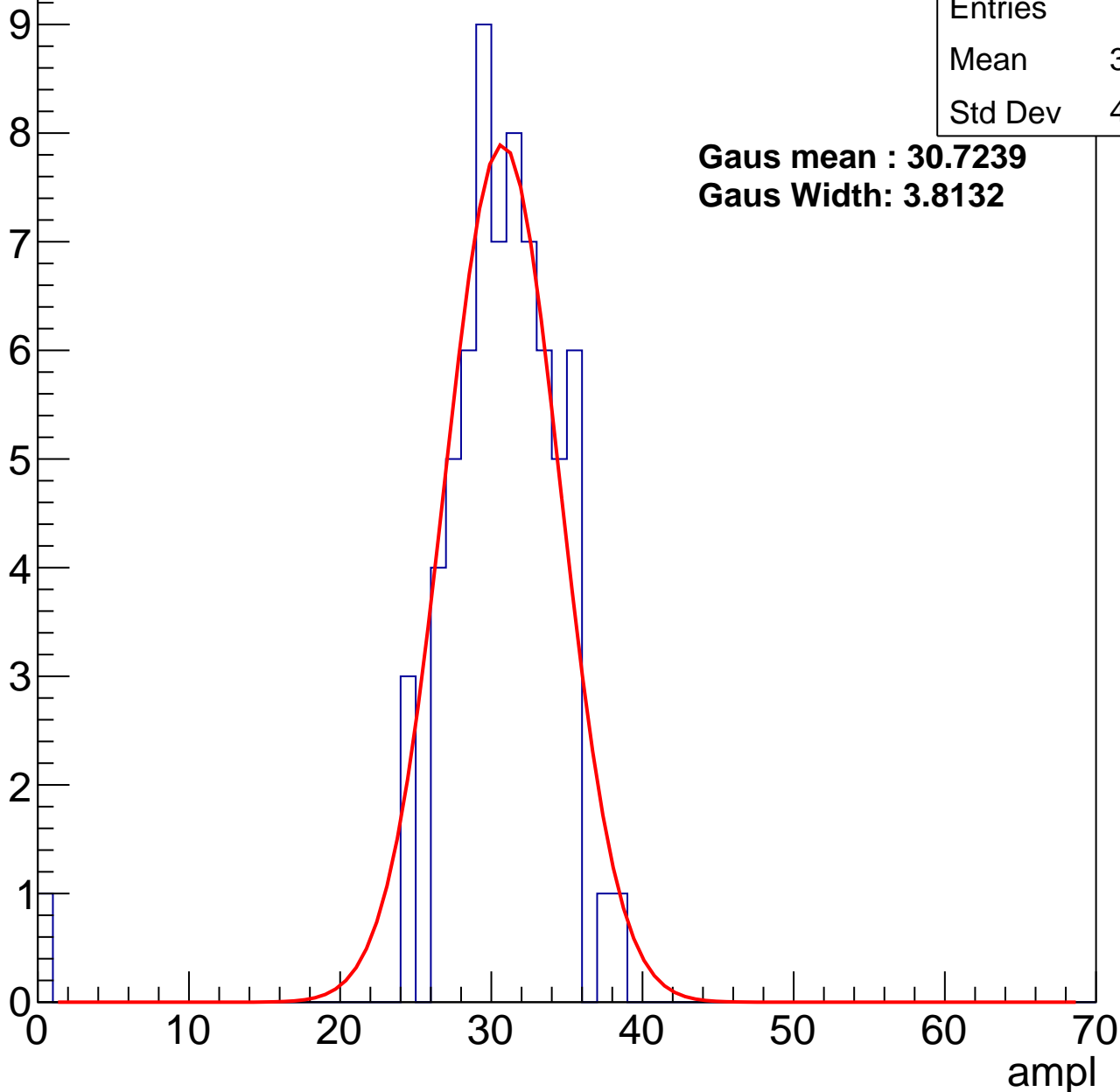
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 30.07 |
| Std Dev | 4.789 |

Gaus mean : 30.7239

Gaus Width: 3.8132



B1L100S, U9-ch11, adc1

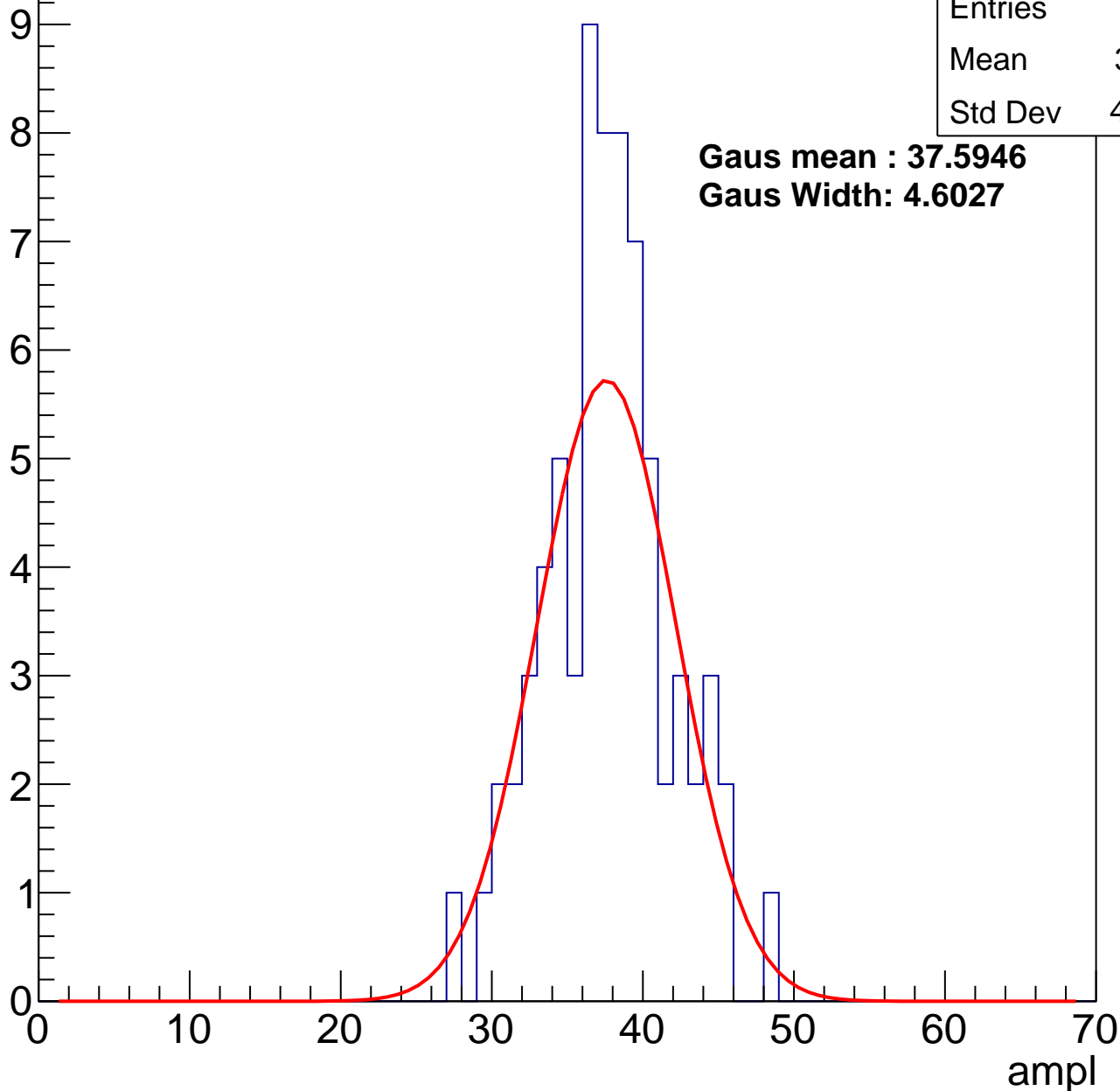
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.21 |
| Std Dev | 4.097 |

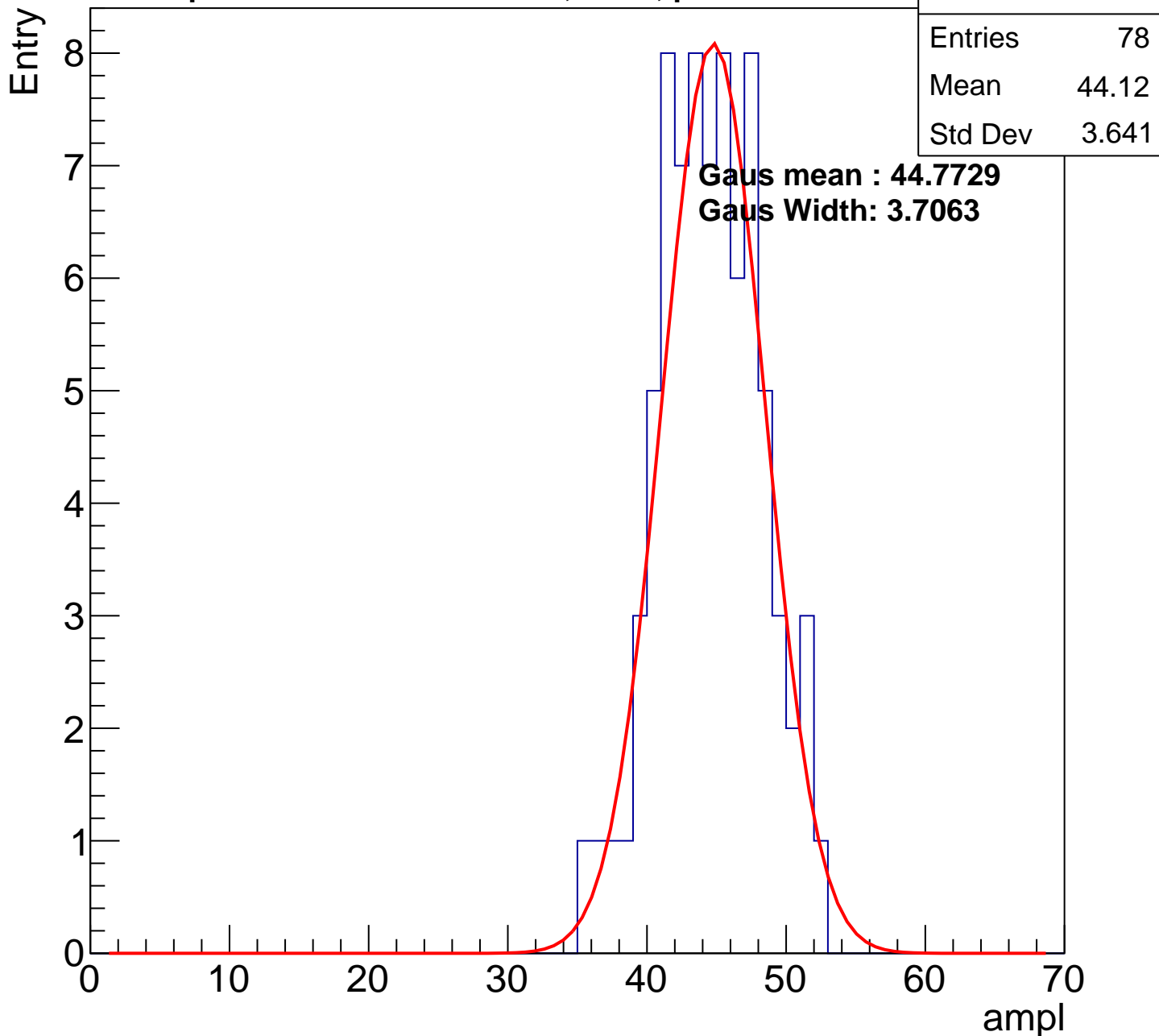
Gaus mean : 37.5946

Gaus Width: 4.6027



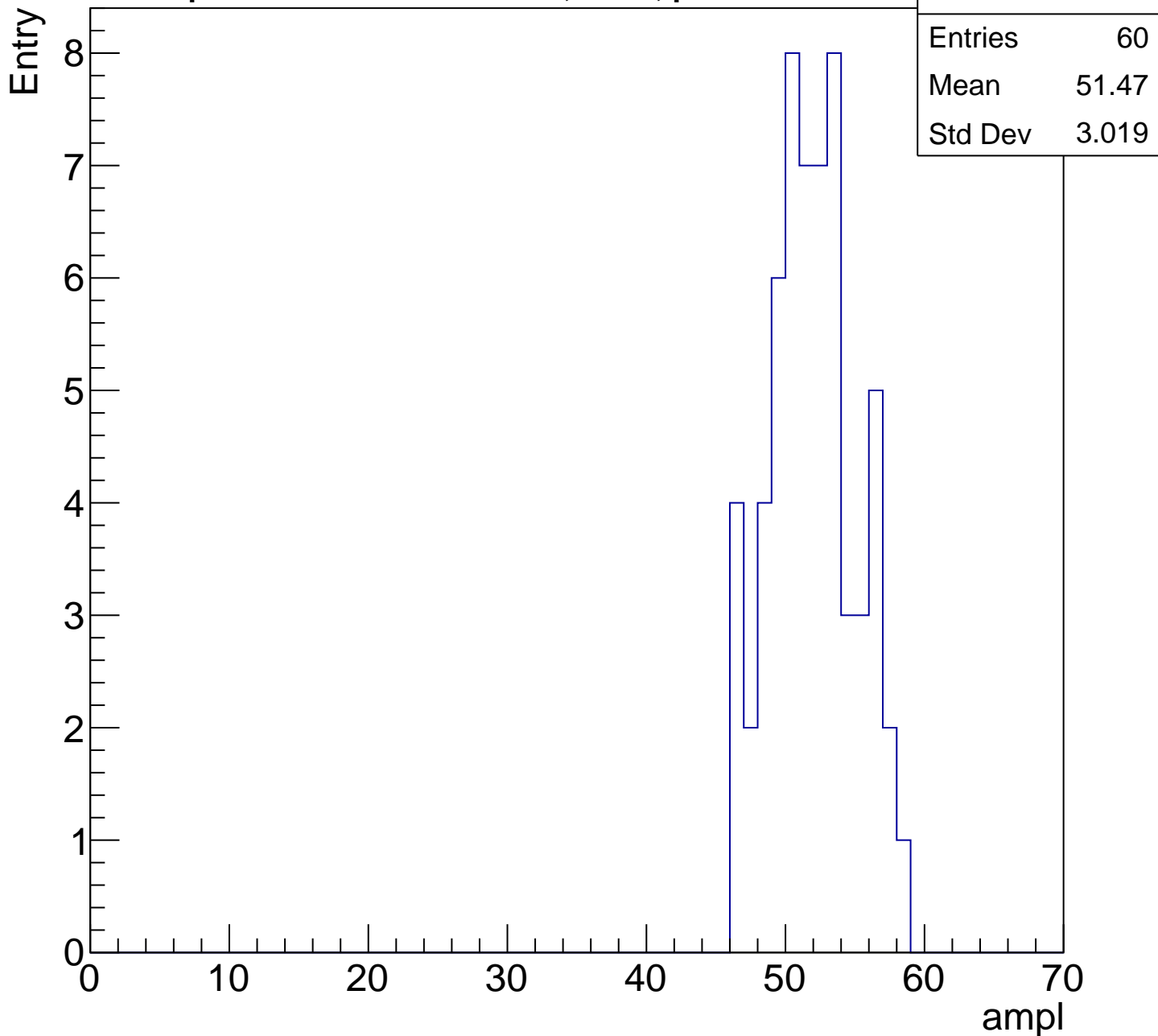
B1L100S, U9-ch11, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch11, adc3

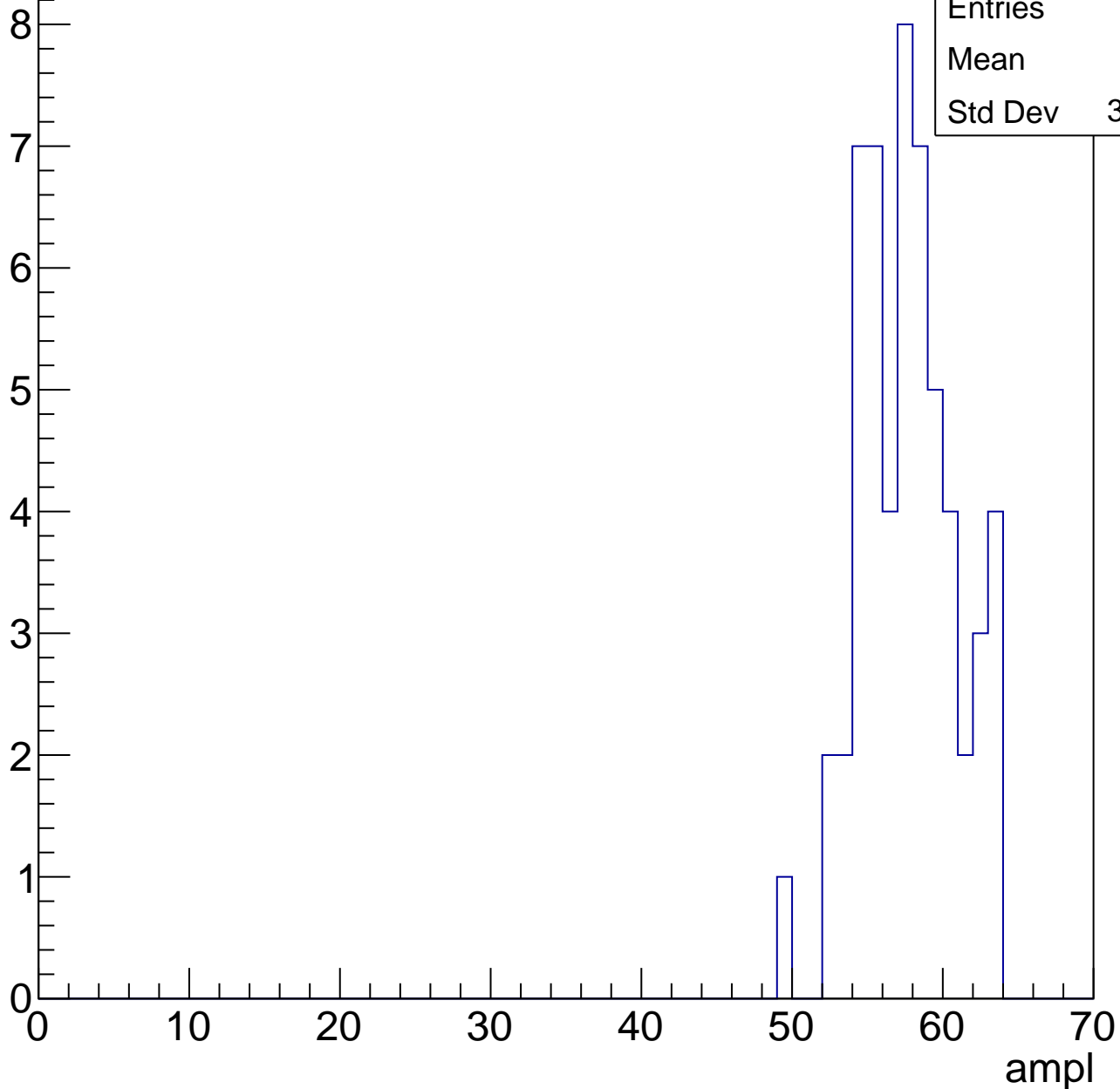
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



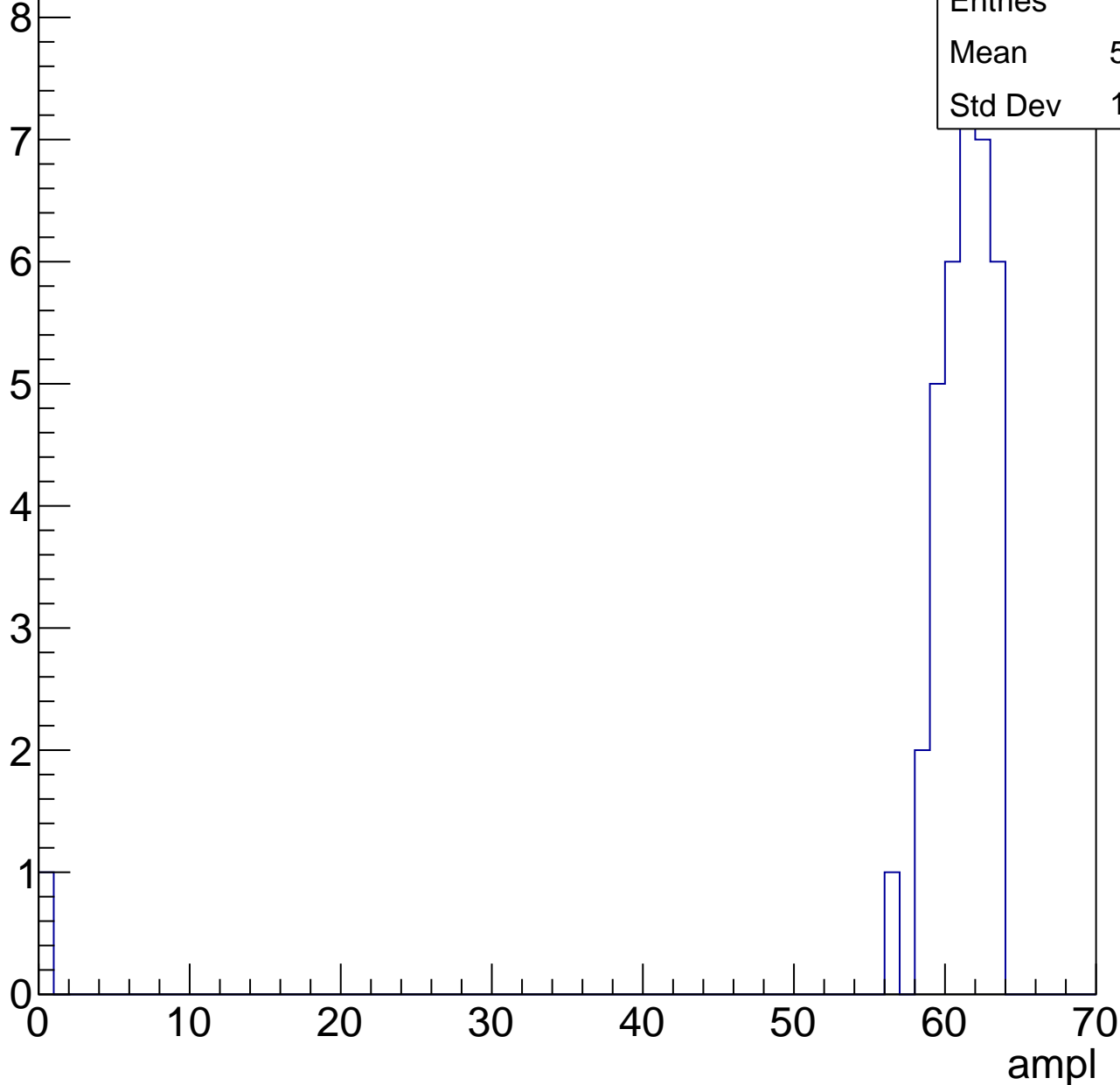
| | |
|---------|-------|
| Entries | 56 |
| Mean | 57.2 |
| Std Dev | 3.142 |

B1L100S, U9-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 36 |
| Mean | 59.08 |
| Std Dev | 10.12 |



B1L100S, U9-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch12, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 30.73 |
| Std Dev | 5.967 |

Gaus mean : 31.5333

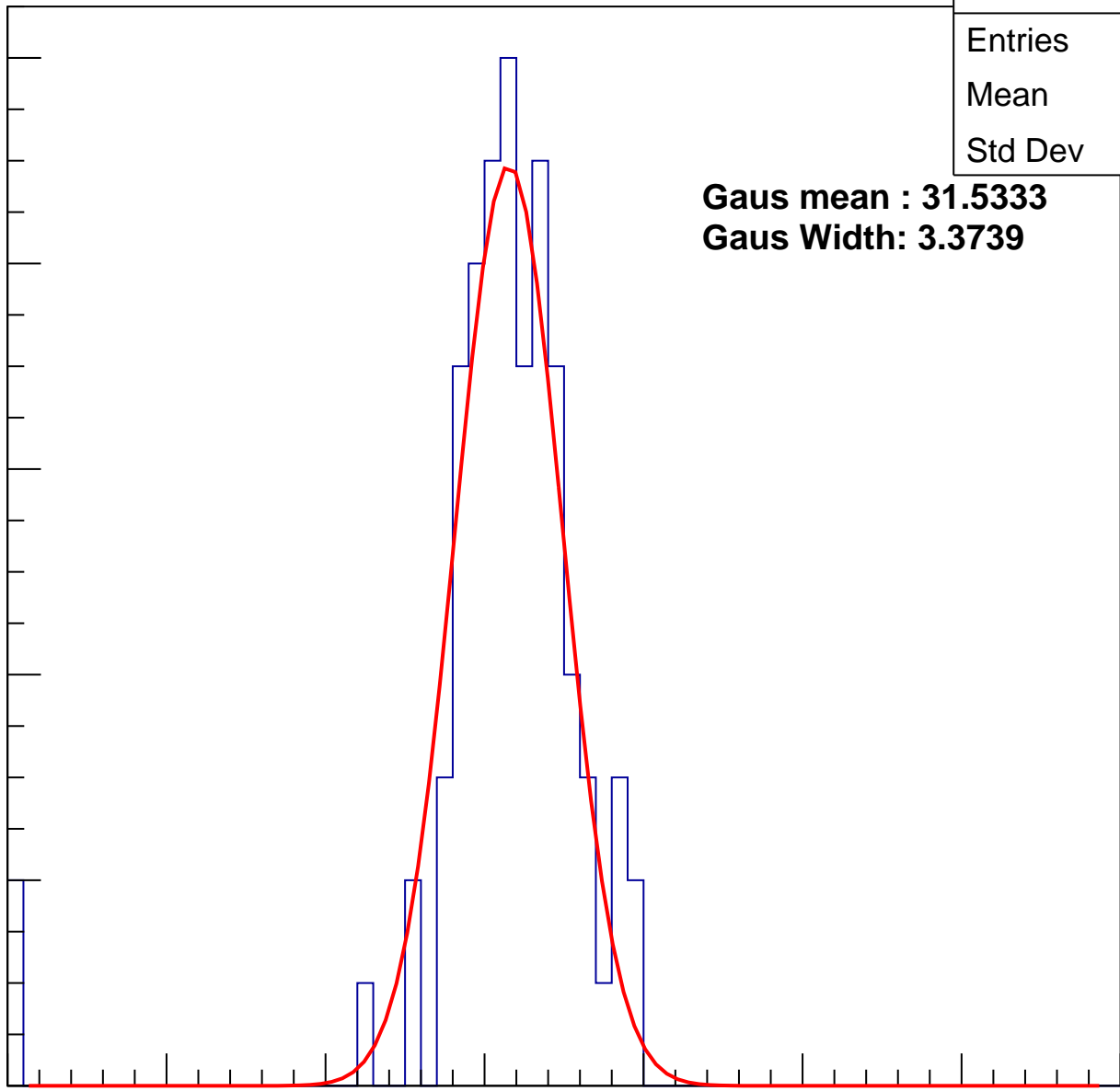
Gaus Width: 3.3739

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch12, adc1

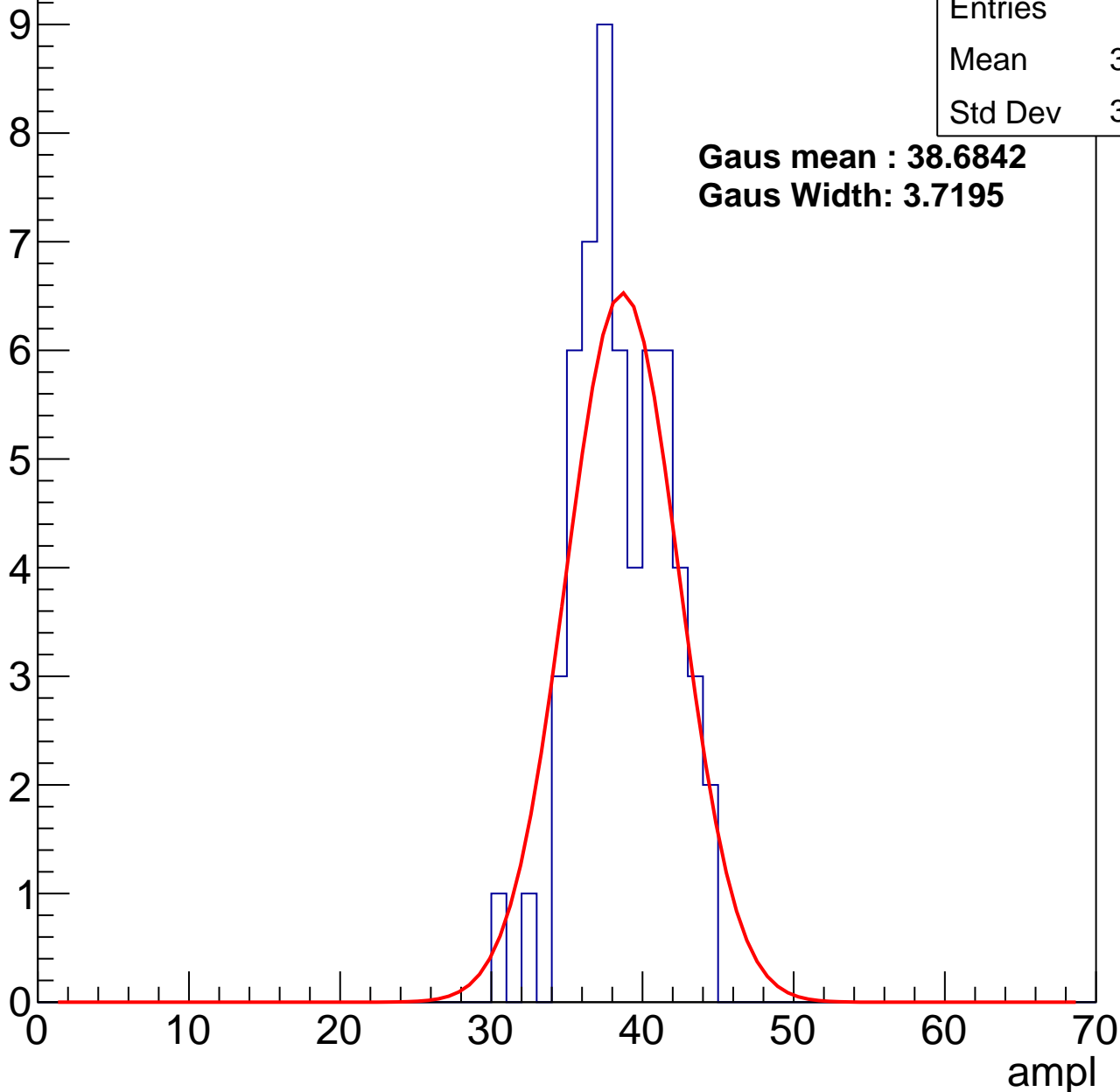
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 38.17 |
| Std Dev | 3.024 |

Gaus mean : 38.6842

Gaus Width: 3.7195



B1L100S, U9-ch12, adc2

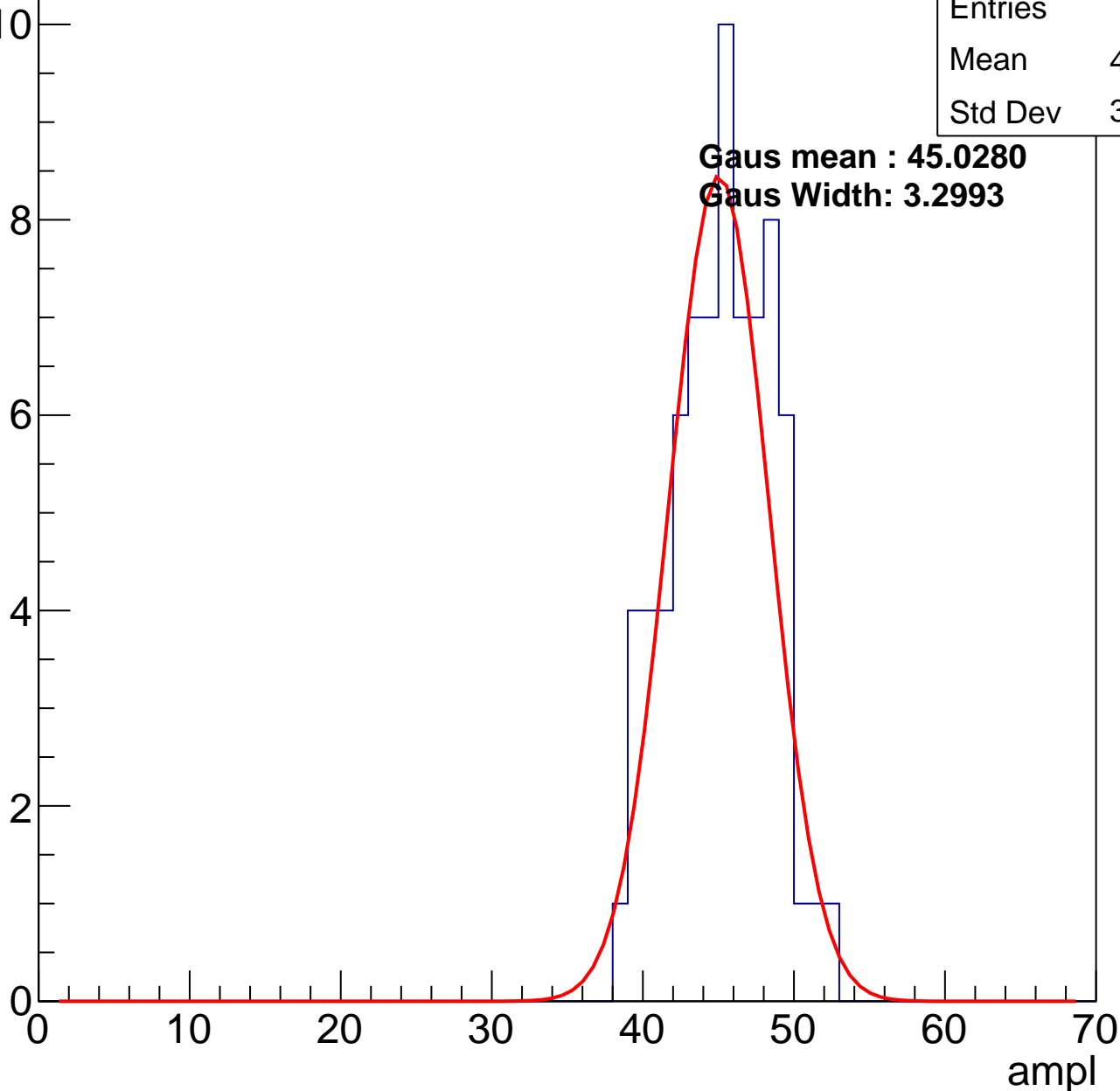
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 44.74 |
| Std Dev | 3.192 |

Gaus mean : 45.0280

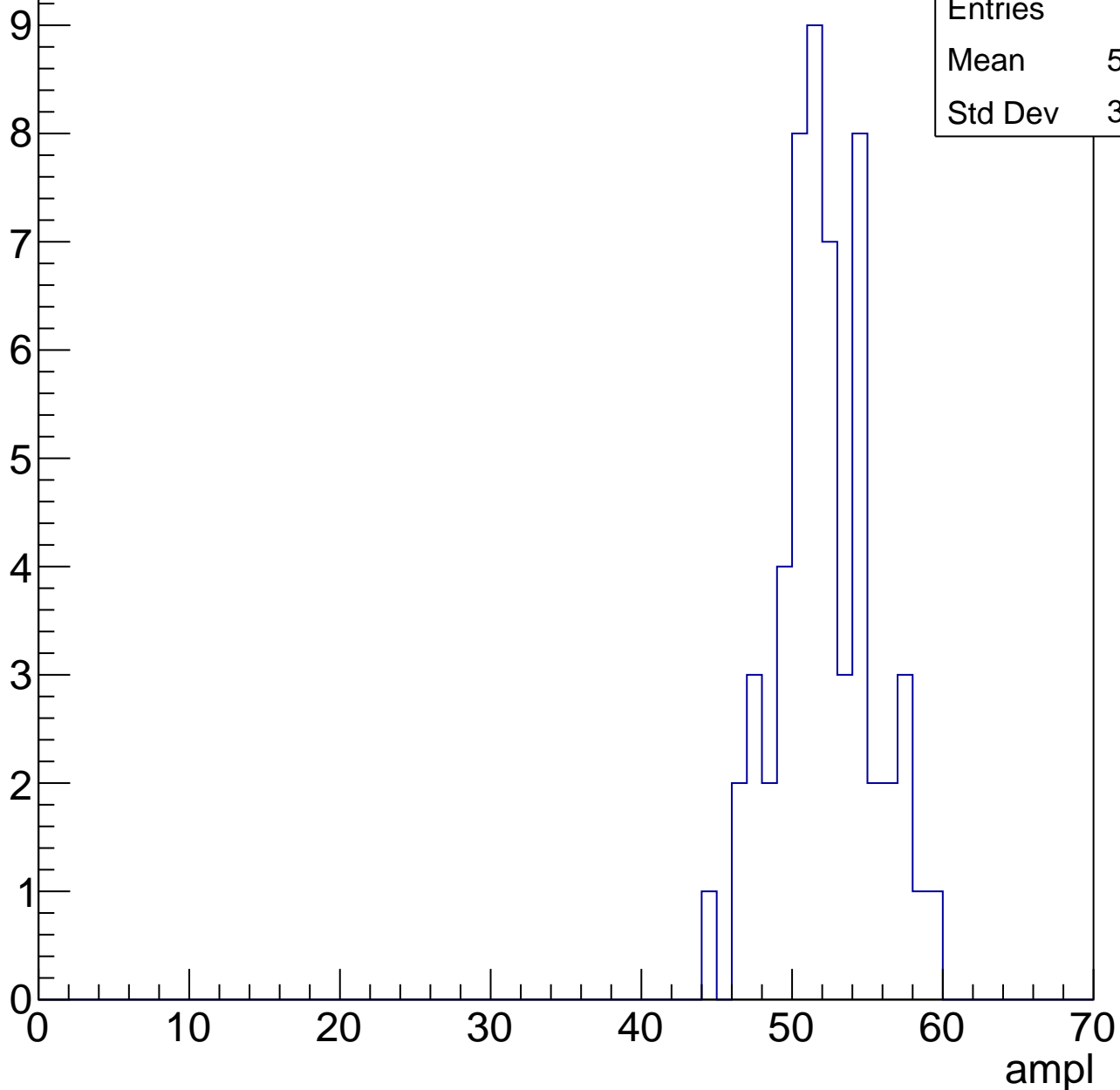
Gaus Width: 3.2993



B1L100S, U9-ch12, adc3

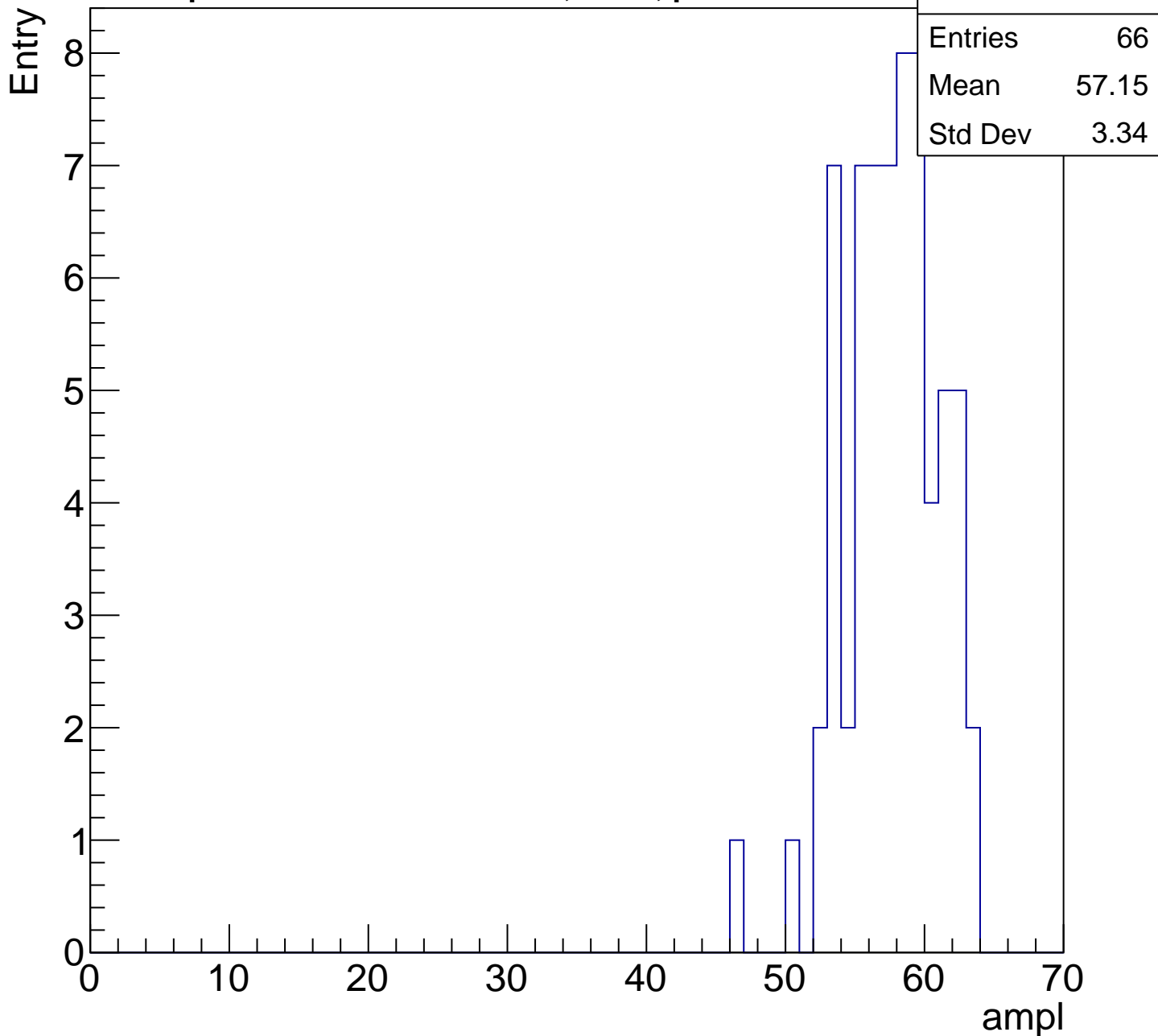
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

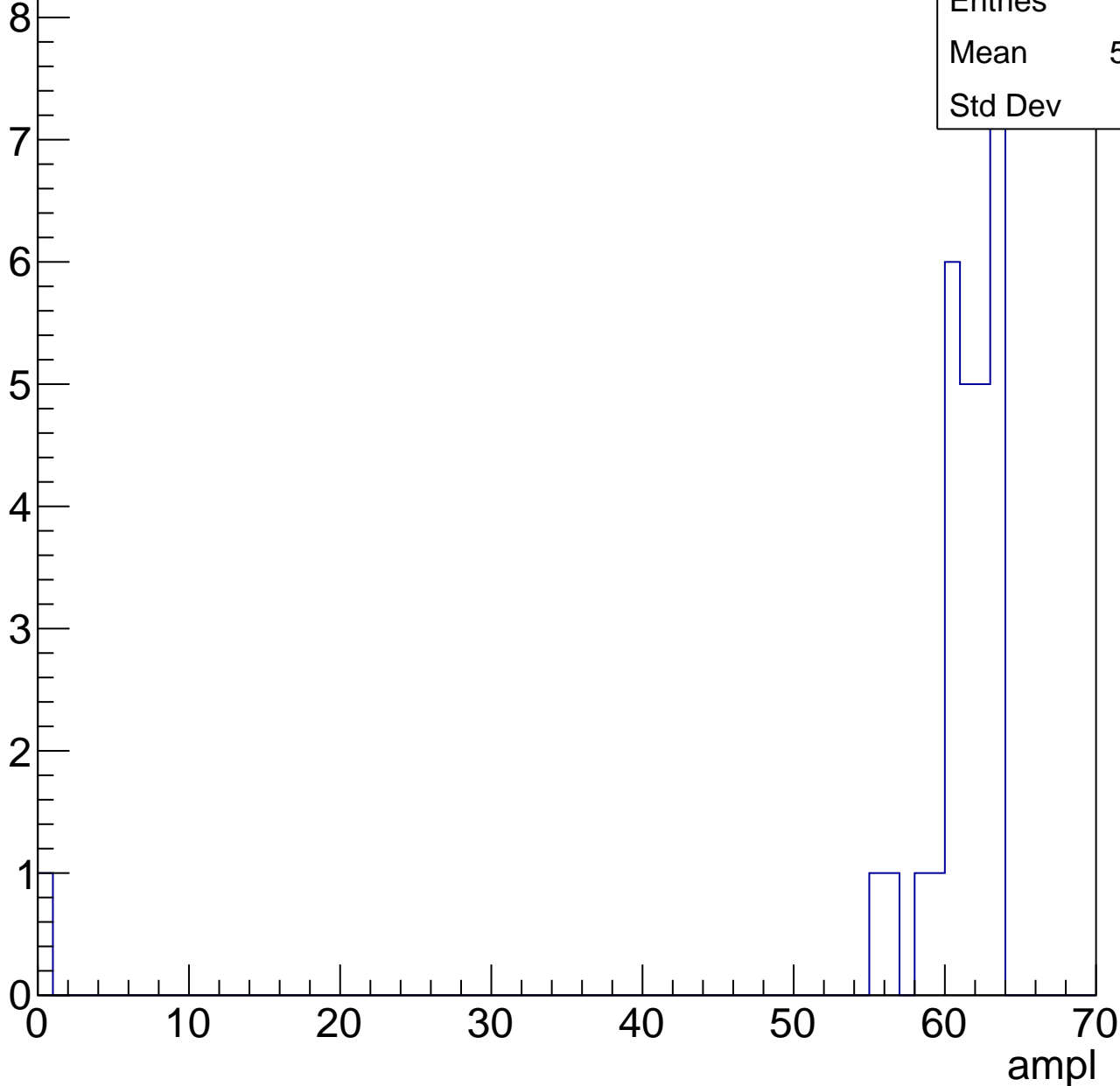


B1L100S, U9-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.86 |
| Std Dev | 11.3 |



B1L100S, U9-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch13, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 85 |
| Mean | 29.38 |
| Std Dev | 5.851 |

Gaus mean : 30.6072

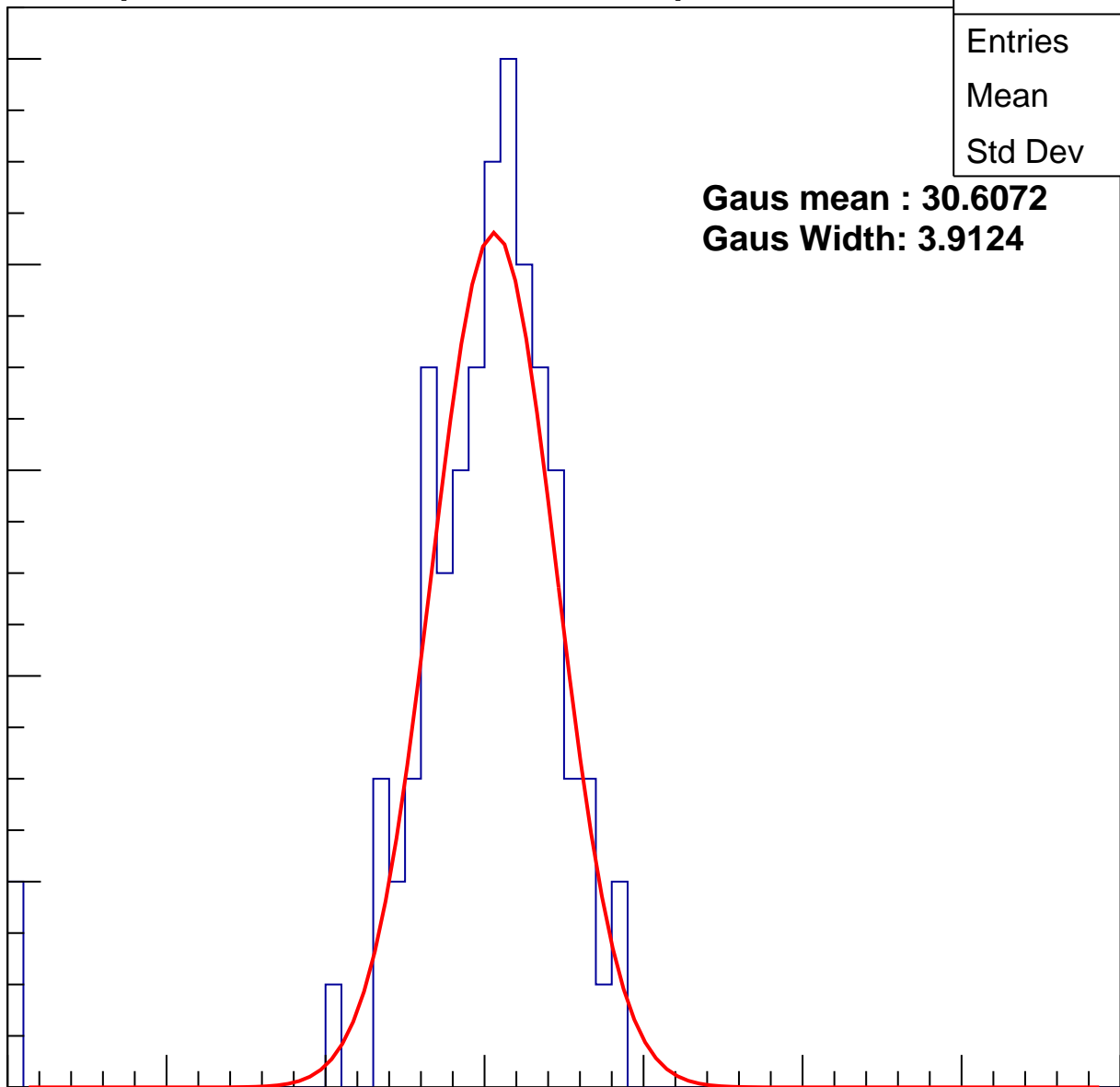
Gaus Width: 3.9124

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch13, adc1

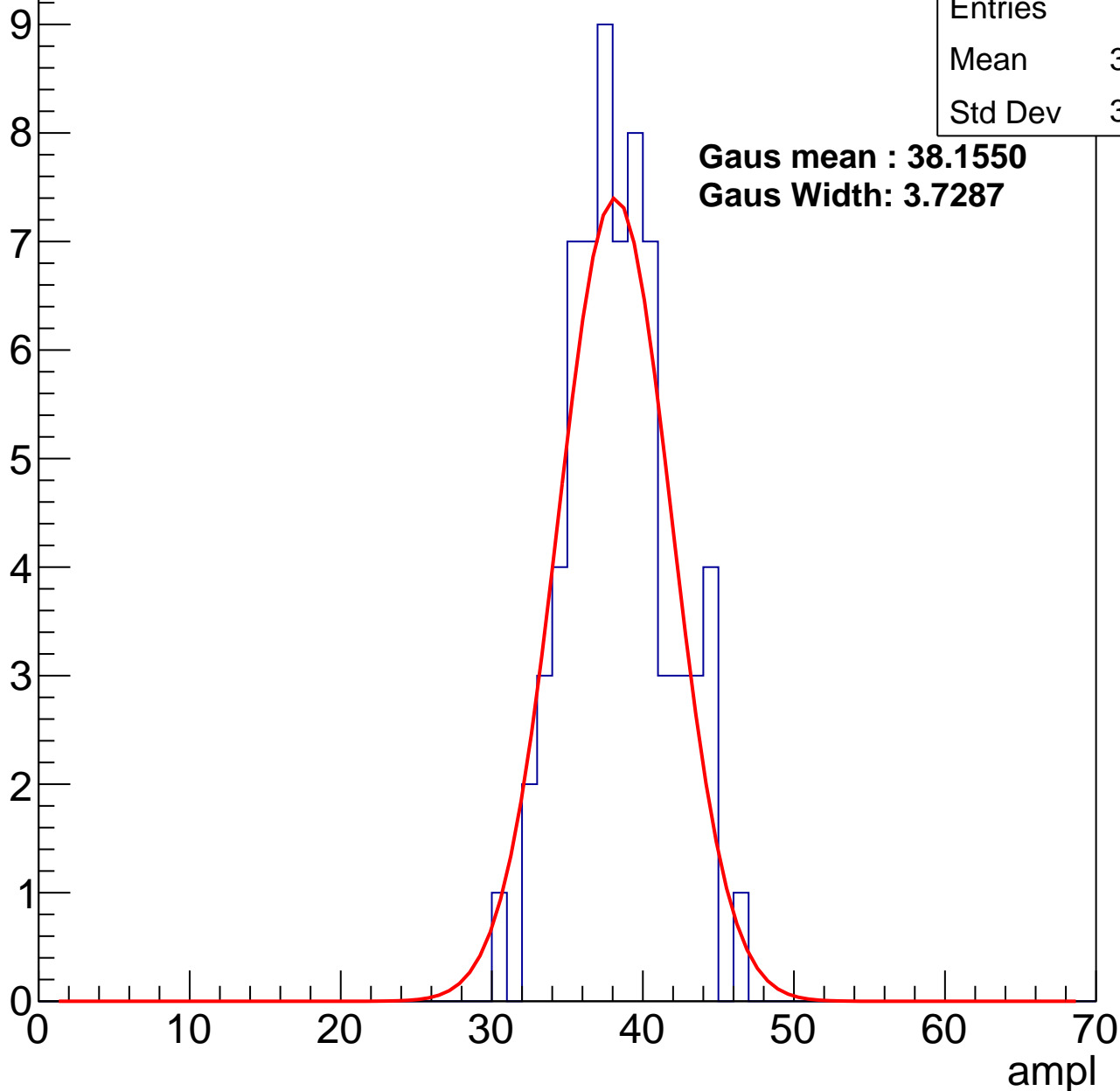
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 37.93 |
| Std Dev | 3.338 |

Gaus mean : 38.1550

Gaus Width: 3.7287



B1L100S, U9-ch13, adc2

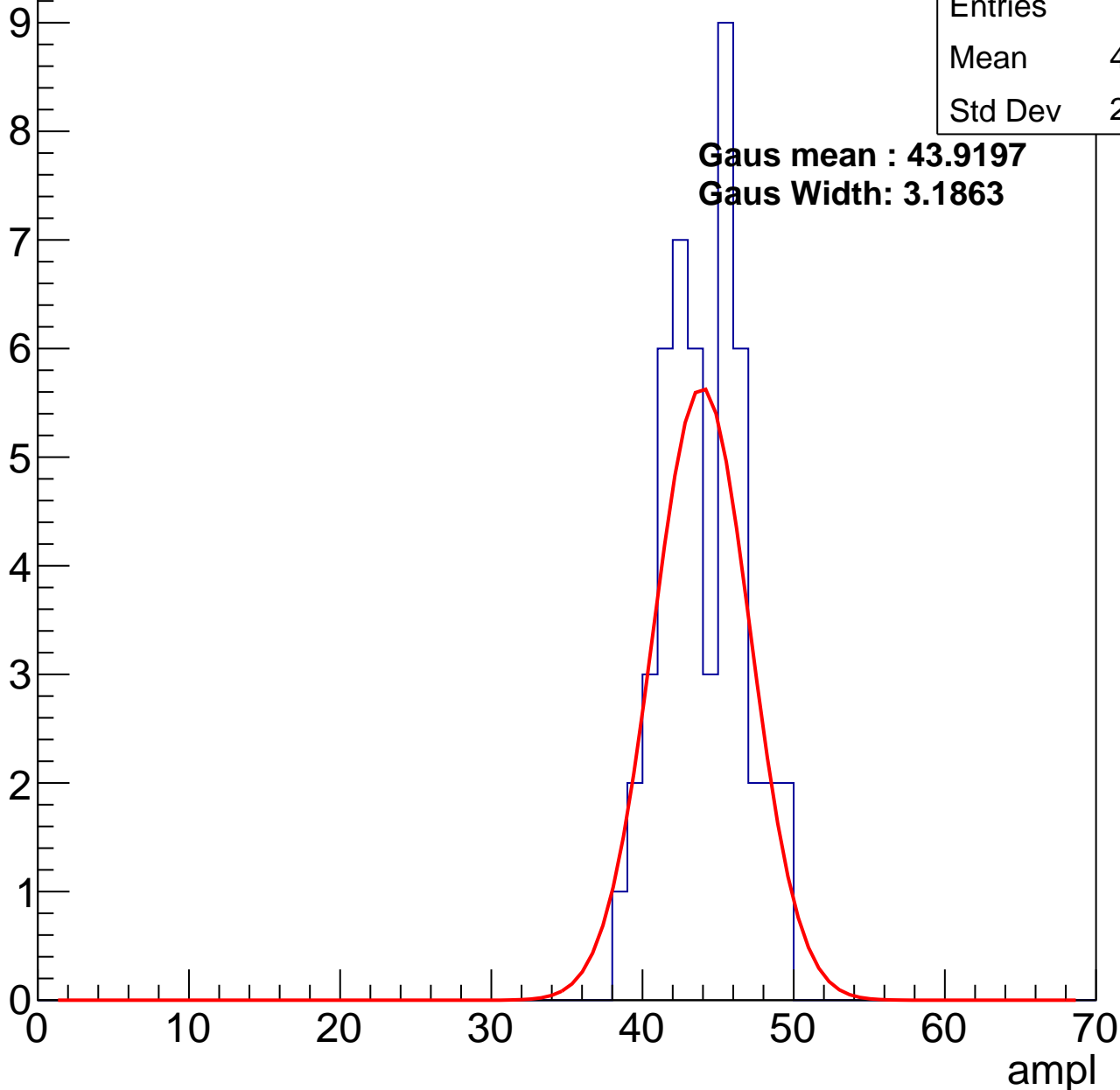
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.57 |
| Std Dev | 2.657 |

Gaus mean : 43.9197

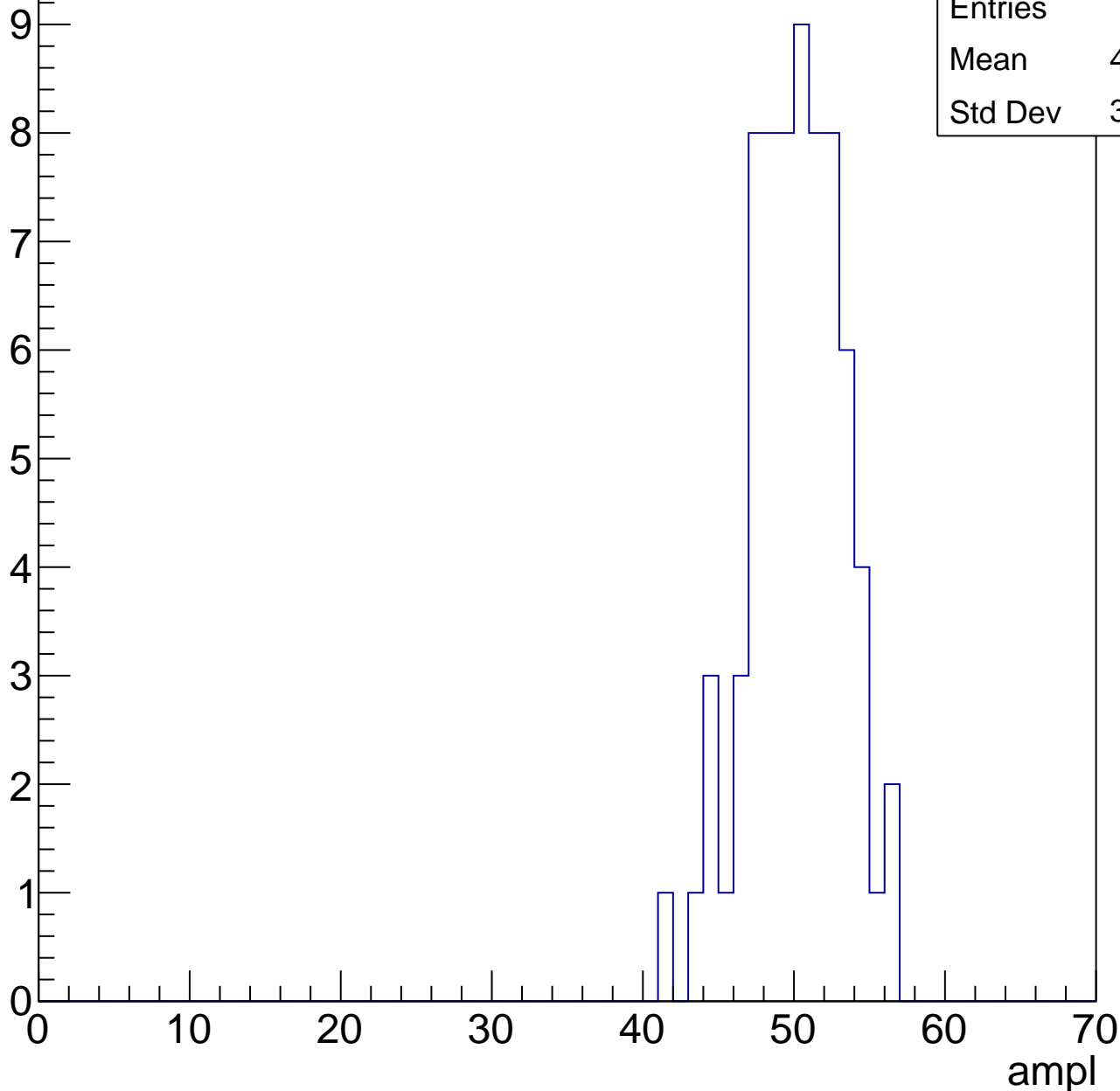
Gaus Width: 3.1863



B1L100S, U9-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

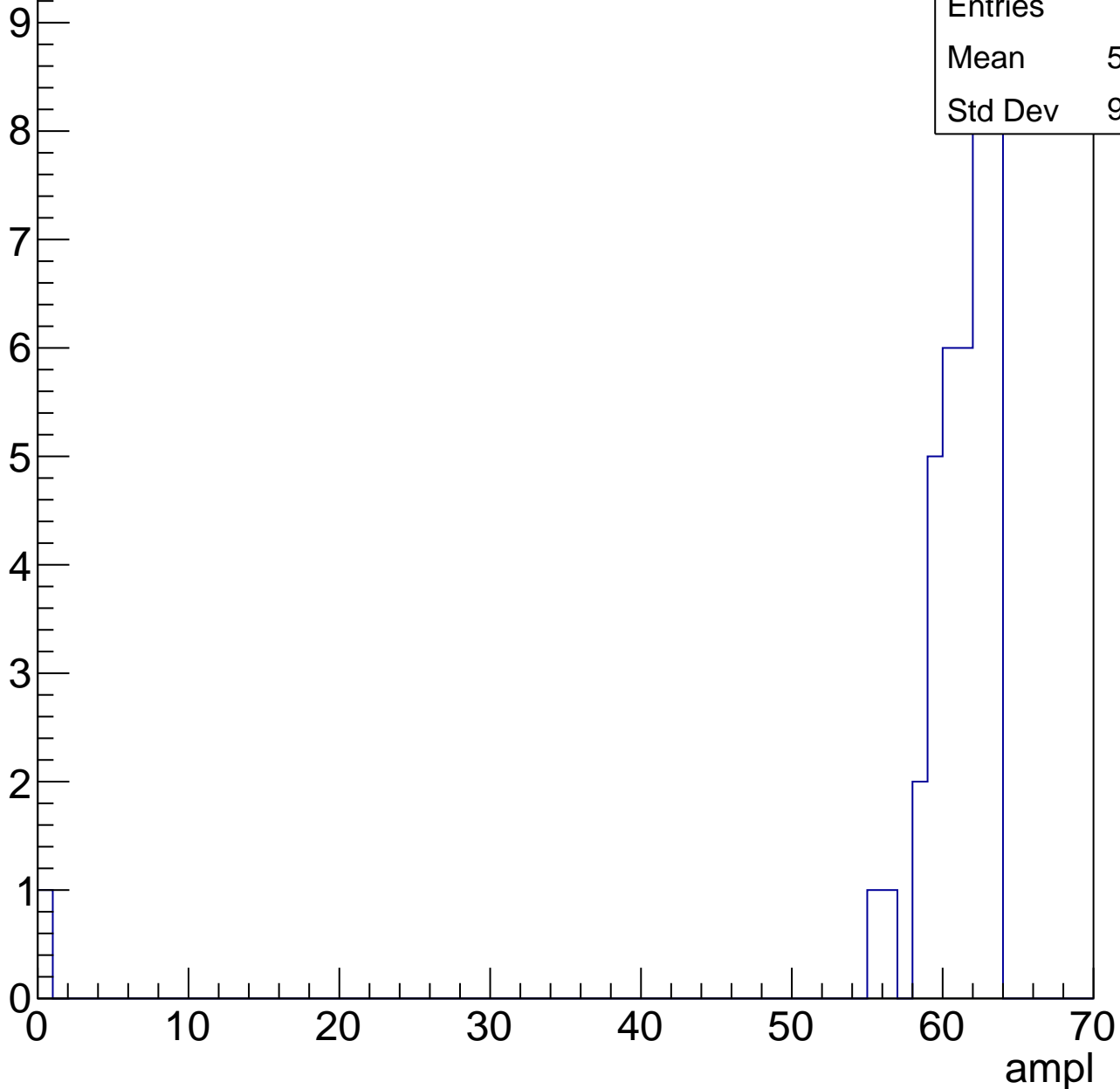
| | |
|---------|-------|
| Entries | 64 |
| Mean | 56.62 |
| Std Dev | 2.886 |

B1L100S, U9-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 59.26 |
| Std Dev | 9.808 |



B1L100S, U9-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch14, adc0

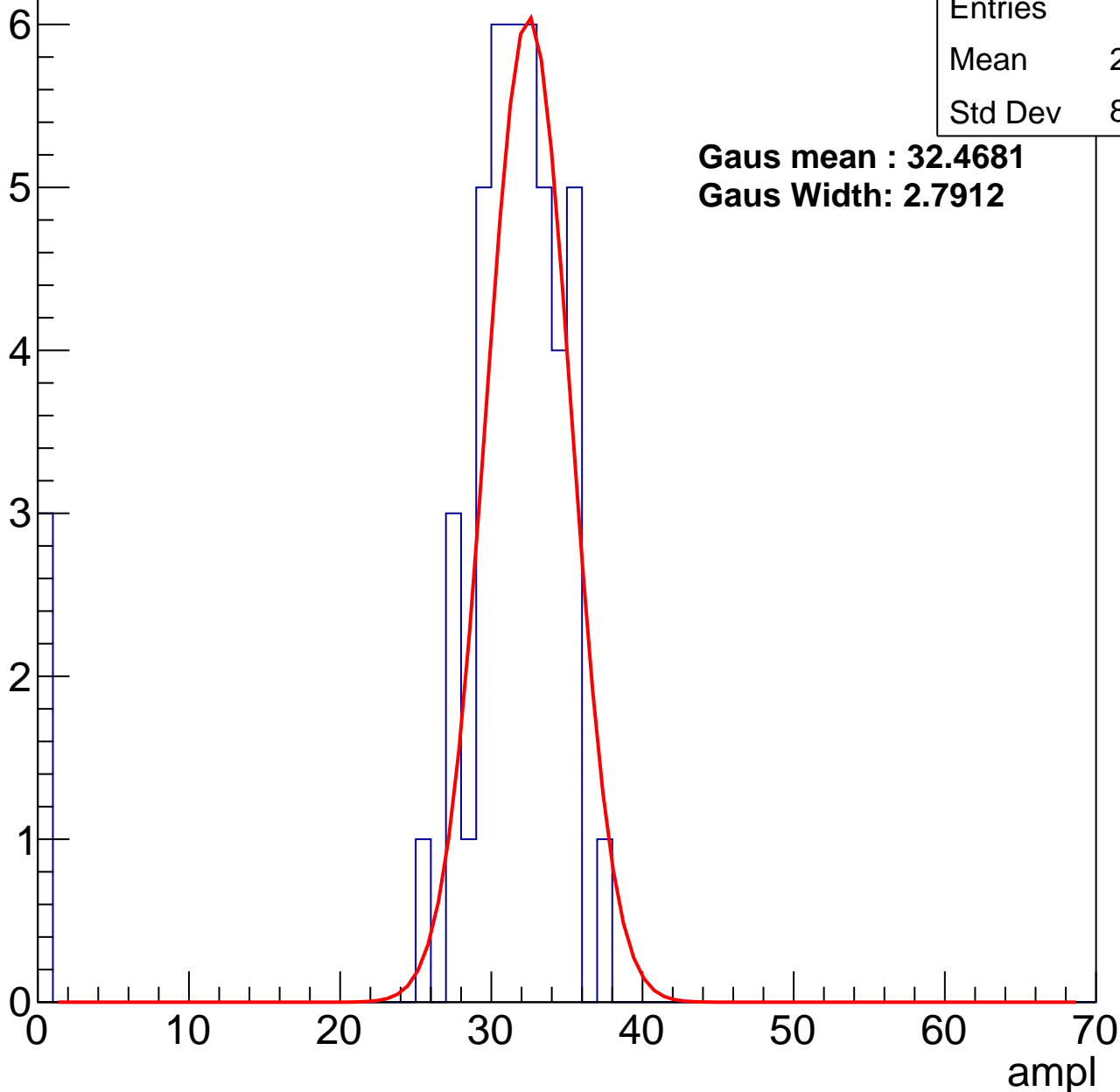
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 29.35 |
| Std Dev | 8.149 |

Gaus mean : 32.4681

Gaus Width: 2.7912



B1L100S, U9-ch14, adc1

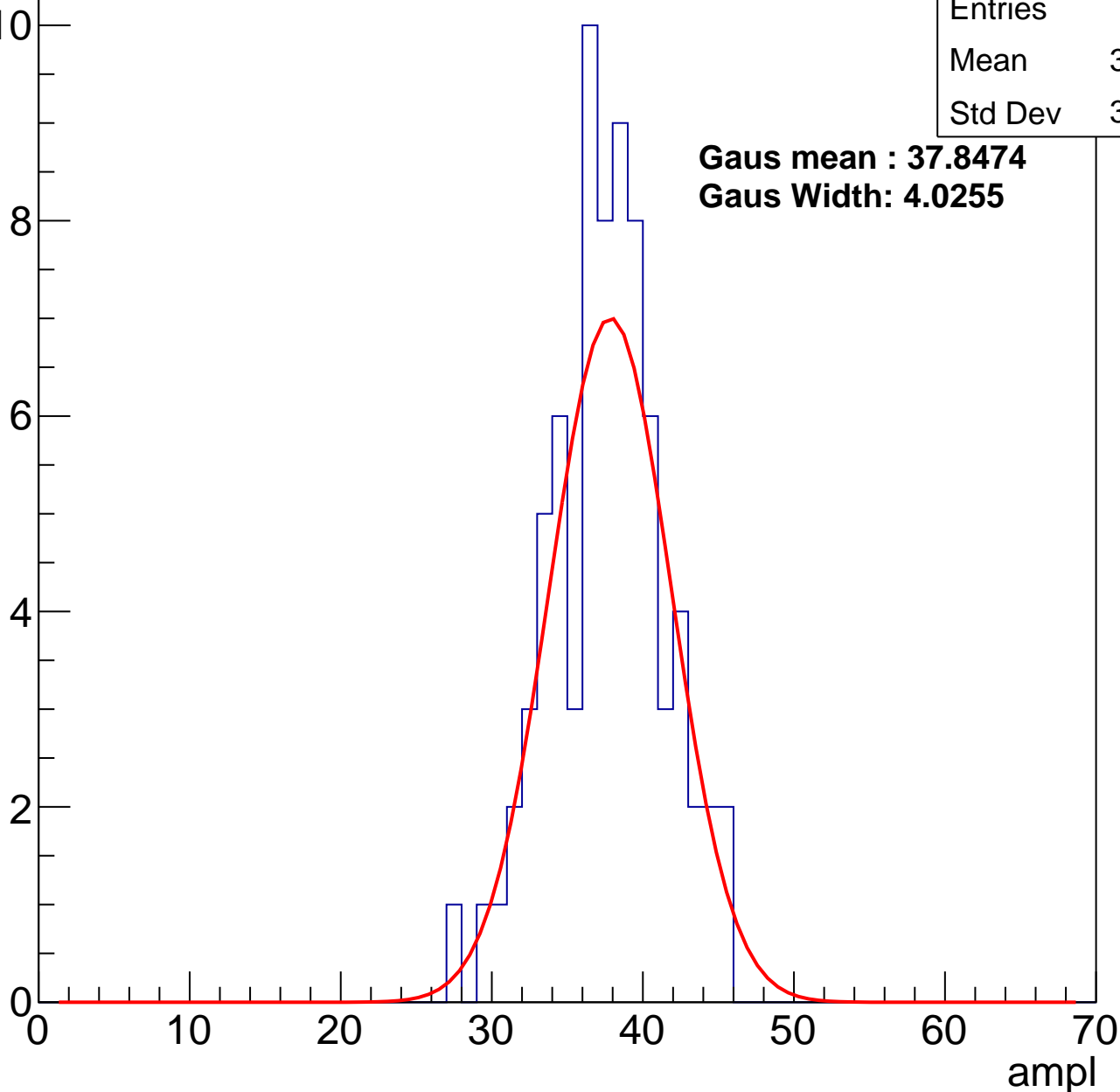
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 37.14 |
| Std Dev | 3.727 |

Gaus mean : 37.8474

Gaus Width: 4.0255



B1L100S, U9-ch14, adc2

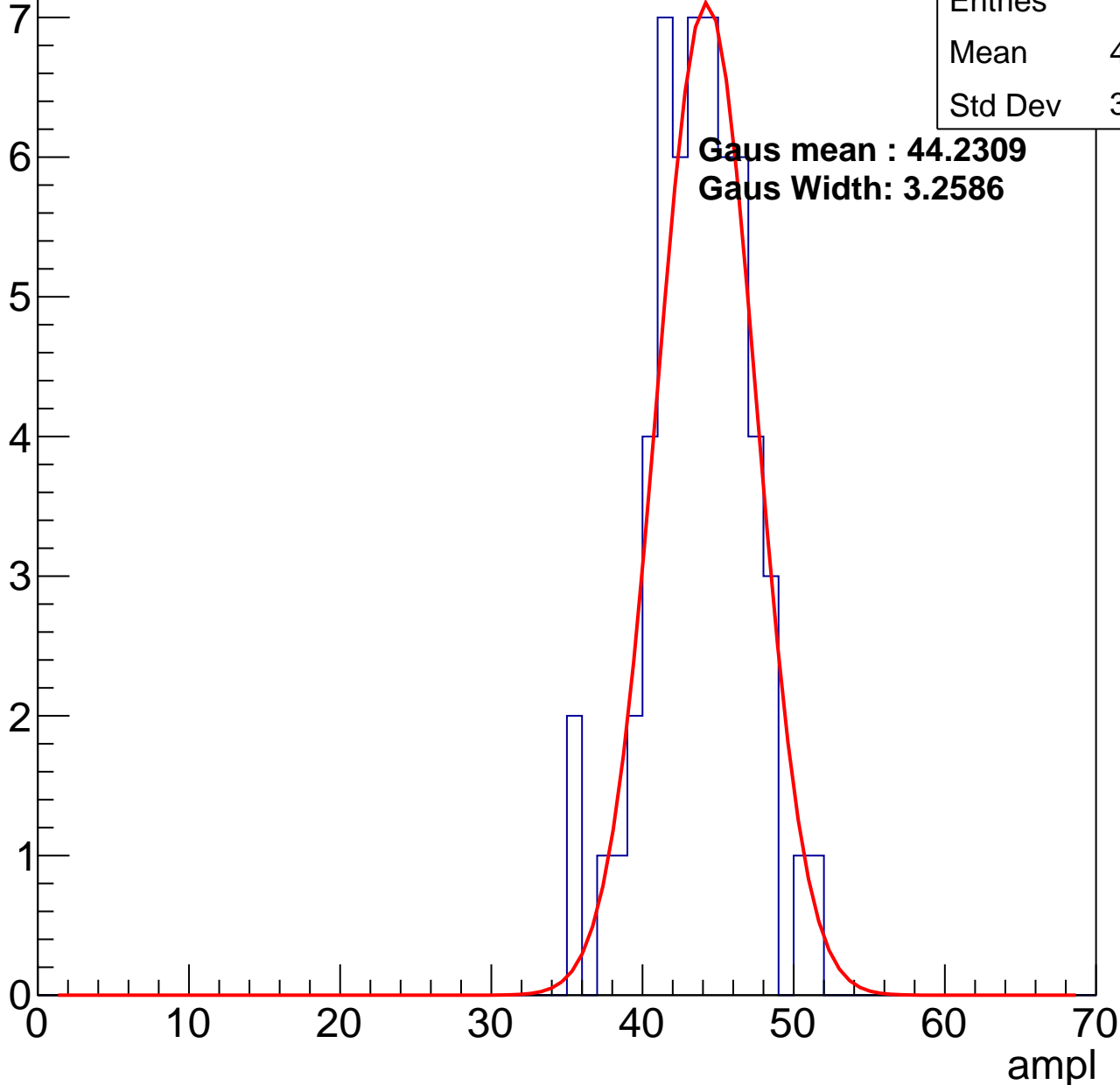
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 43.28 |
| Std Dev | 3.279 |

Gaus mean : 44.2309

Gaus Width: 3.2586

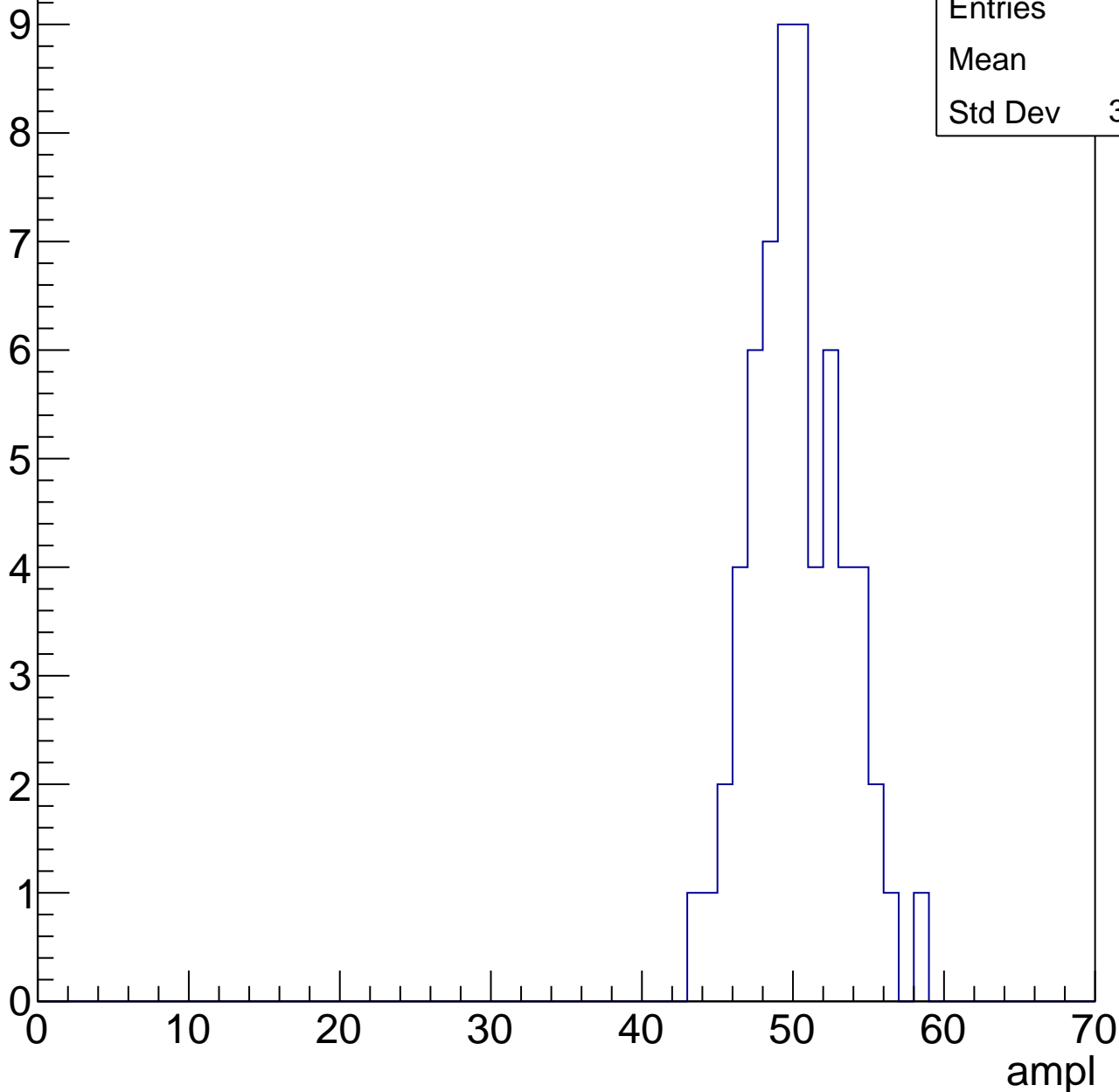


B1L100S, U9-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.8 |
| Std Dev | 3.045 |



B1L100S, U9-ch14, adc4

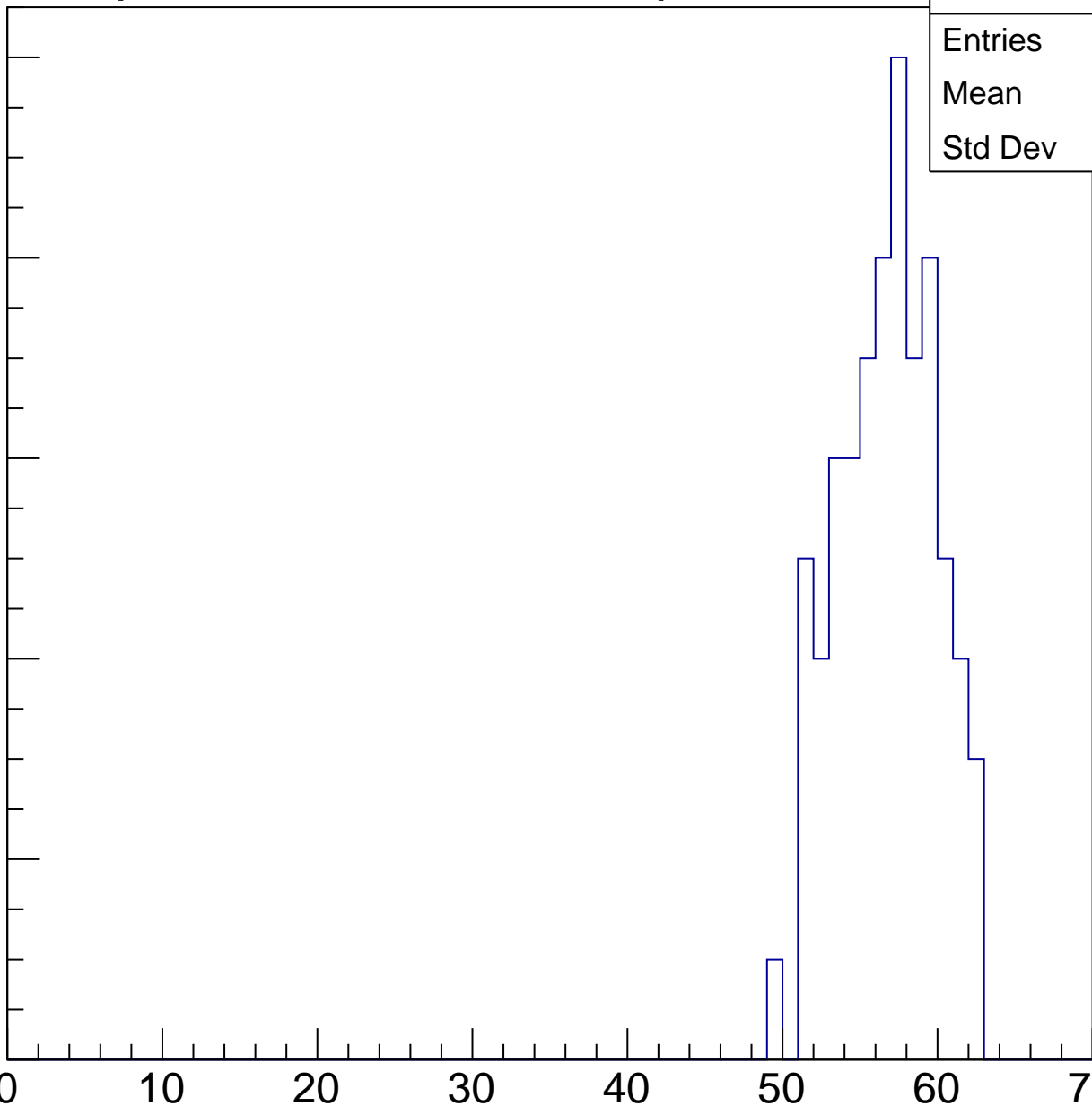
calib_packv5_042523_0143.root, FC#4, port A2

Entry

10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 74 |
| Mean | 56.28 |
| Std Dev | 3.108 |

ampl

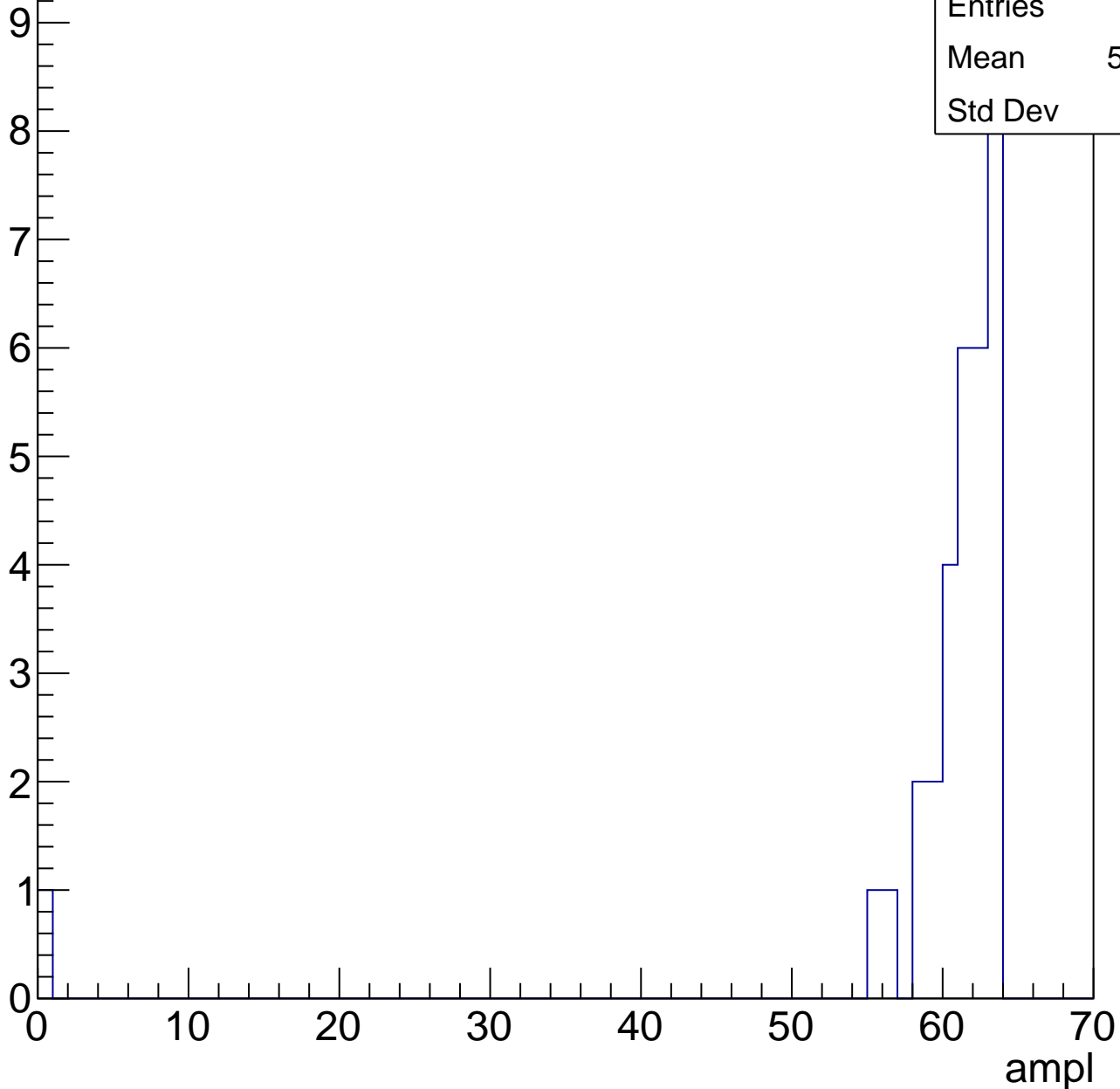


B1L100S, U9-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

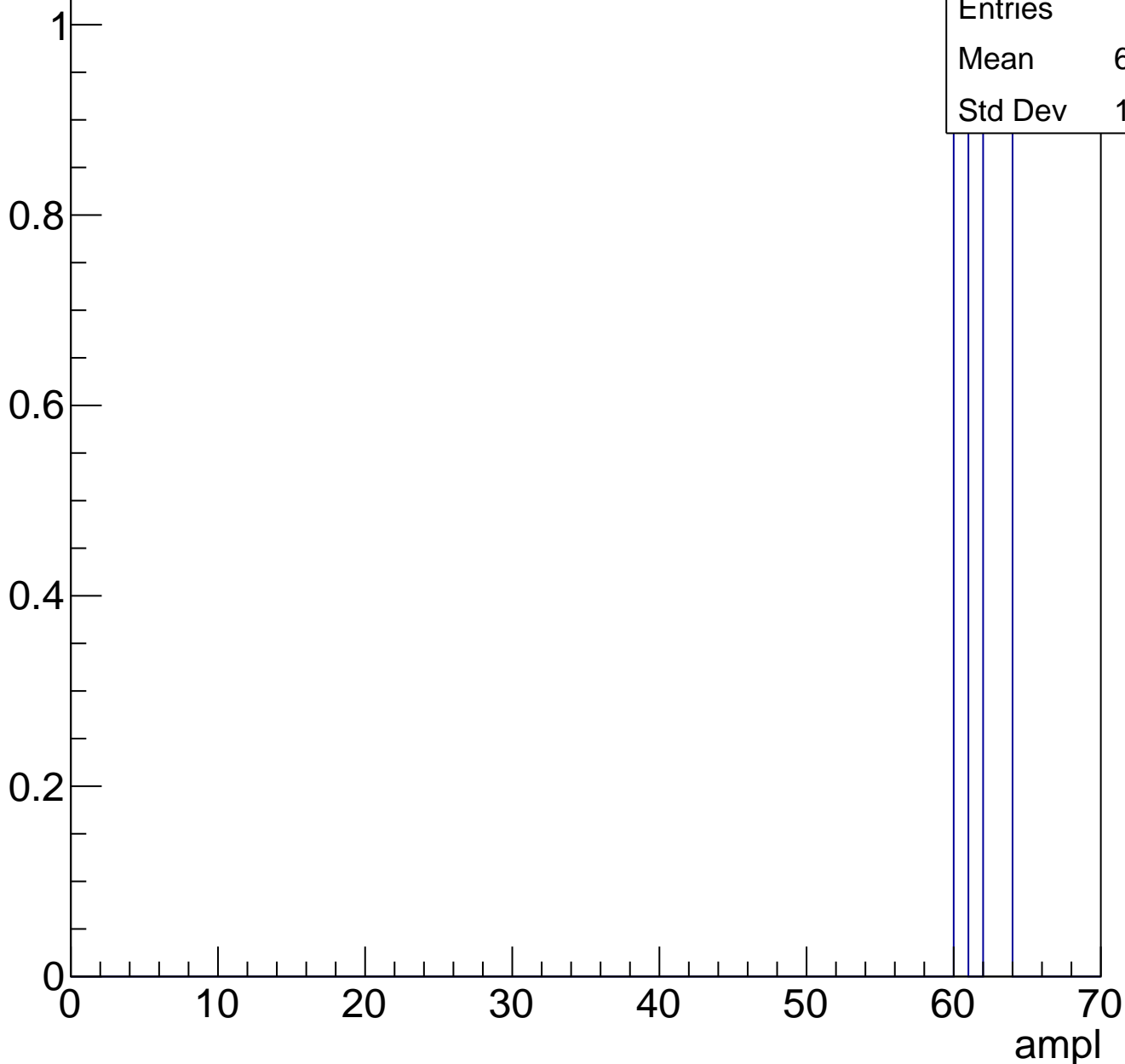
| | |
|---------|-------|
| Entries | 32 |
| Mean | 59.06 |
| Std Dev | 10.8 |



B1L100S, U9-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch15, adc0

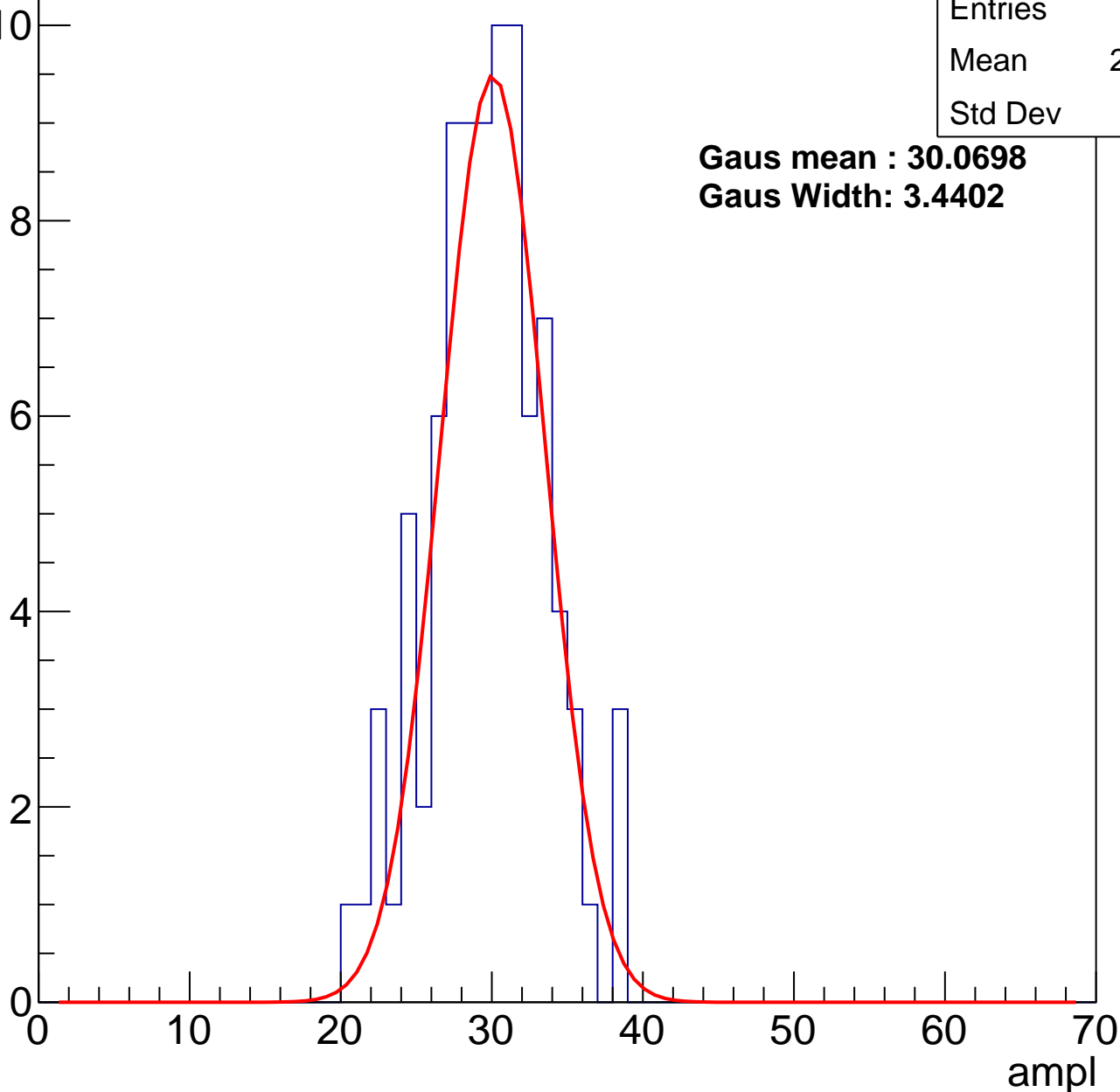
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 90 |
| Mean | 29.29 |
| Std Dev | 3.79 |

Gaus mean : 30.0698

Gaus Width: 3.4402



B1L100S, U9-ch15, adc1

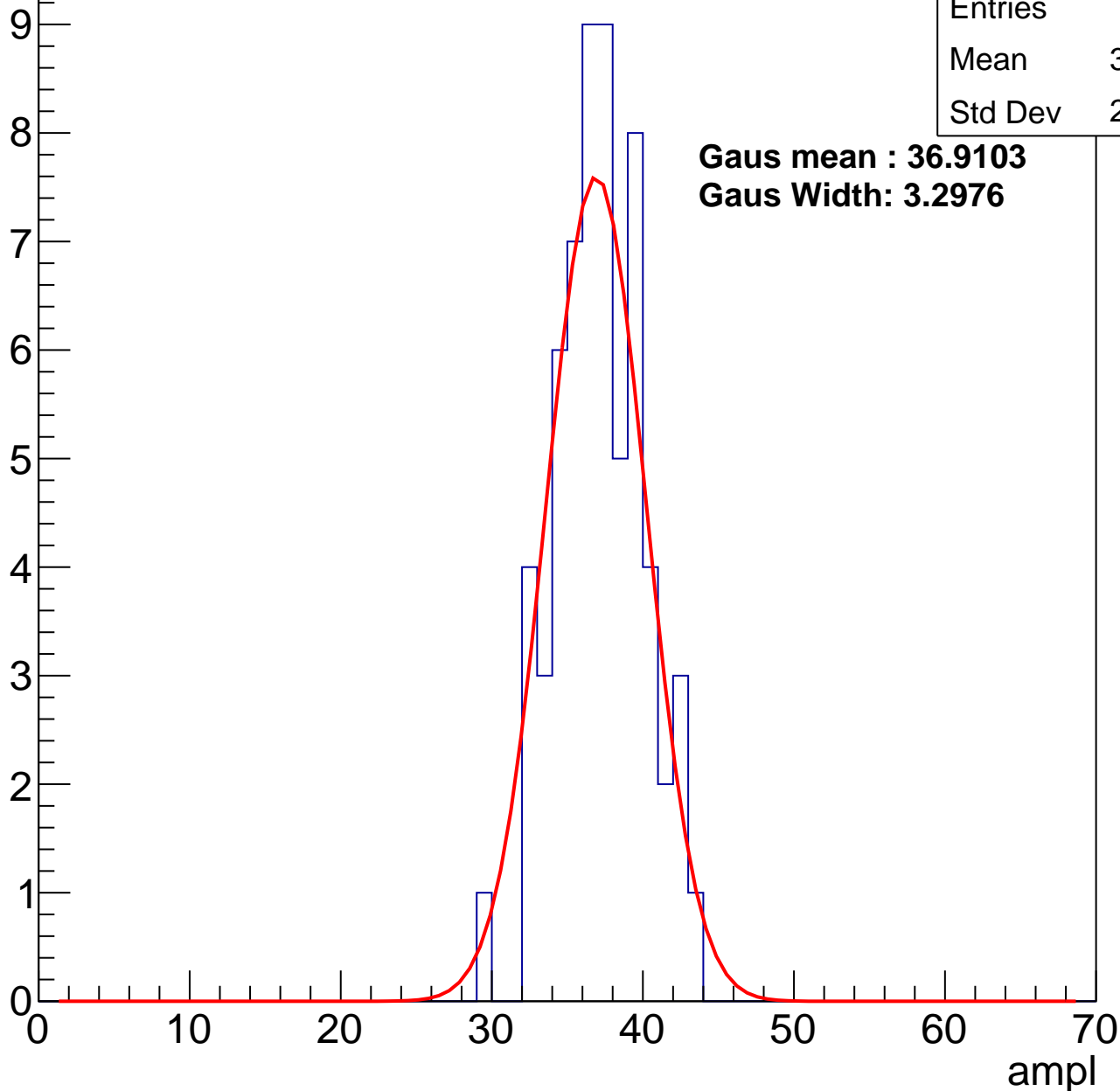
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 36.69 |
| Std Dev | 2.877 |

Gaus mean : 36.9103

Gaus Width: 3.2976



B1L100S, U9-ch15, adc2

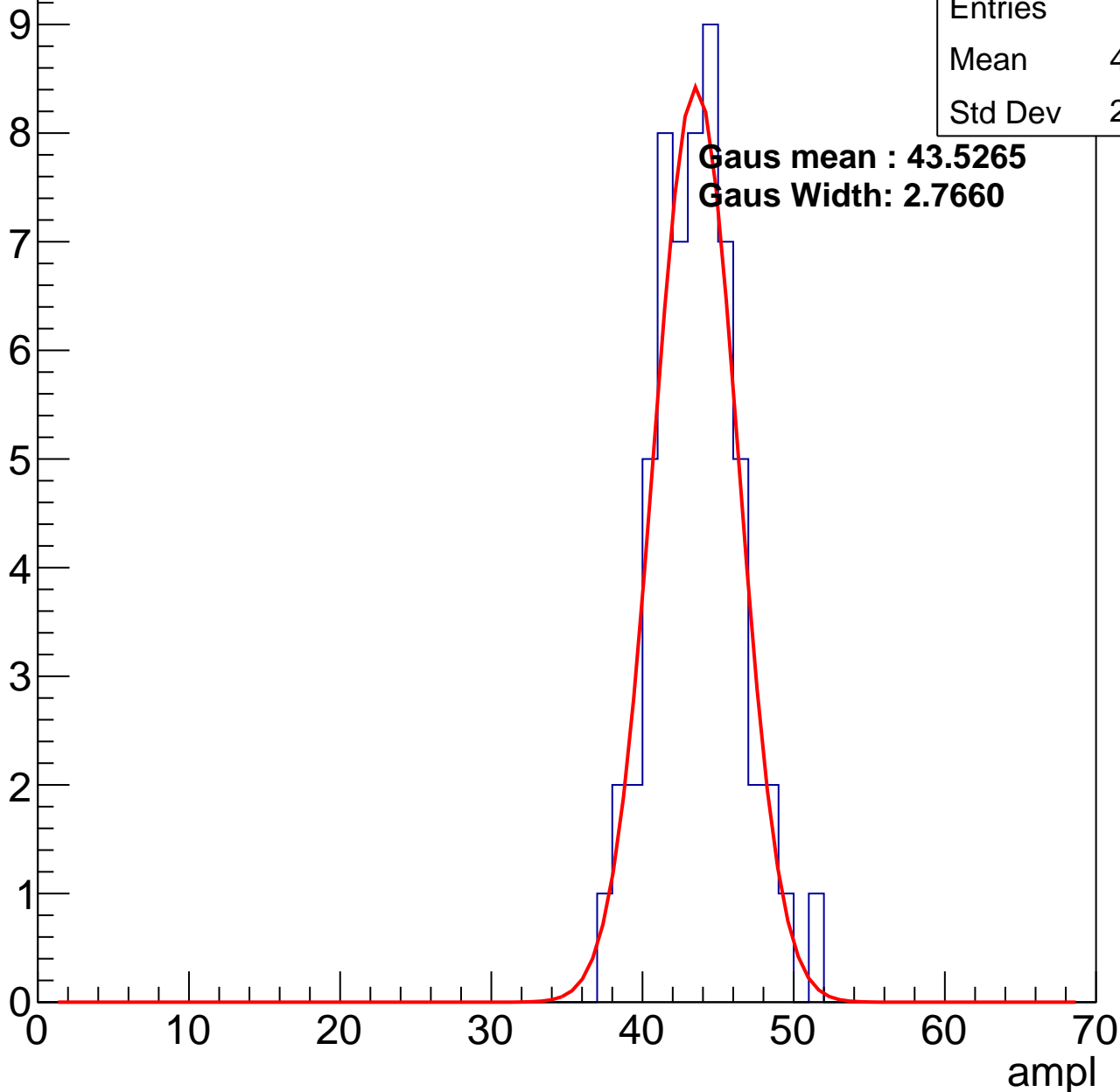
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 43.13 |
| Std Dev | 2.784 |

Gaus mean : 43.5265

Gaus Width: 2.7660



B1L100S, U9-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 50.28 |
| Std Dev | 3.6 |

Entry

10

8

6

4

2

0

0

10

20

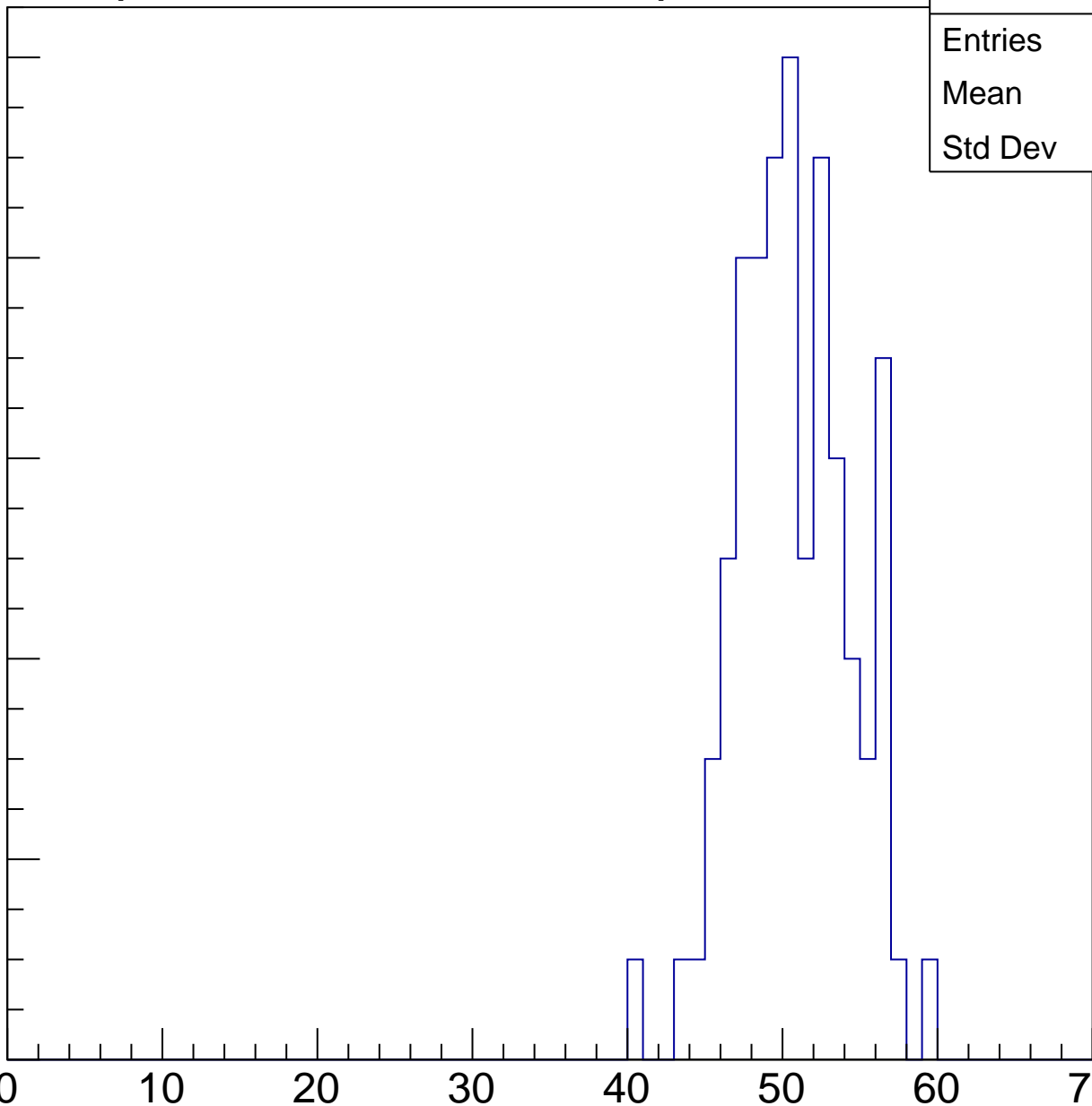
30

40

50

60

ampl



B1L100S, U9-ch15, adc4

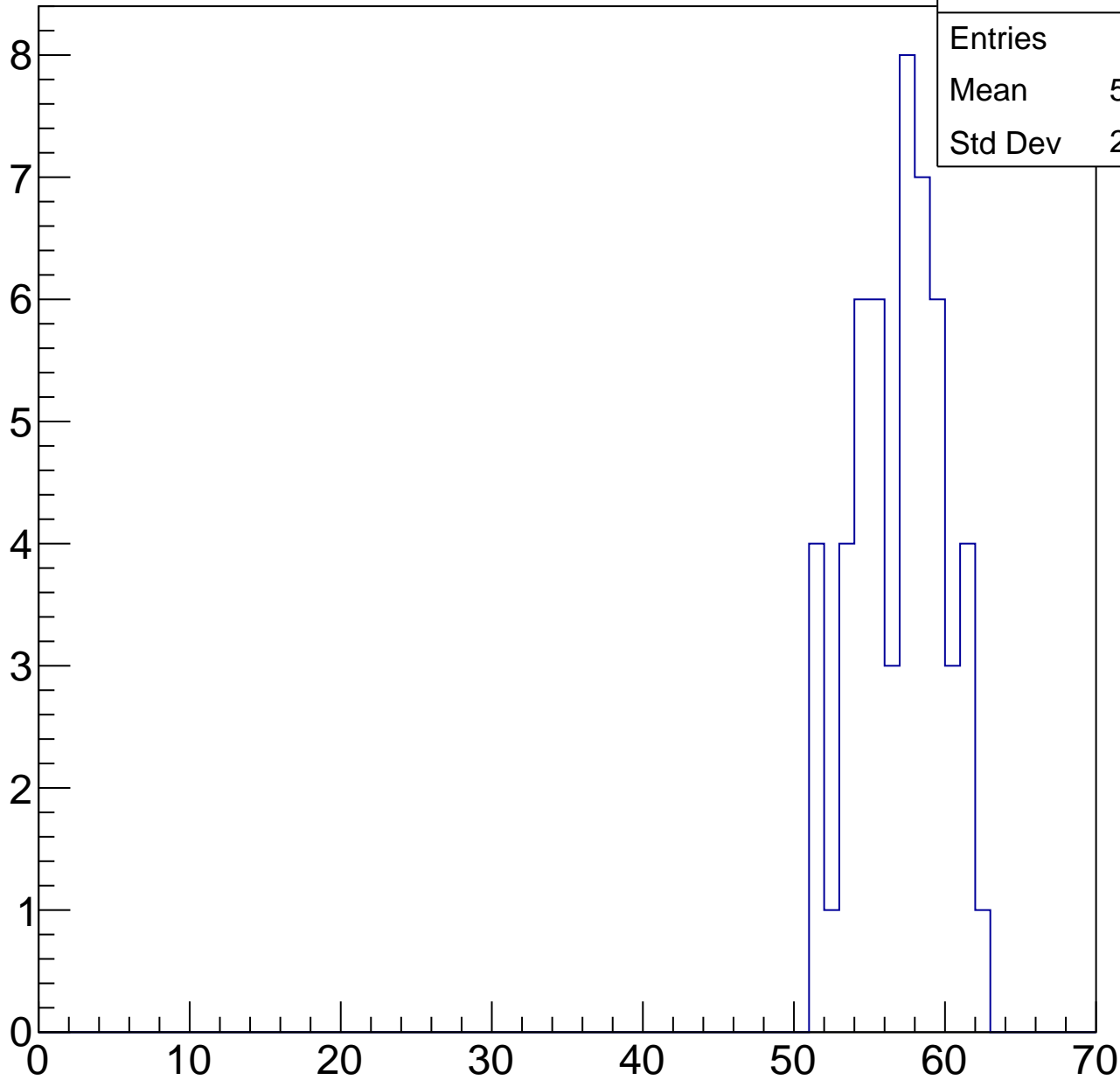
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 56.45 |
| Std Dev | 2.898 |

ampl

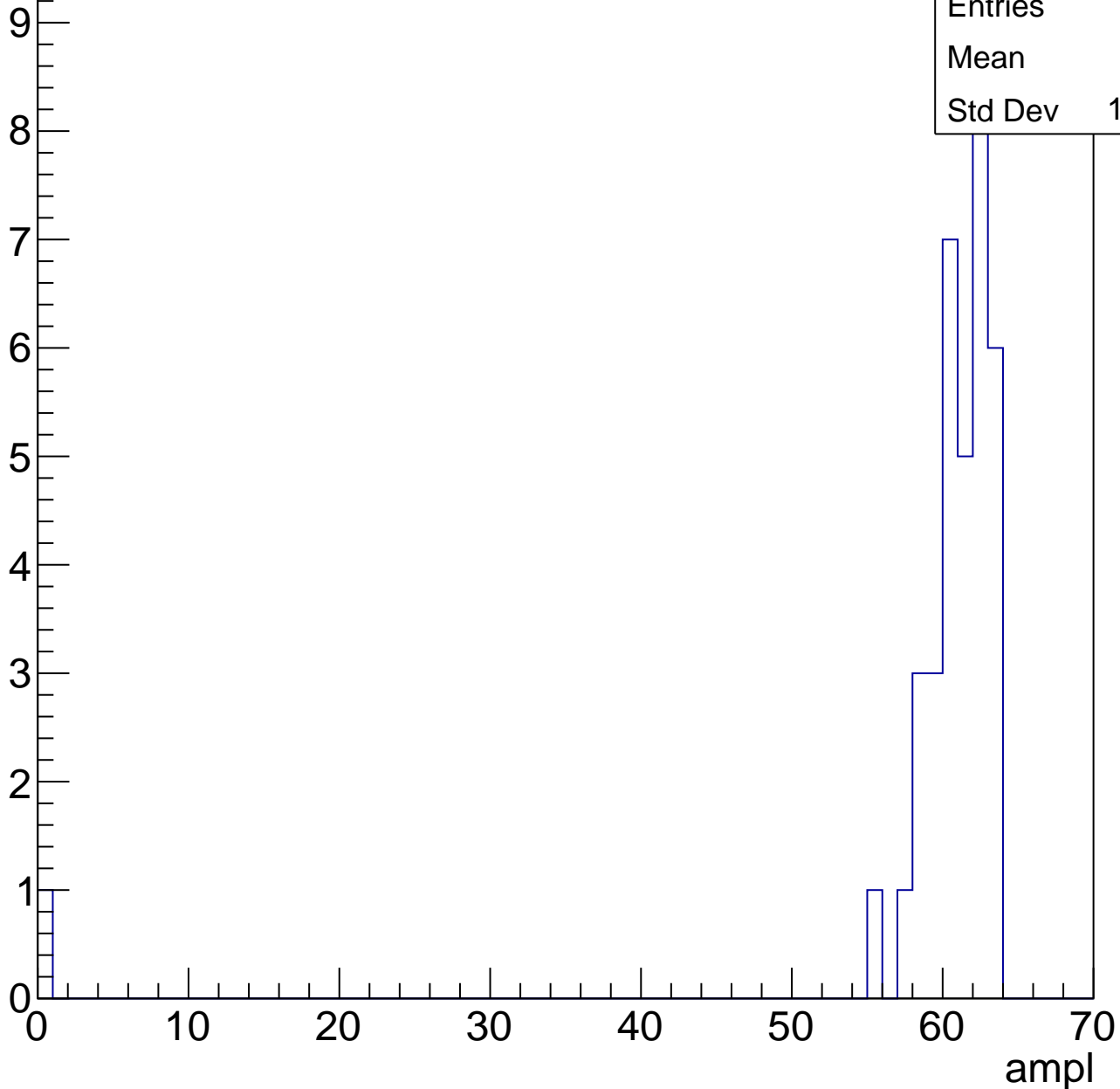


B1L100S, U9-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

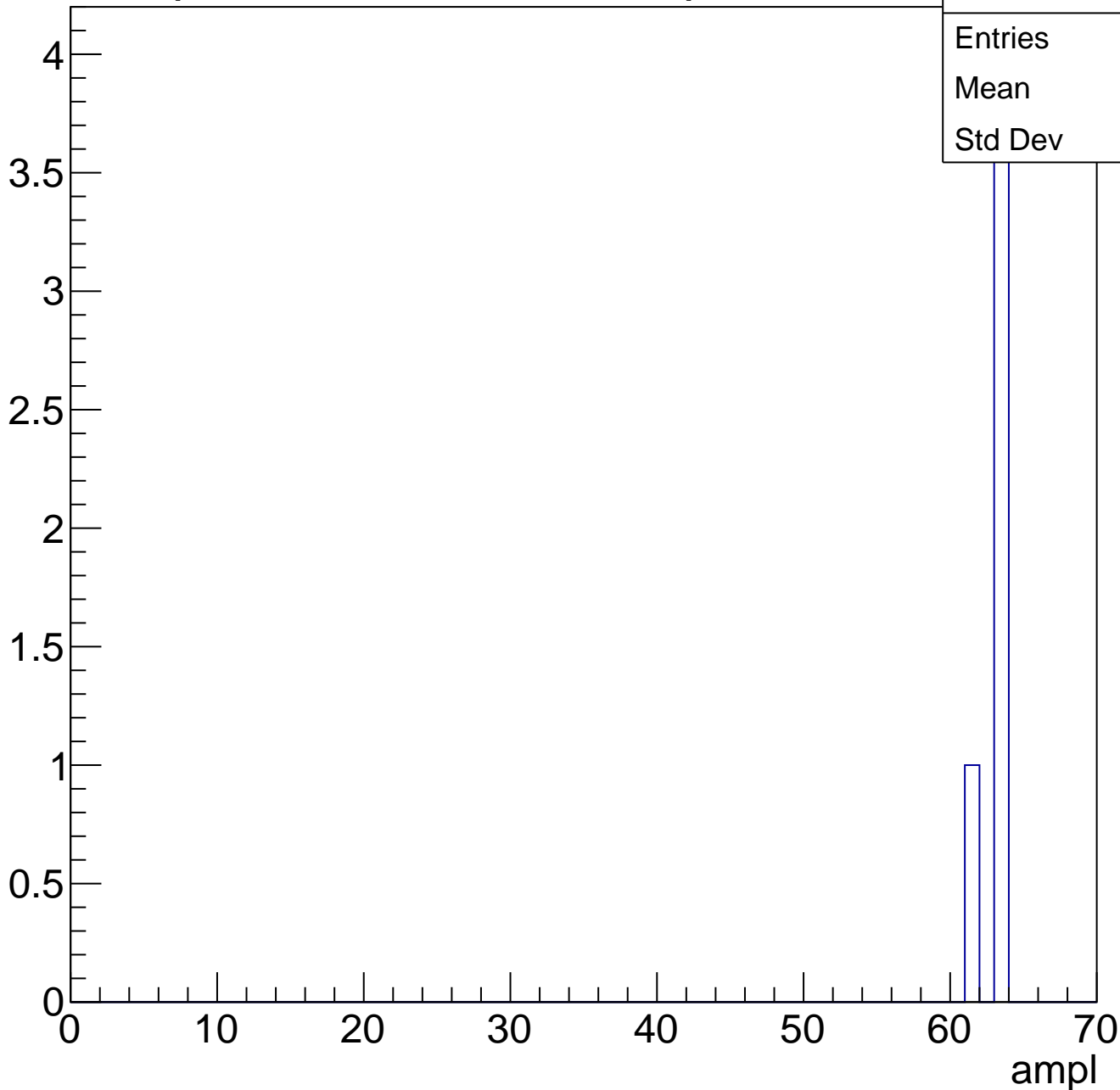
| | |
|---------|-------|
| Entries | 36 |
| Mean | 59 |
| Std Dev | 10.15 |



B1L100S, U9-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch16, adc0

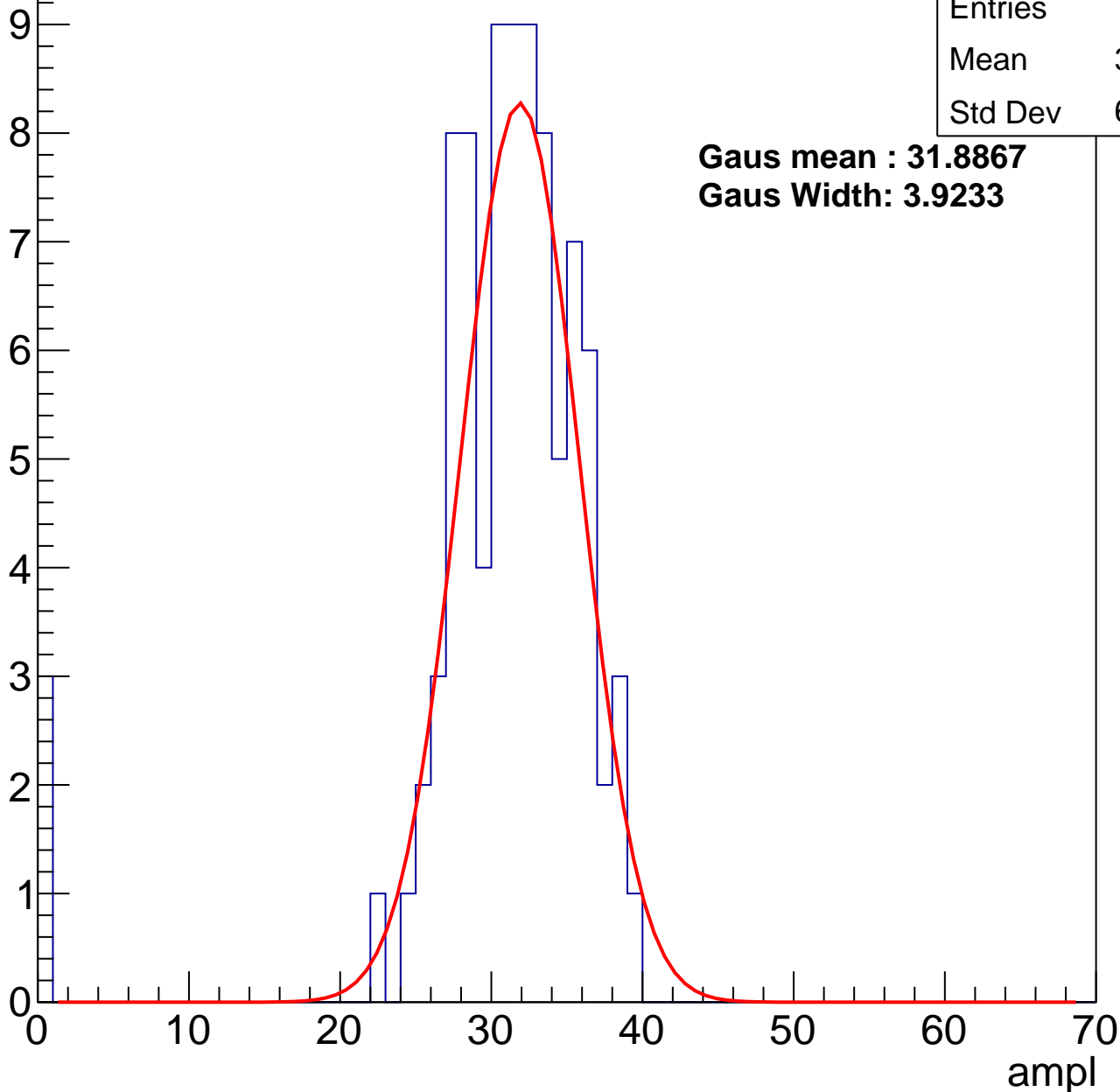
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 30.21 |
| Std Dev | 6.671 |

Gaus mean : 31.8867

Gaus Width: 3.9233



B1L100S, U9-ch16, adc1

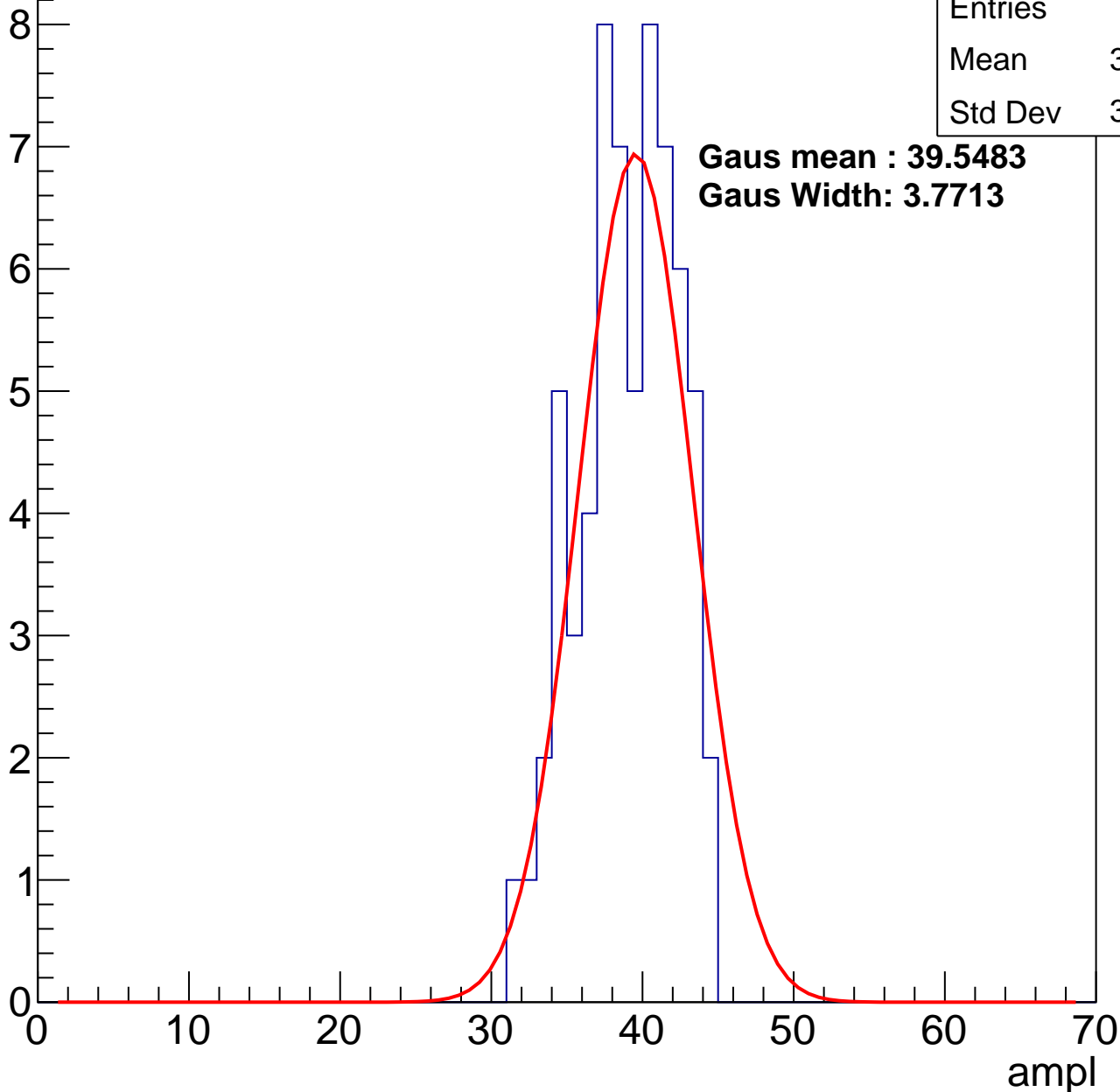
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 38.55 |
| Std Dev | 3.162 |

Gaus mean : 39.5483

Gaus Width: 3.7713



B1L100S, U9-ch16, adc2

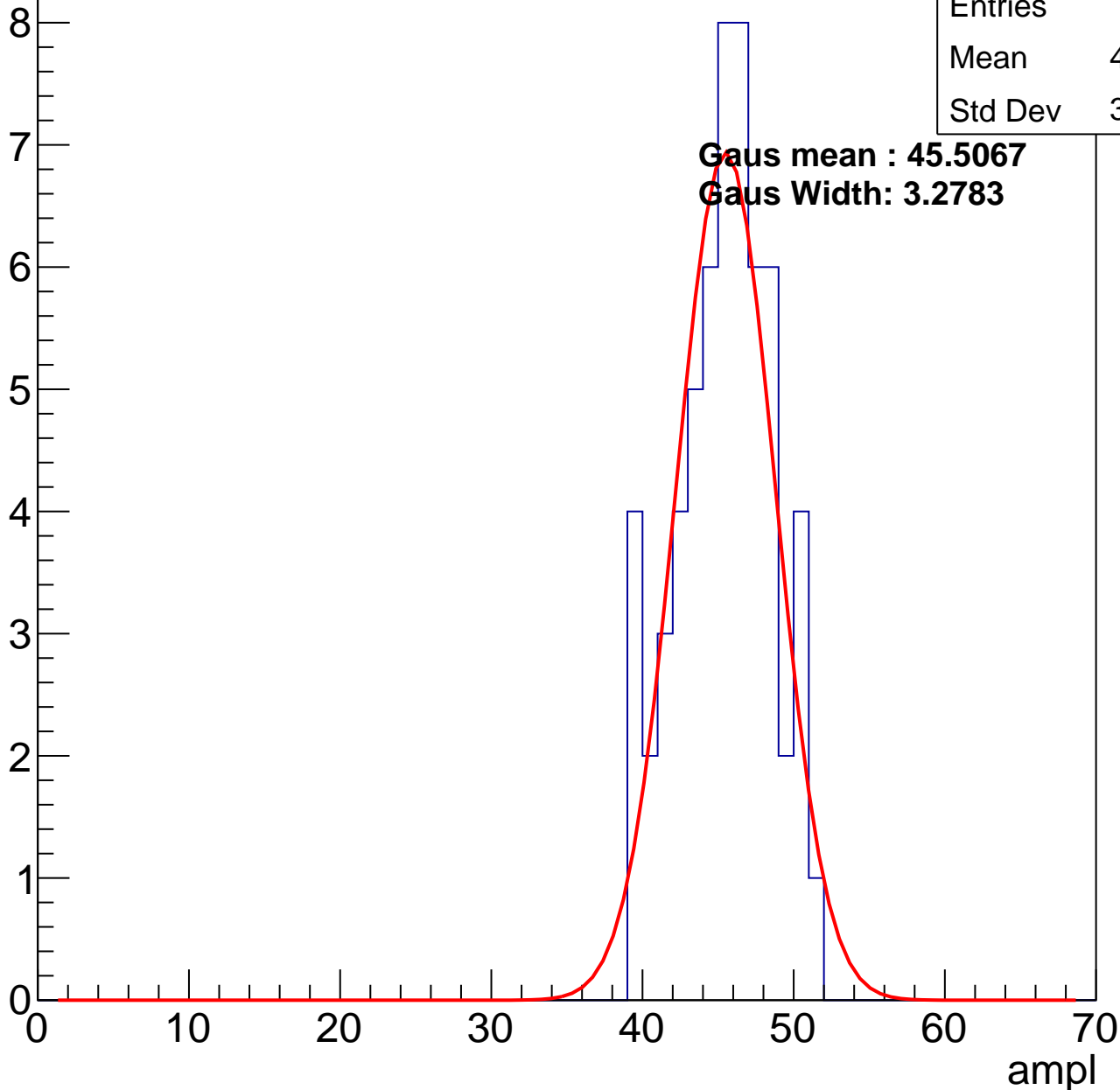
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 44.97 |
| Std Dev | 3.075 |

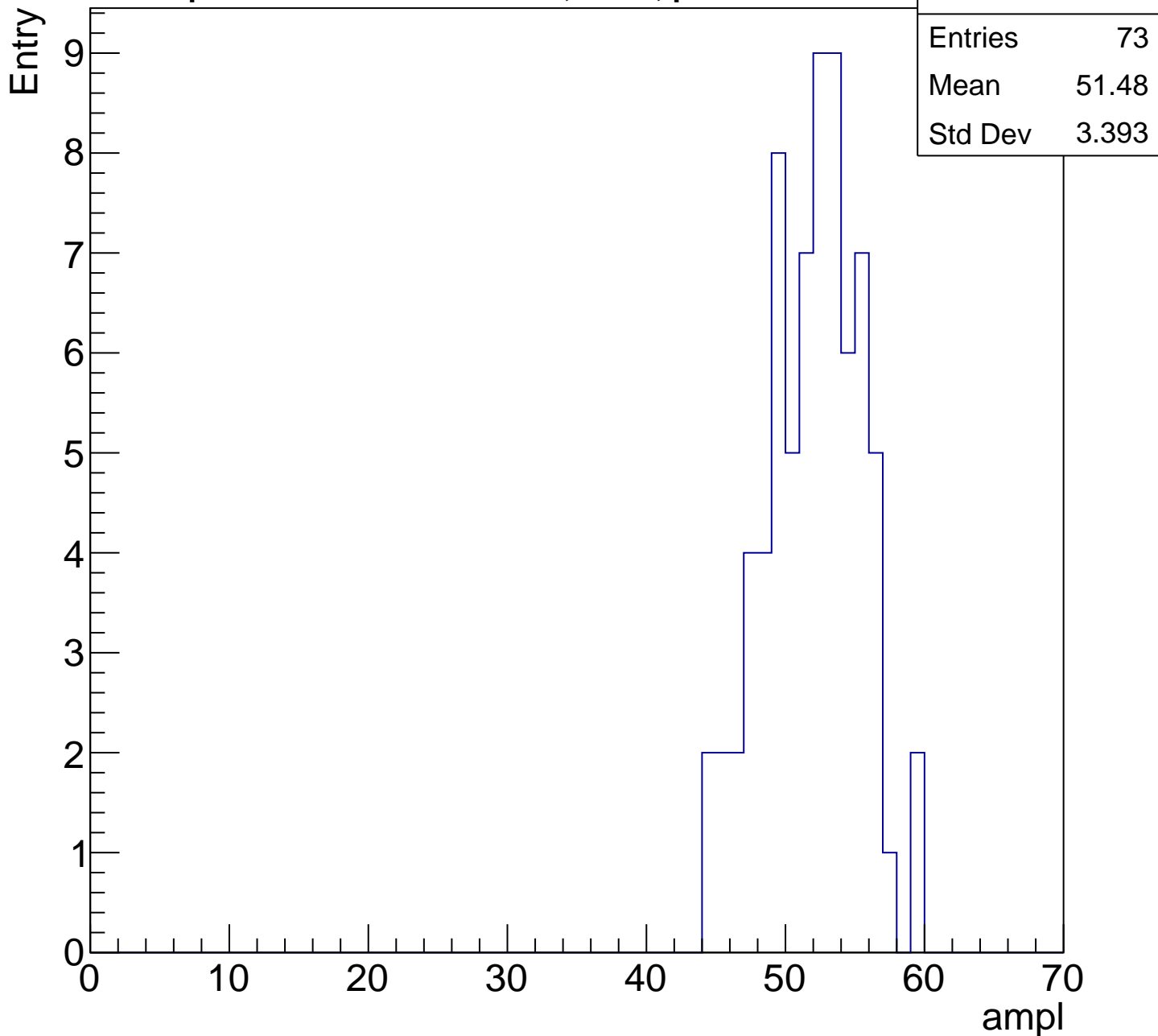
Gaus mean : 45.5067

Gaus Width: 3.2783



B1L100S, U9-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

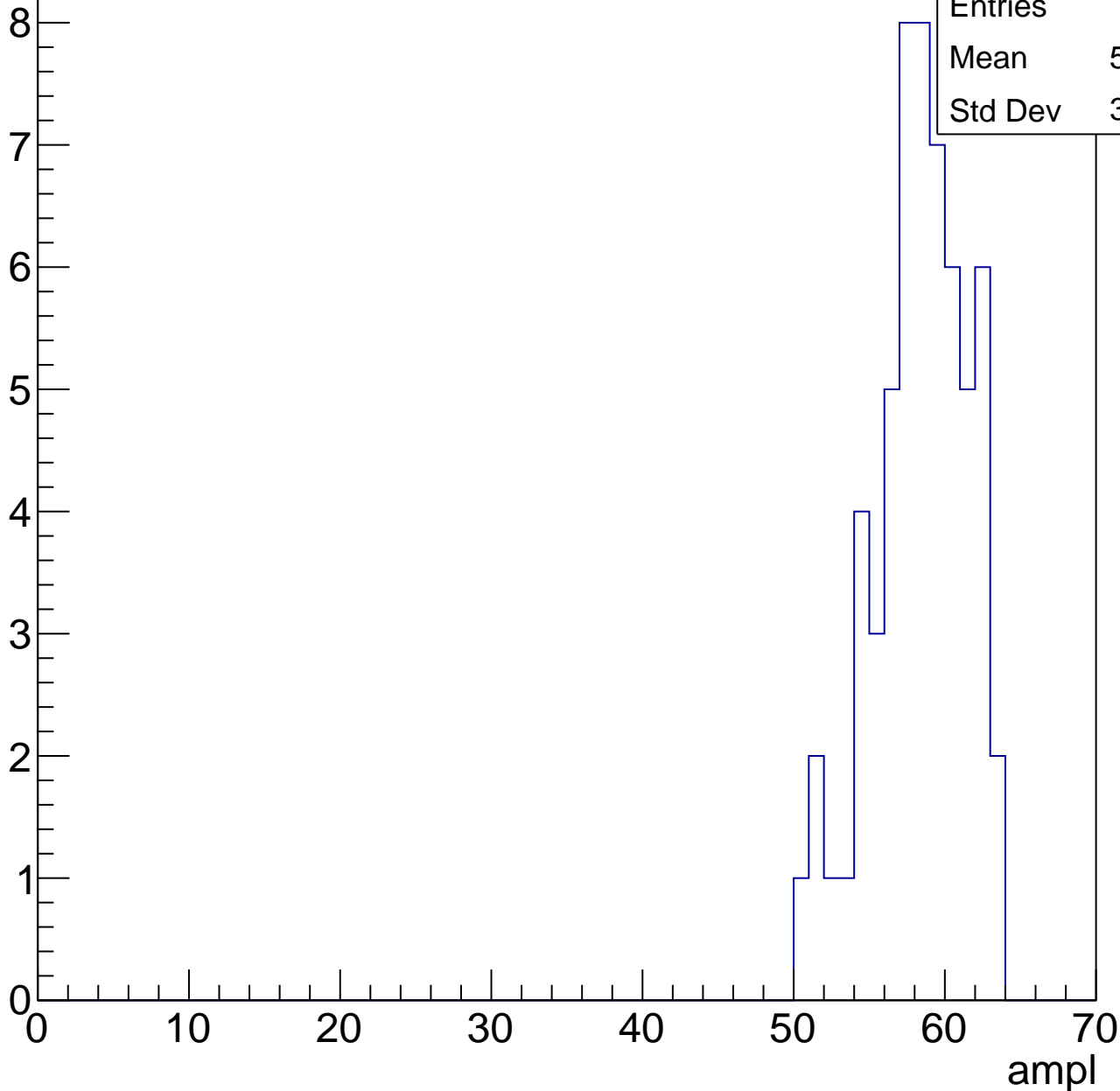


B1L100S, U9-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 57.86 |
| Std Dev | 3.089 |

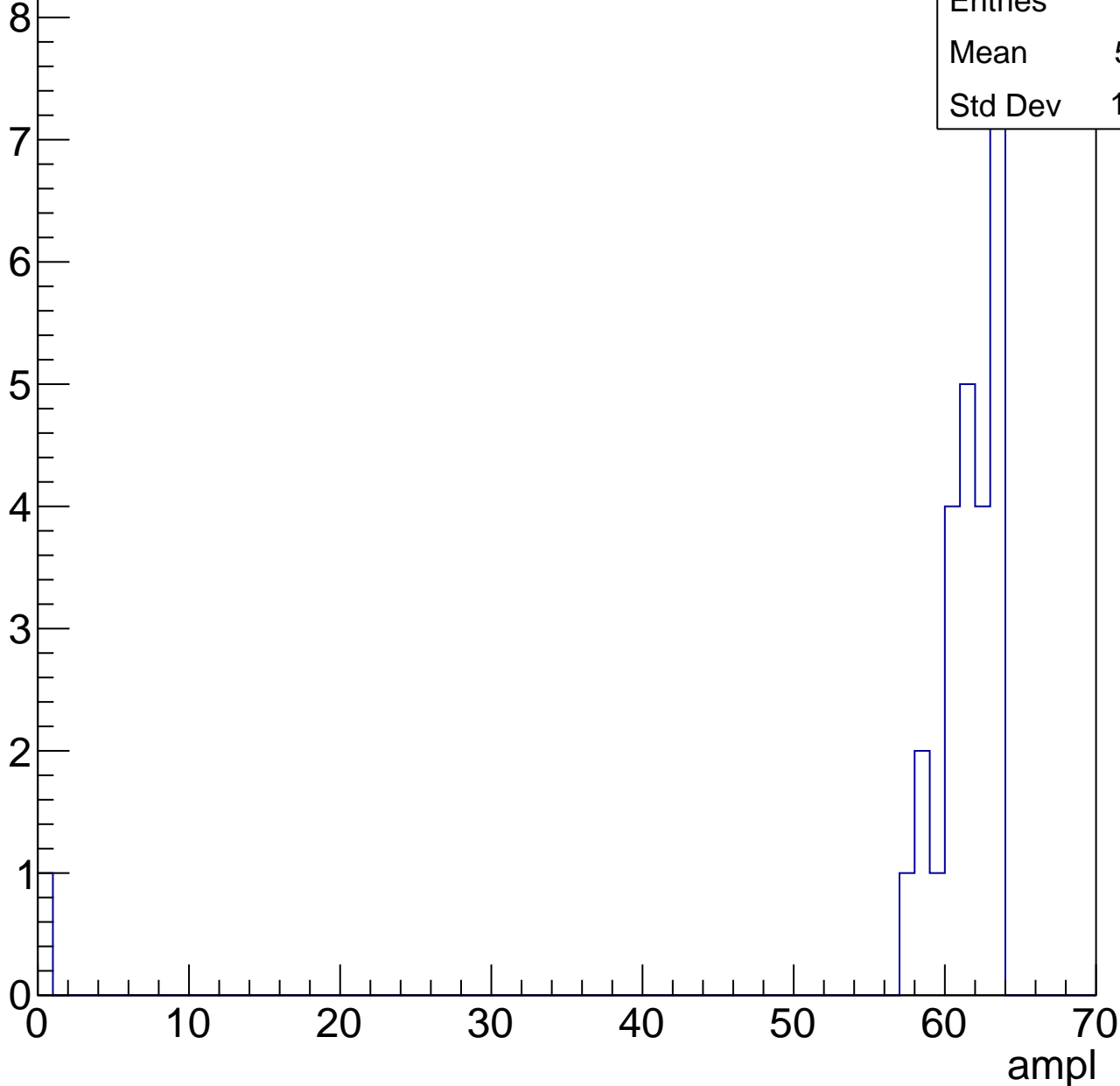


B1L100S, U9-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 58.81 |
| Std Dev | 11.89 |



B1L100S, U9-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch17, adc0

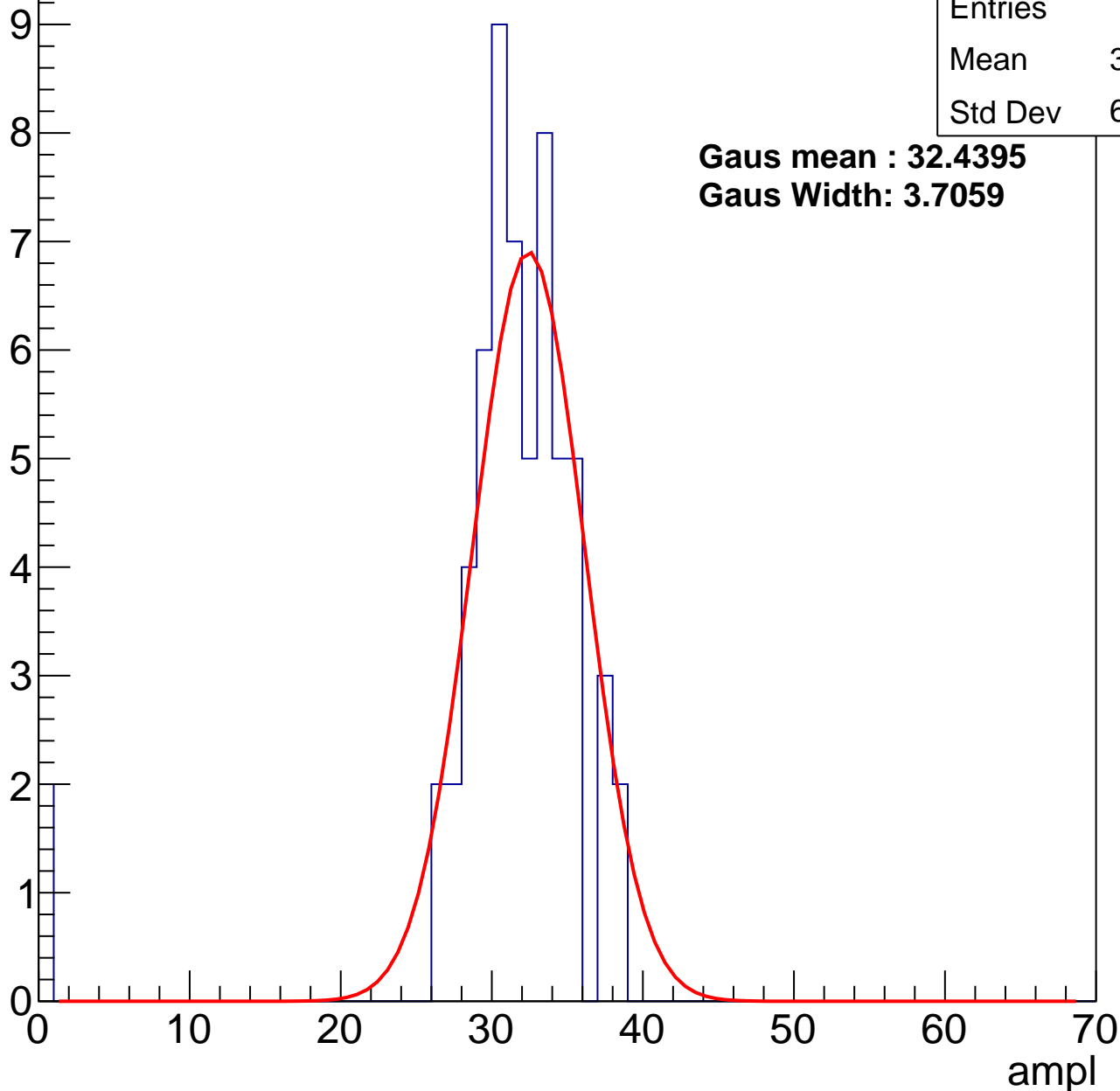
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 30.58 |
| Std Dev | 6.365 |

Gaus mean : 32.4395

Gaus Width: 3.7059



B1L100S, U9-ch17, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 92 |
| Mean | 38.63 |
| Std Dev | 3.75 |

Gaus mean : 39.9468

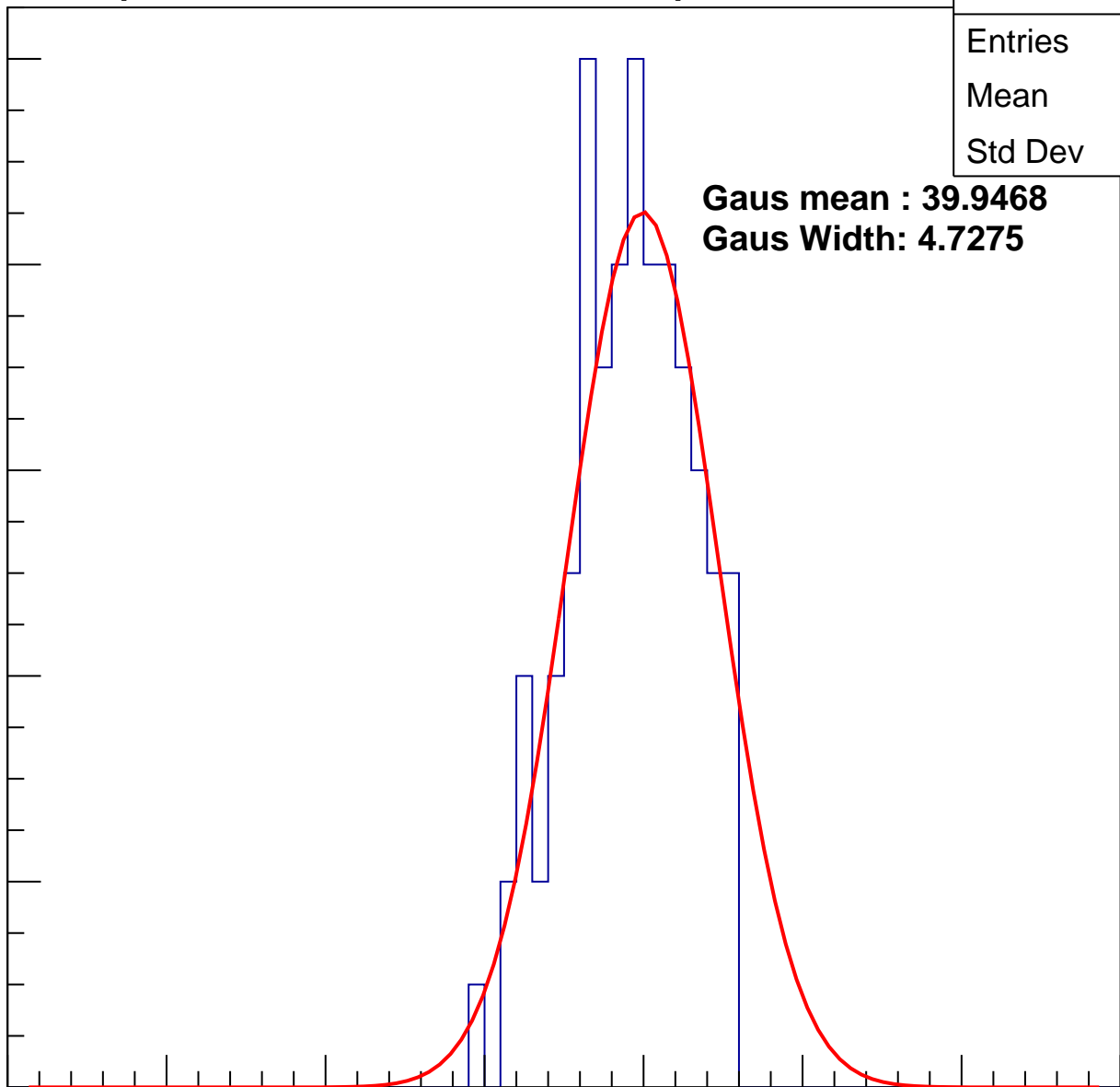
Gaus Width: 4.7275

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch17, adc2

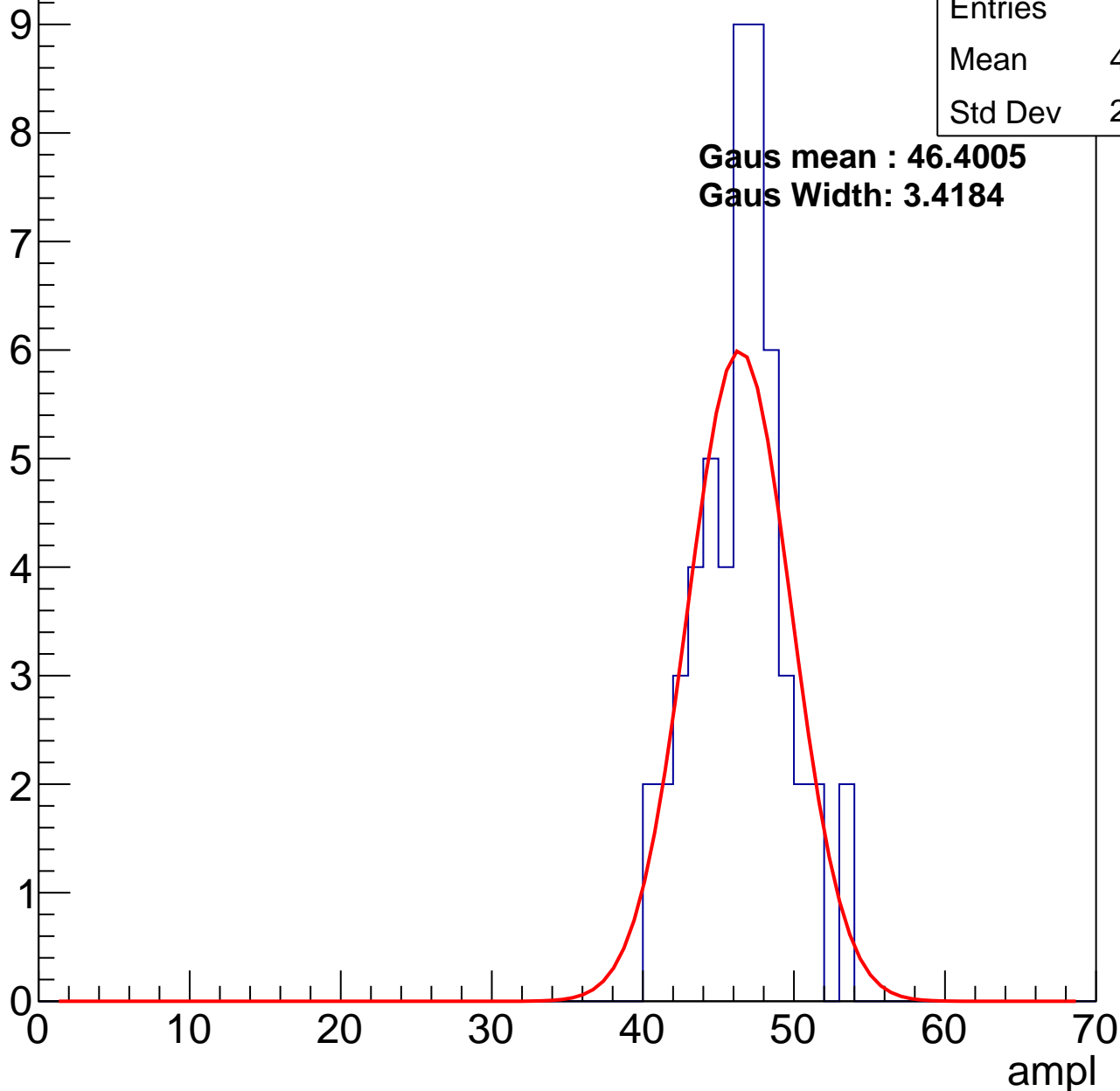
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 46.04 |
| Std Dev | 2.978 |

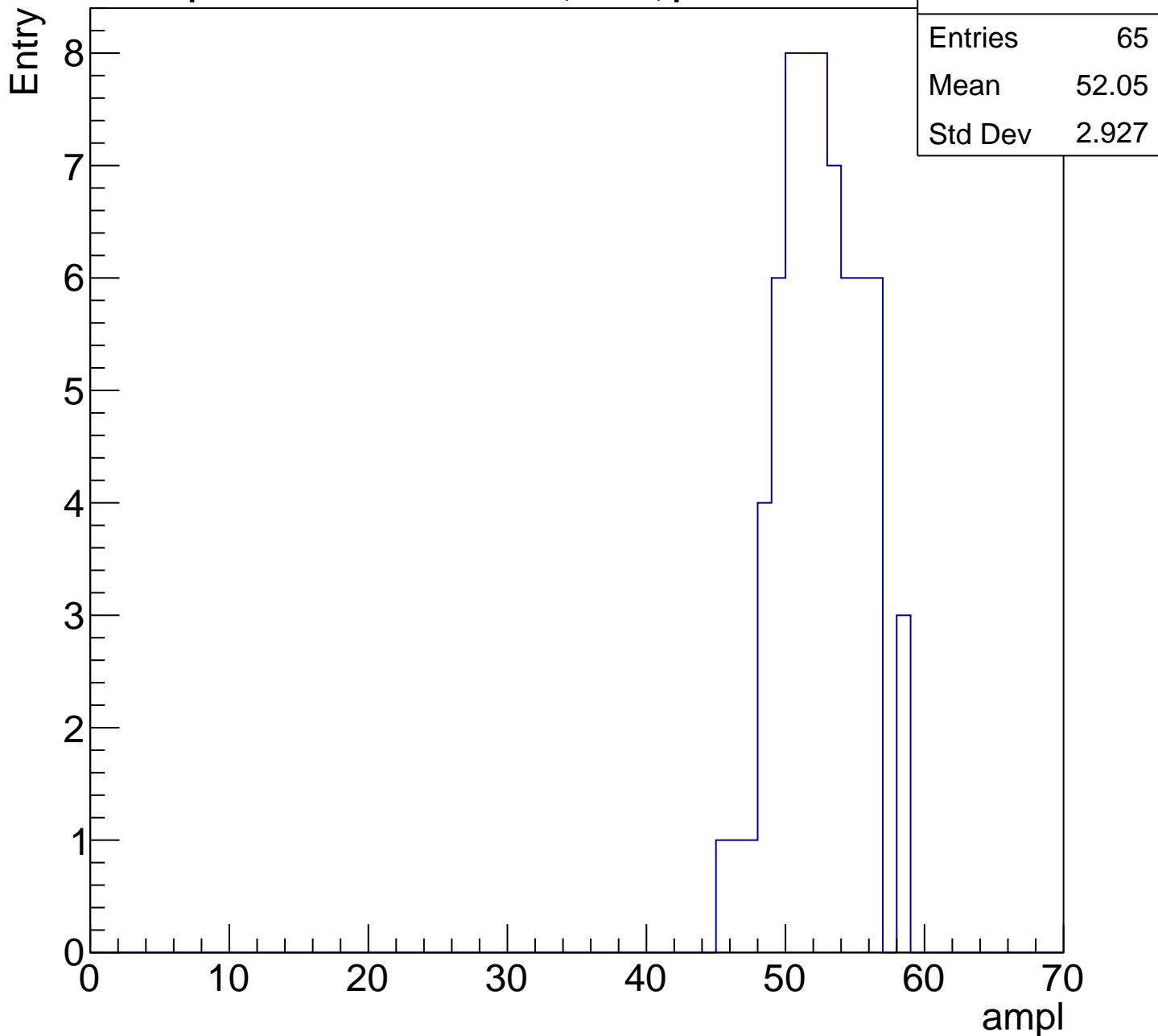
Gaus mean : 46.4005

Gaus Width: 3.4184



B1L100S, U9-ch17, adc3

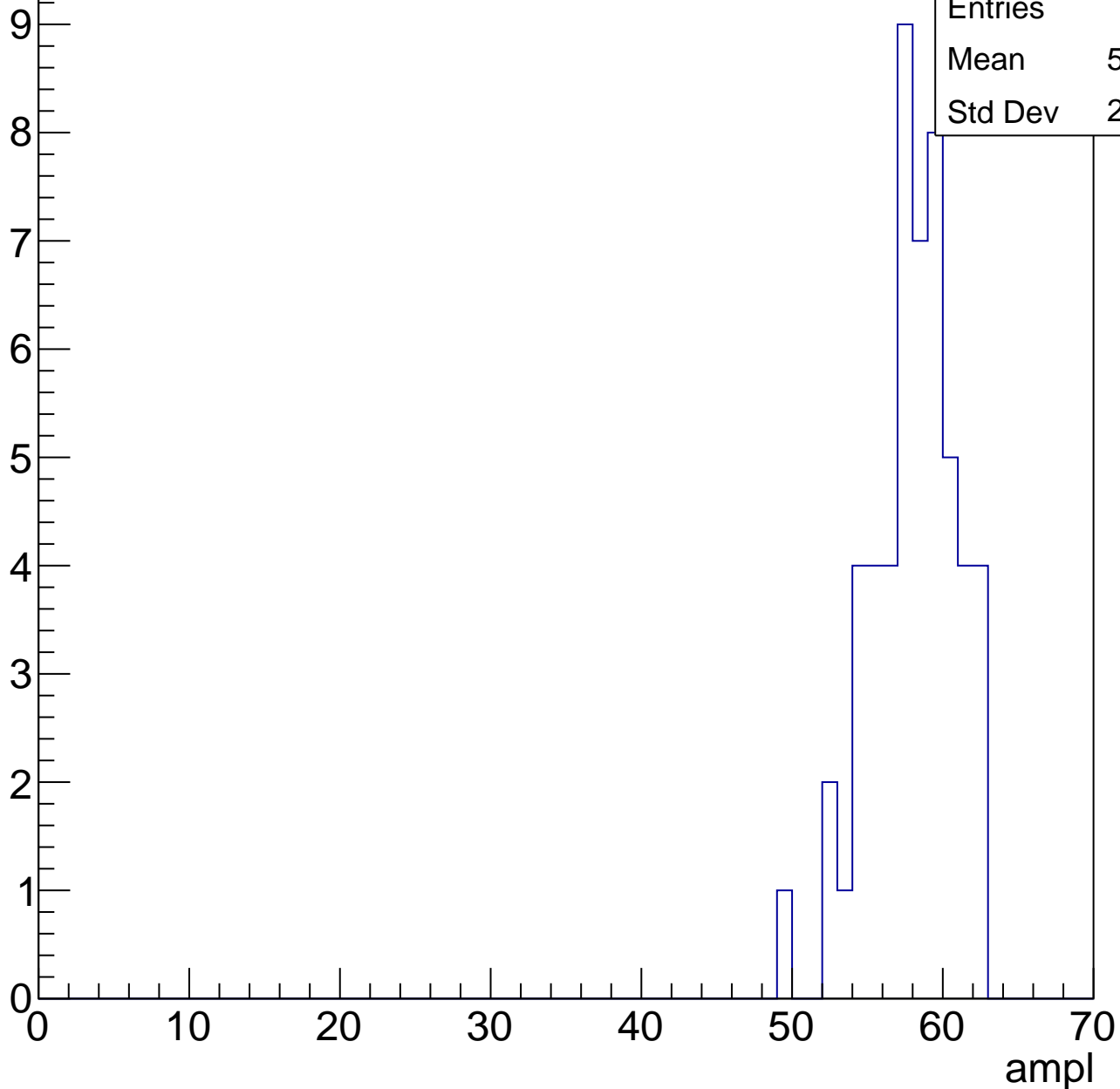
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

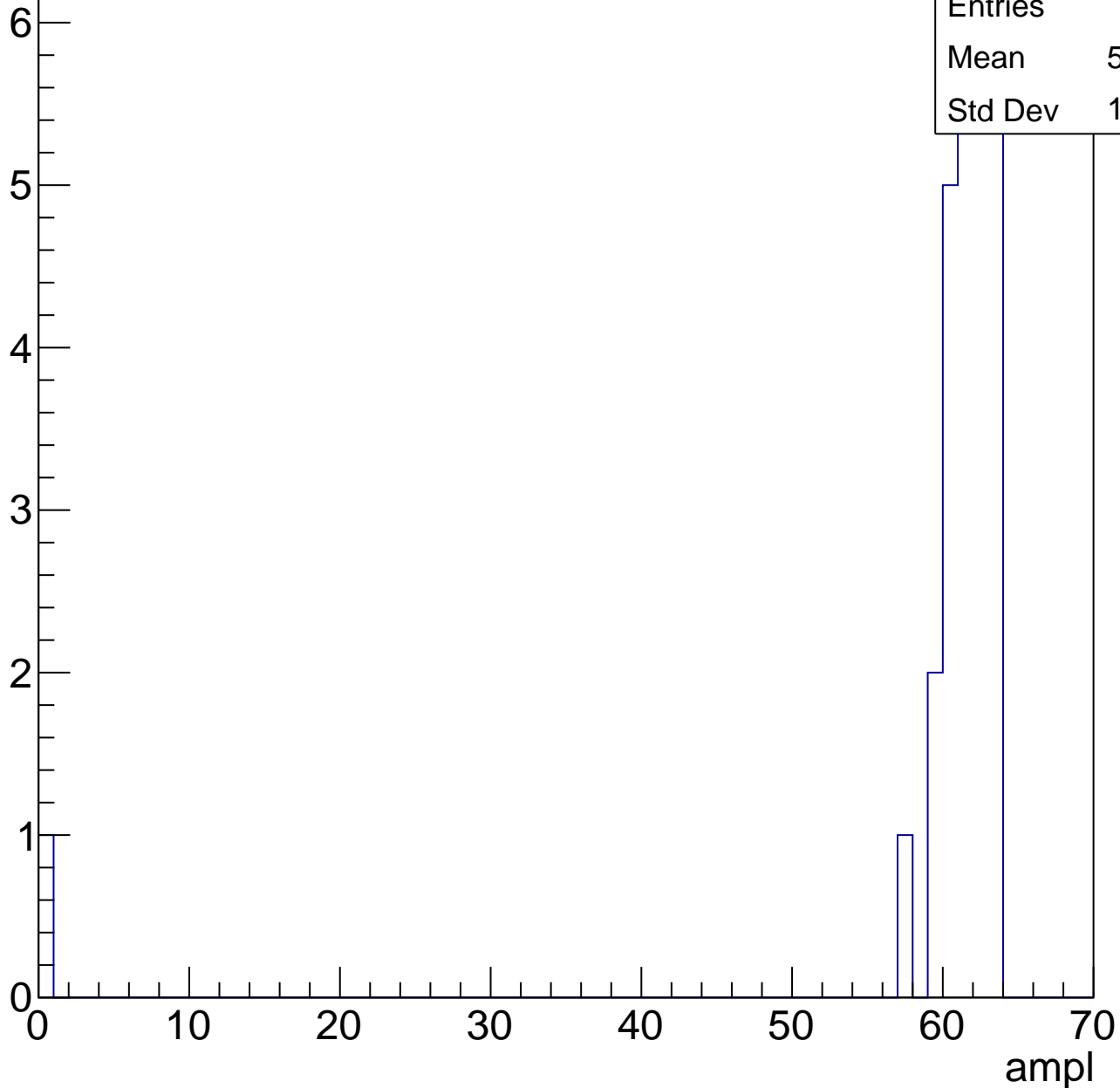


| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.53 |
| Std Dev | 2.812 |

B1L100S, U9-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

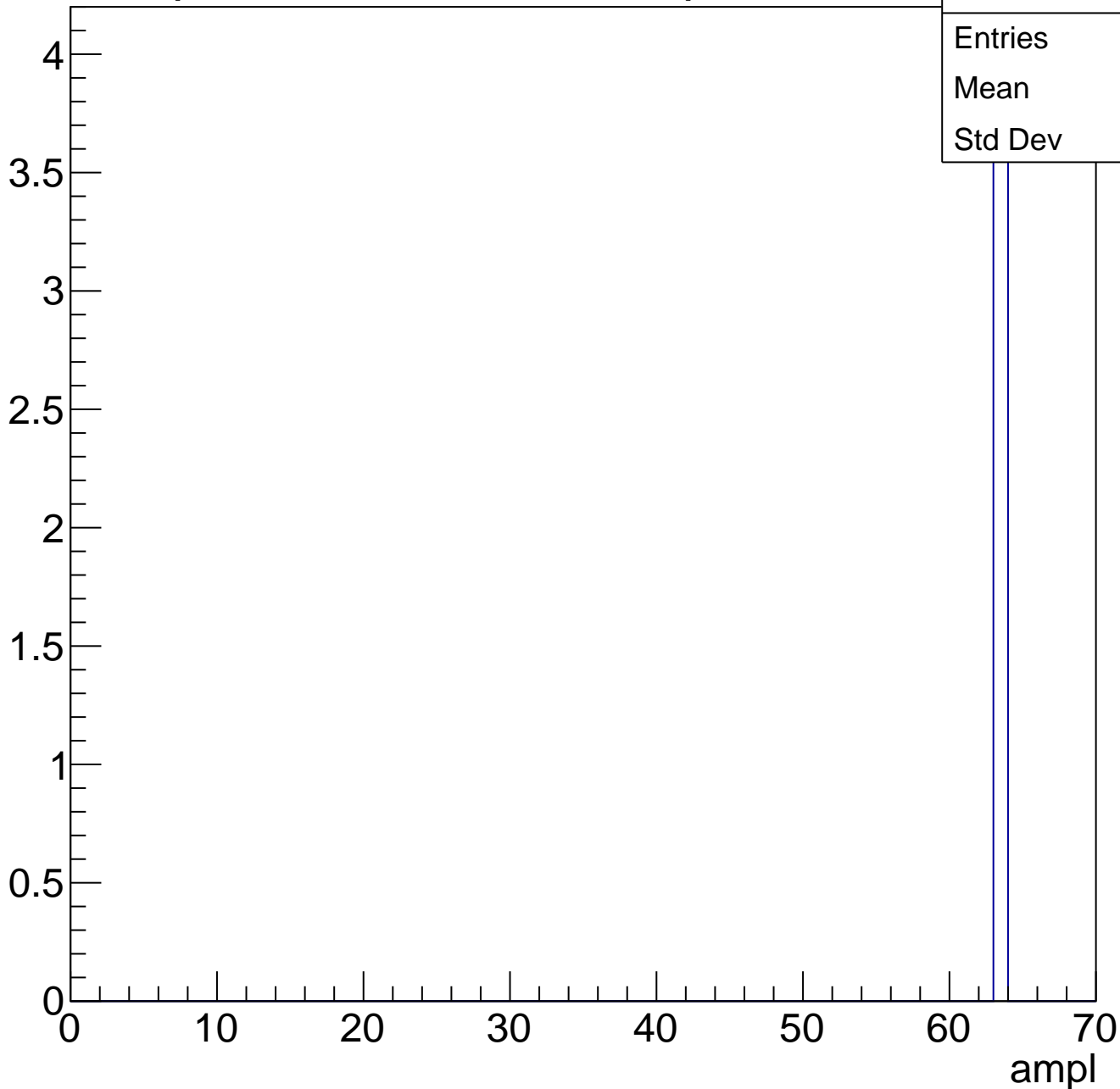
Entry



B1L100S, U9-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch18, adc0

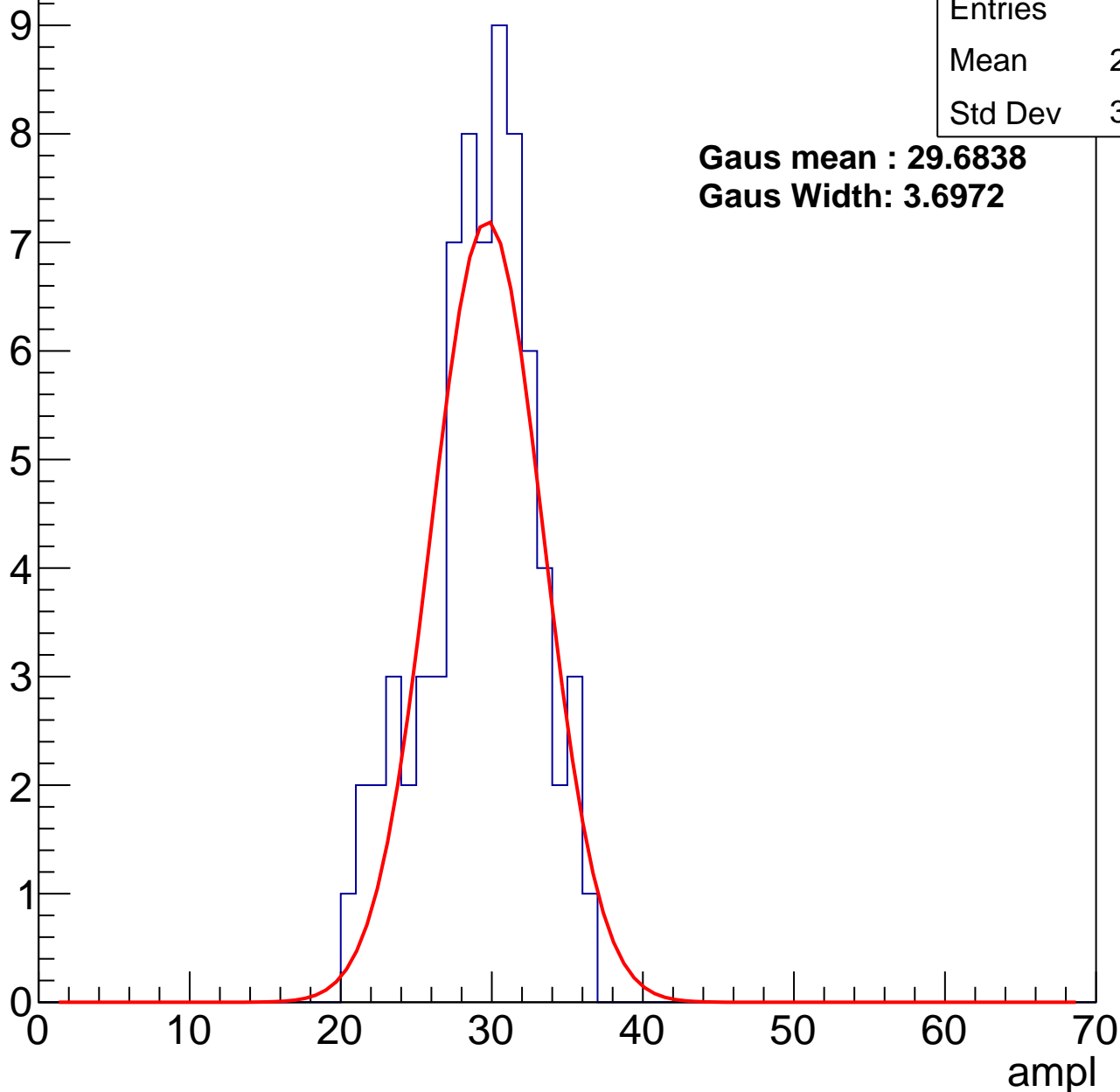
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.77 |
| Std Dev | 3.635 |

Gaus mean : 29.6838

Gaus Width: 3.6972



B1L100S, U9-ch18, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.72 |
| Std Dev | 3.604 |

Gaus mean : 36.7995

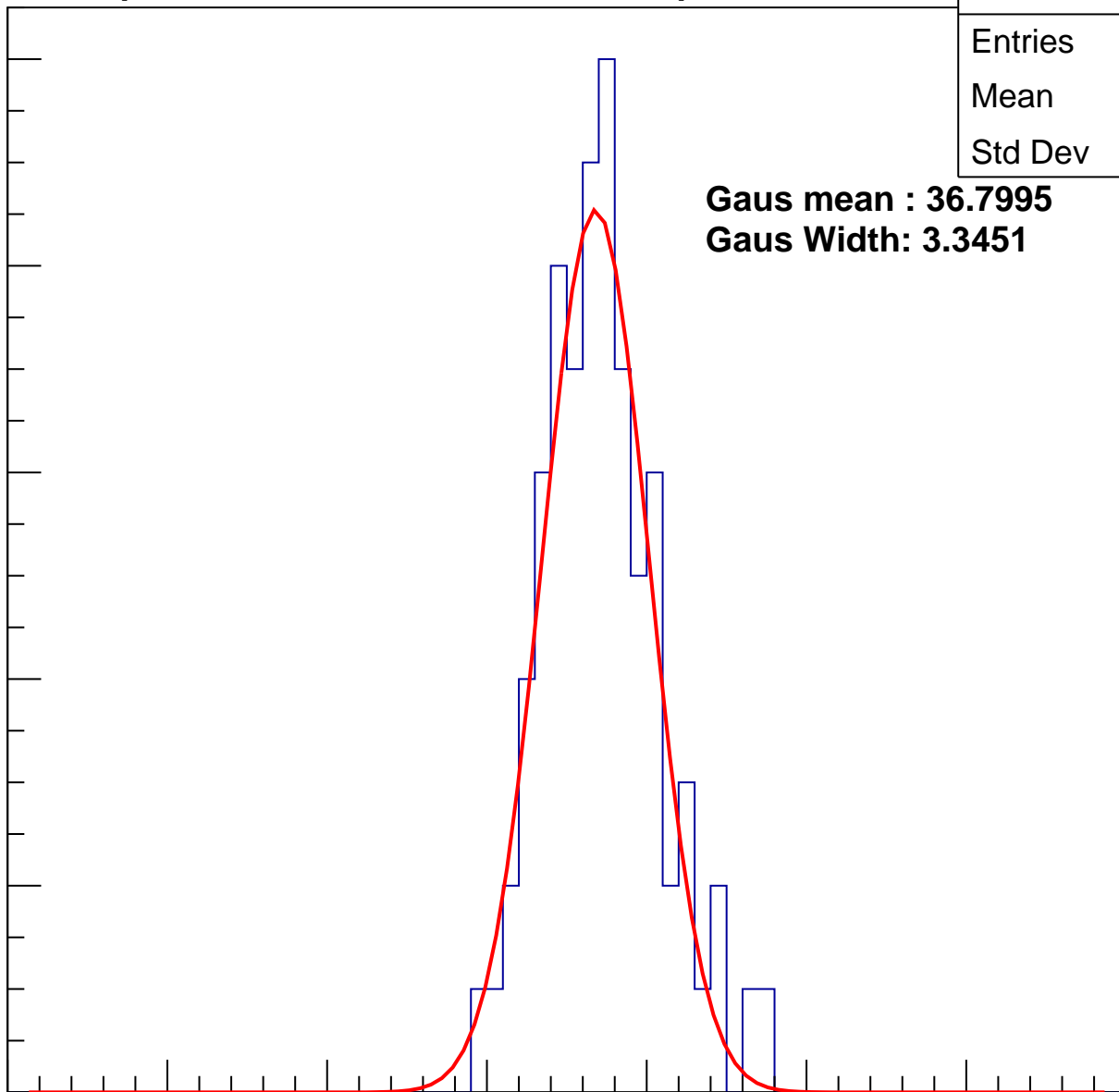
Gaus Width: 3.3451

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch18, adc2

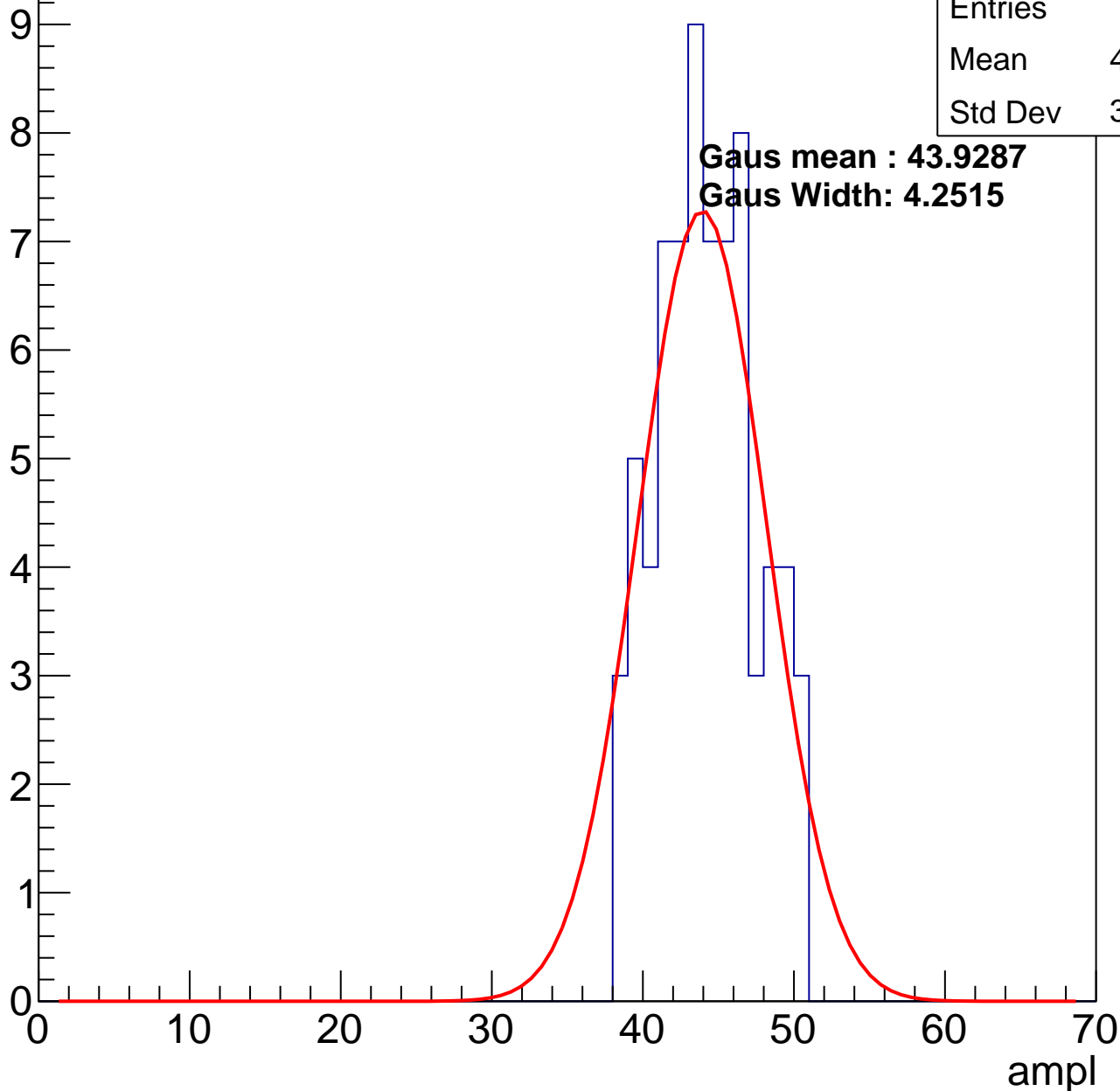
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 43.76 |
| Std Dev | 3.209 |

Gaus mean : 43.9287

Gaus Width: 4.2515

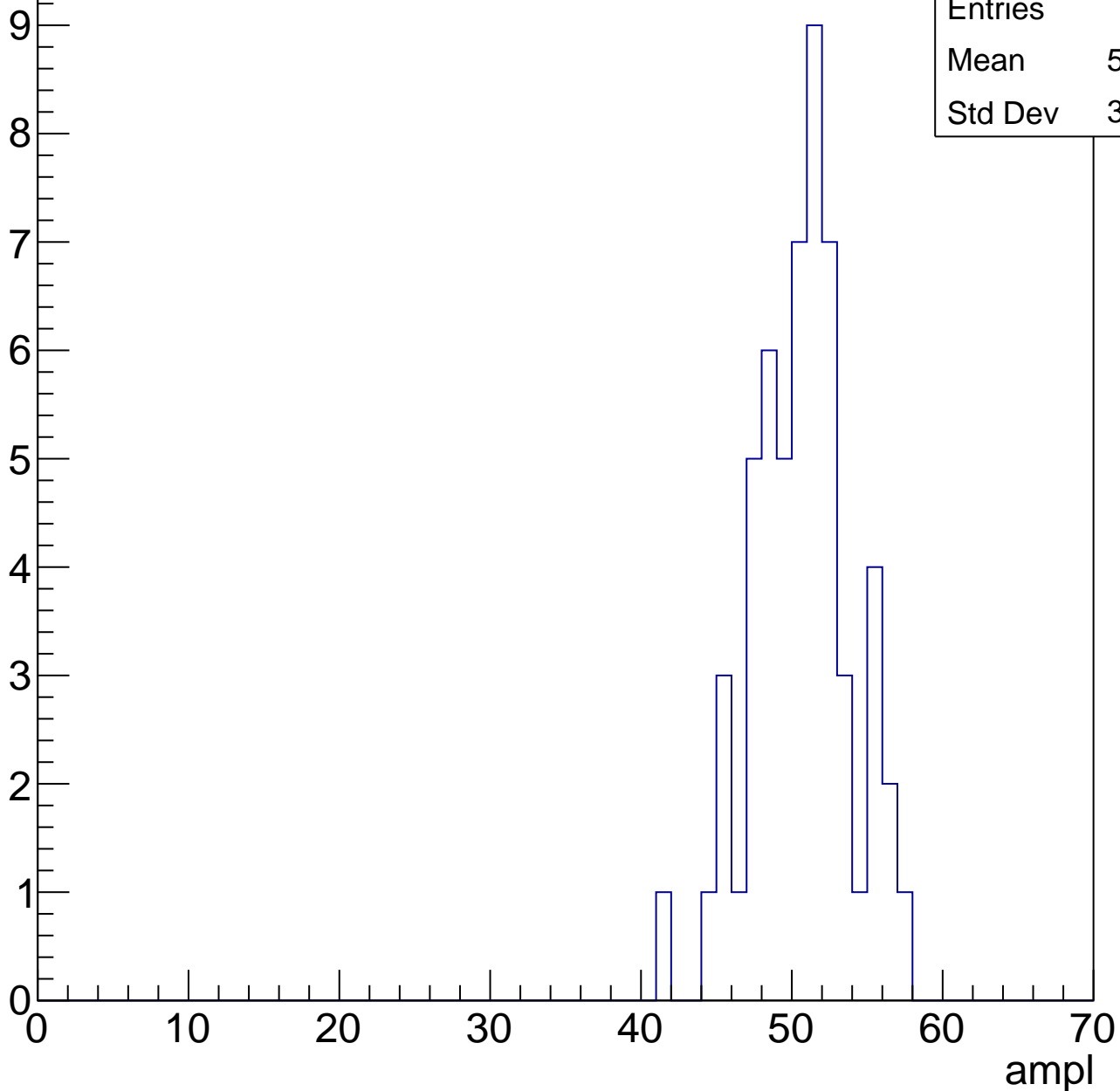


B1L100S, U9-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

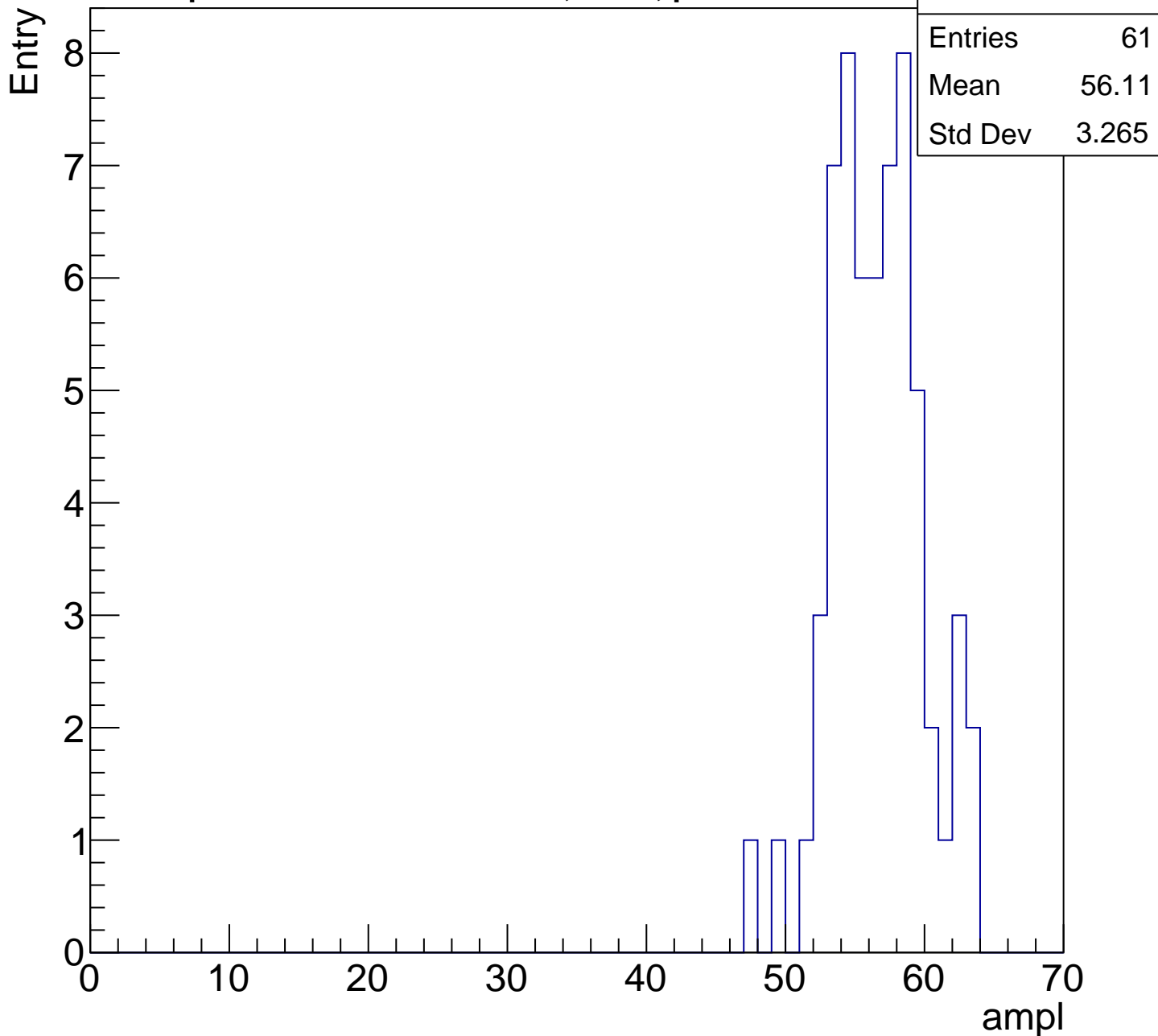
Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 50.16 |
| Std Dev | 3.223 |



B1L100S, U9-ch18, adc4

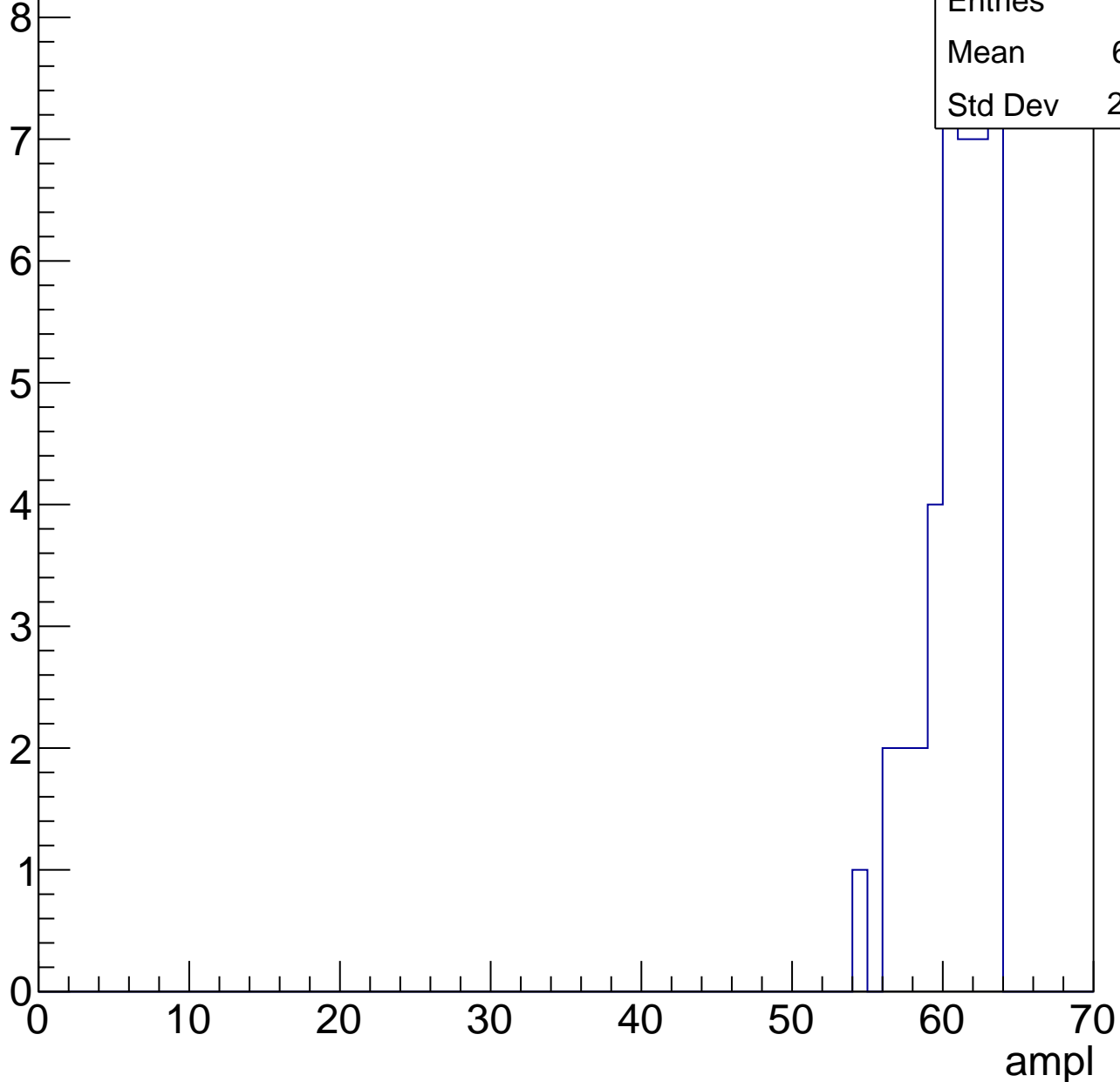
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

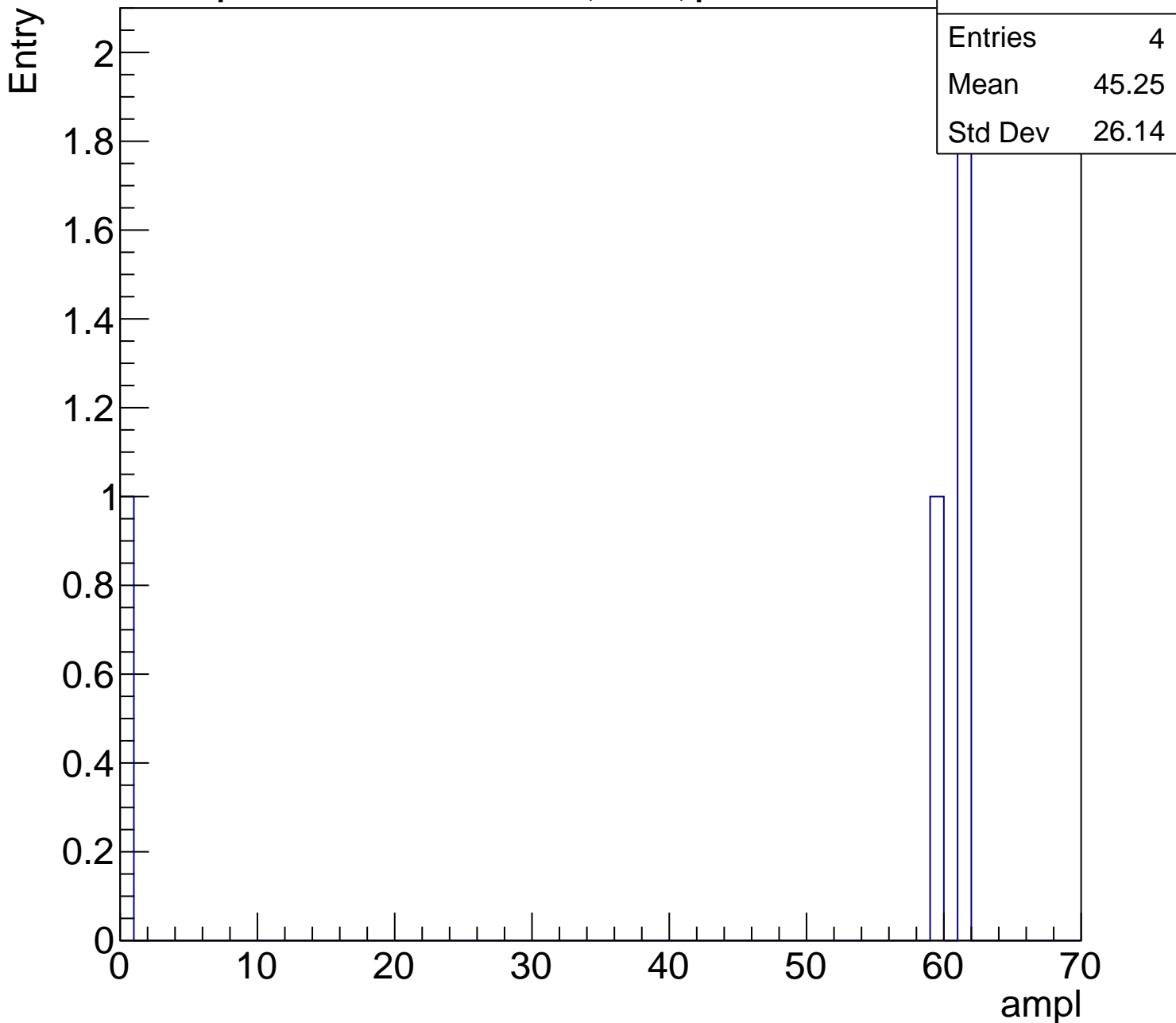
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 45.25 |
| Std Dev | 26.14 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch19, adc0

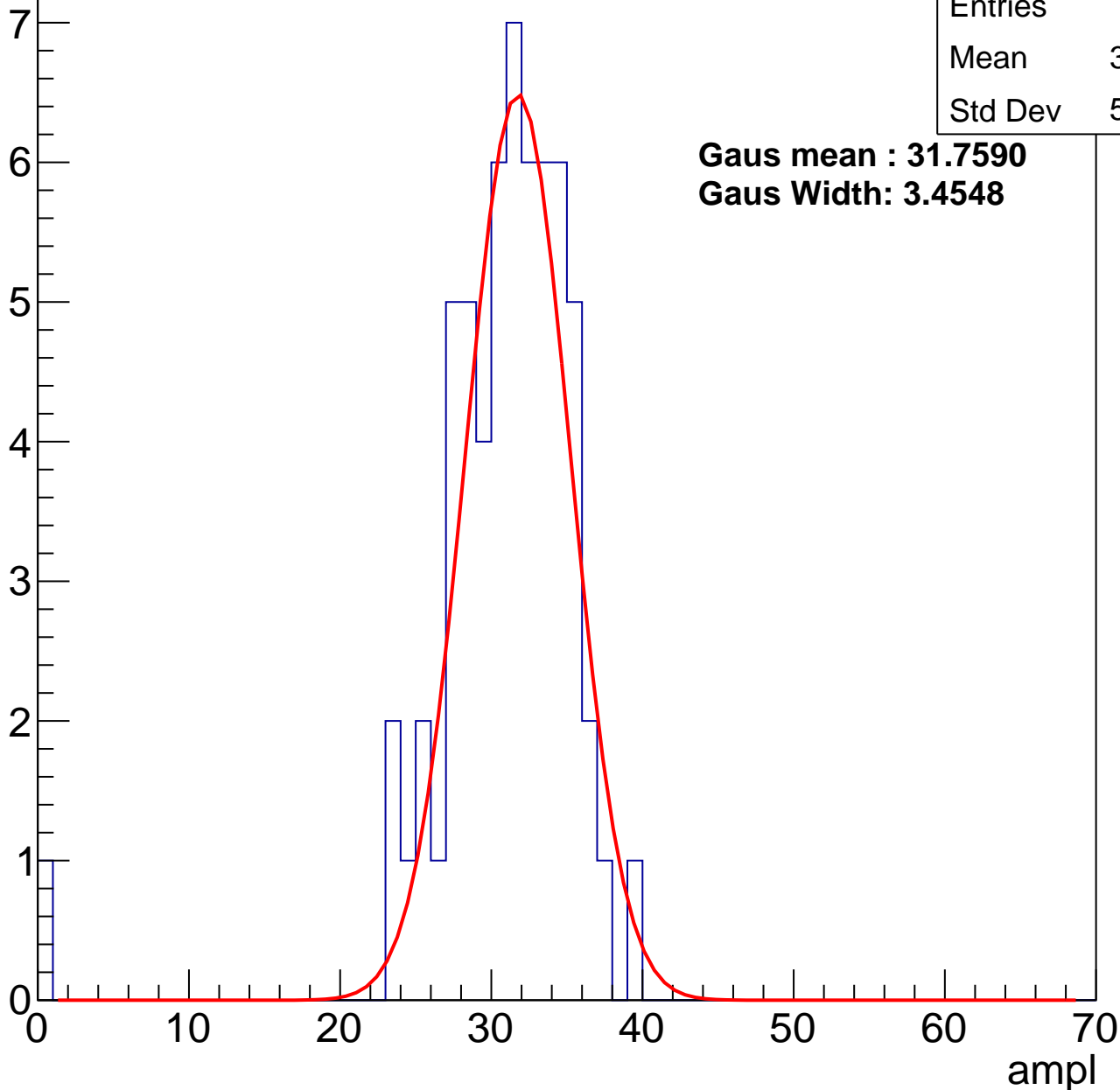
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 30.34 |
| Std Dev | 5.232 |

Gaus mean : 31.7590

Gaus Width: 3.4548



B1L100S, U9-ch19, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 37.49 |
| Std Dev | 3.424 |

Gaus mean : 38.8347

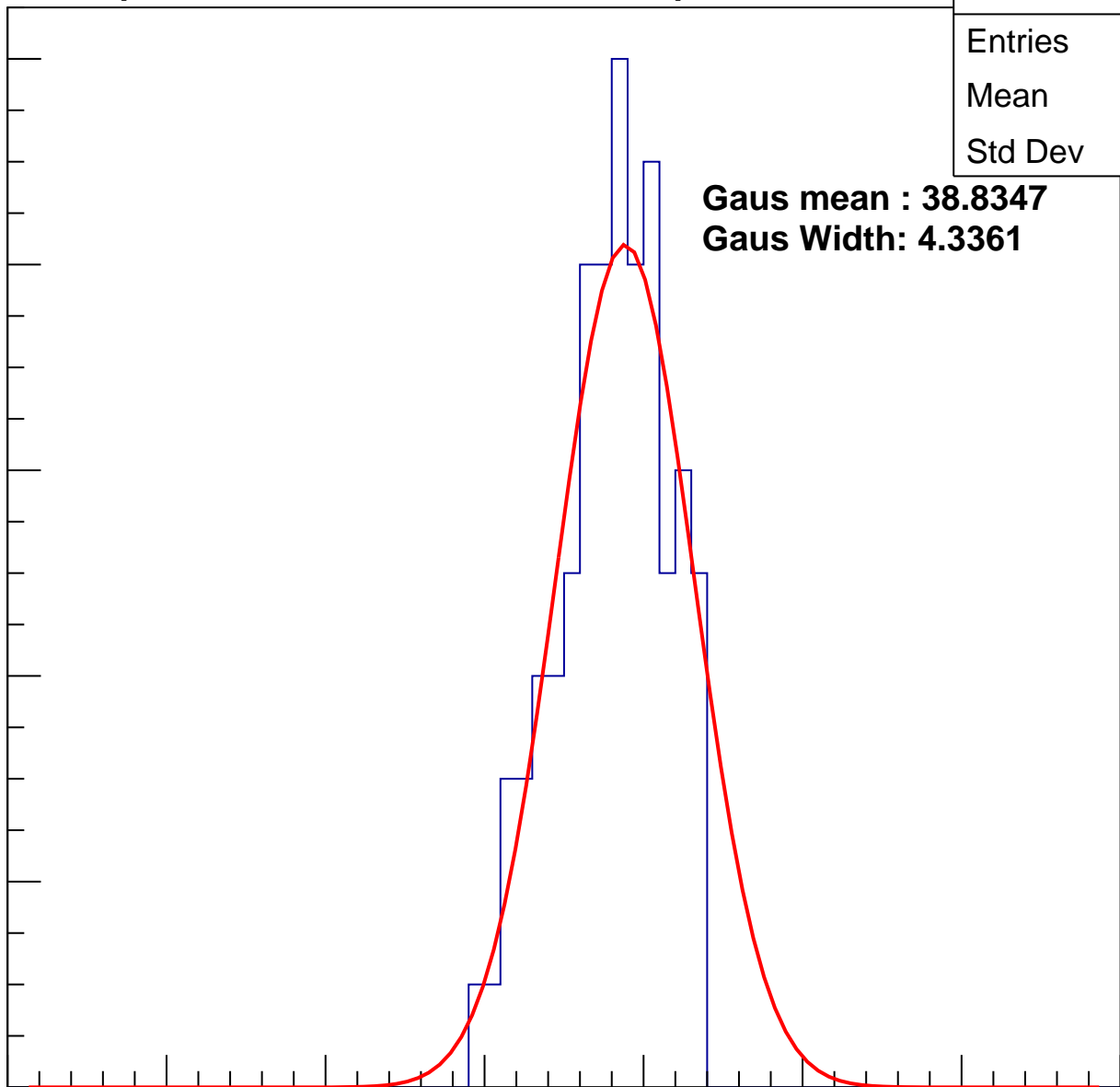
Gaus Width: 4.3361

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch19, adc2

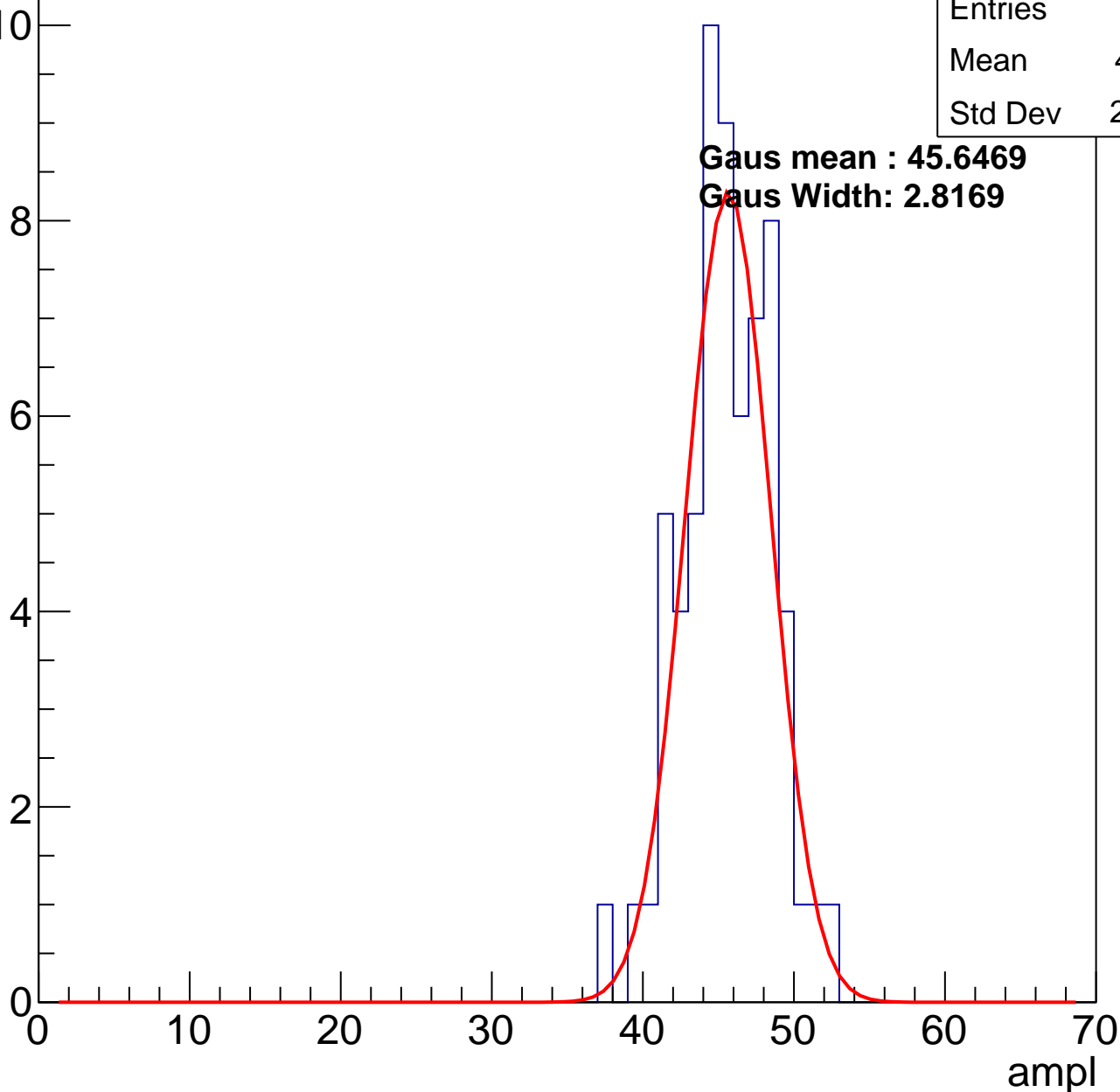
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.11 |
| Std Dev | 2.932 |

Gaus mean : 45.6469

Gaus Width: 2.8169

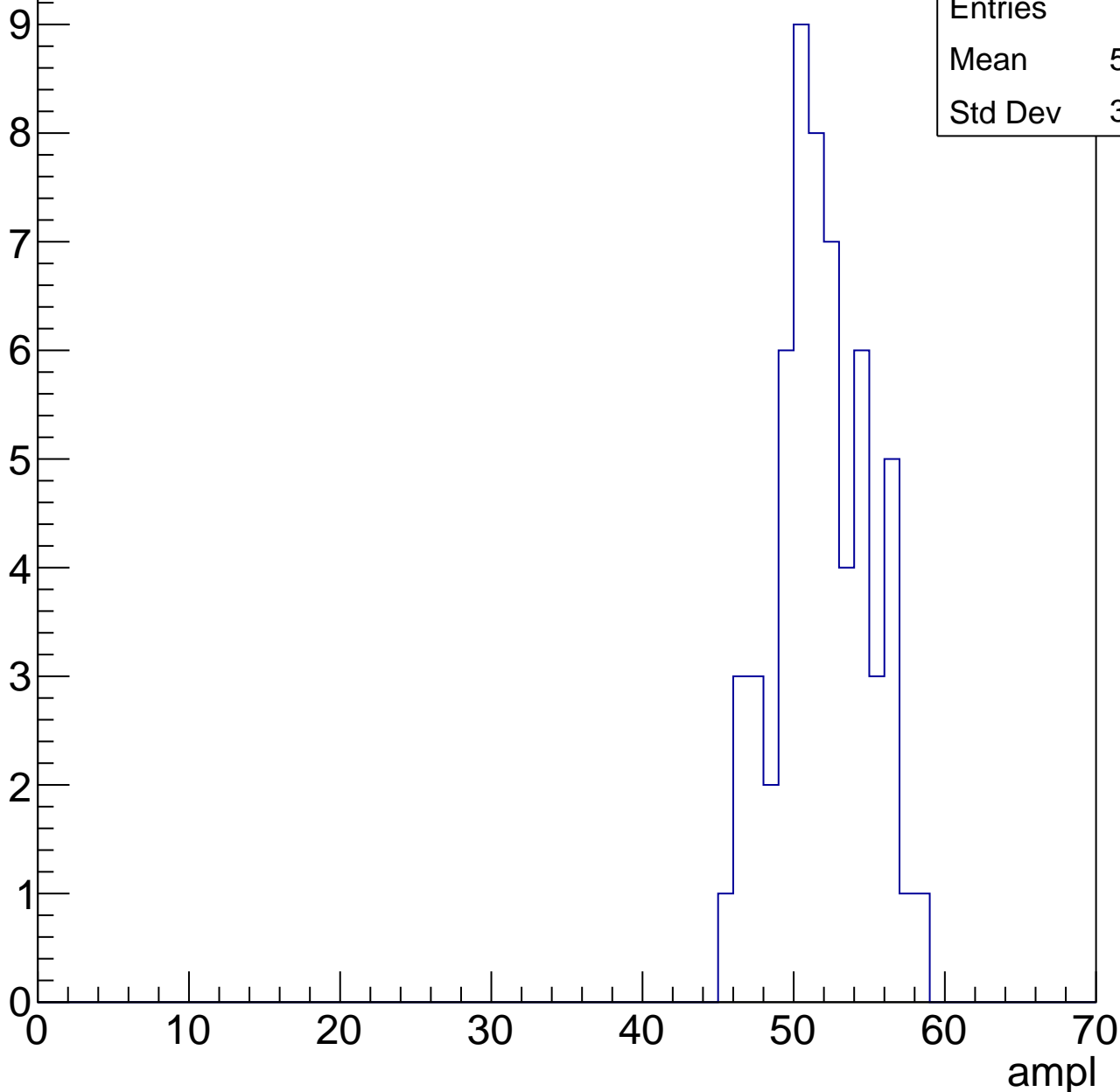


B1L100S, U9-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 51.39 |
| Std Dev | 3.014 |

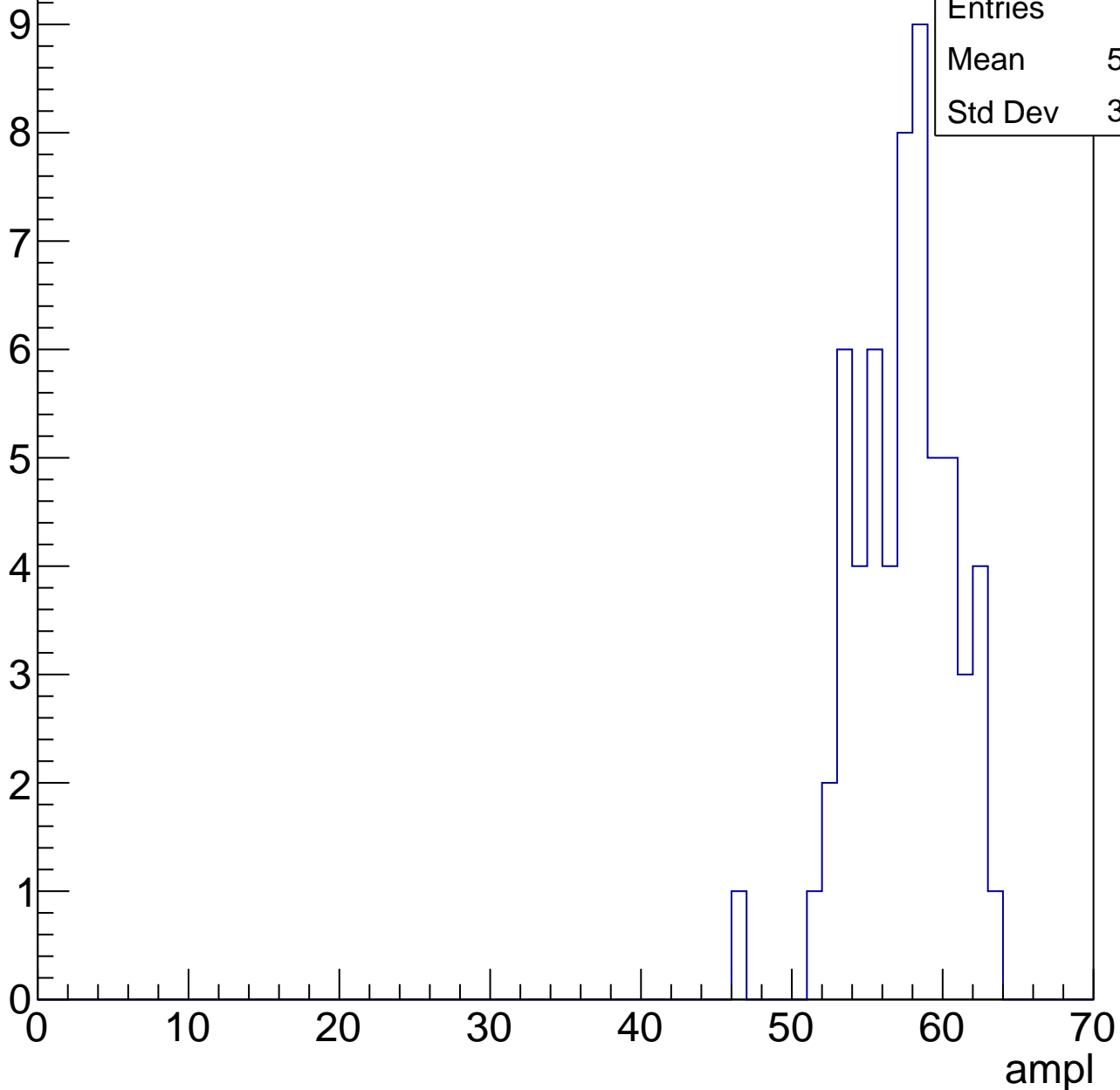


B1L100S, U9-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.88 |
| Std Dev | 3.253 |

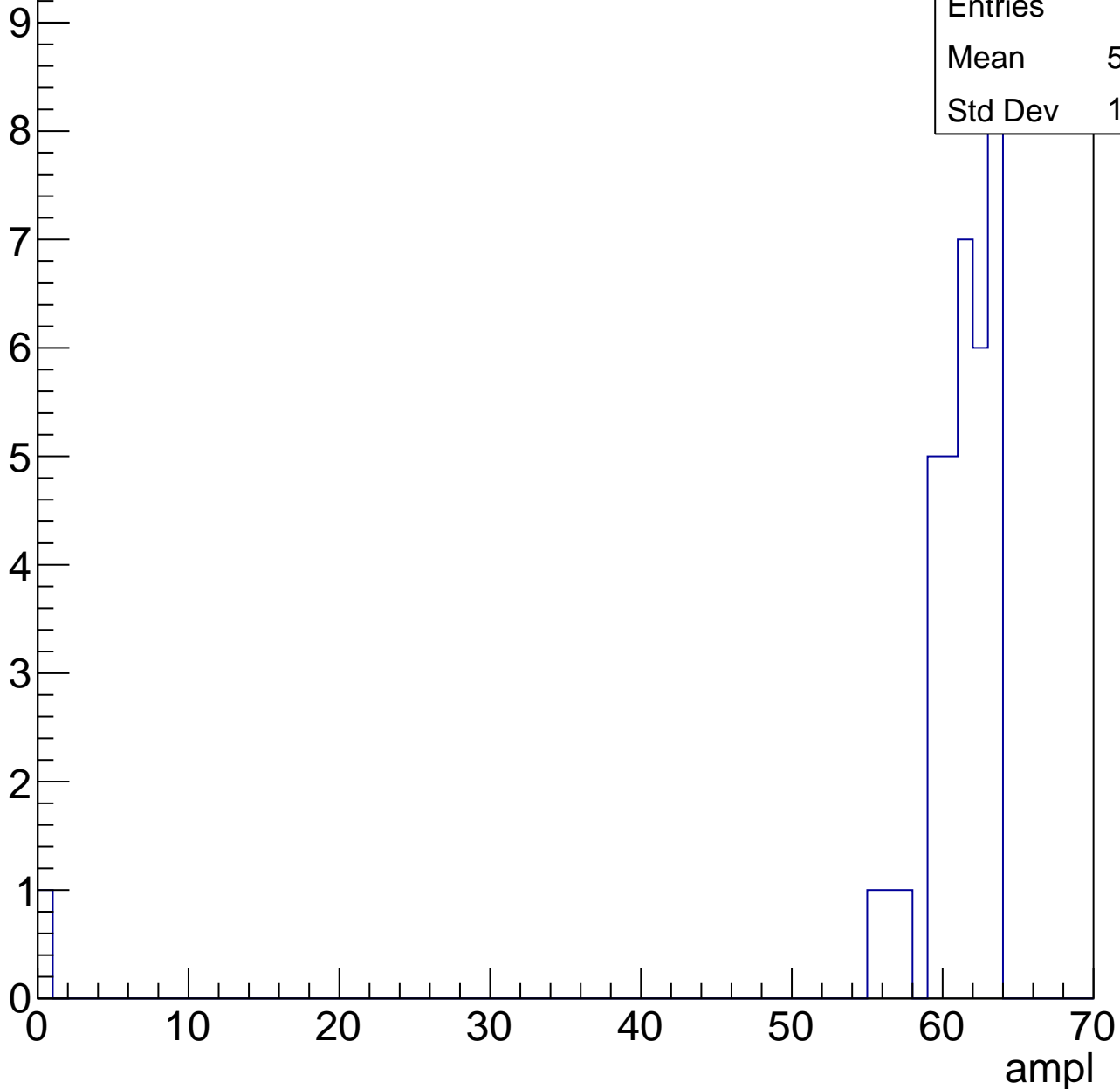


B1L100S, U9-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 36 |
| Mean | 59.14 |
| Std Dev | 10.19 |



B1L100S, U9-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

B1L100S, U9-ch20, adc0

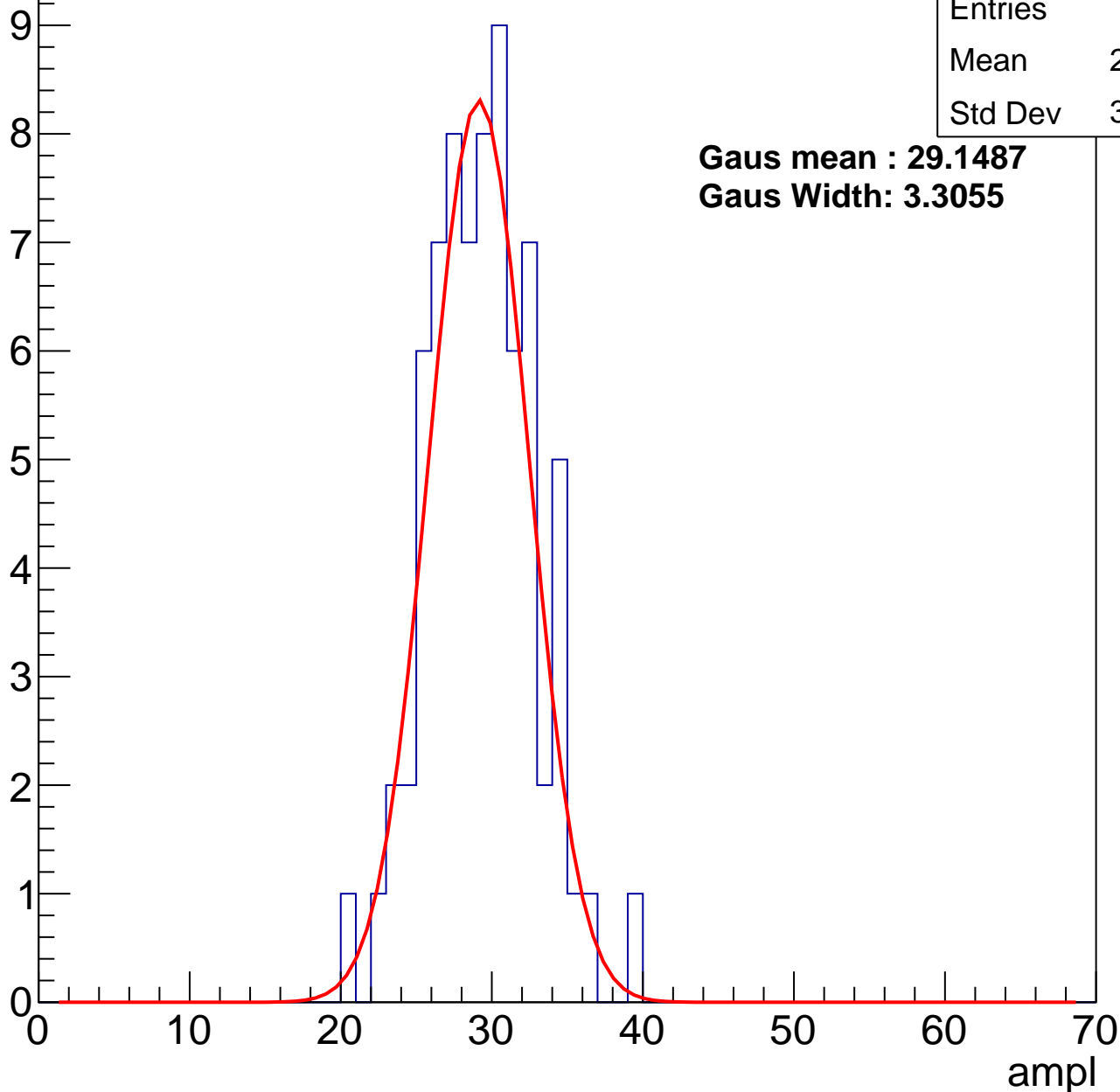
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.89 |
| Std Dev | 3.462 |

Gaus mean : 29.1487

Gaus Width: 3.3055



B1L100S, U9-ch20, adc1

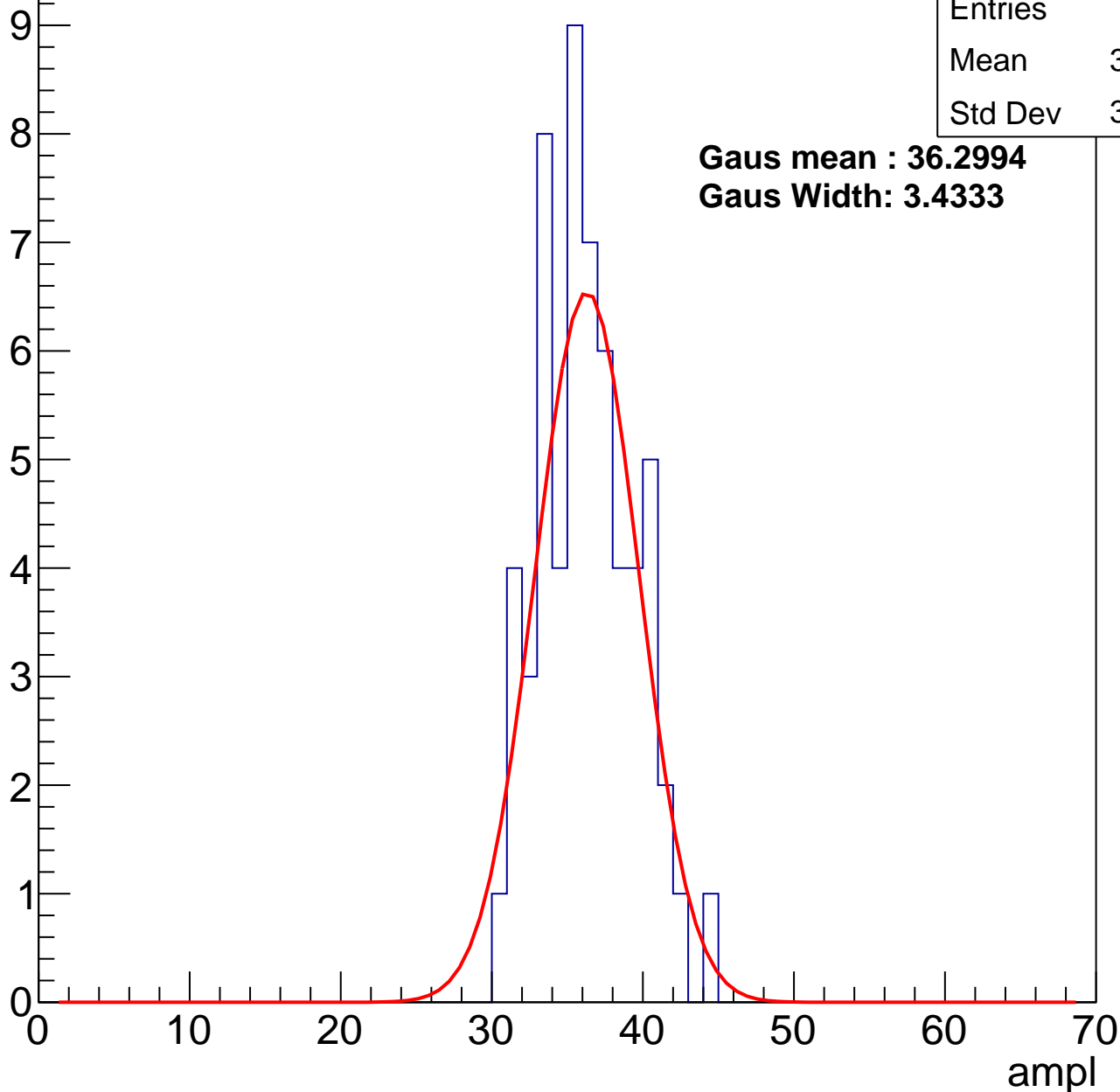
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.85 |
| Std Dev | 3.102 |

Gaus mean : 36.2994

Gaus Width: 3.4333



B1L100S, U9-ch20, adc2

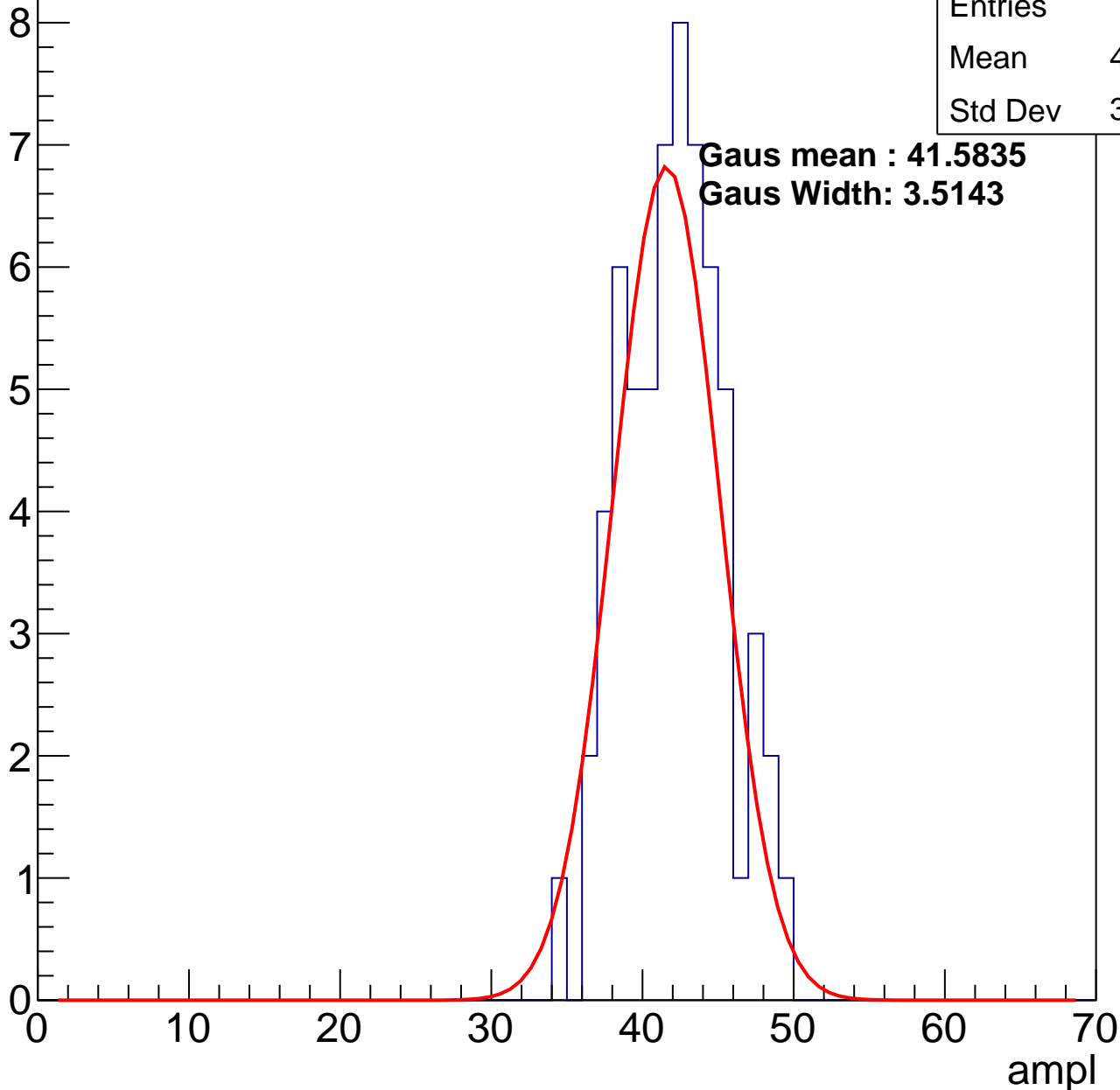
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.62 |
| Std Dev | 3.292 |

Gaus mean : 41.5835

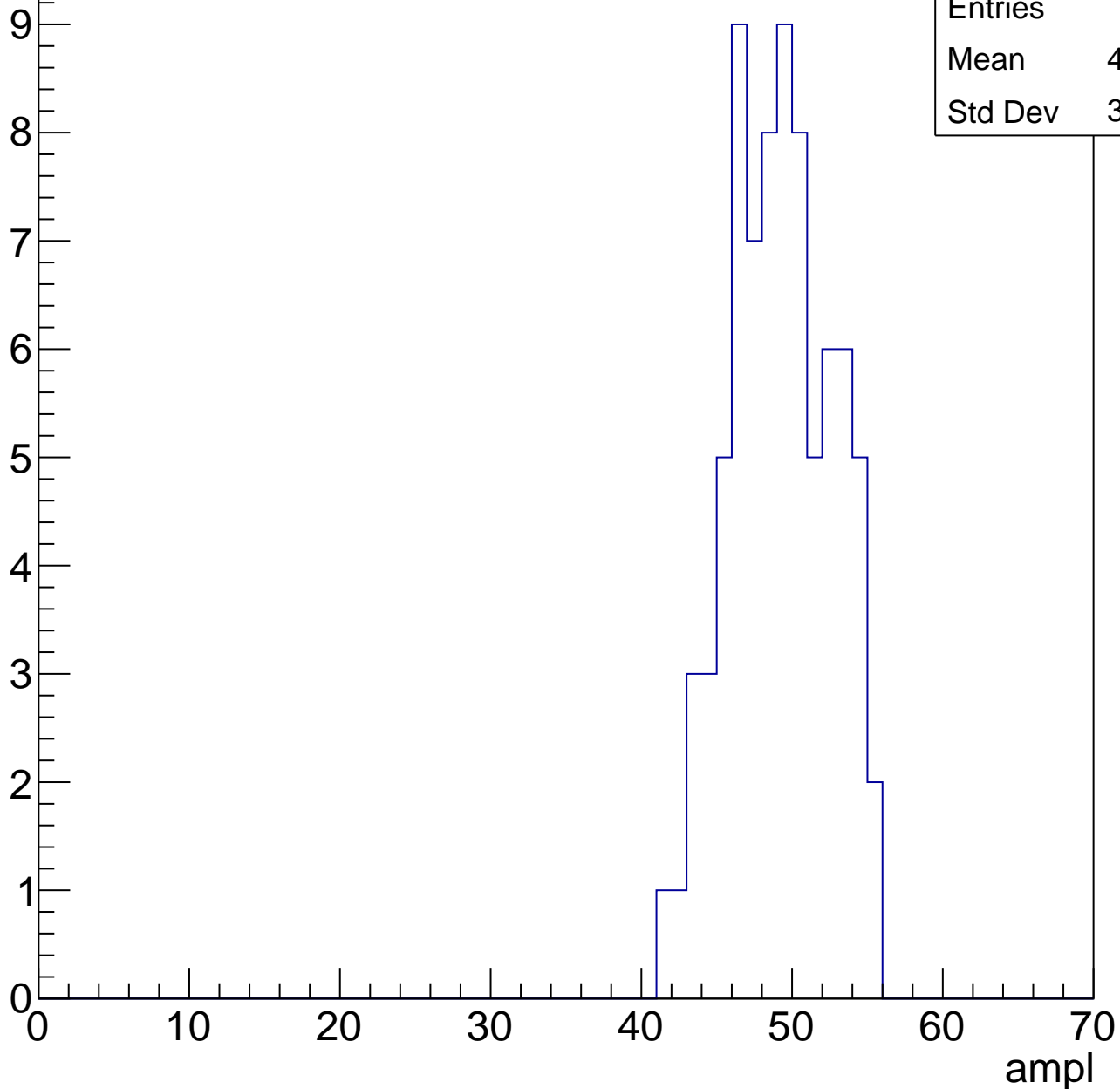
Gaus Width: 3.5143



B1L100S, U9-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



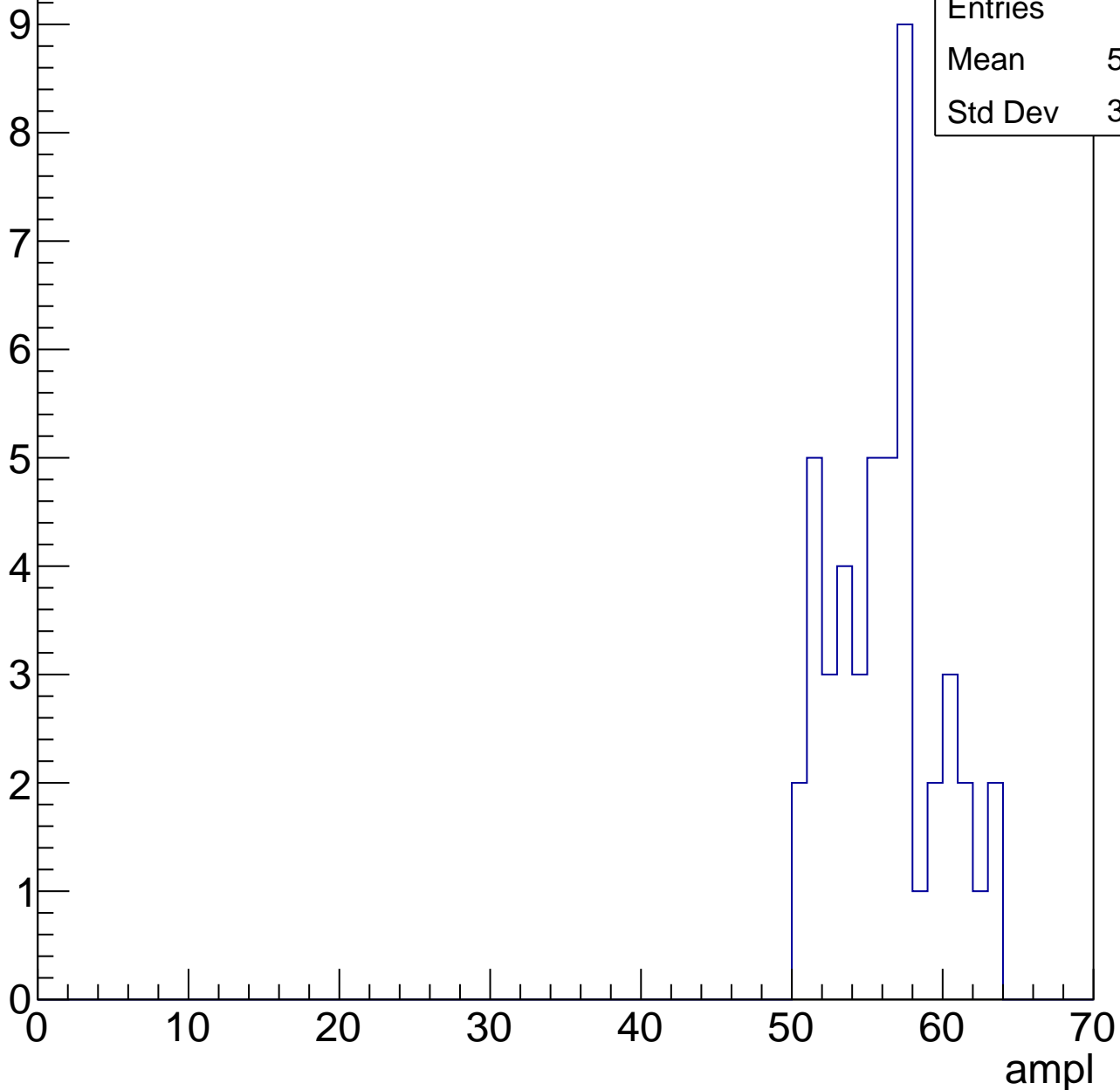
| | |
|---------|-------|
| Entries | 78 |
| Mean | 48.74 |
| Std Dev | 3.326 |

B1L100S, U9-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

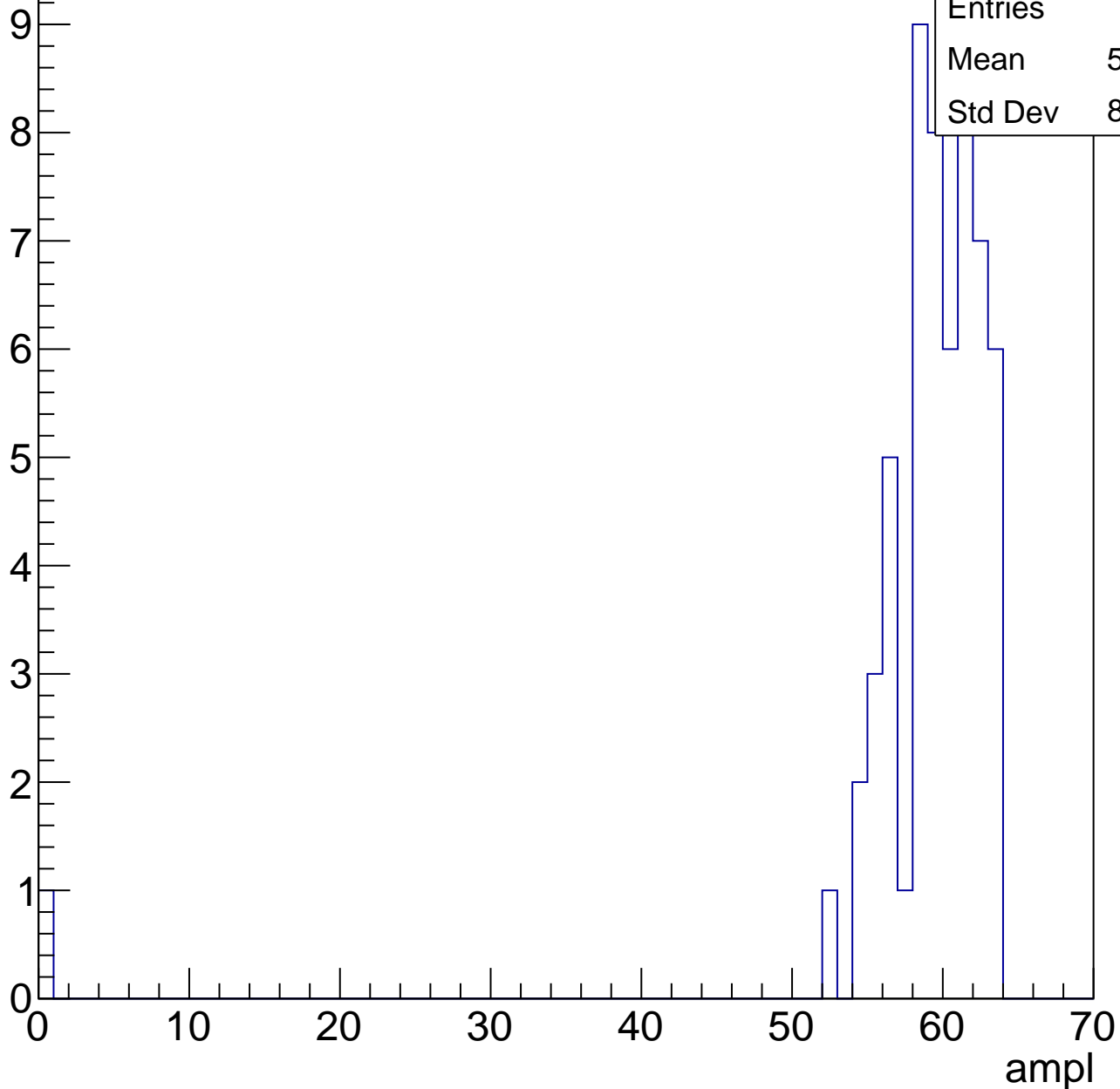
| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.72 |
| Std Dev | 3.444 |



B1L100S, U9-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch21, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 32 |
| Std Dev | 4.993 |

Gaus mean : 32.2979

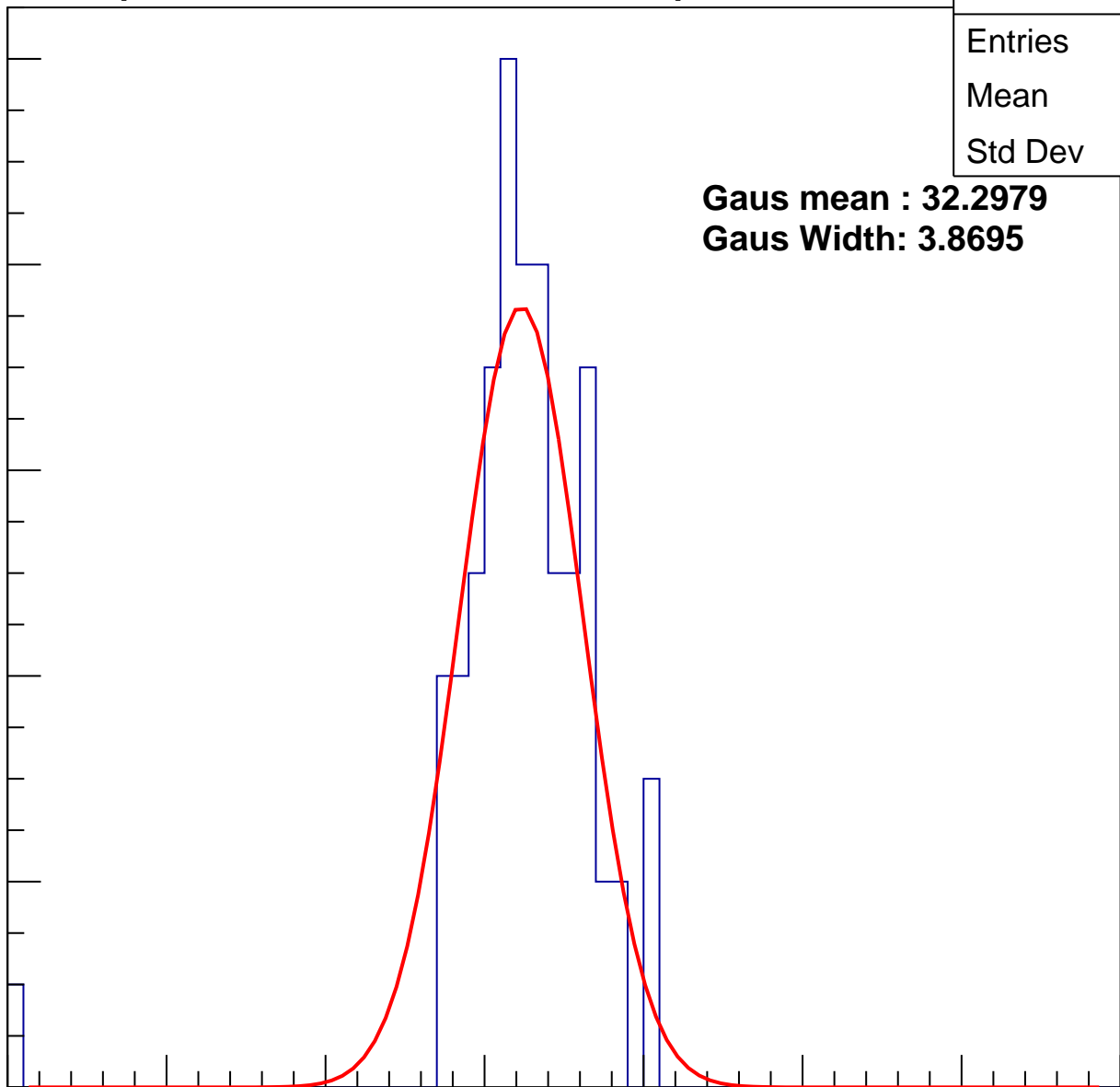
Gaus Width: 3.8695

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch21, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 39.35 |
| Std Dev | 3.284 |

Gaus mean : 39.8470

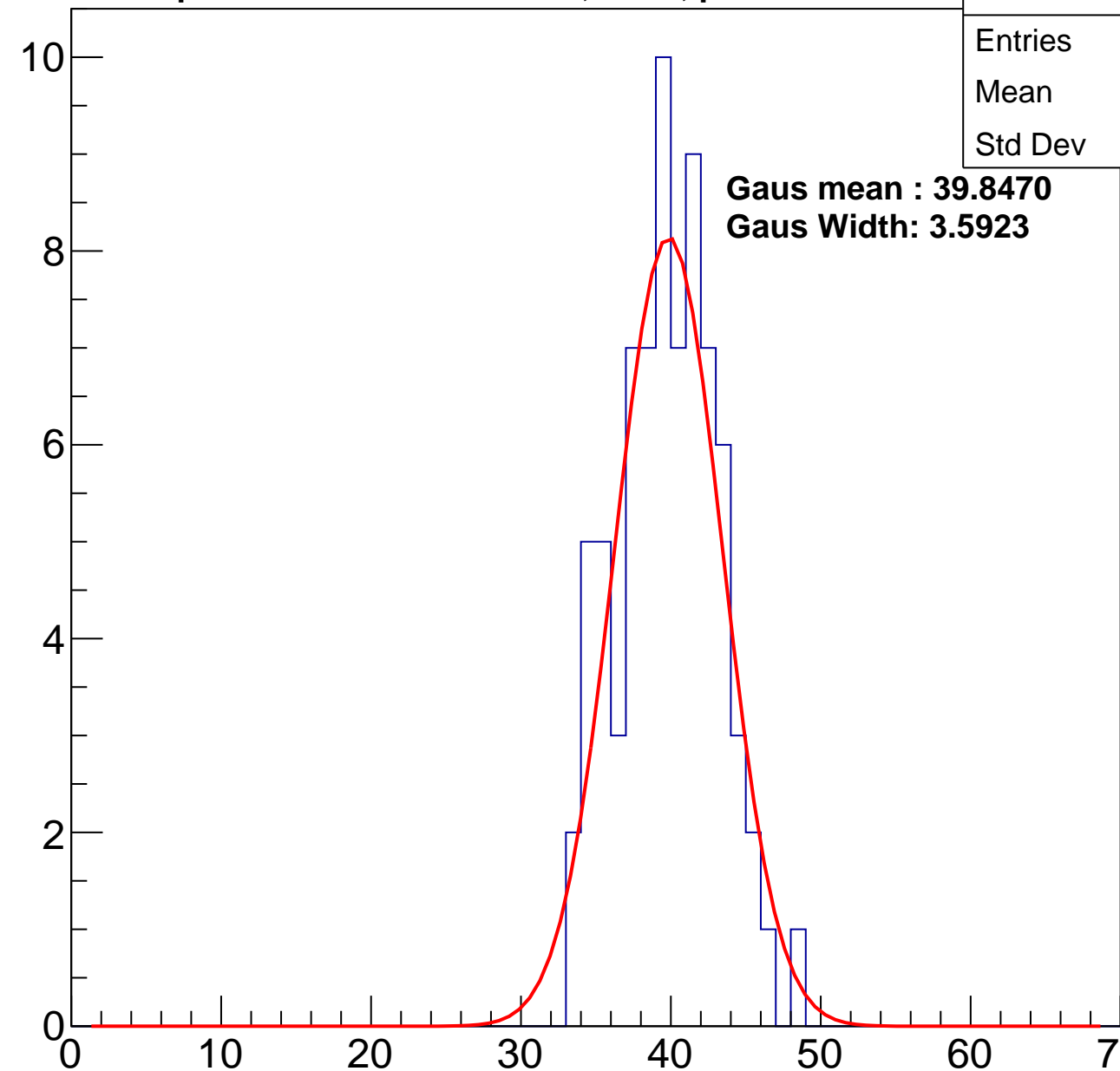
Gaus Width: 3.5923

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch21, adc2

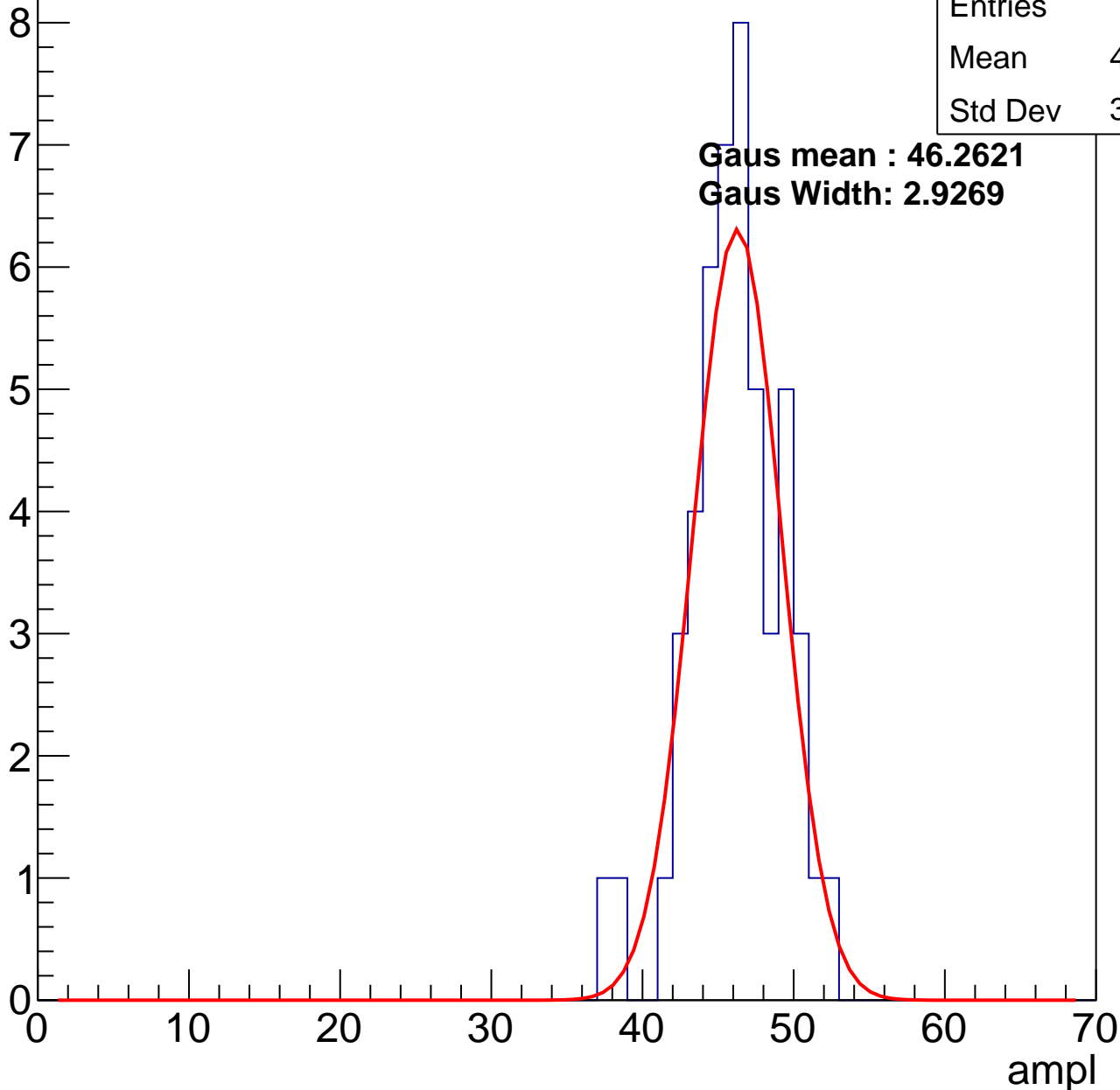
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 45.67 |
| Std Dev | 3.033 |

Gaus mean : 46.2621

Gaus Width: 2.9269

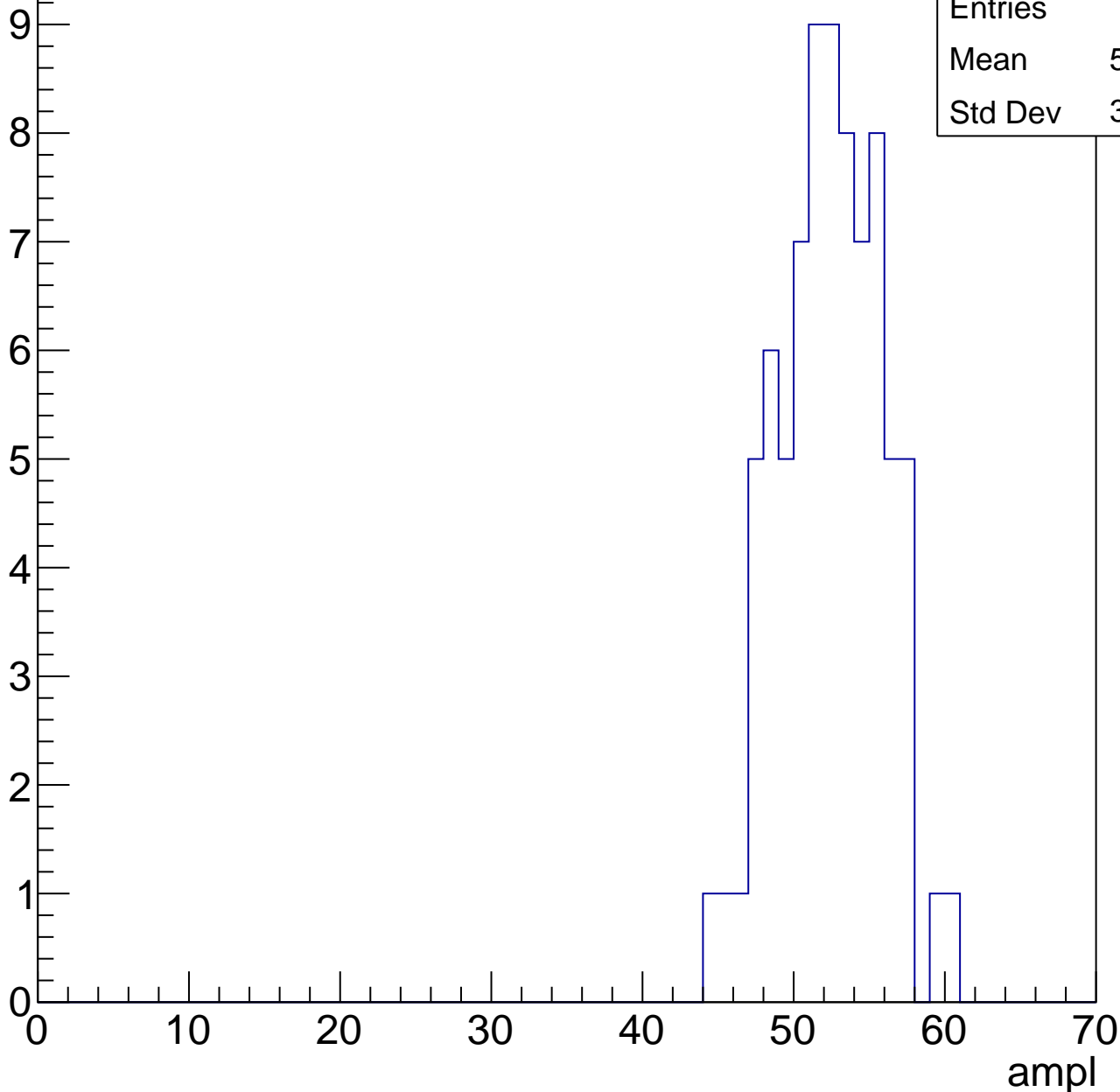


B1L100S, U9-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 51.97 |
| Std Dev | 3.334 |

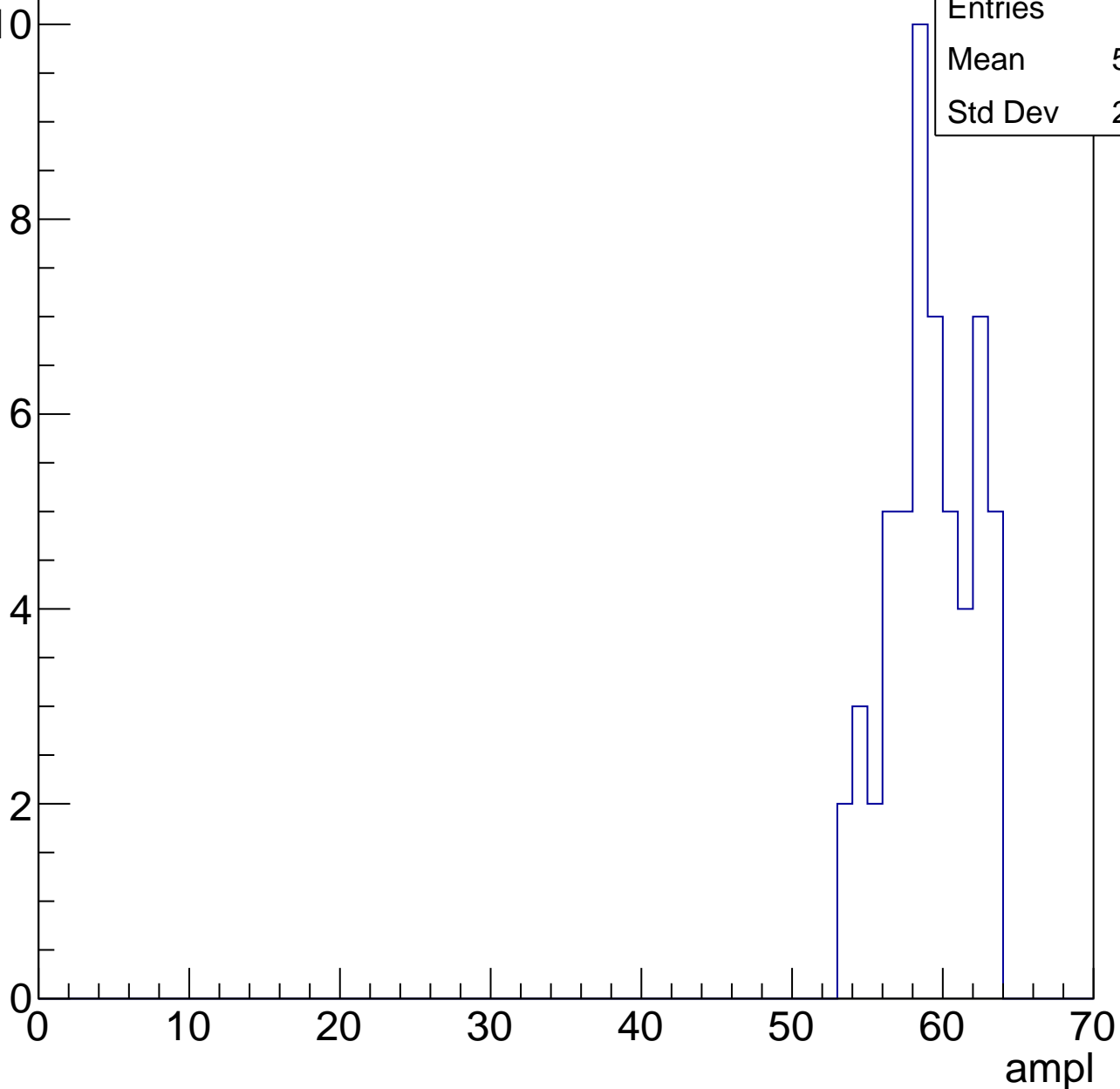


B1L100S, U9-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.71 |
| Std Dev | 2.741 |

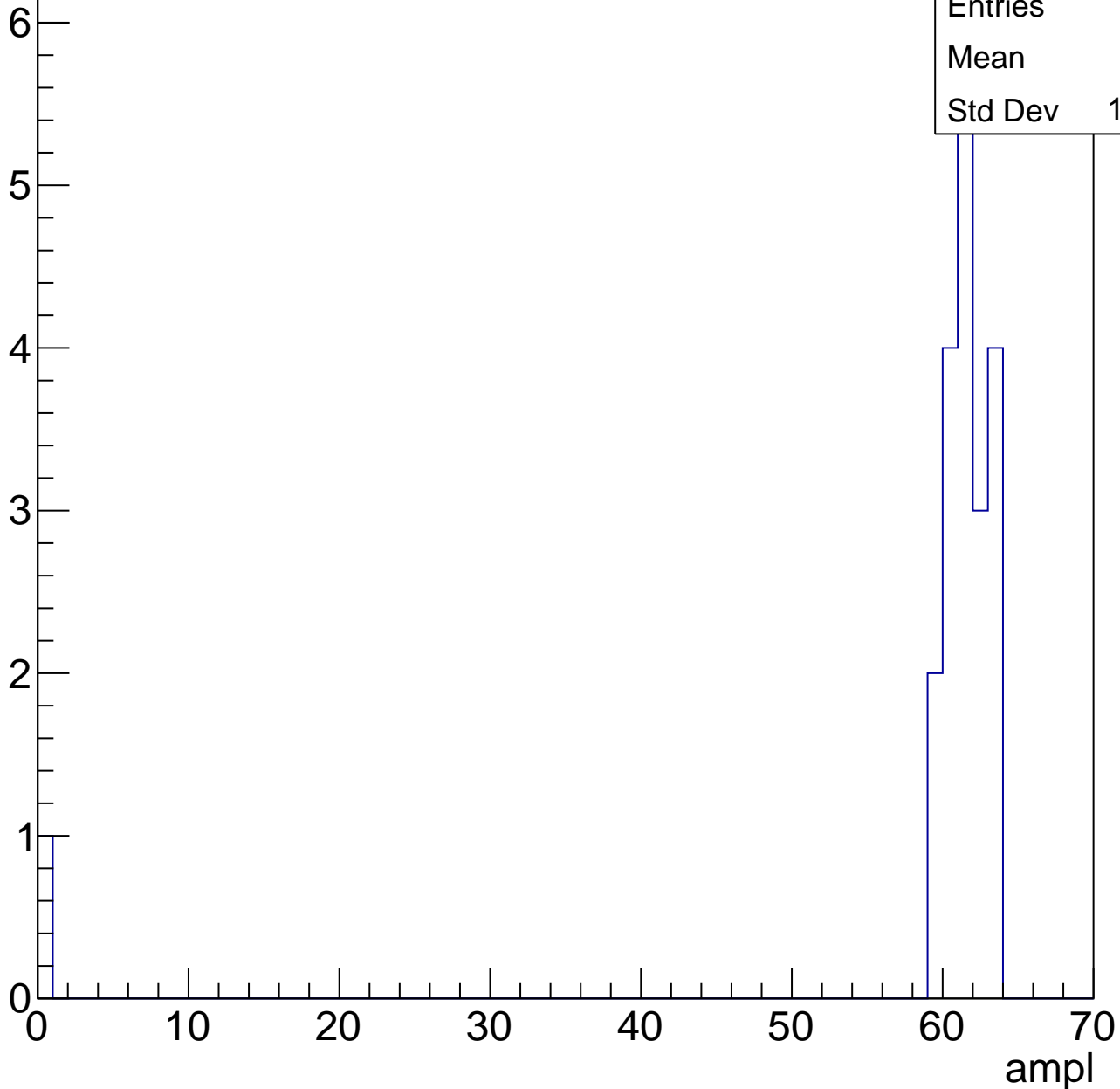


B1L100S, U9-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

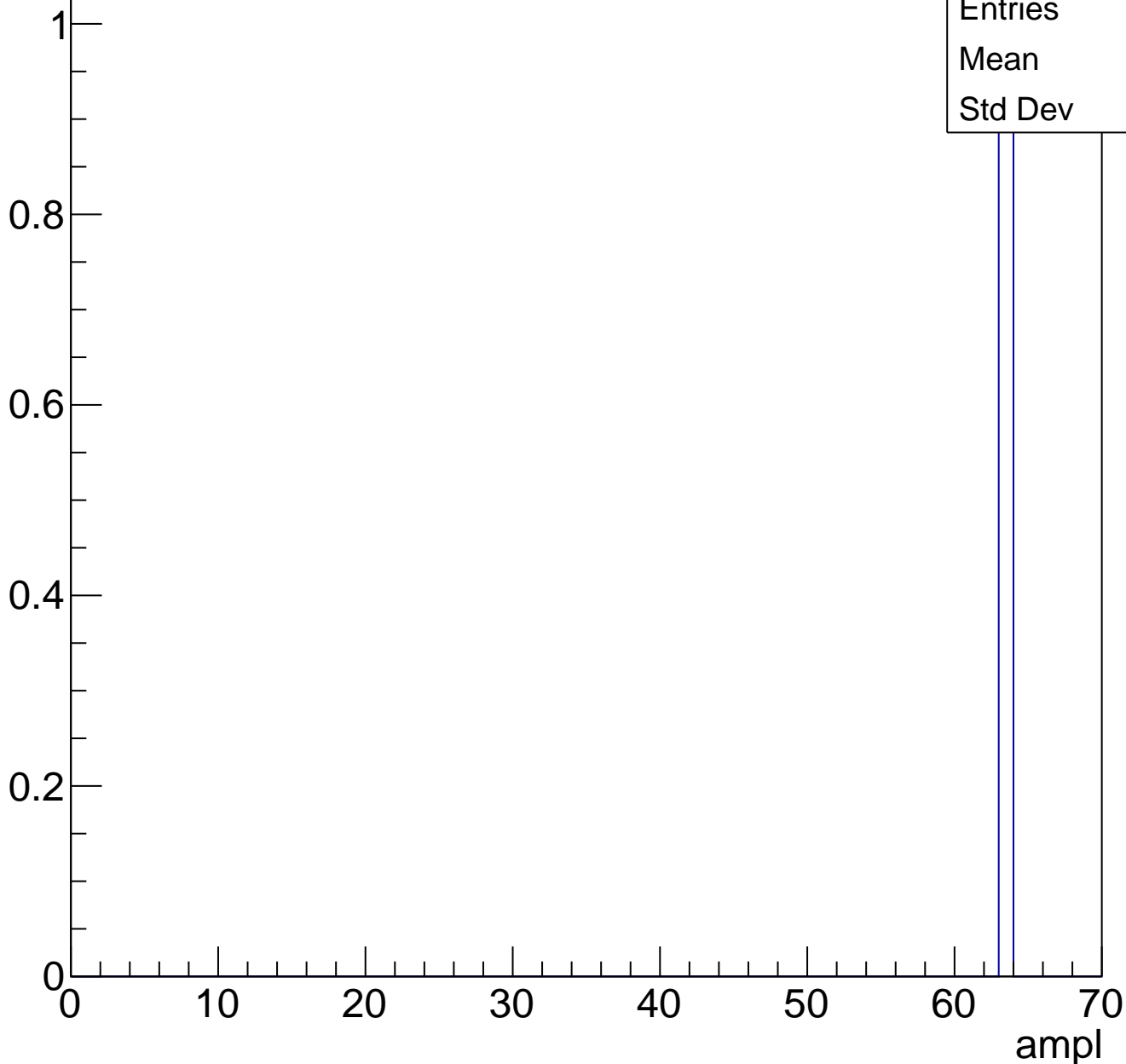
| | |
|---------|-------|
| Entries | 20 |
| Mean | 58.1 |
| Std Dev | 13.39 |



B1L100S, U9-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

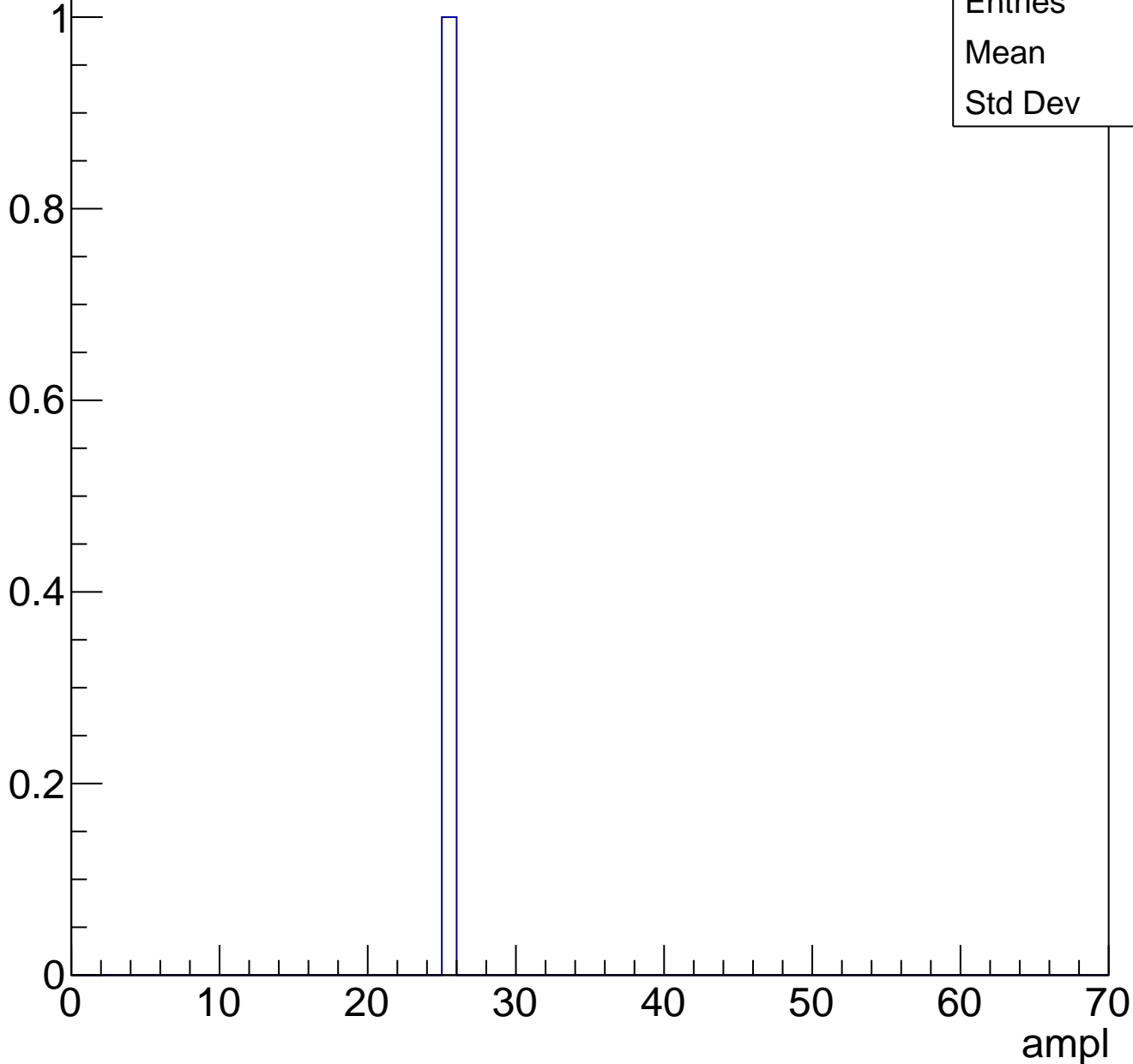
Entry



B1L100S, U9-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch22, adc0

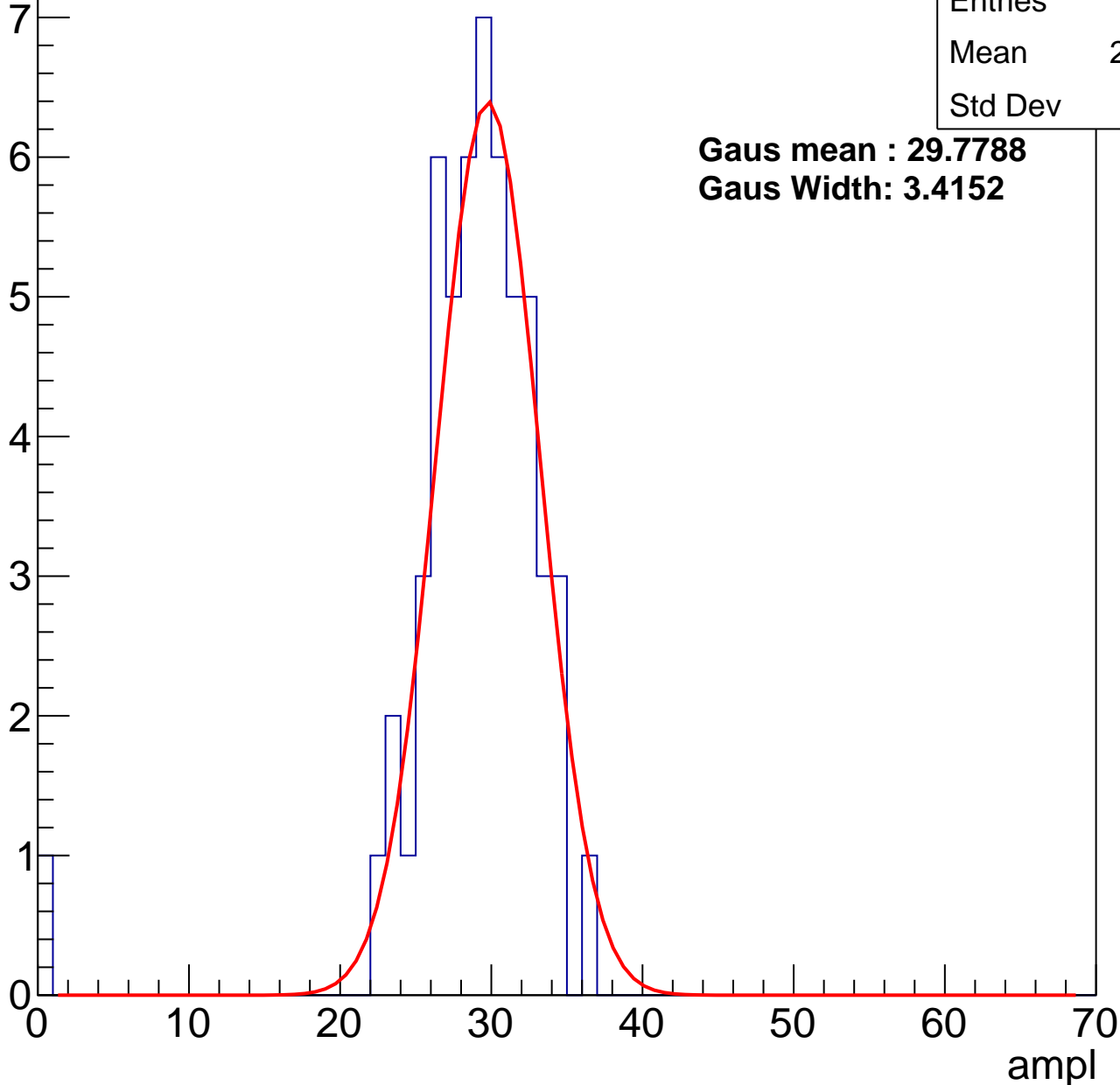
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 28.38 |
| Std Dev | 4.93 |

Gaus mean : 29.7788

Gaus Width: 3.4152



B1L100S, U9-ch22, adc1

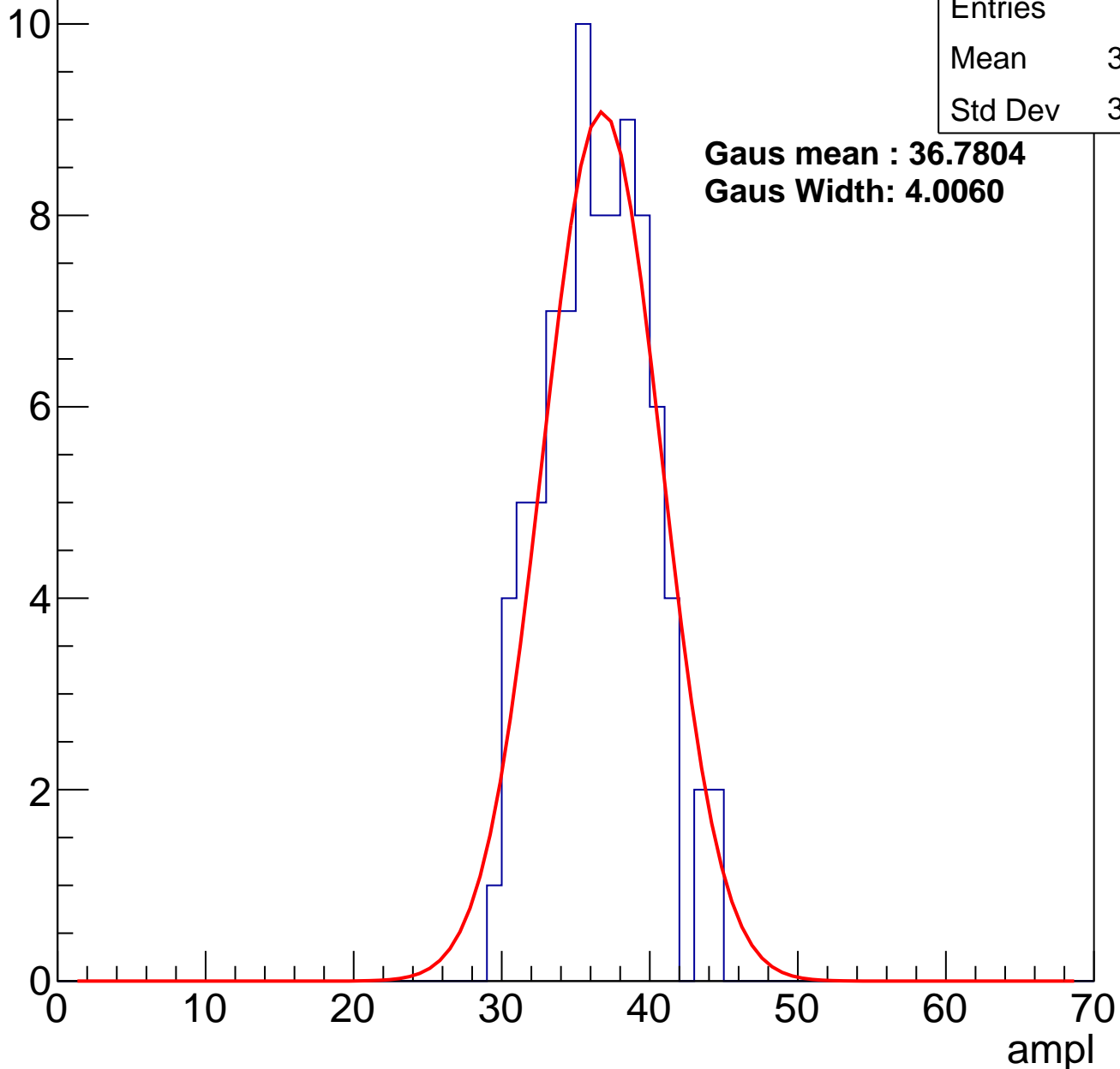
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 86 |
| Mean | 36.03 |
| Std Dev | 3.472 |

Gaus mean : 36.7804

Gaus Width: 4.0060

Entry



B1L100S, U9-ch22, adc2

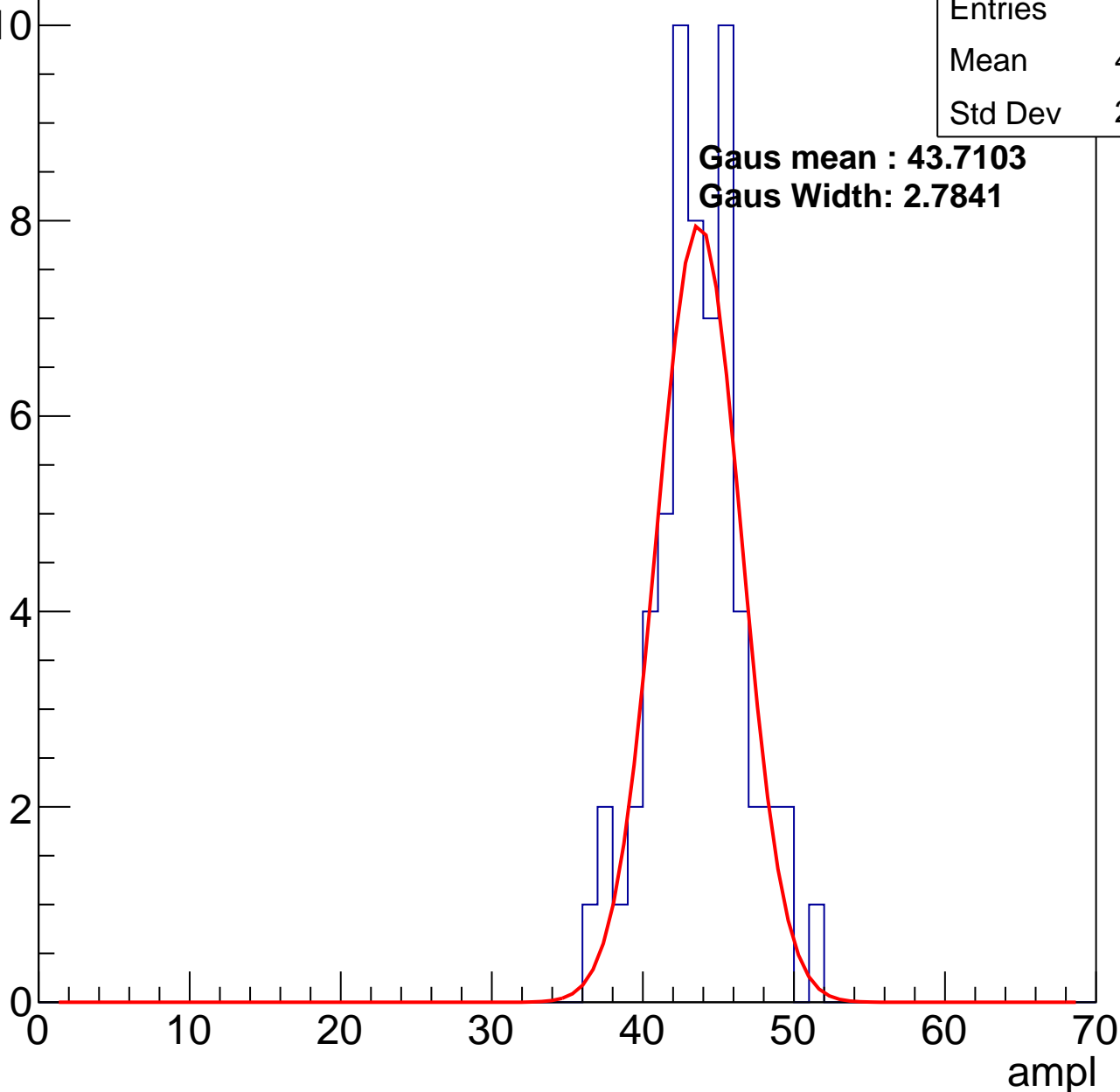
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 43.21 |
| Std Dev | 2.981 |

Gaus mean : 43.7103

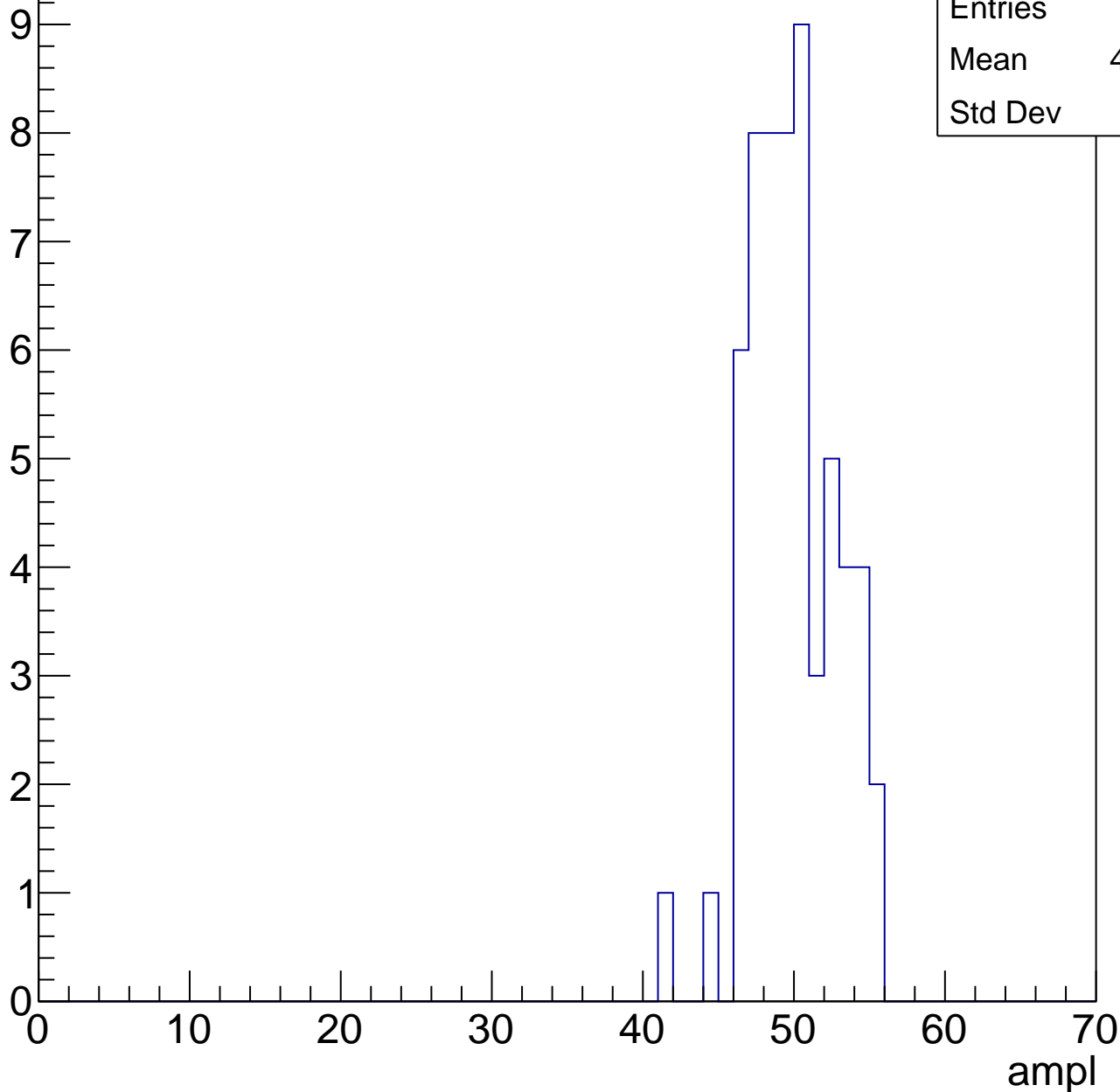
Gaus Width: 2.7841



B1L100S, U9-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

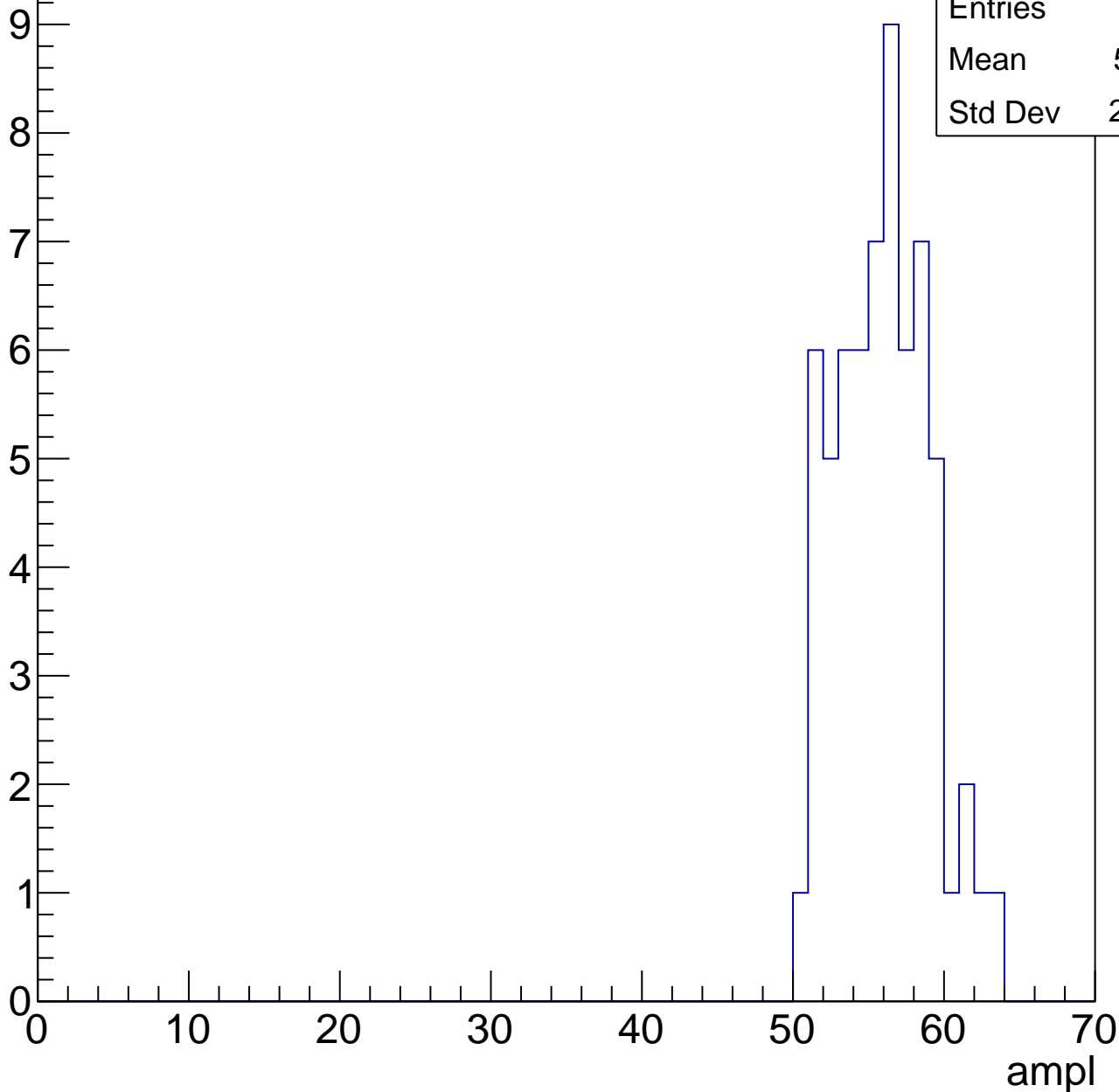


B1L100S, U9-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 55.51 |
| Std Dev | 2.997 |



B1L100S, U9-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

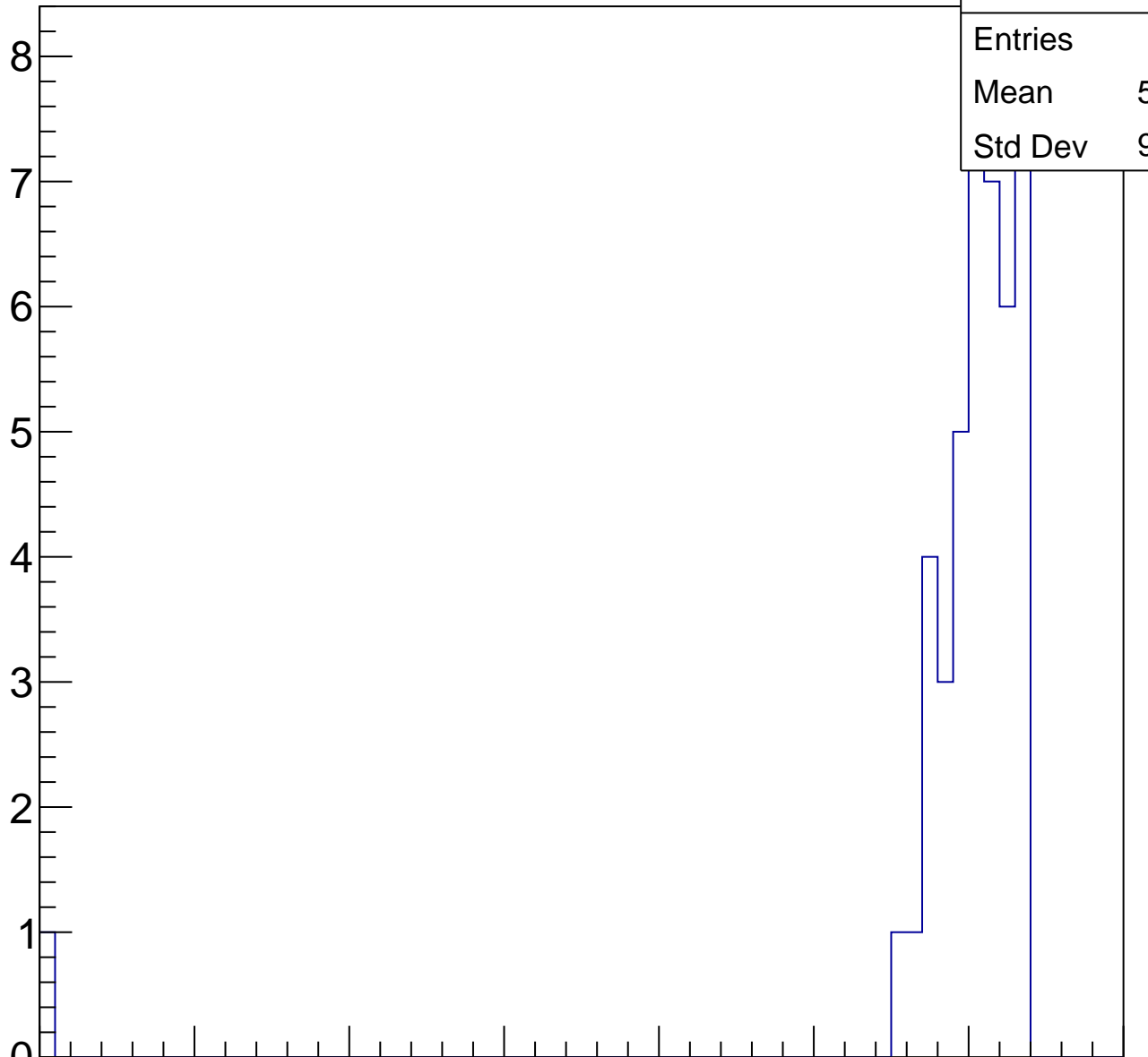
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.89 |
| Std Dev | 9.223 |

ampl

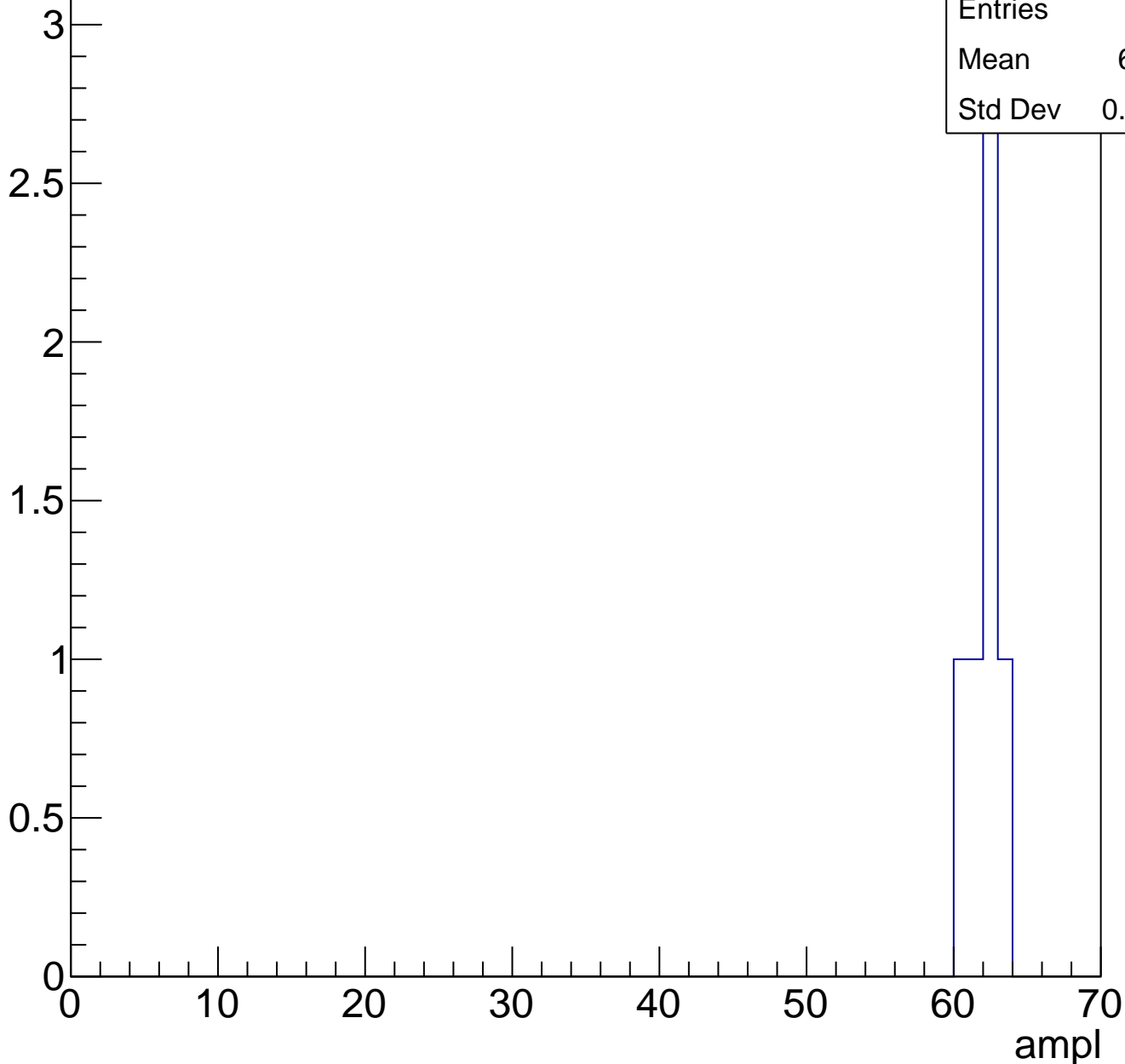
0 10 20 30 40 50 60 70



B1L100S, U9-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch23, adc0

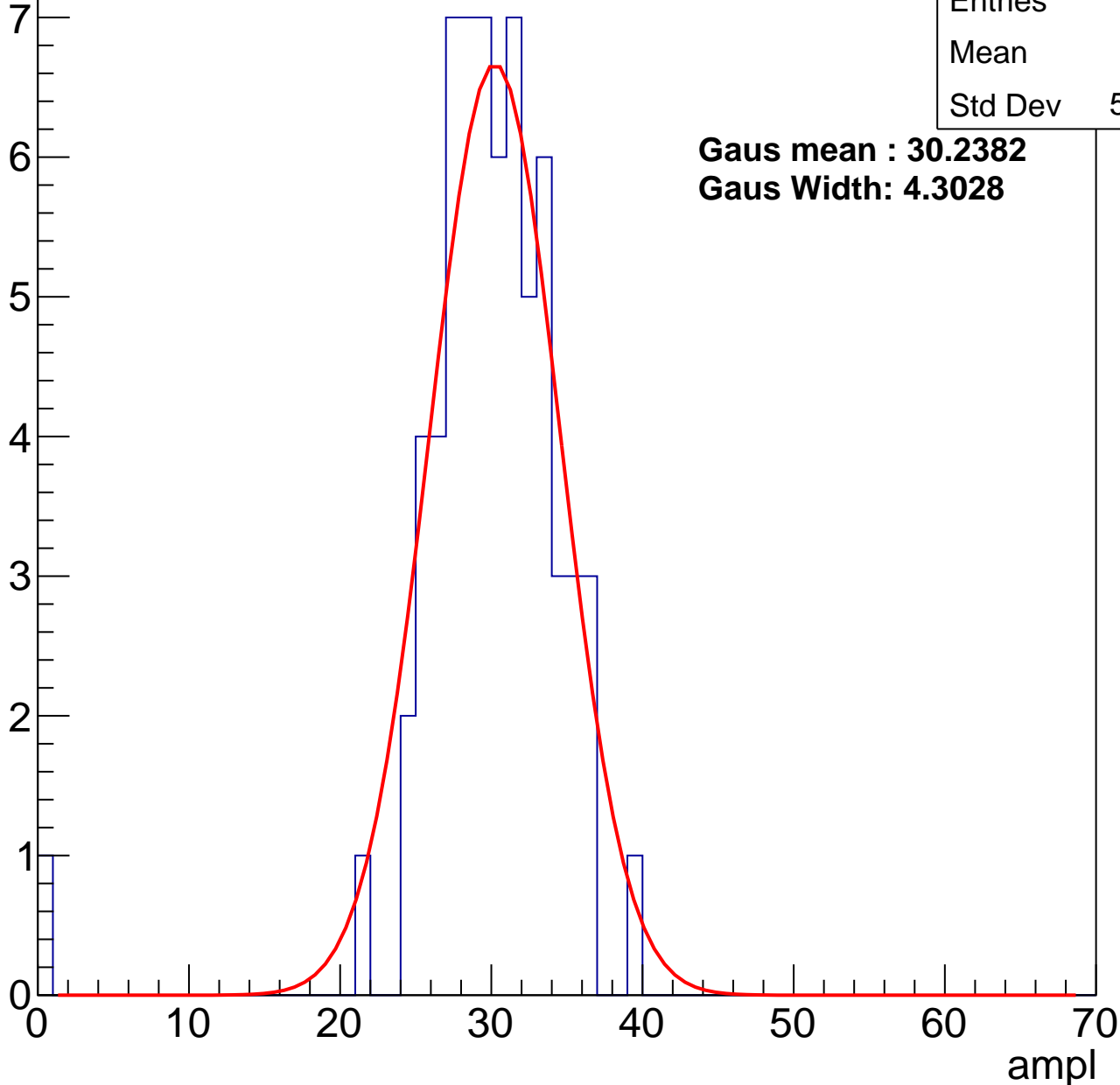
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 29.4 |
| Std Dev | 5.014 |

Gaus mean : 30.2382

Gaus Width: 4.3028



B1L100S, U9-ch23, adc1

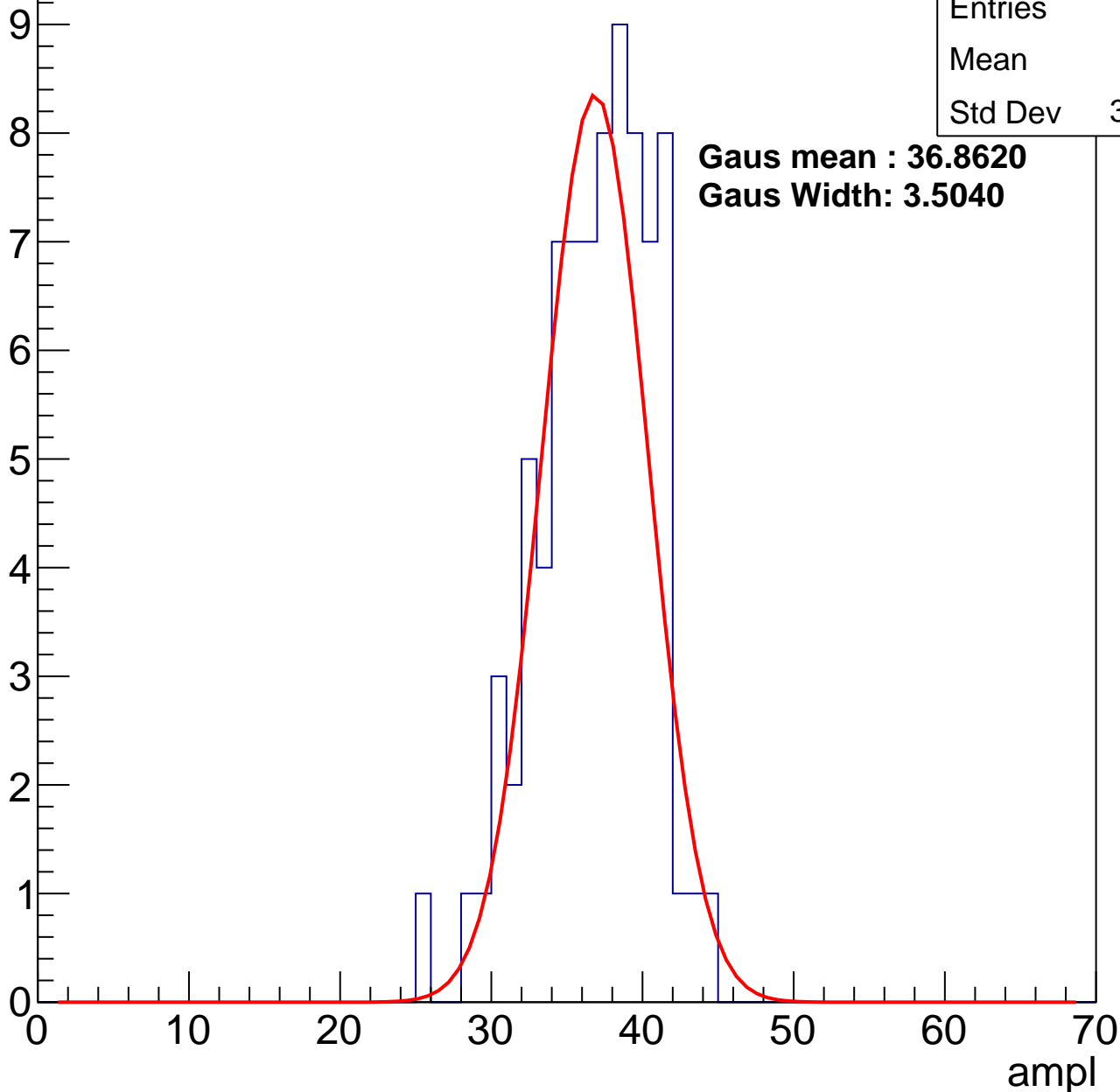
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 36.4 |
| Std Dev | 3.697 |

Gaus mean : 36.8620

Gaus Width: 3.5040



B1L100S, U9-ch23, adc2

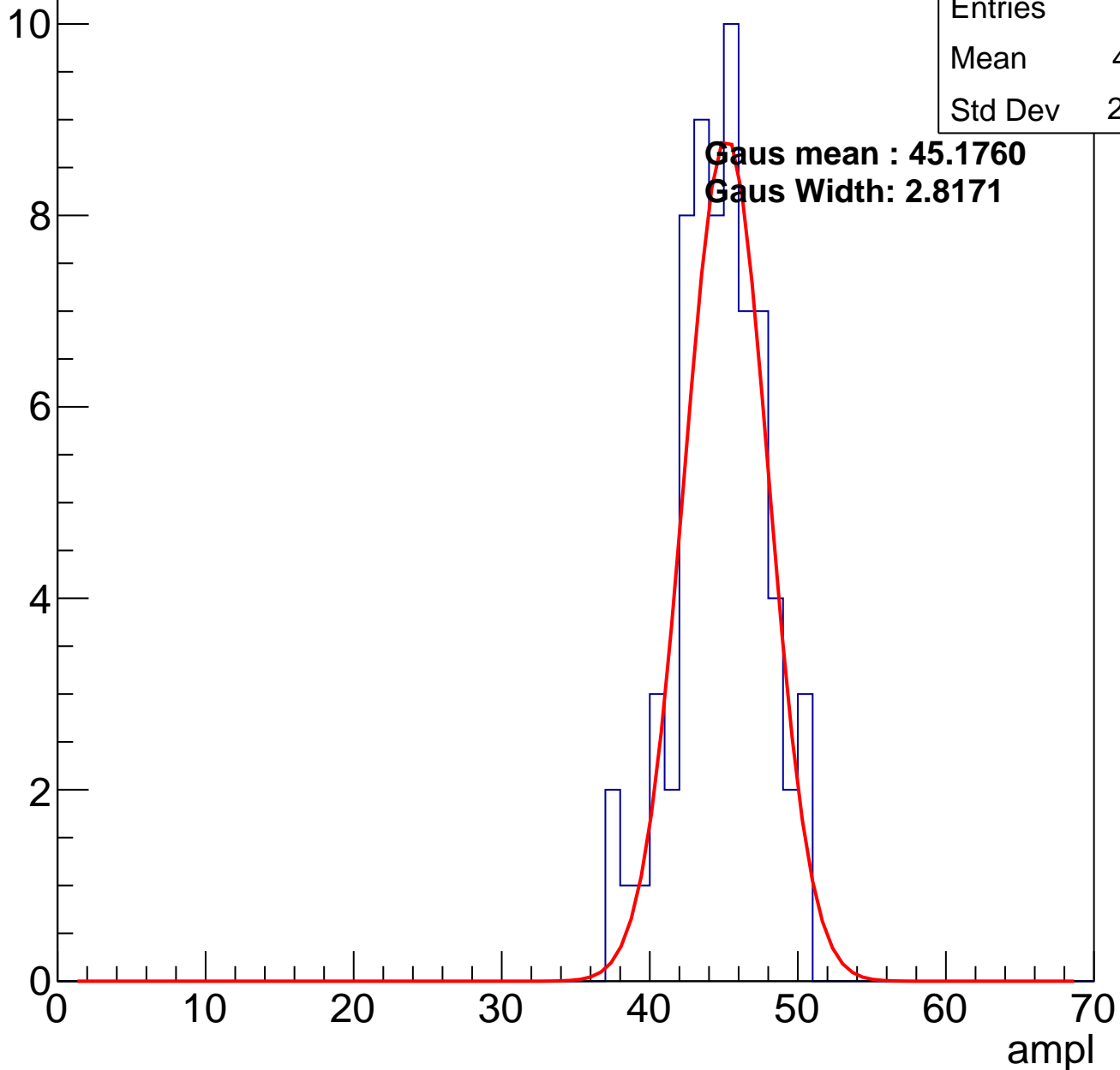
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.31 |
| Std Dev | 2.948 |

Gaus mean : 45.1760

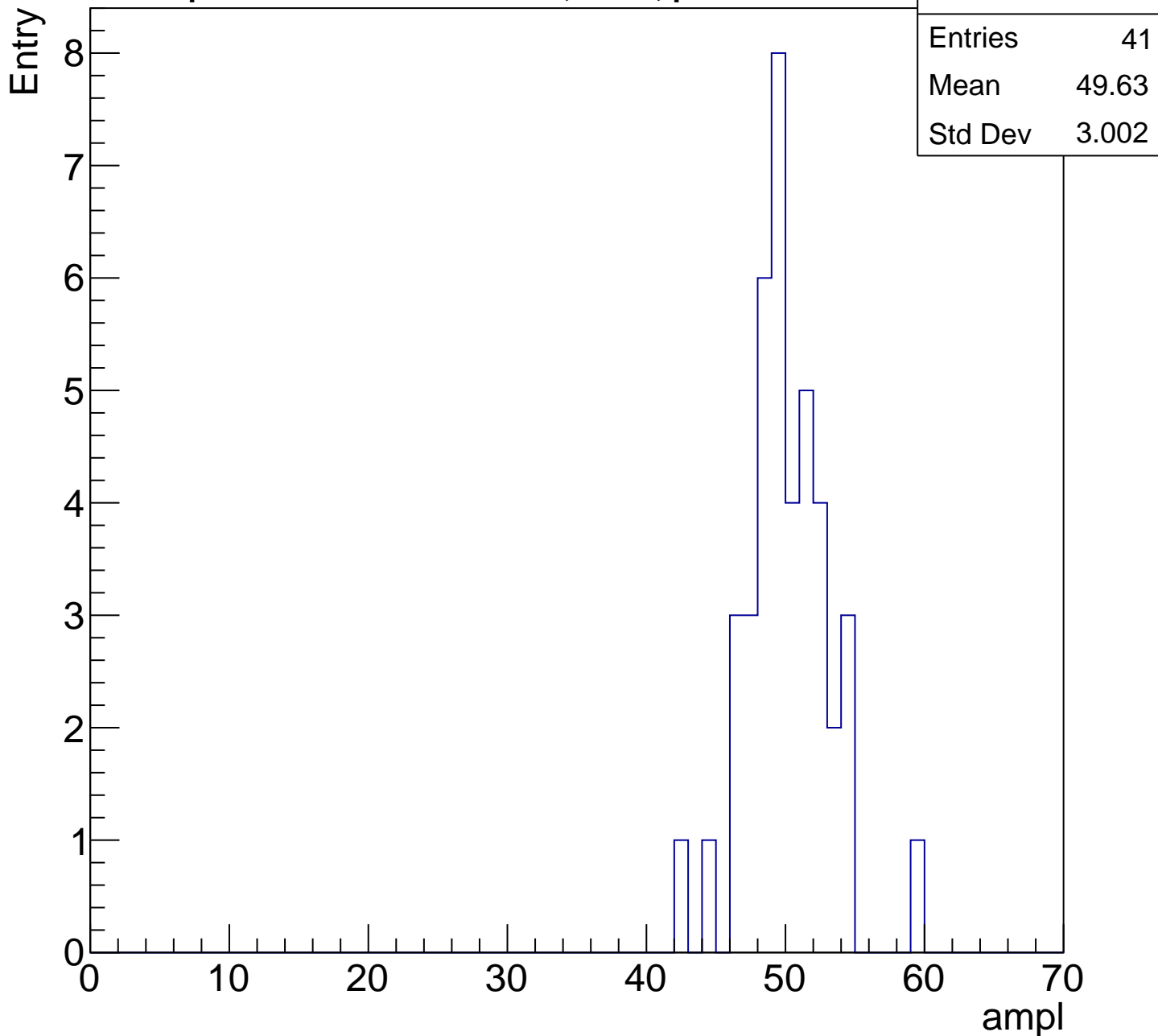
Gaus Width: 2.8171

Entry



B1L100S, U9-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

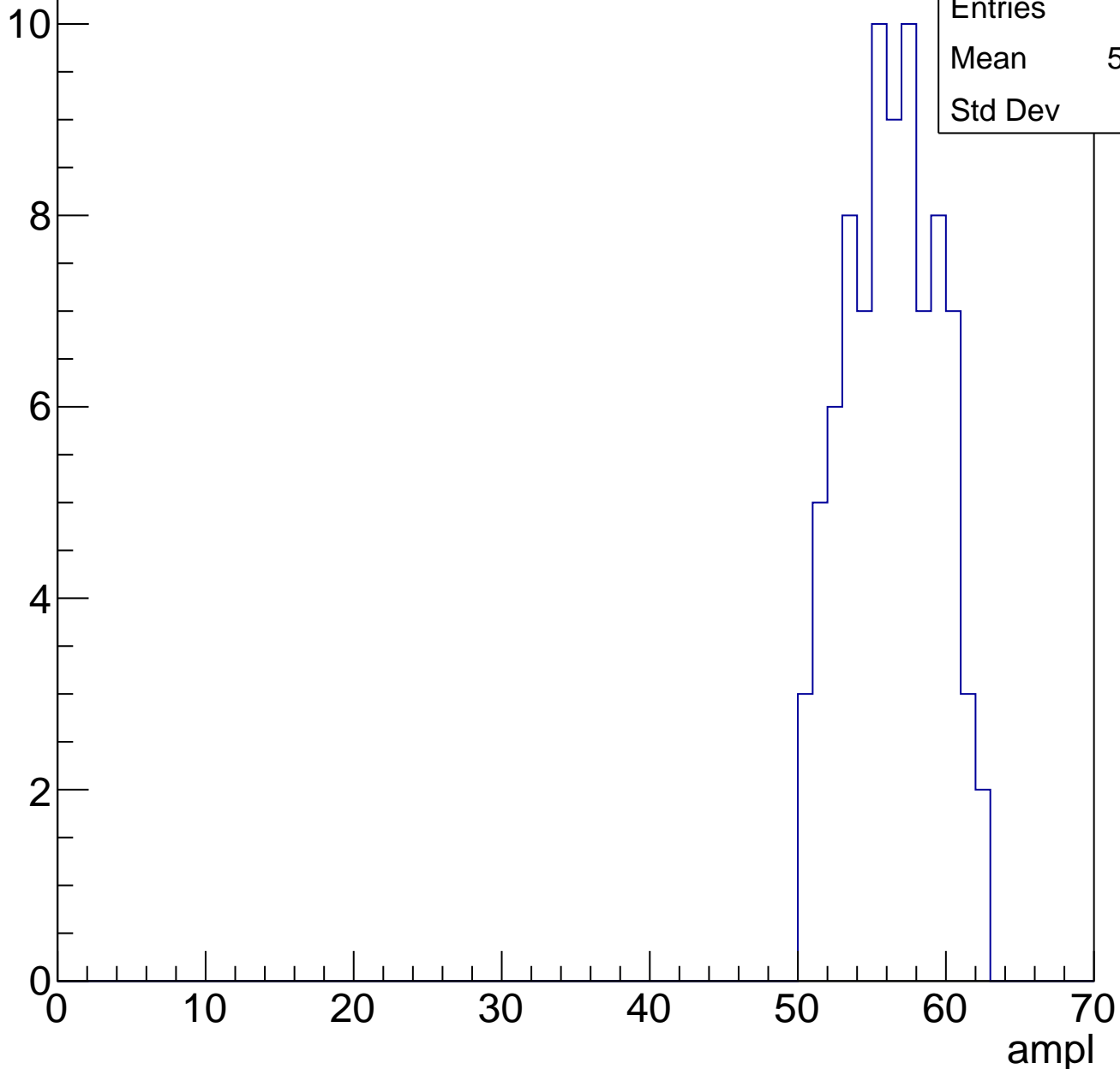


B1L100S, U9-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 85 |
| Mean | 55.86 |
| Std Dev | 3.08 |

Entry

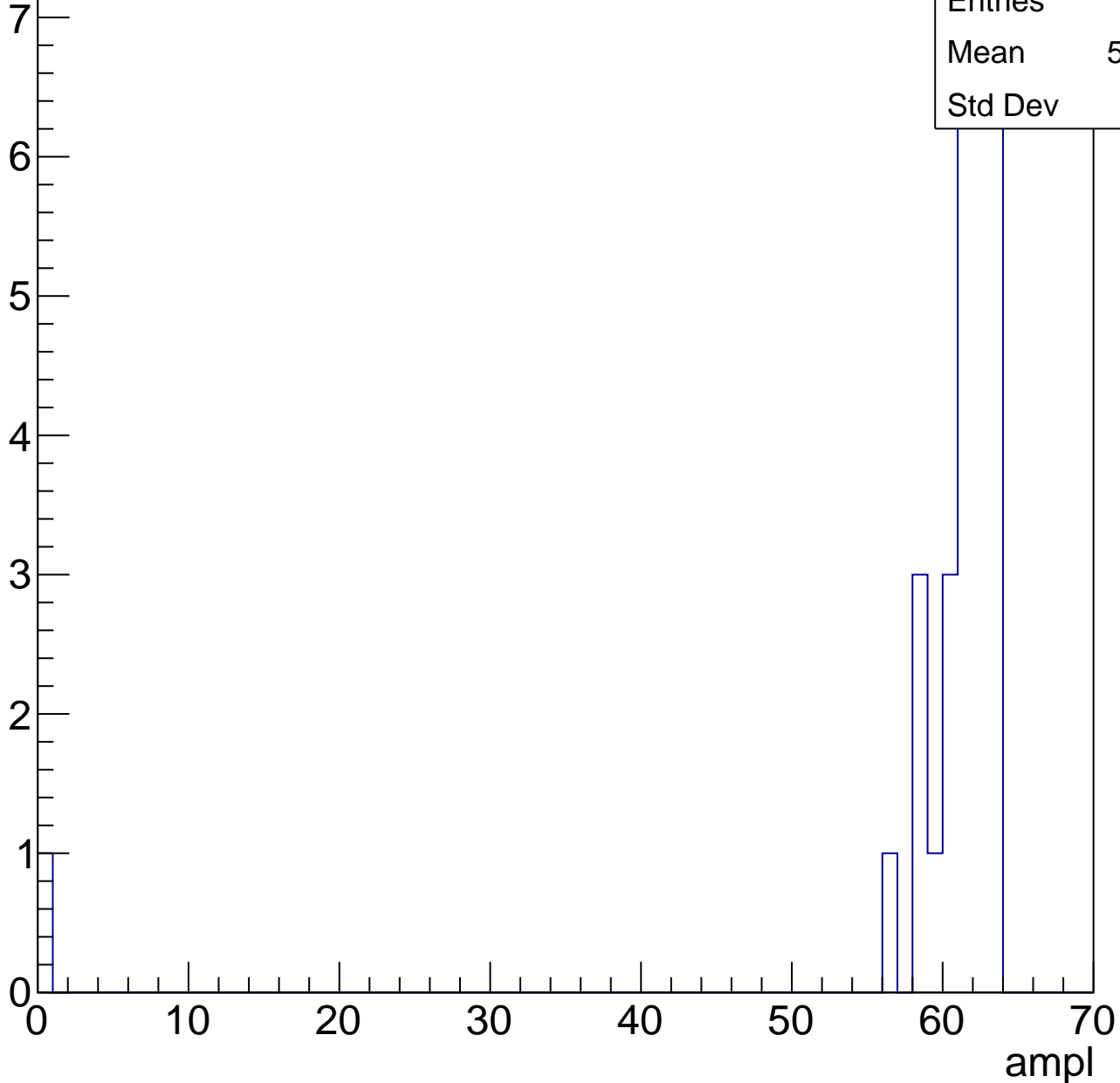


B1L100S, U9-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

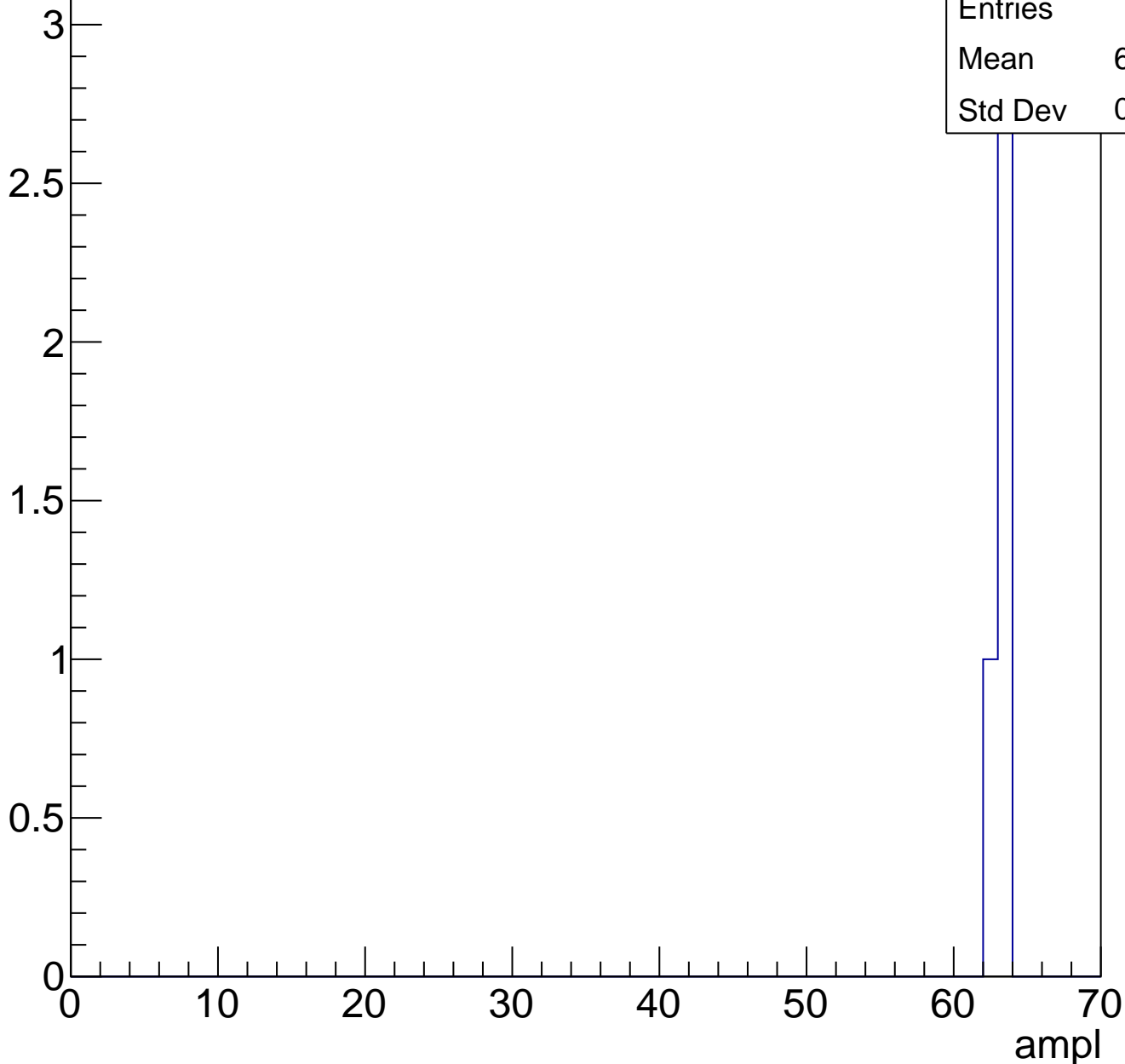
| | |
|---------|-------|
| Entries | 30 |
| Mean | 59.03 |
| Std Dev | 11.1 |



B1L100S, U9-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch24, adc0

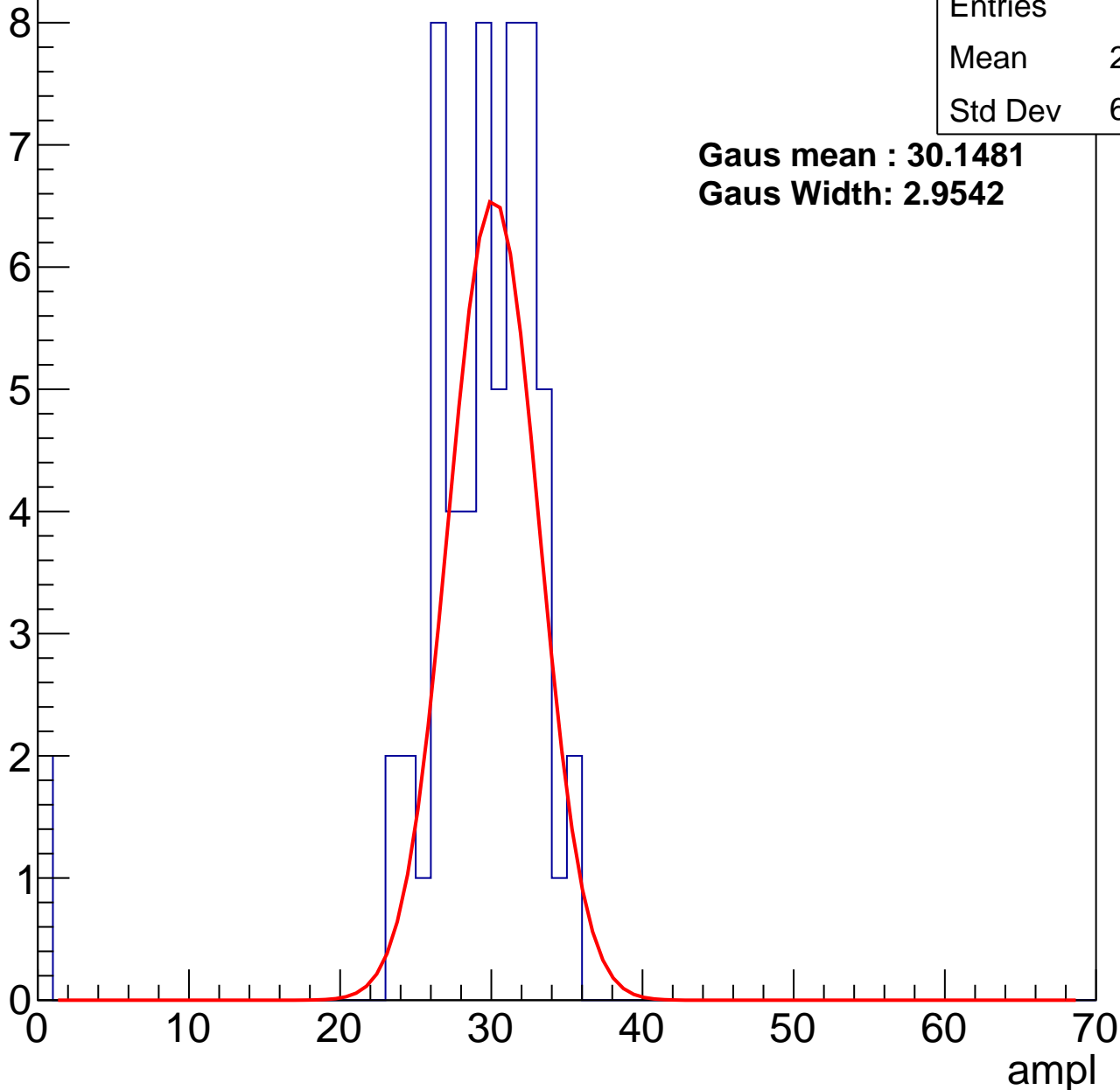
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 28.37 |
| Std Dev | 6.019 |

Gaus mean : 30.1481

Gaus Width: 2.9542



B1L100S, U9-ch24, adc1

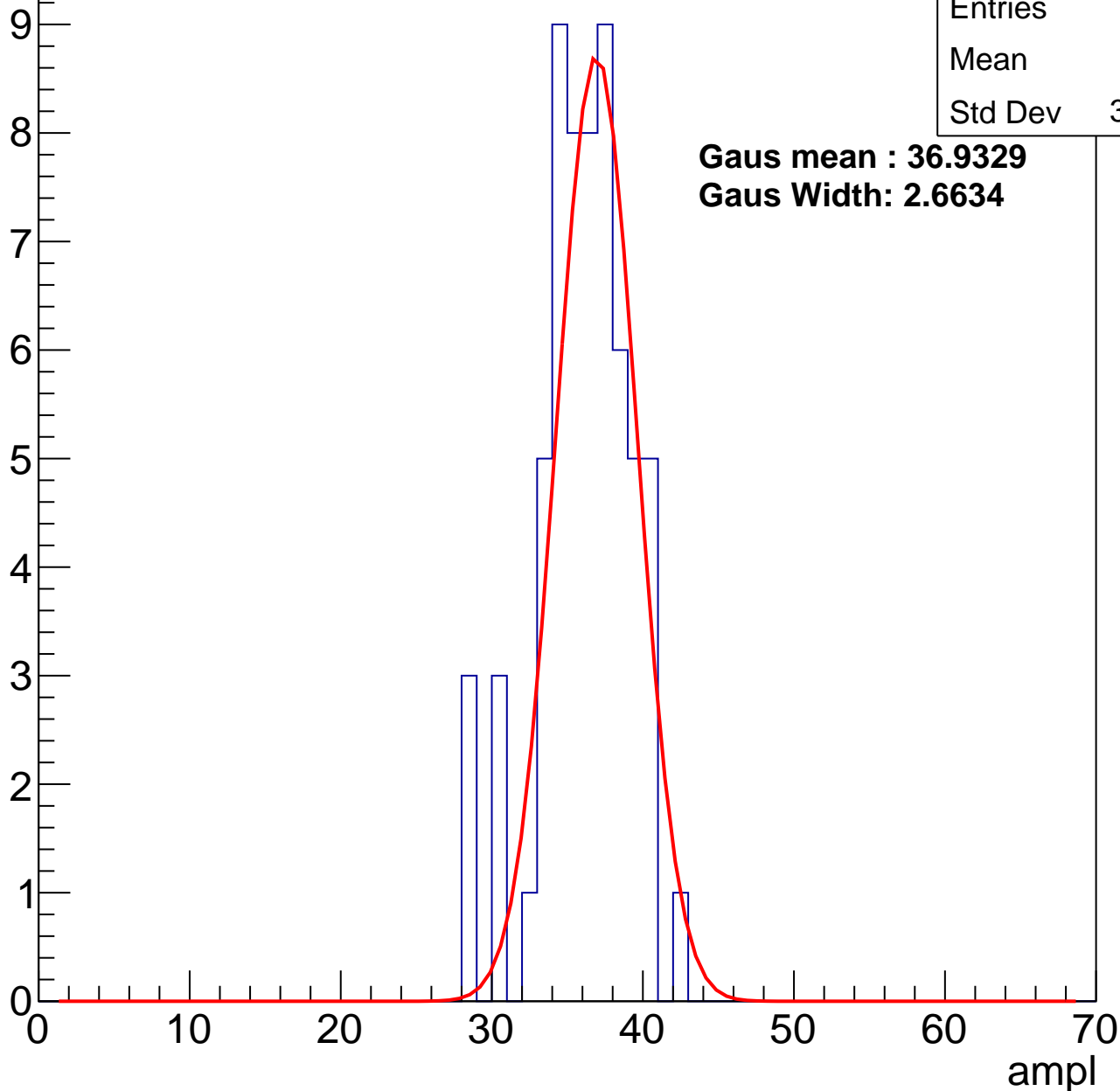
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.6 |
| Std Dev | 3.058 |

Gaus mean : 36.9329

Gaus Width: 2.6634



B1L100S, U9-ch24, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.41 |
| Std Dev | 3.4 |

Gaus mean : 43.0405

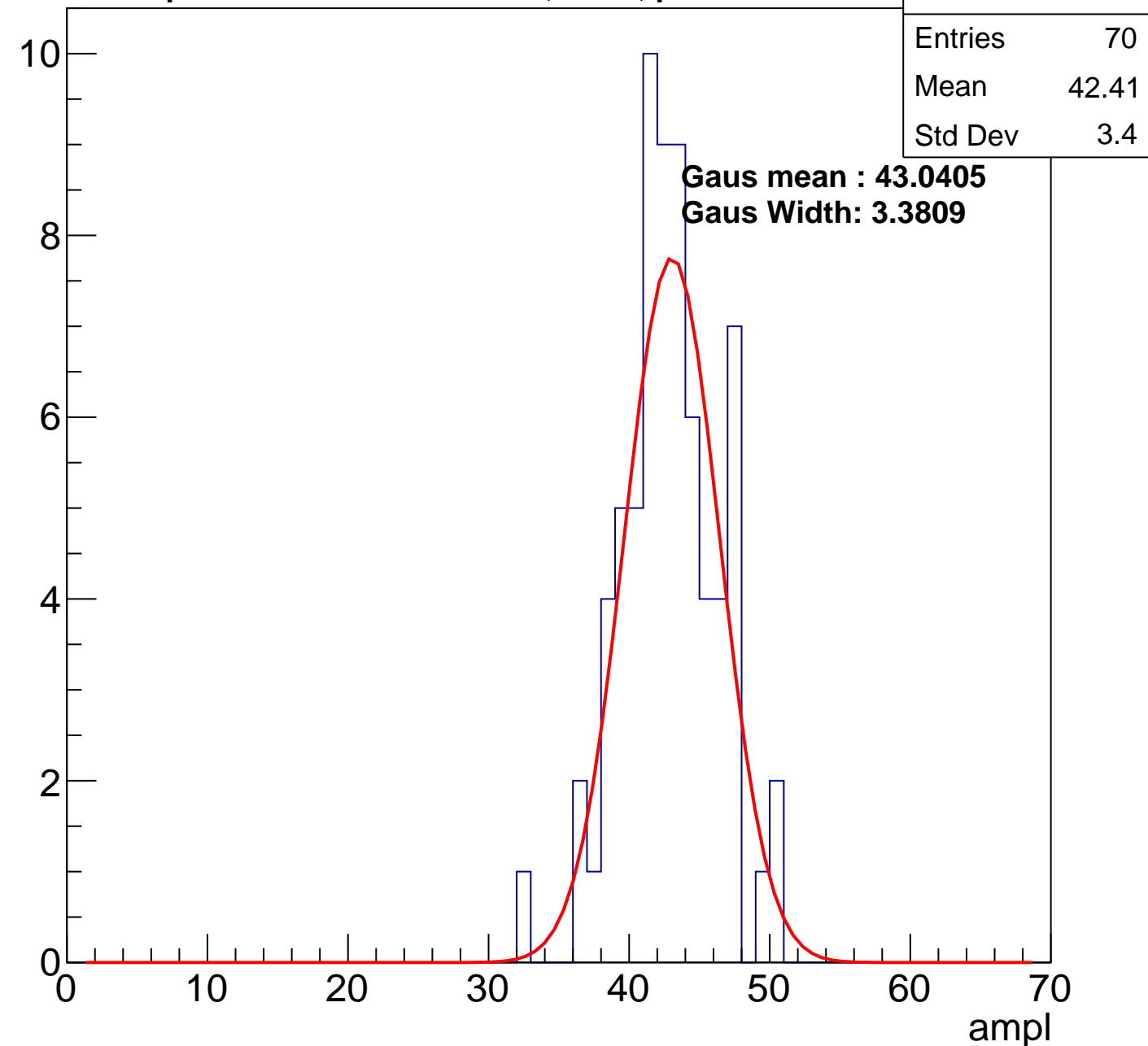
Gaus Width: 3.3809

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

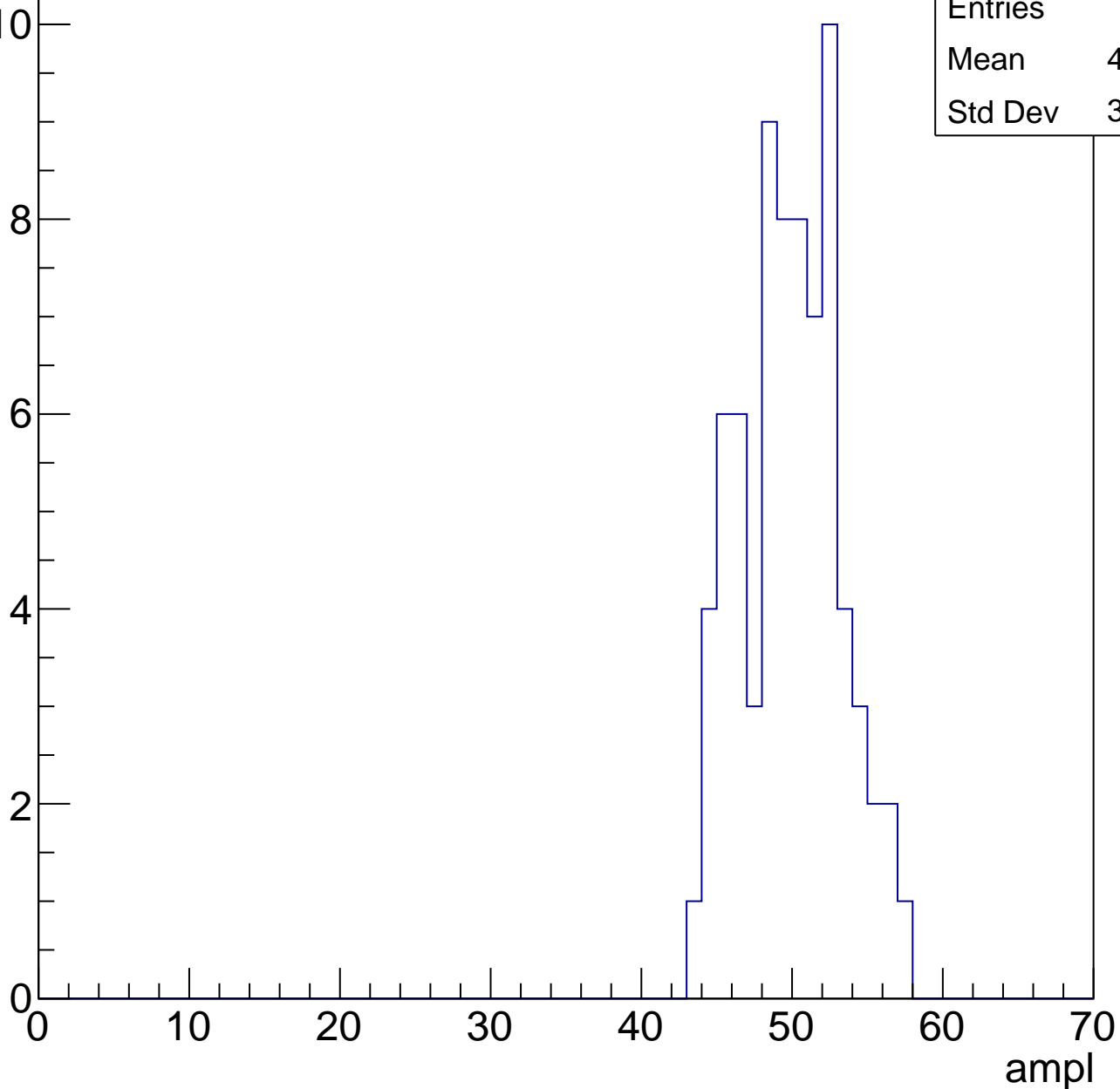


B1L100S, U9-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 49.46 |
| Std Dev | 3.268 |

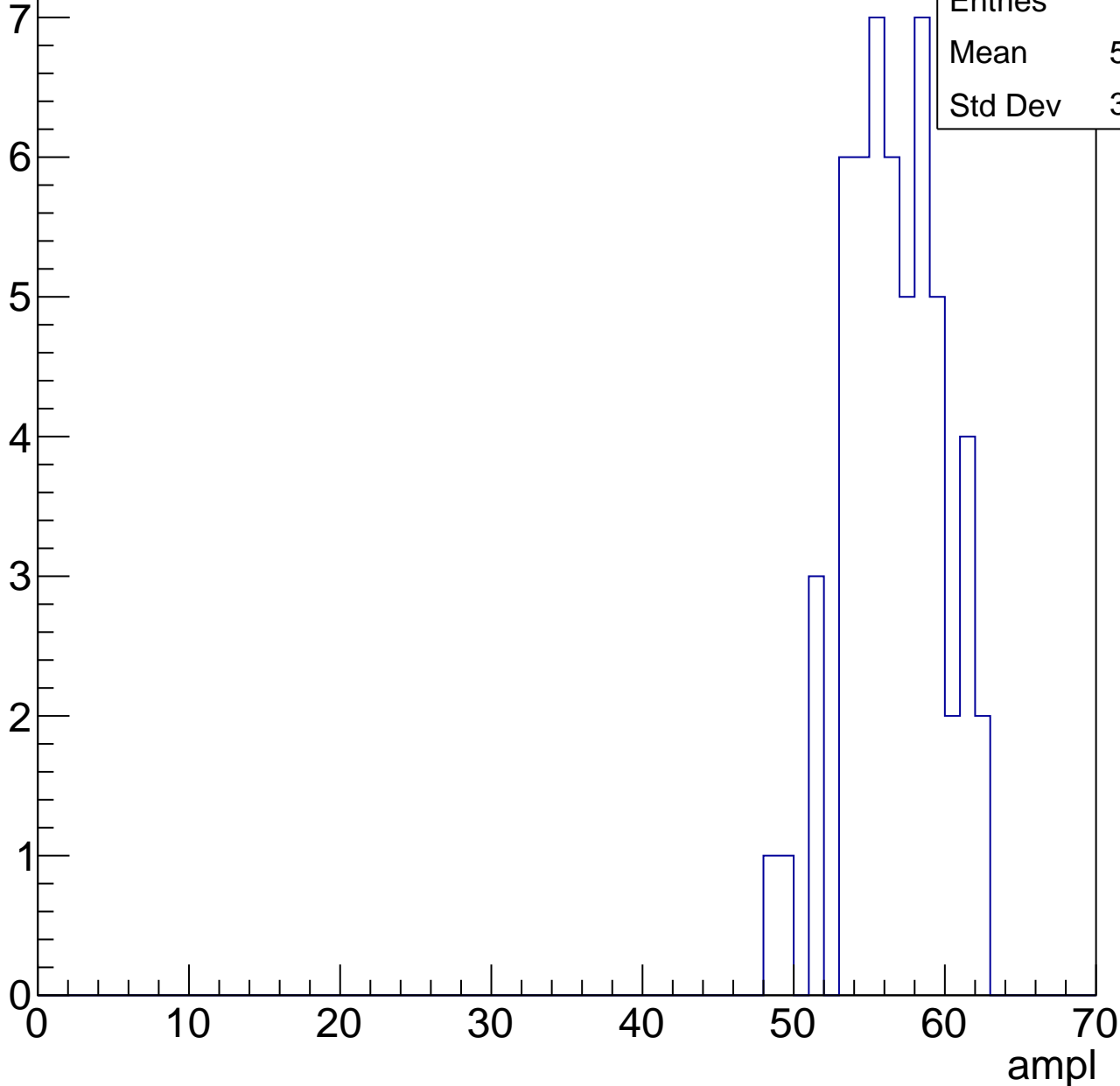


B1L100S, U9-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 56.13 |
| Std Dev | 3.174 |



B1L100S, U9-ch24, adc5

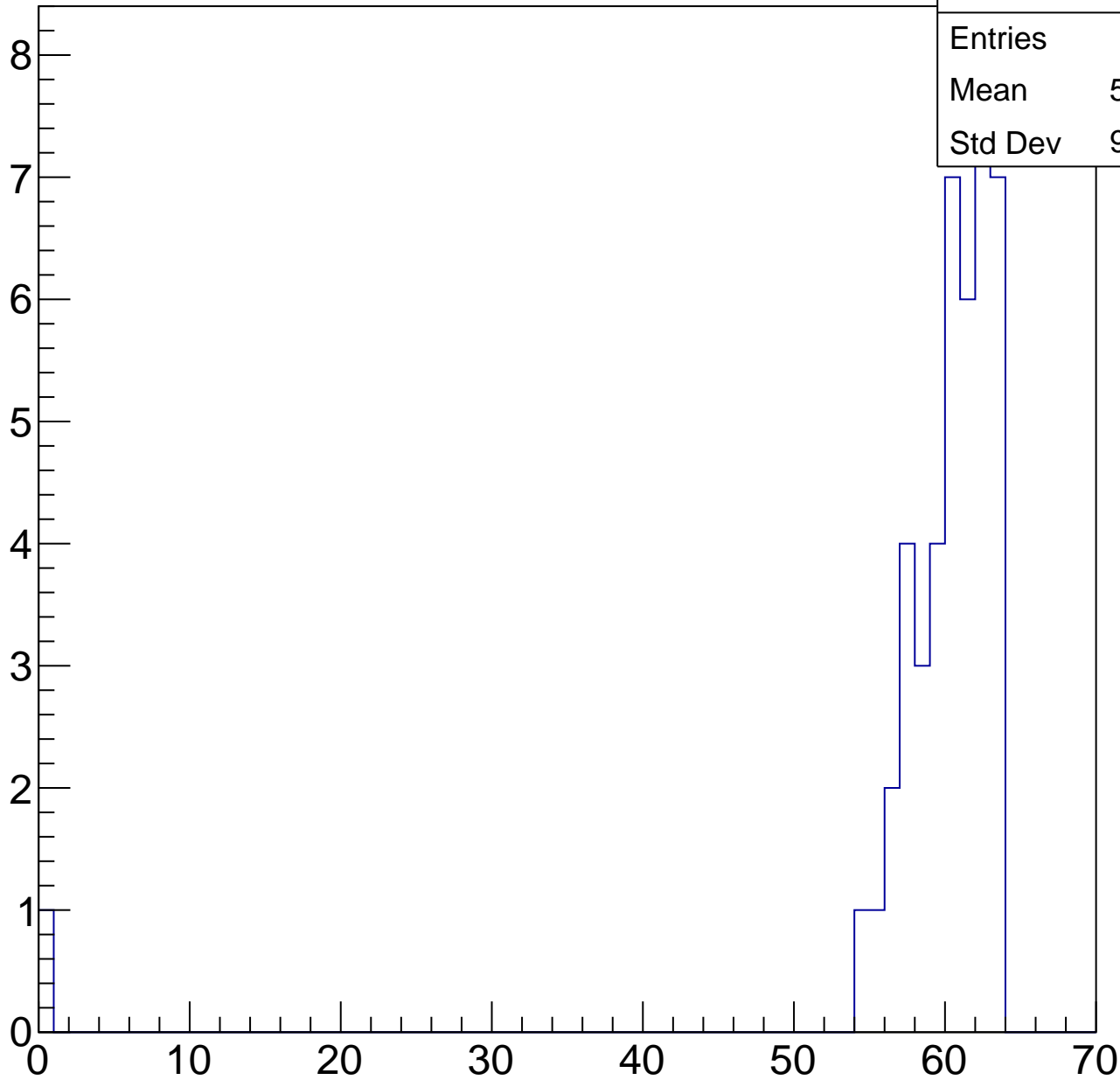
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.68 |
| Std Dev | 9.256 |

ampl



B1L100S, U9-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

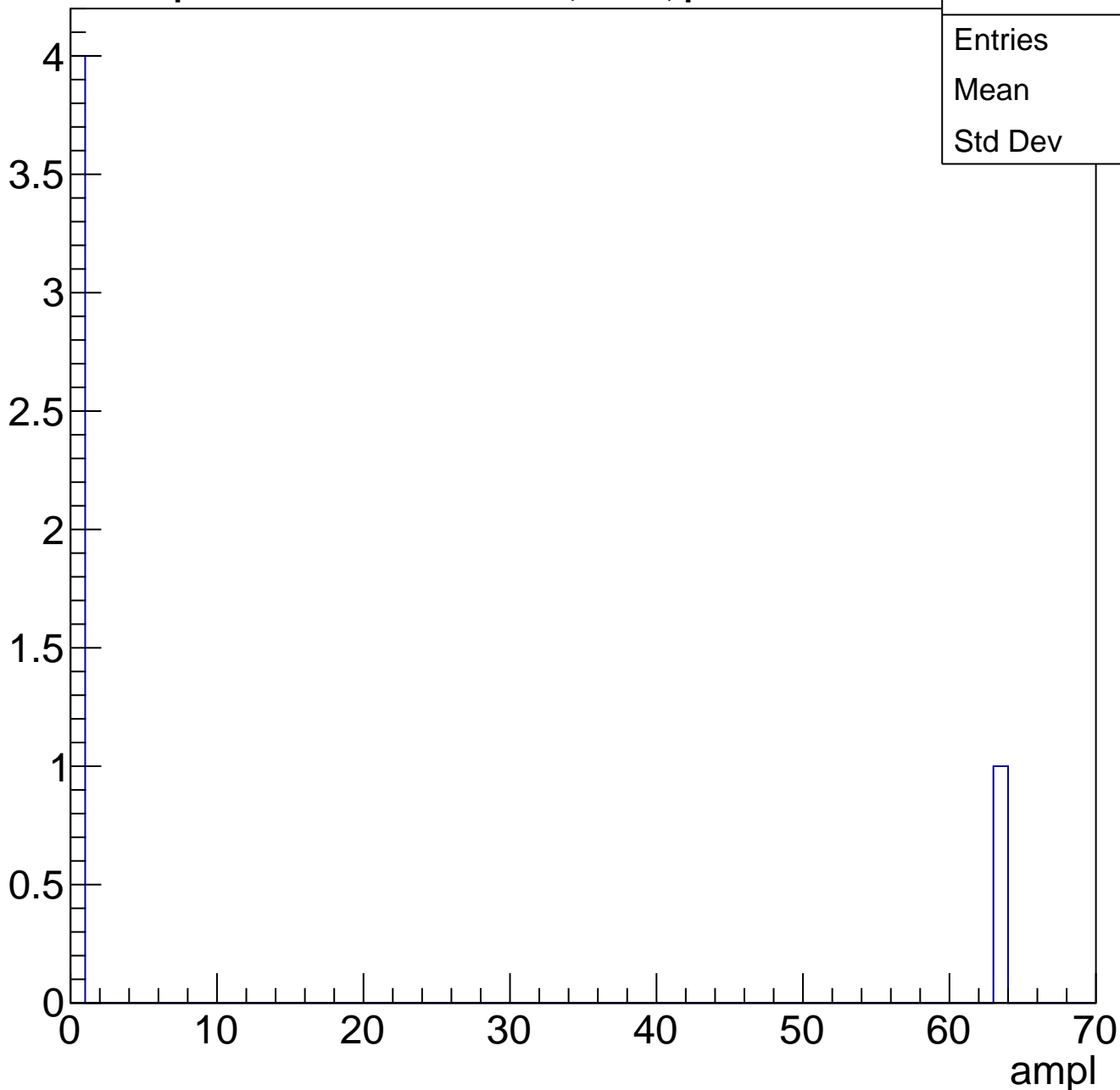
Entry



B1L100S, U9-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch25, adc0

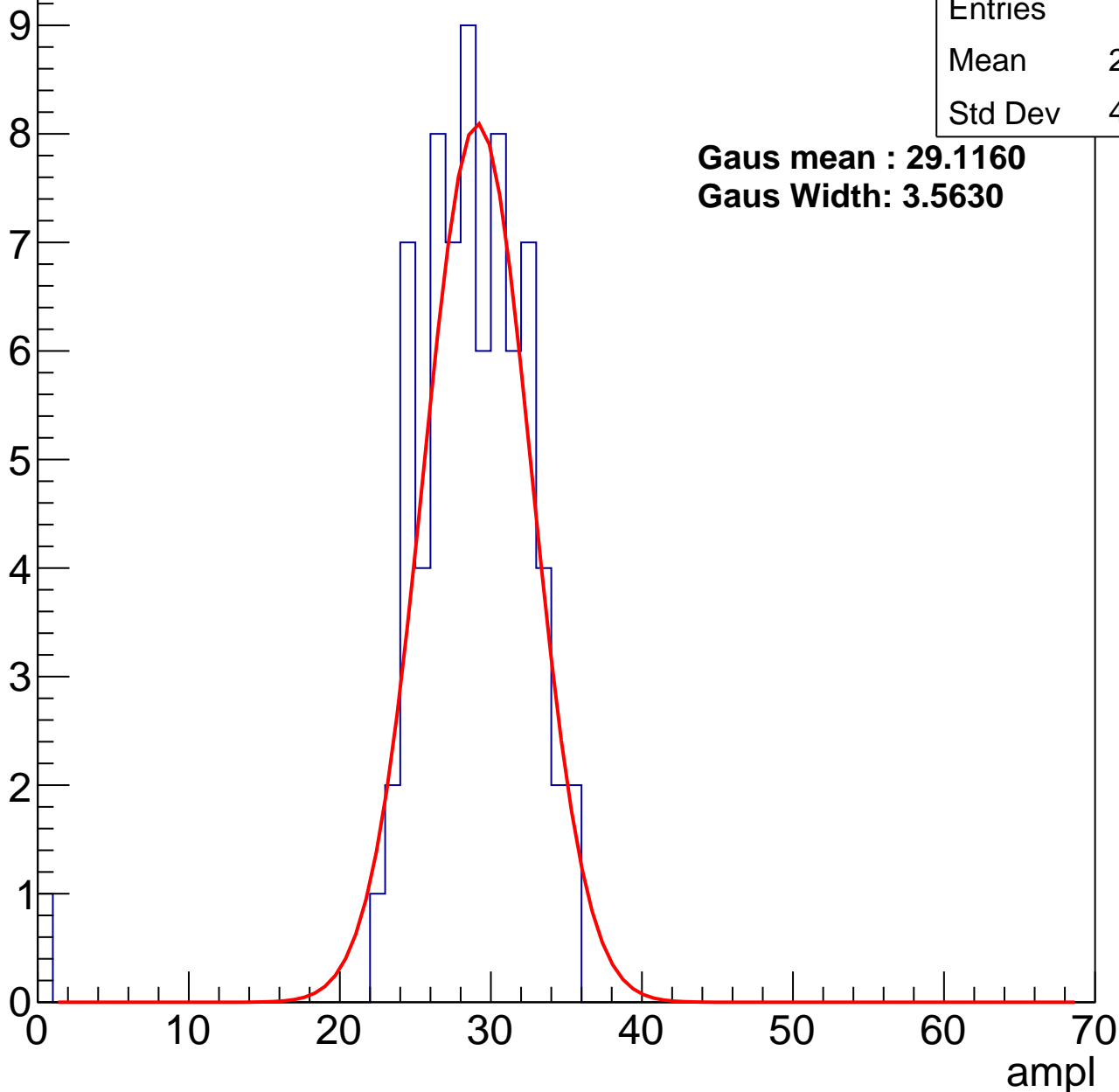
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.09 |
| Std Dev | 4.548 |

Gaus mean : 29.1160

Gaus Width: 3.5630



B1L100S, U9-ch25, adc1

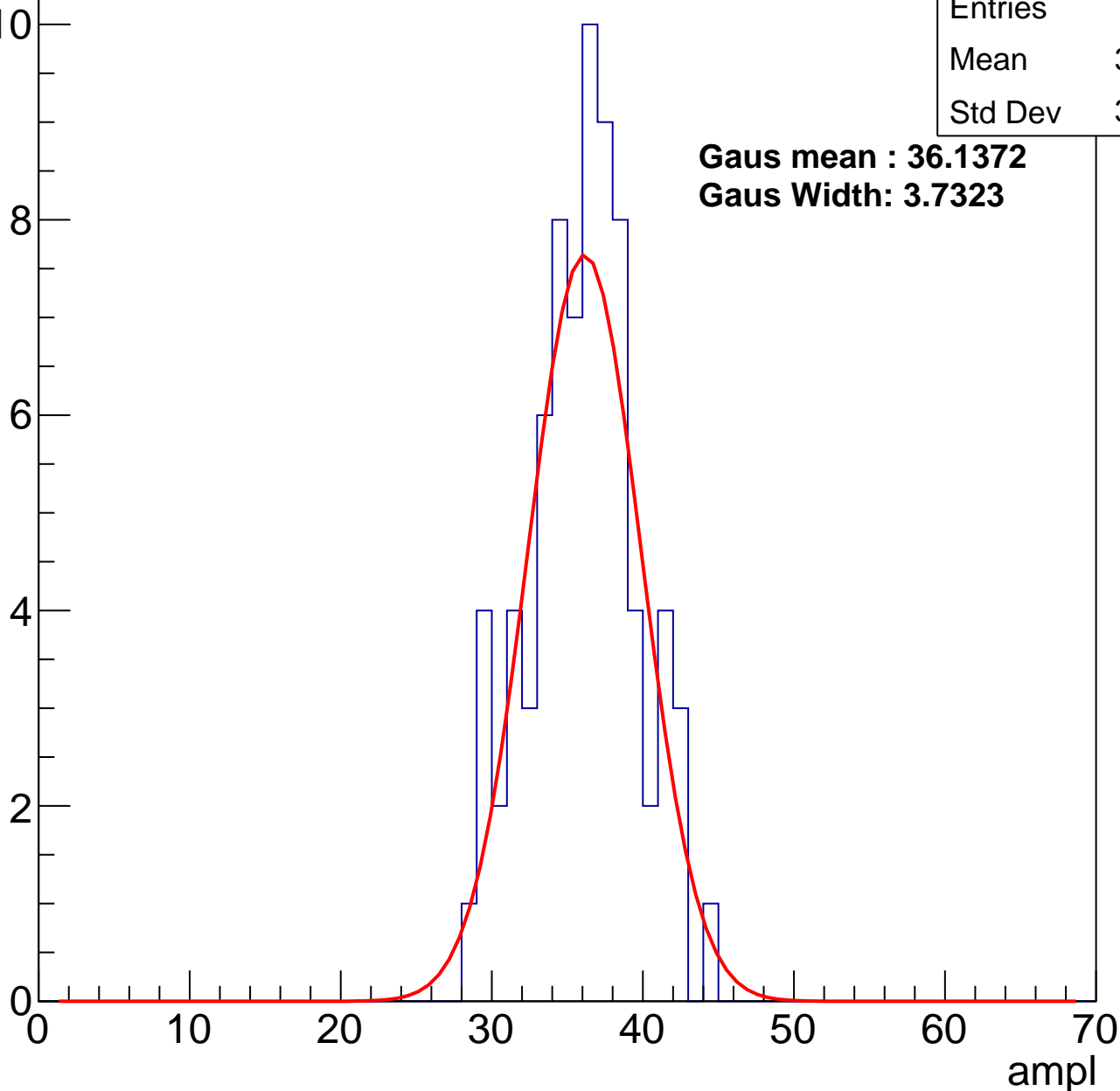
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.61 |
| Std Dev | 3.521 |

Gaus mean : 36.1372

Gaus Width: 3.7323



B1L100S, U9-ch25, adc2

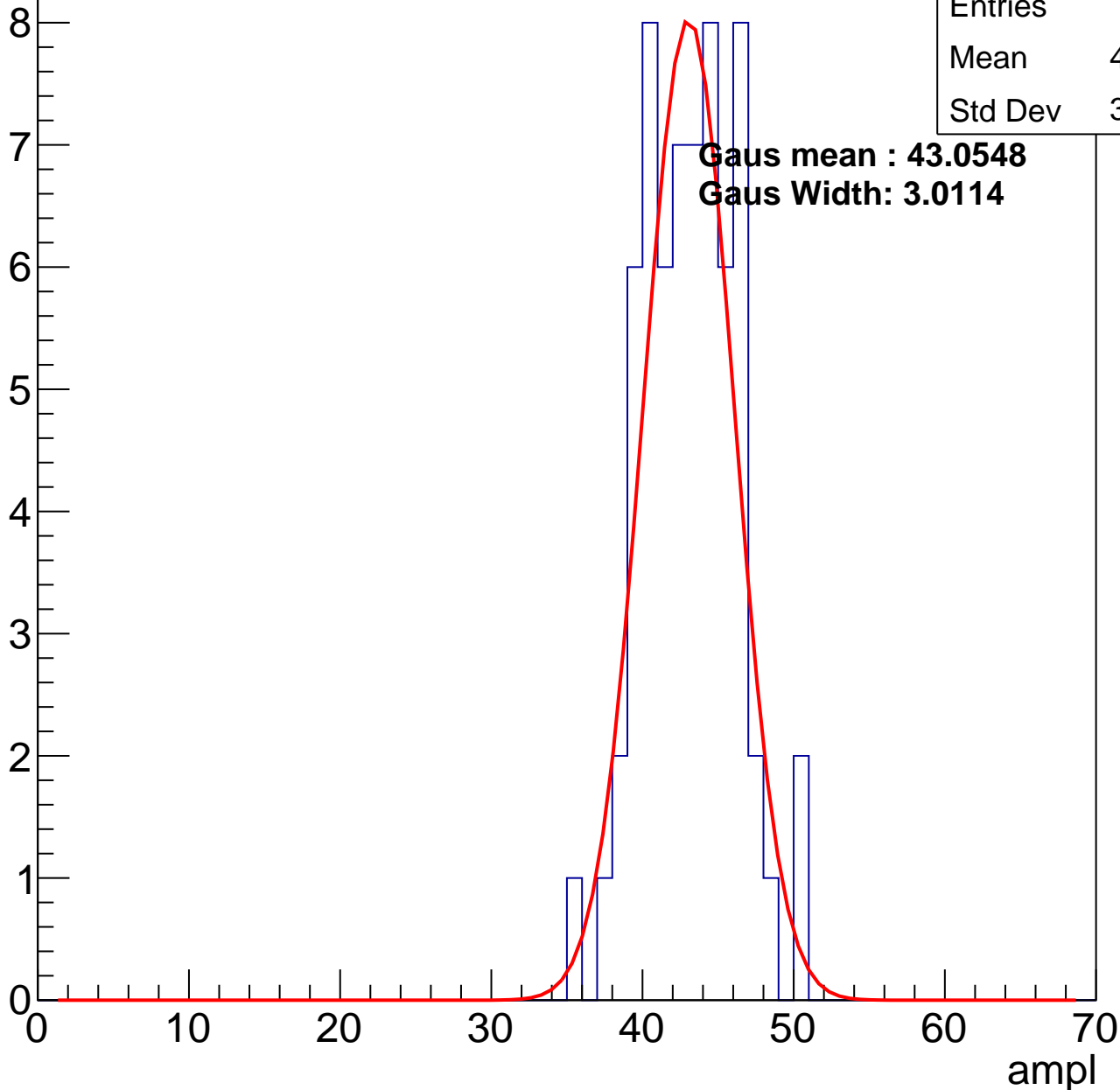
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 42.69 |
| Std Dev | 3.043 |

Gaus mean : 43.0548

Gaus Width: 3.0114

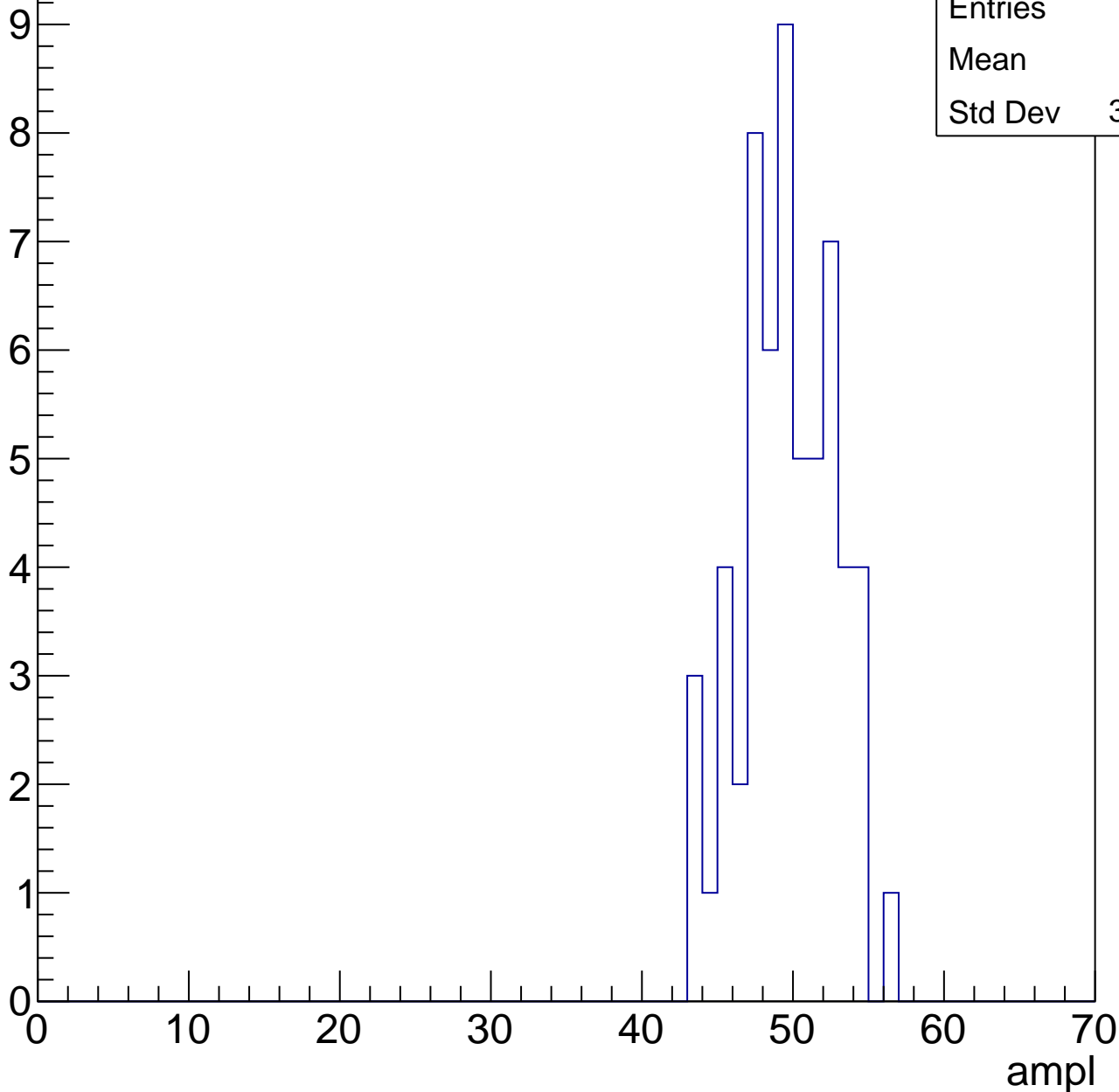


B1L100S, U9-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

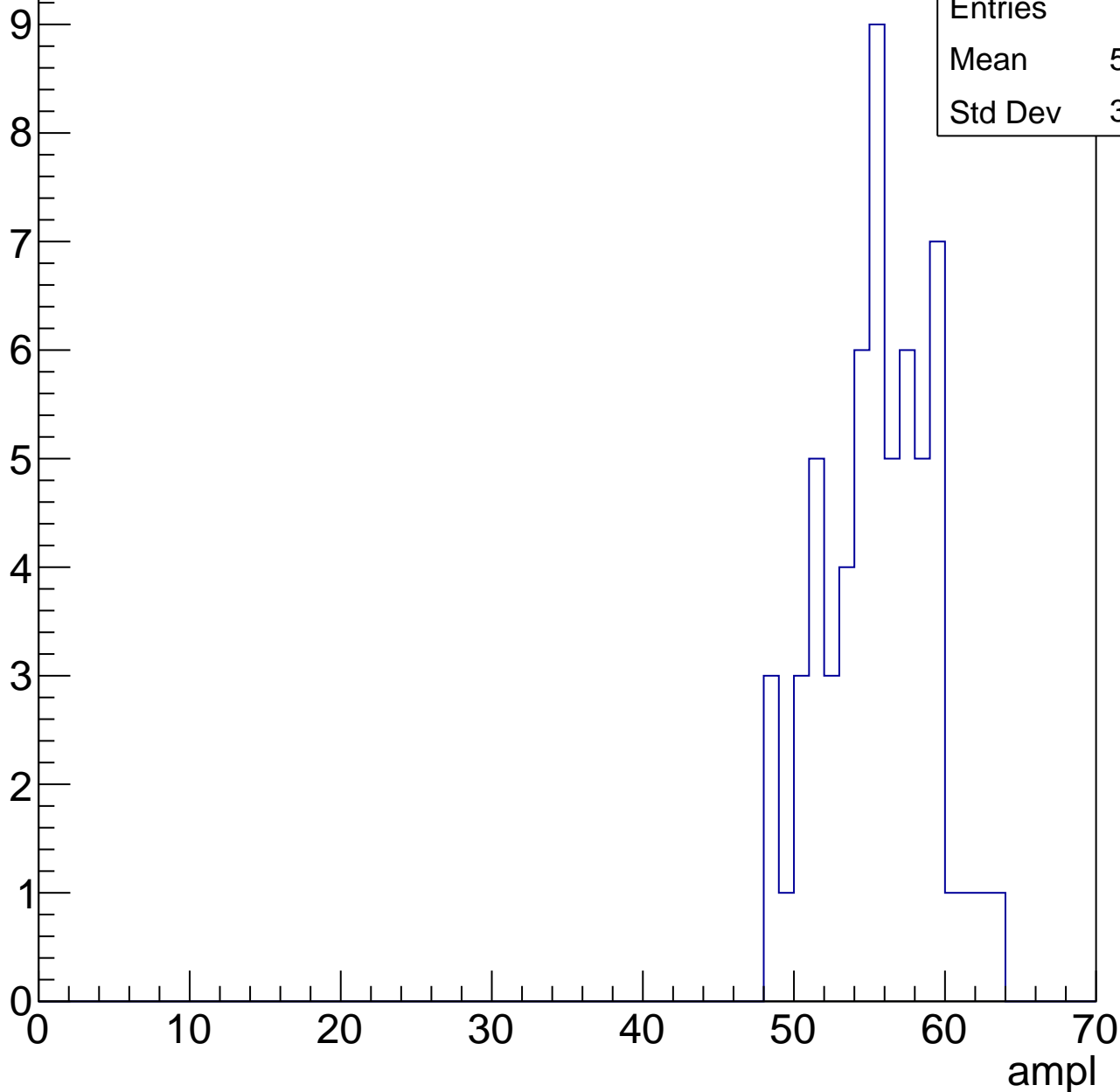
| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.2 |
| Std Dev | 3.058 |



B1L100S, U9-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

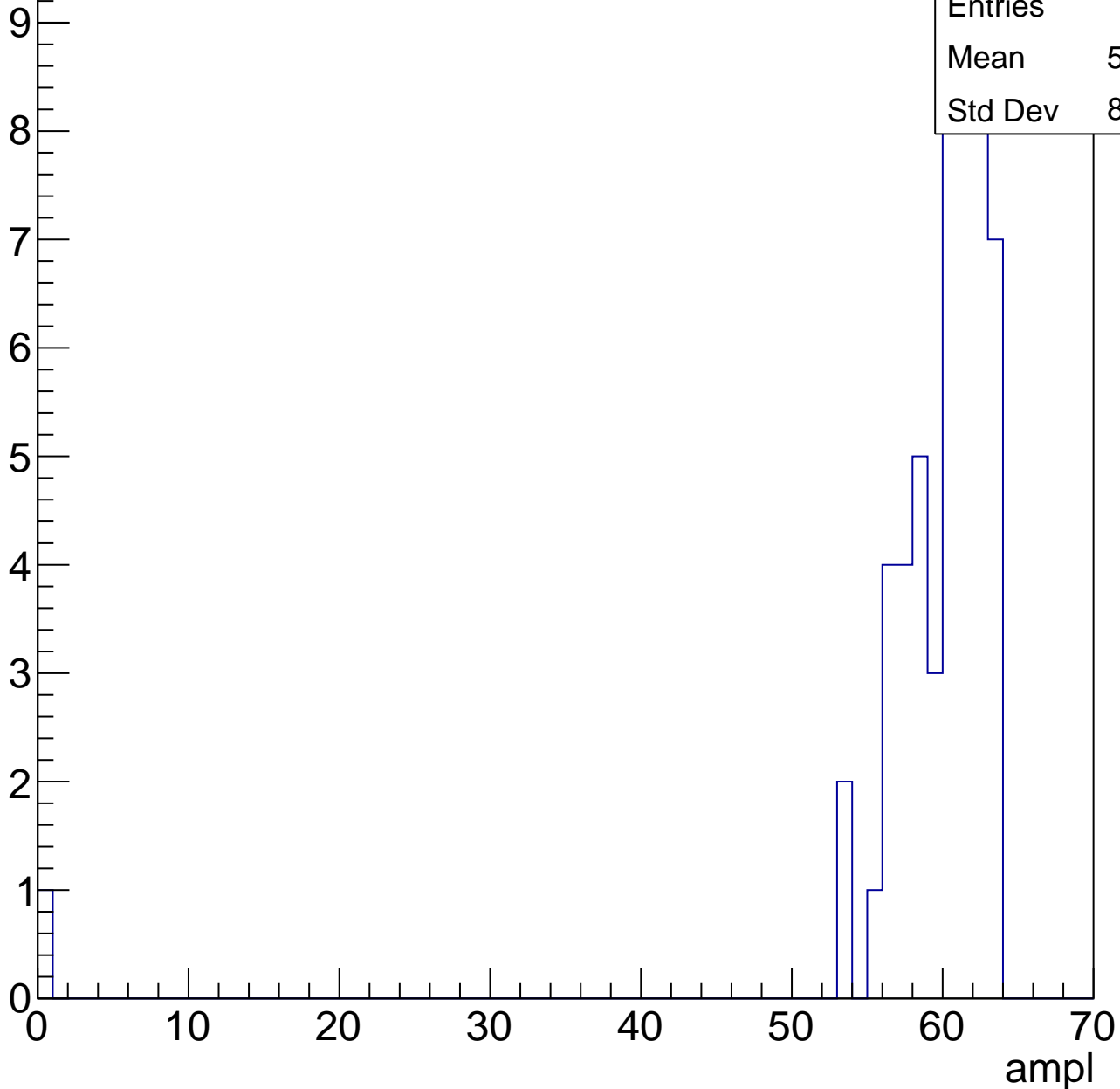


B1L100S, U9-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.62 |
| Std Dev | 8.526 |



B1L100S, U9-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

B1L100S, U9-ch26, adc0

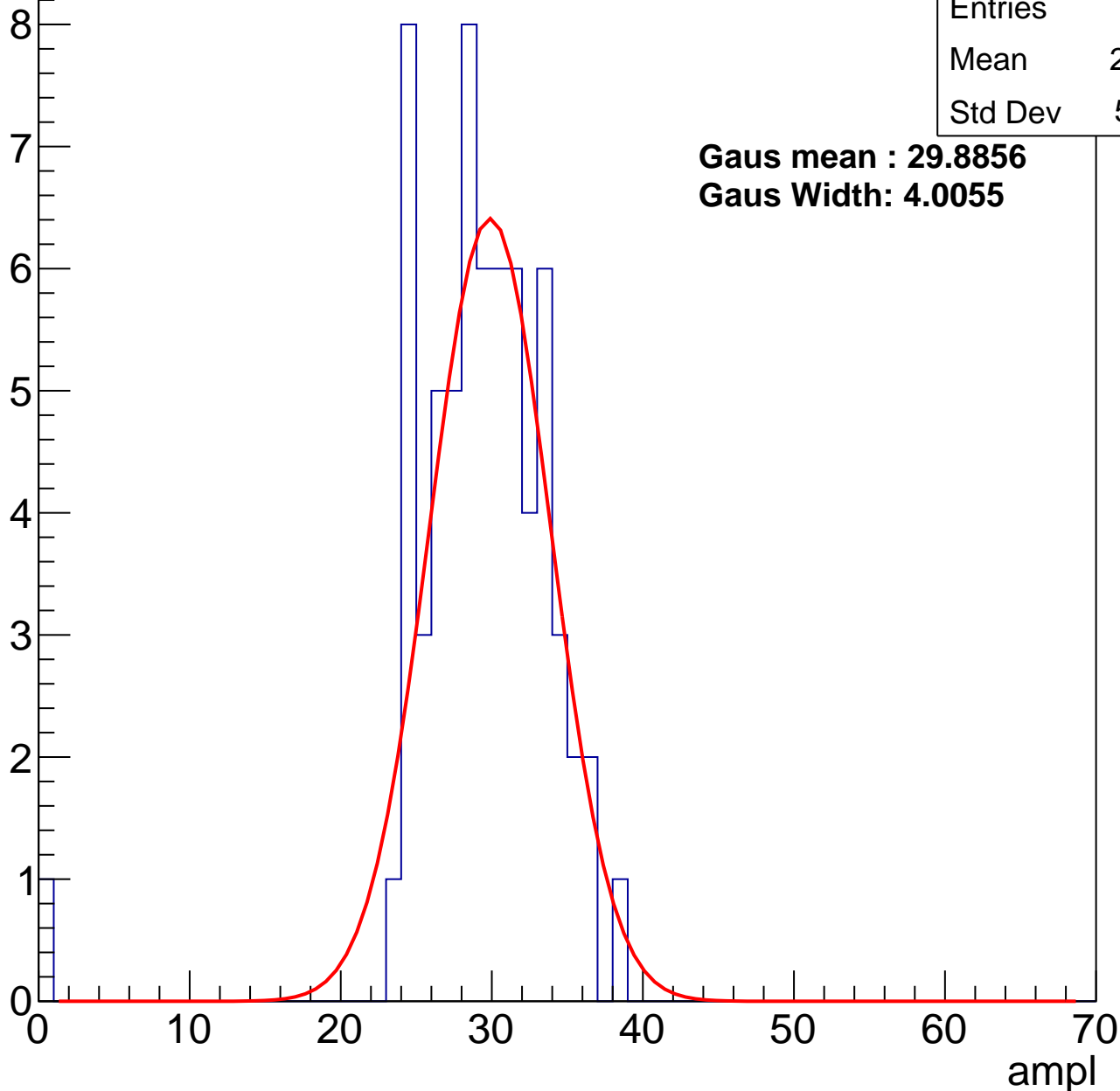
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 28.76 |
| Std Dev | 5.011 |

Gaus mean : 29.8856

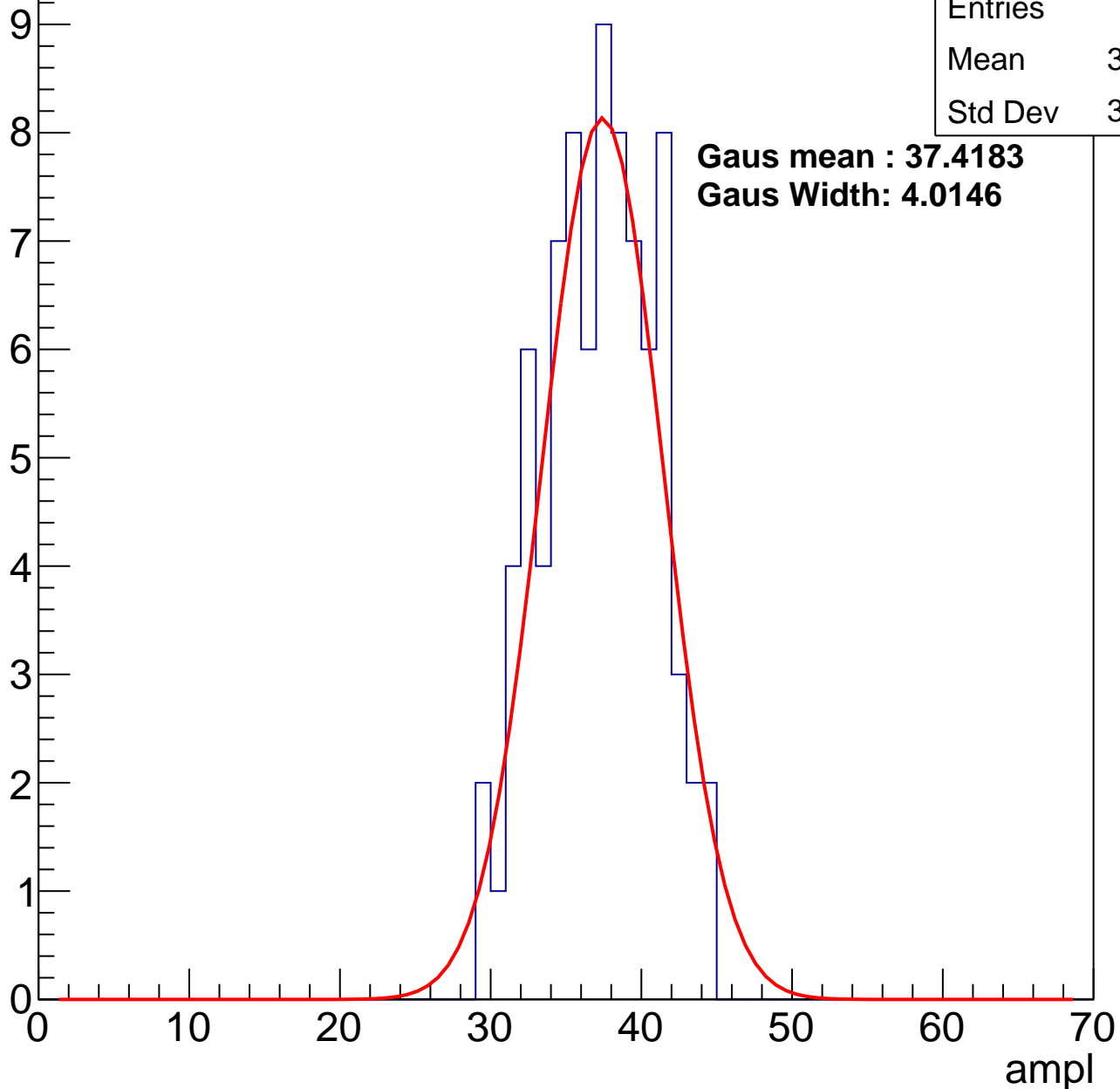
Gaus Width: 4.0055



B1L100S, U9-ch26, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch26, adc2

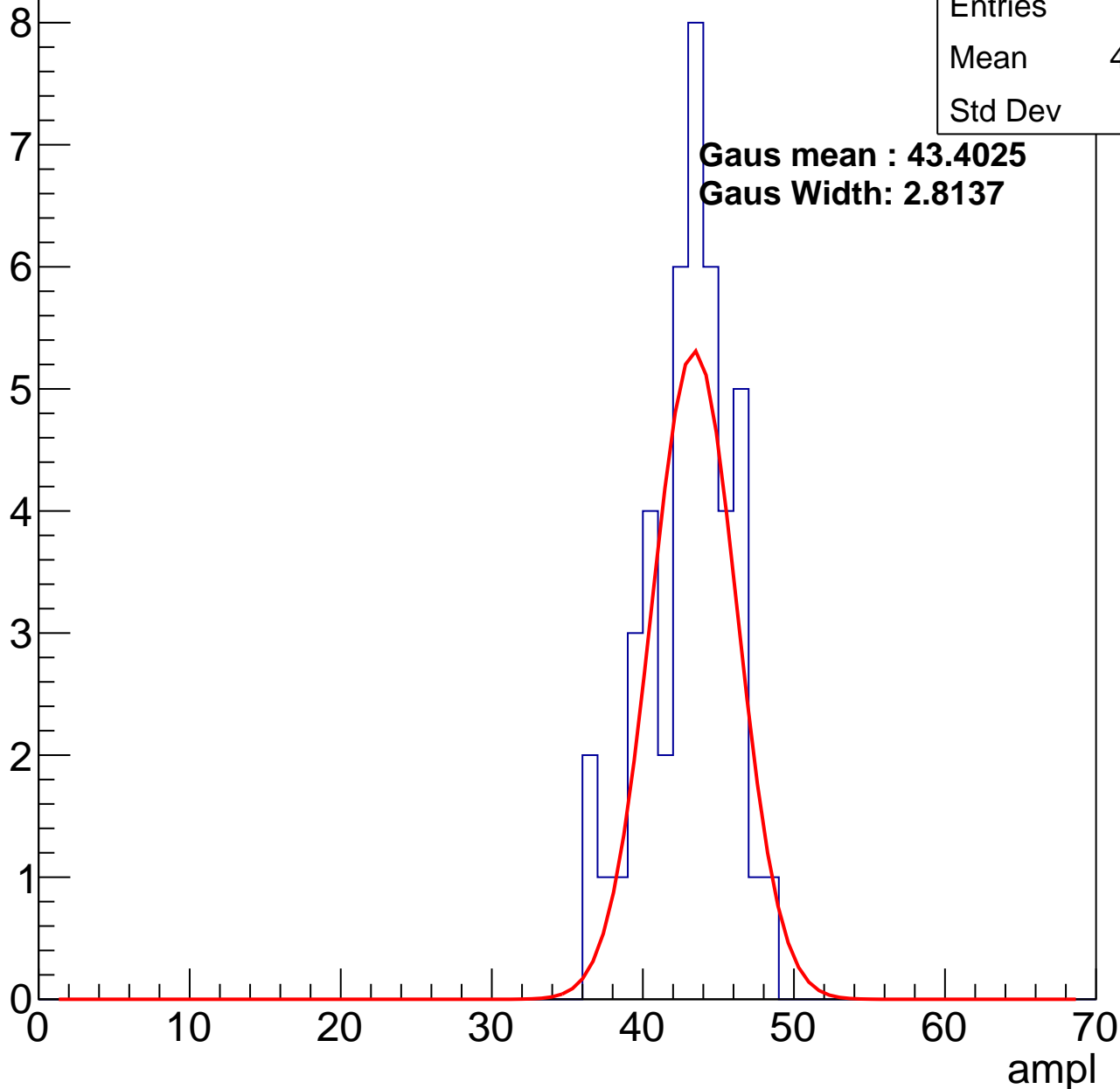
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 42.52 |
| Std Dev | 2.84 |

Gaus mean : 43.4025

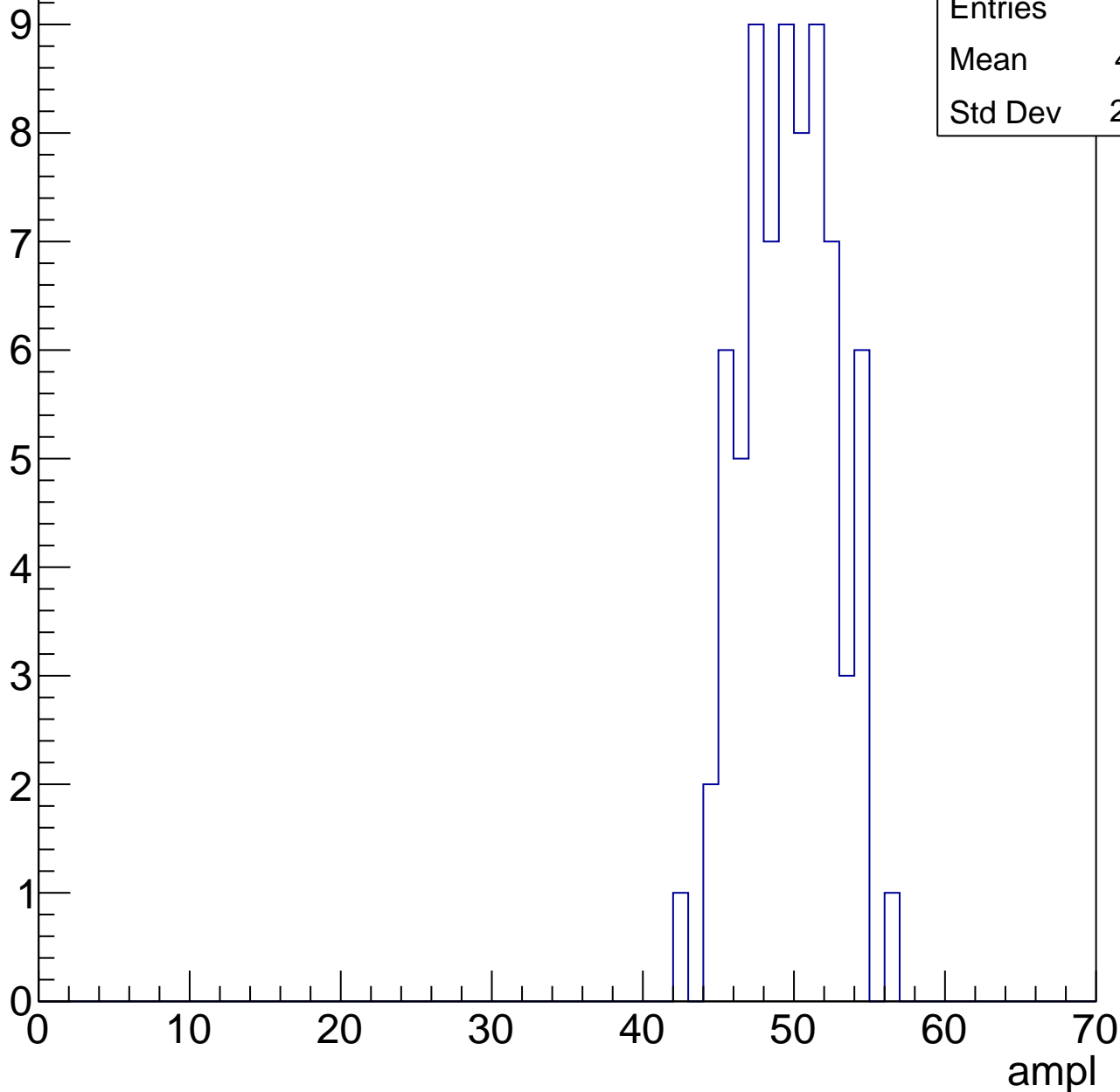
Gaus Width: 2.8137



B1L100S, U9-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

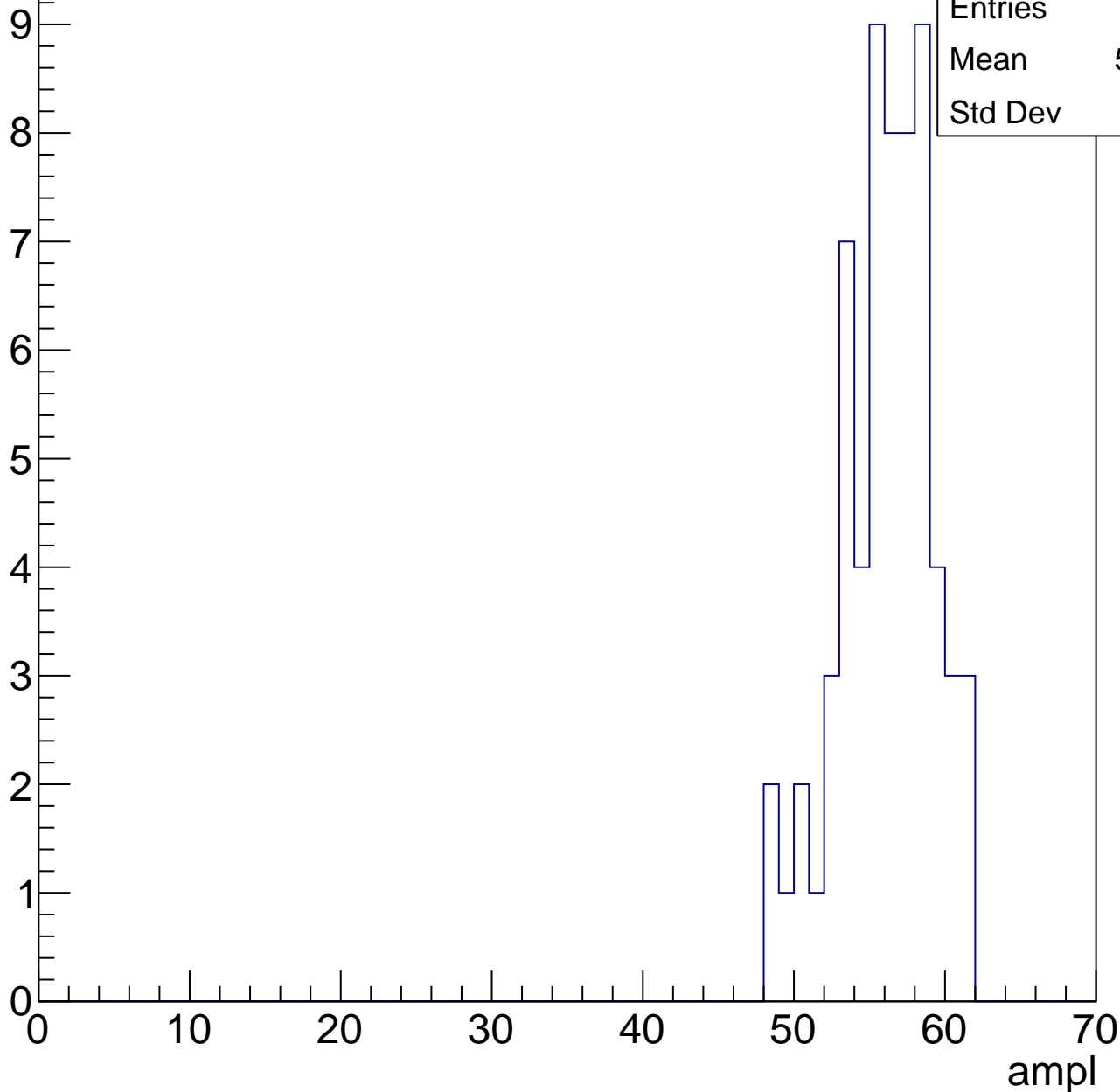
Entry



B1L100S, U9-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



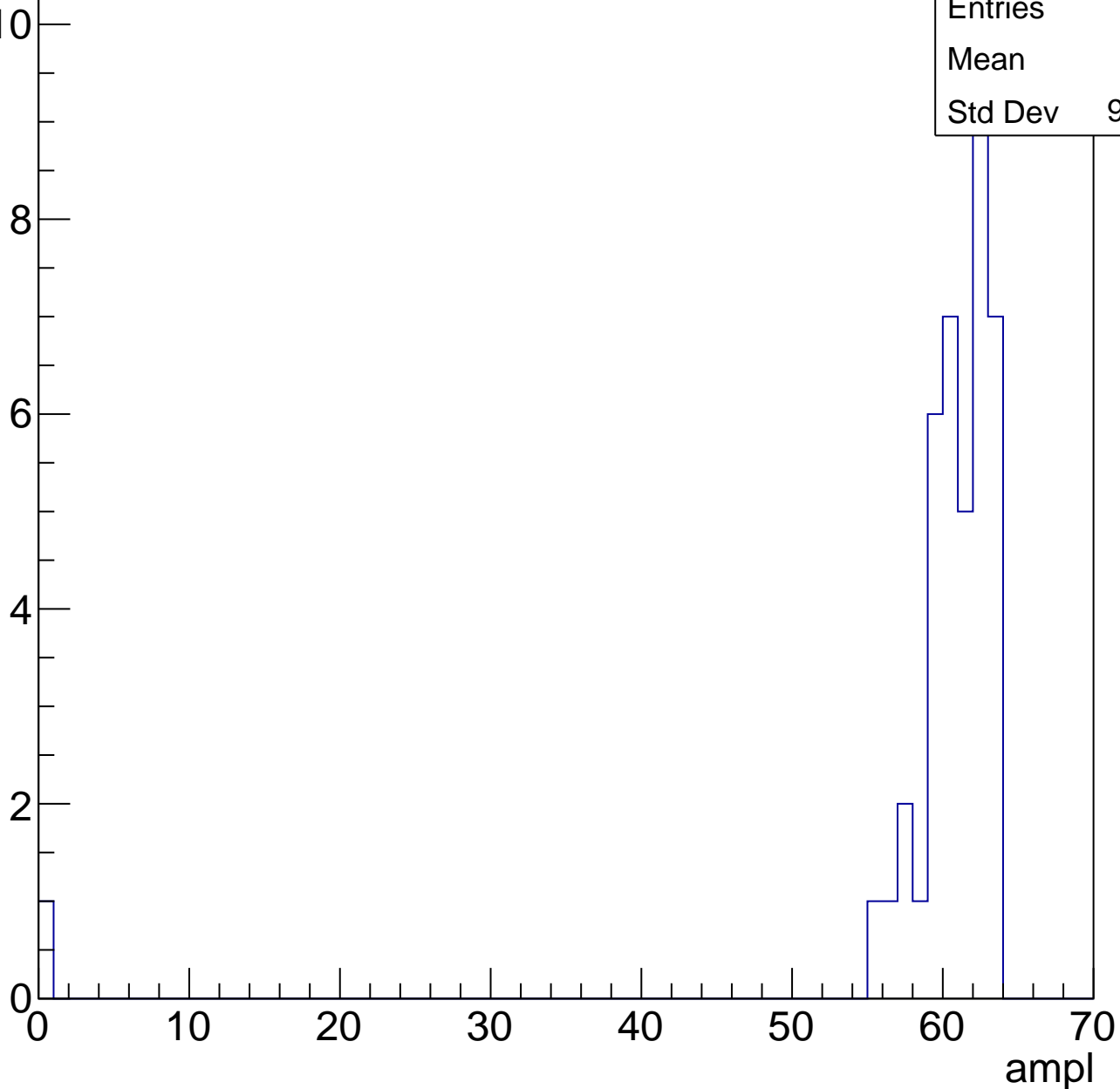
| | |
|---------|-------|
| Entries | 64 |
| Mean | 55.61 |
| Std Dev | 3.07 |

B1L100S, U9-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 59.1 |
| Std Dev | 9.555 |



B1L100S, U9-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch27, adc0

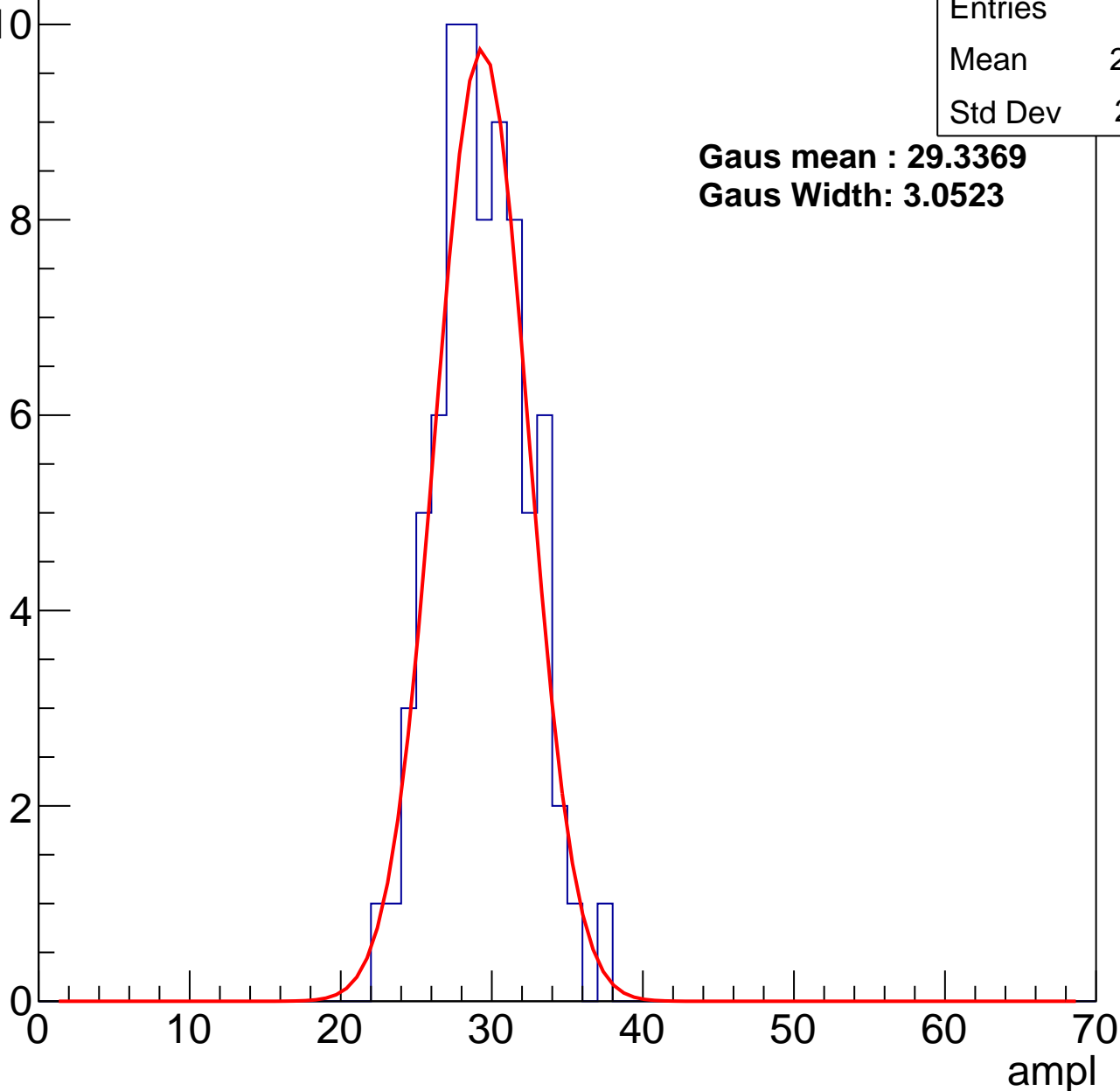
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 28.89 |
| Std Dev | 2.981 |

Gaus mean : 29.3369

Gaus Width: 3.0523



B1L100S, U9-ch27, adc1

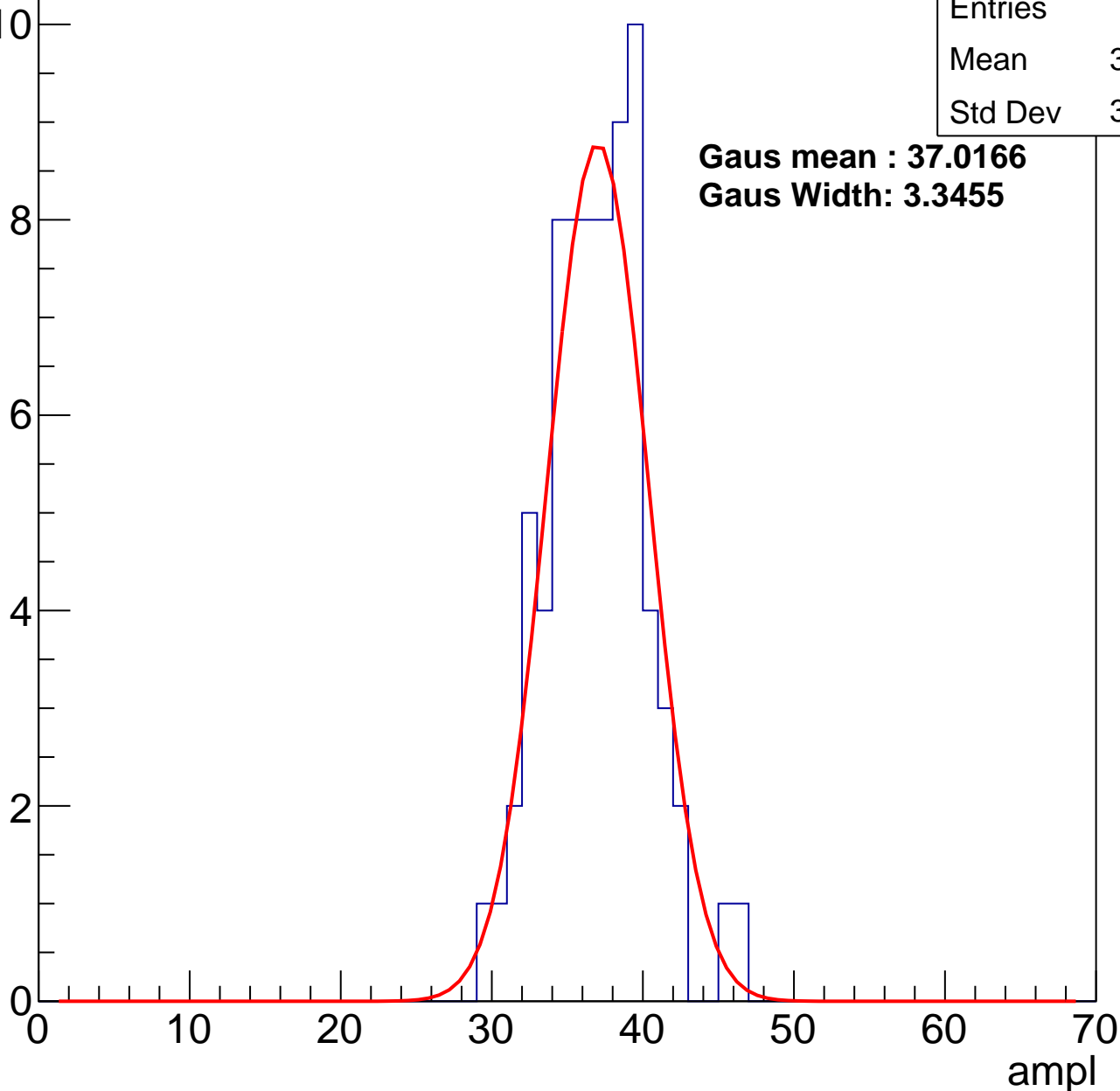
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 36.52 |
| Std Dev | 3.259 |

Gaus mean : 37.0166

Gaus Width: 3.3455



B1L100S, U9-ch27, adc2

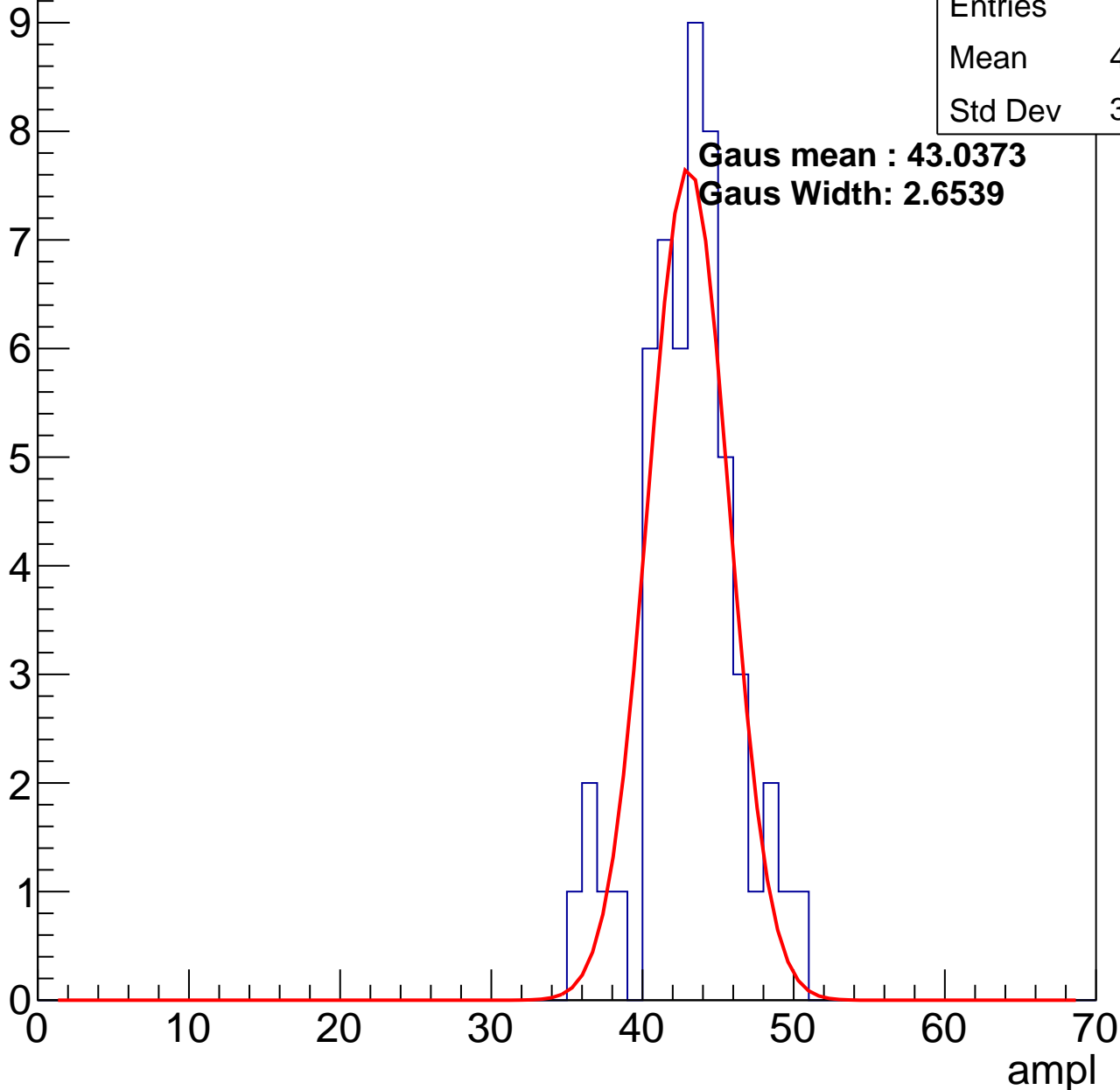
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 42.69 |
| Std Dev | 3.078 |

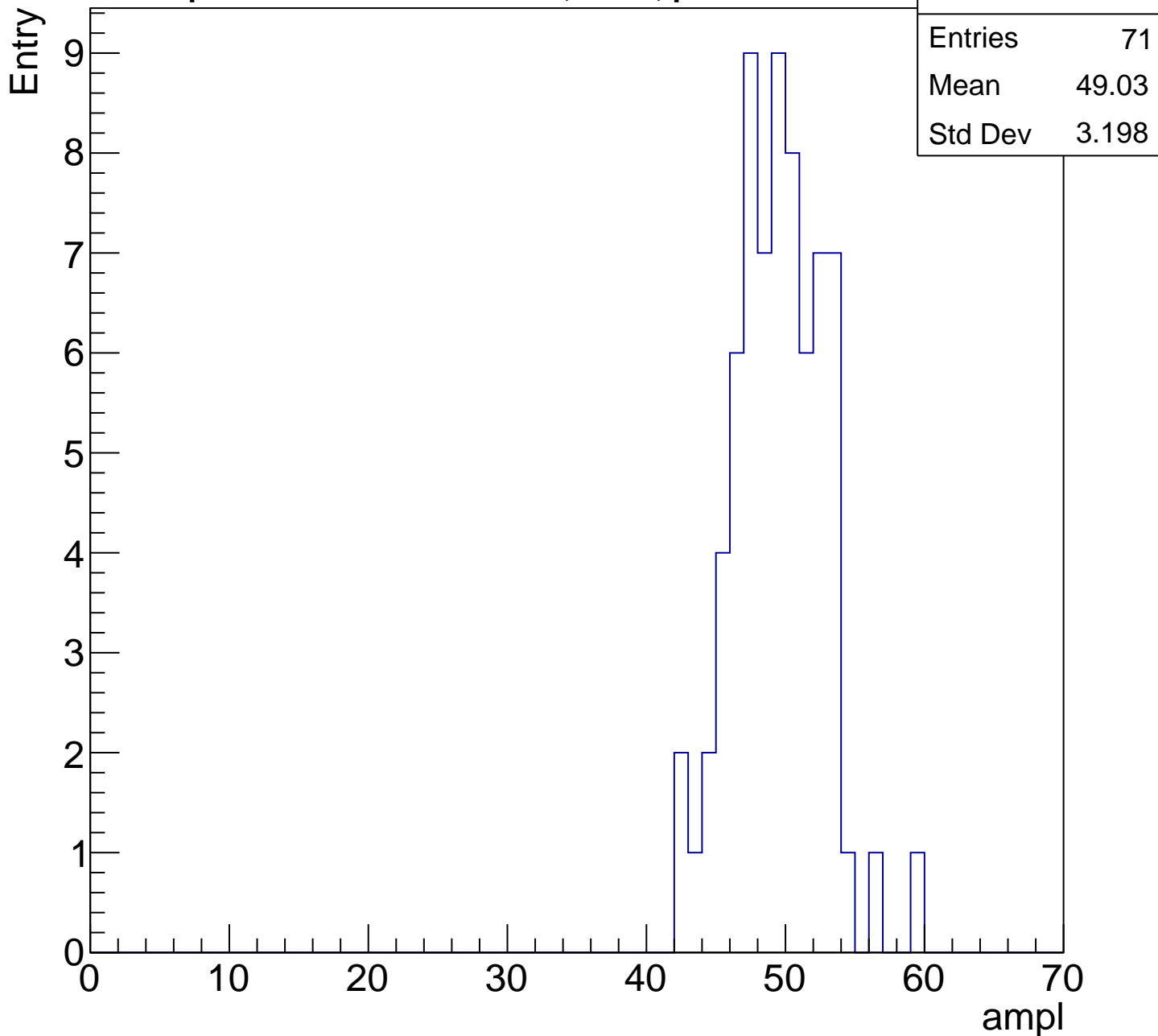
Gaus mean : 43.0373

Gaus Width: 2.6539



B1L100S, U9-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

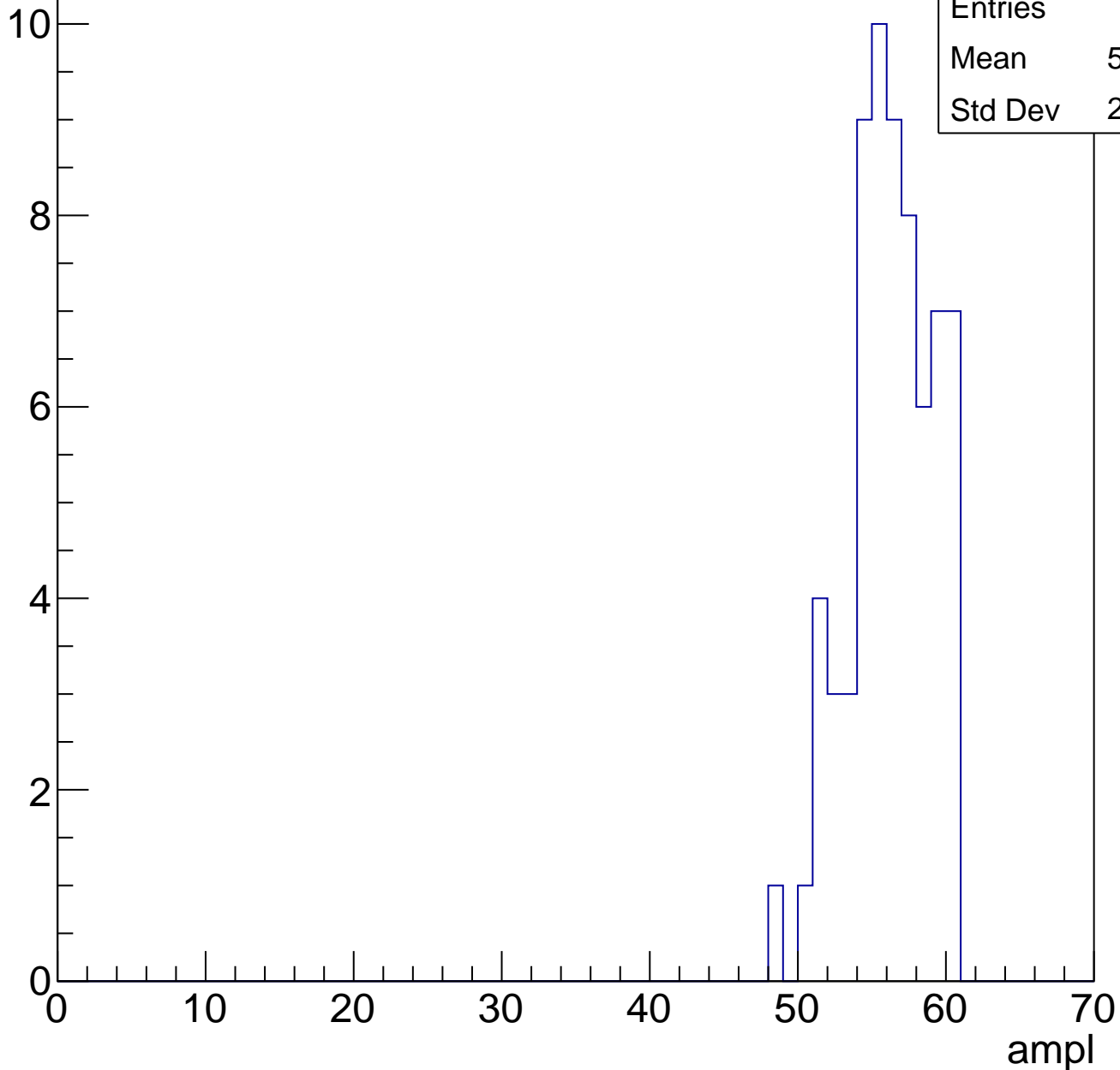


B1L100S, U9-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 68 |
| Mean | 55.79 |
| Std Dev | 2.779 |

Entry

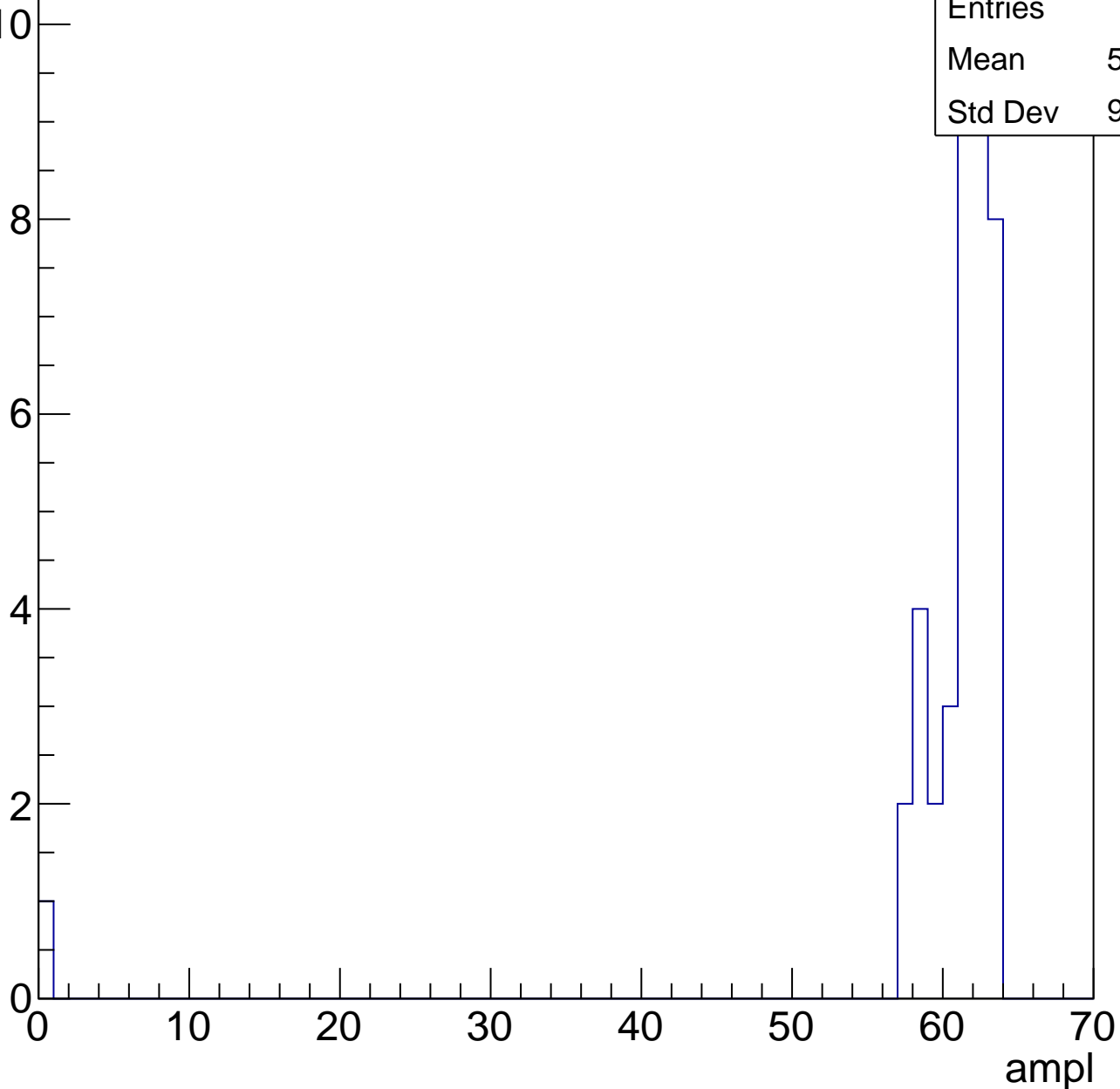


B1L100S, U9-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 59.38 |
| Std Dev | 9.792 |



B1L100S, U9-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

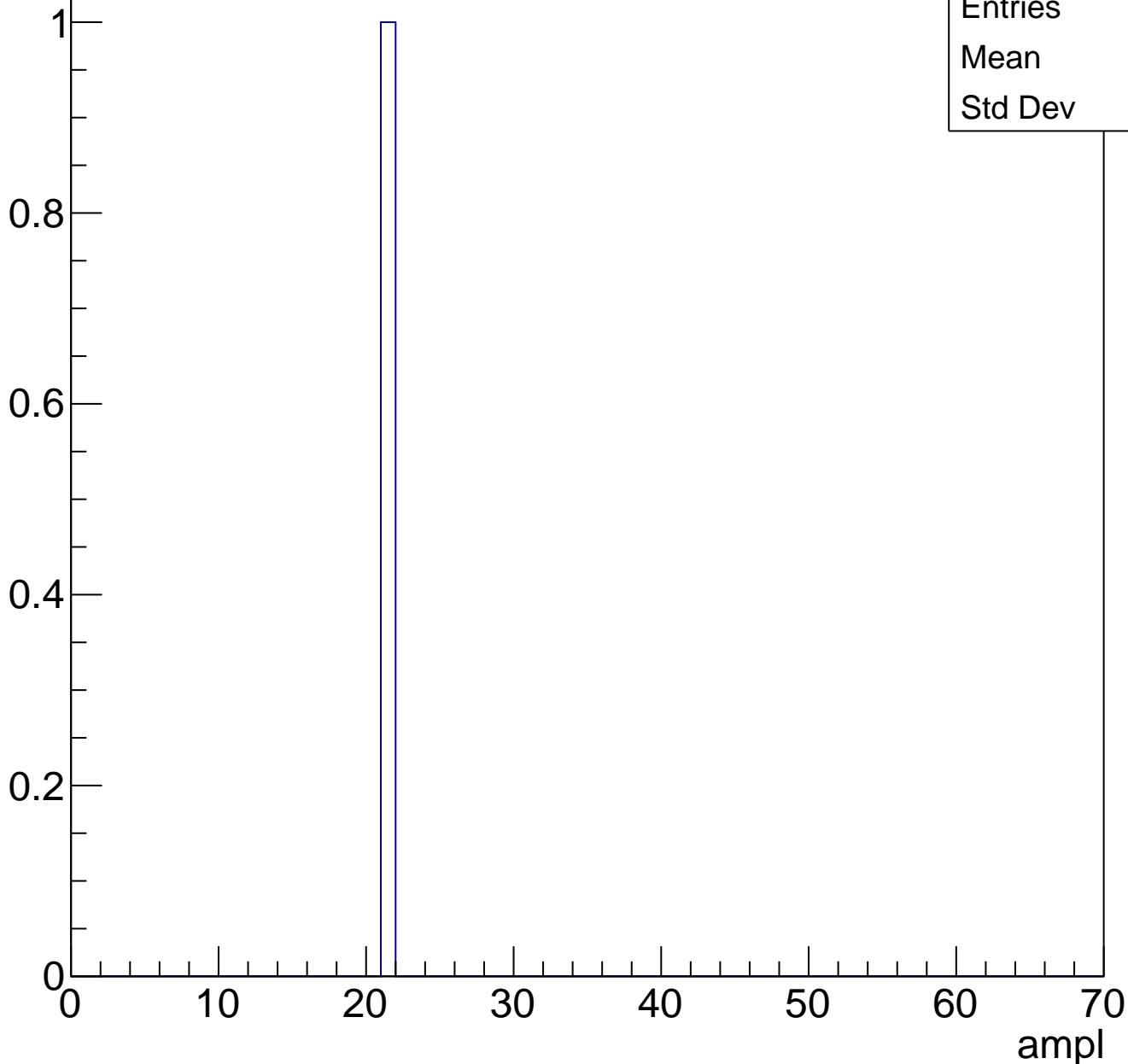
Entry



B1L100S, U9-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch28, adc0

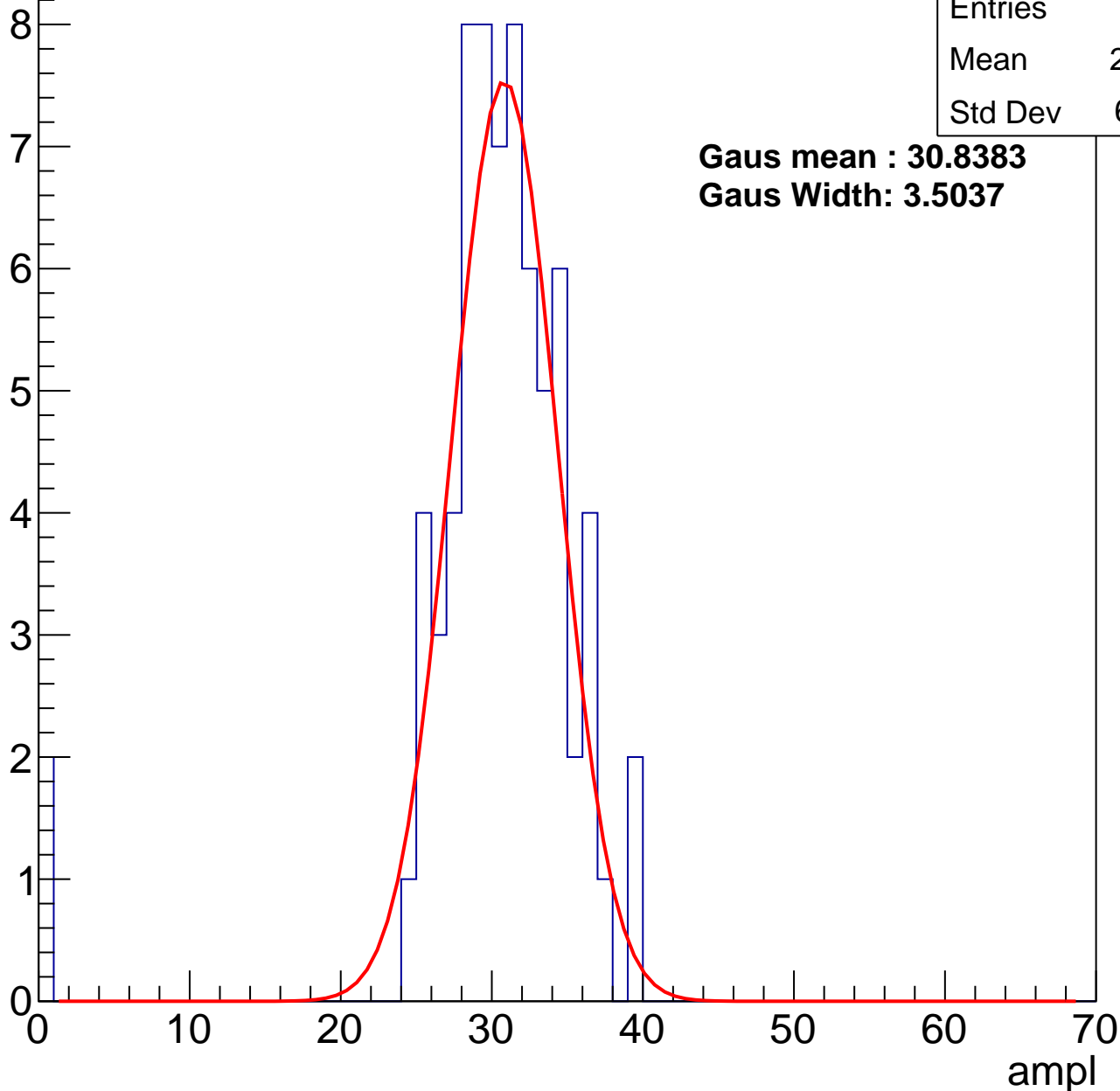
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.77 |
| Std Dev | 6.091 |

Gaus mean : 30.8383

Gaus Width: 3.5037



B1L100S, U9-ch28, adc1

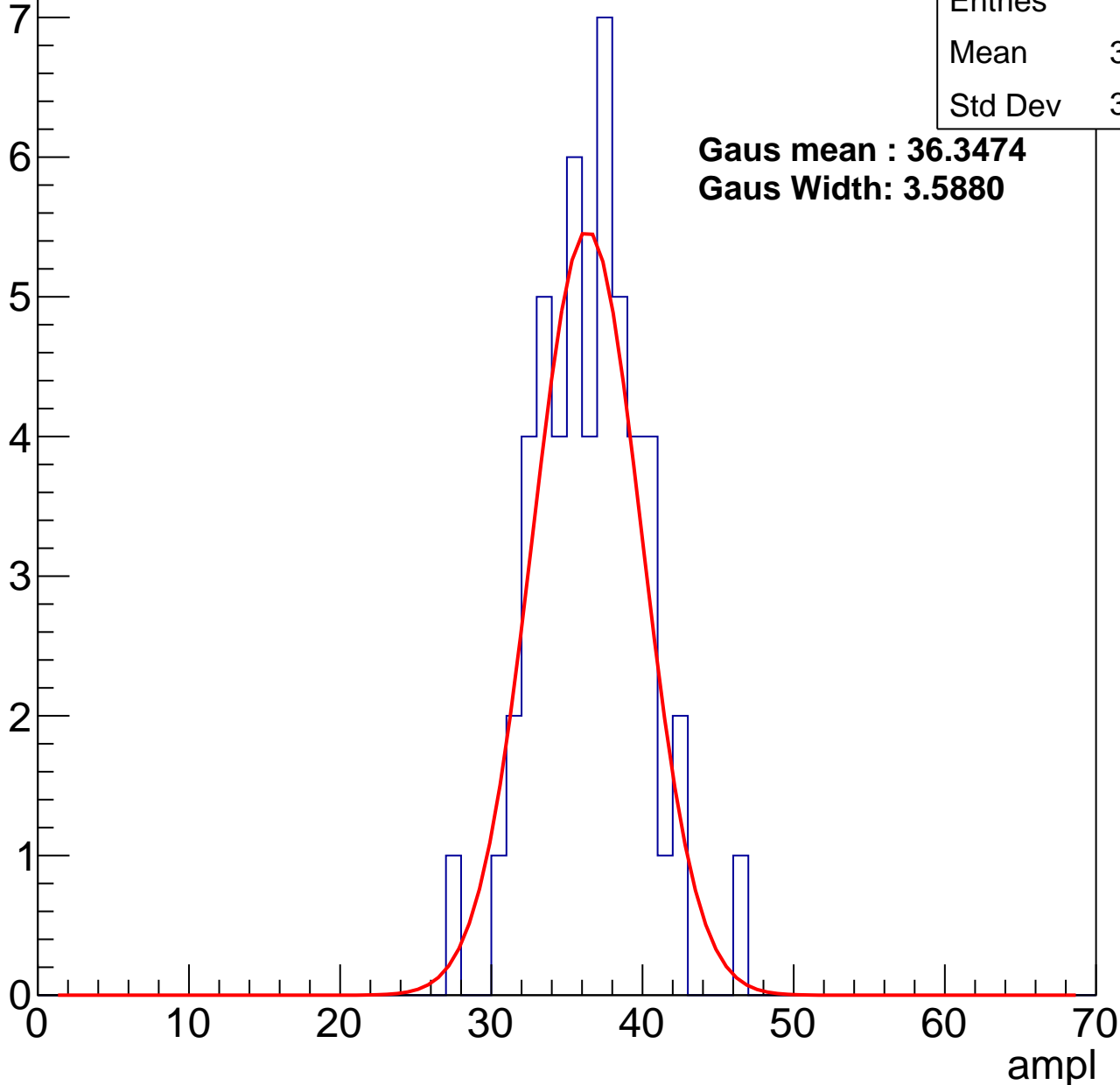
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 36.04 |
| Std Dev | 3.492 |

Gaus mean : 36.3474

Gaus Width: 3.5880



B1L100S, U9-ch28, adc2

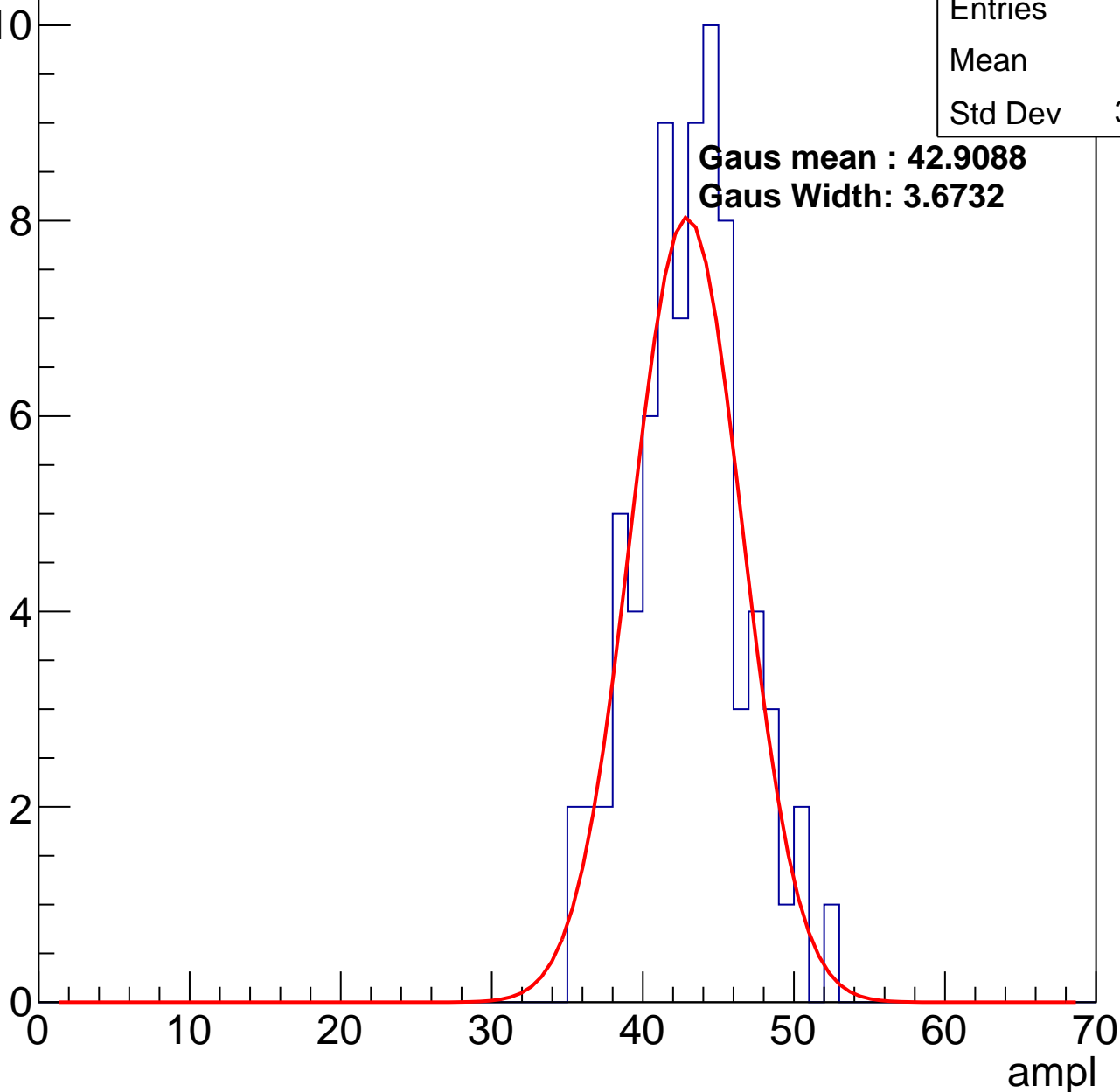
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.6 |
| Std Dev | 3.571 |

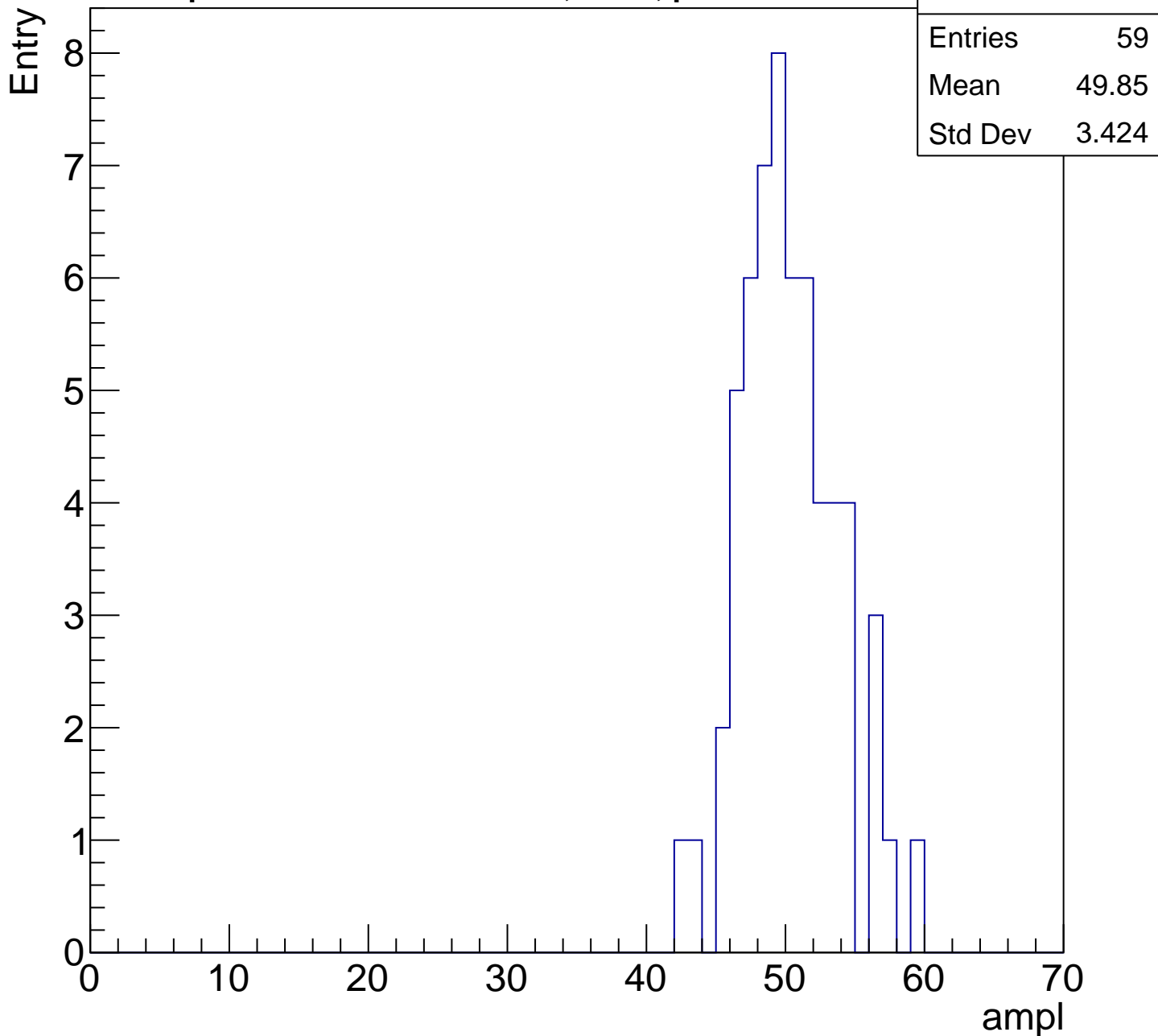
Gaus mean : 42.9088

Gaus Width: 3.6732



B1L100S, U9-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

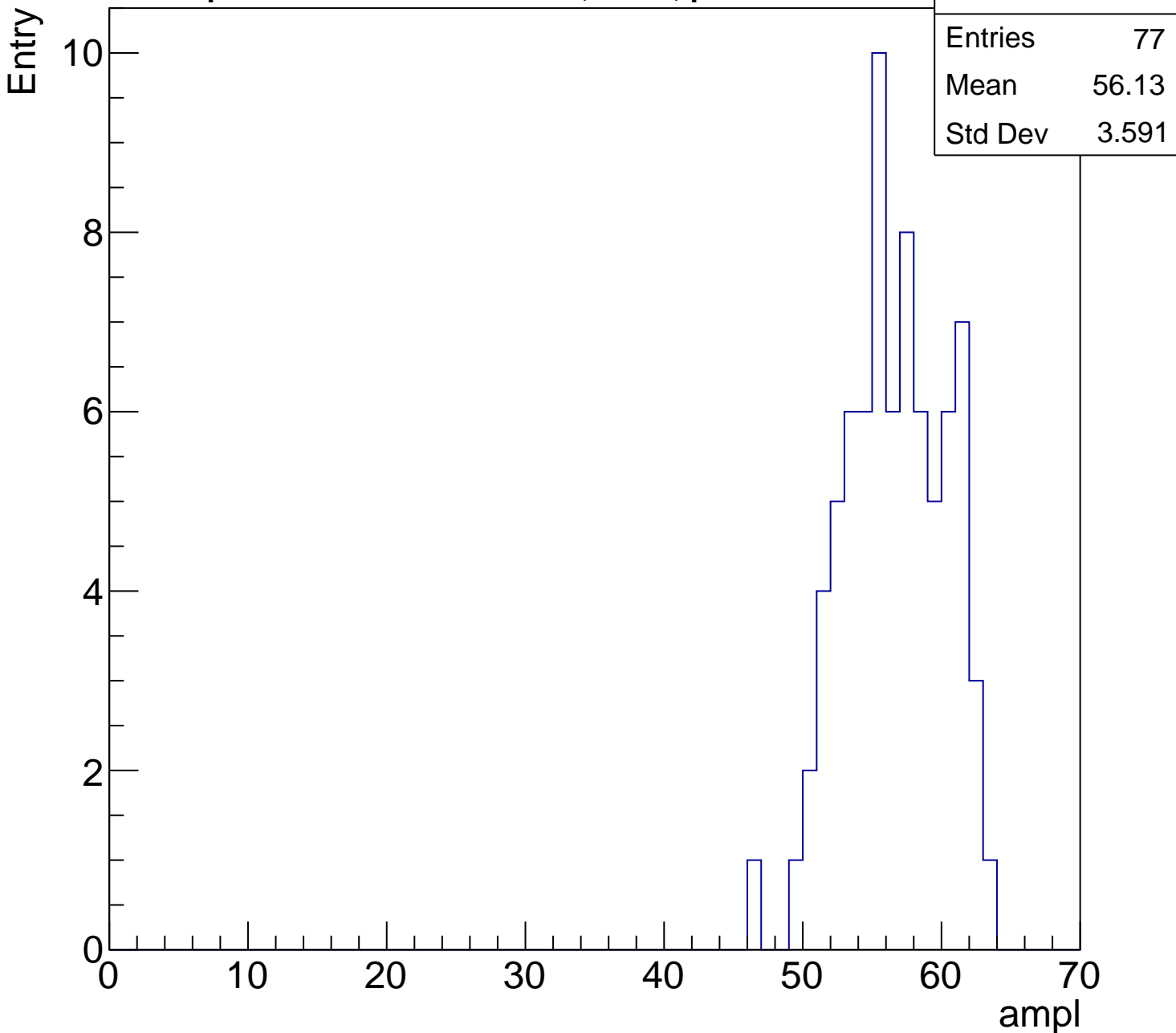
| | |
|---------|-------|
| Entries | 77 |
| Mean | 56.13 |
| Std Dev | 3.591 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

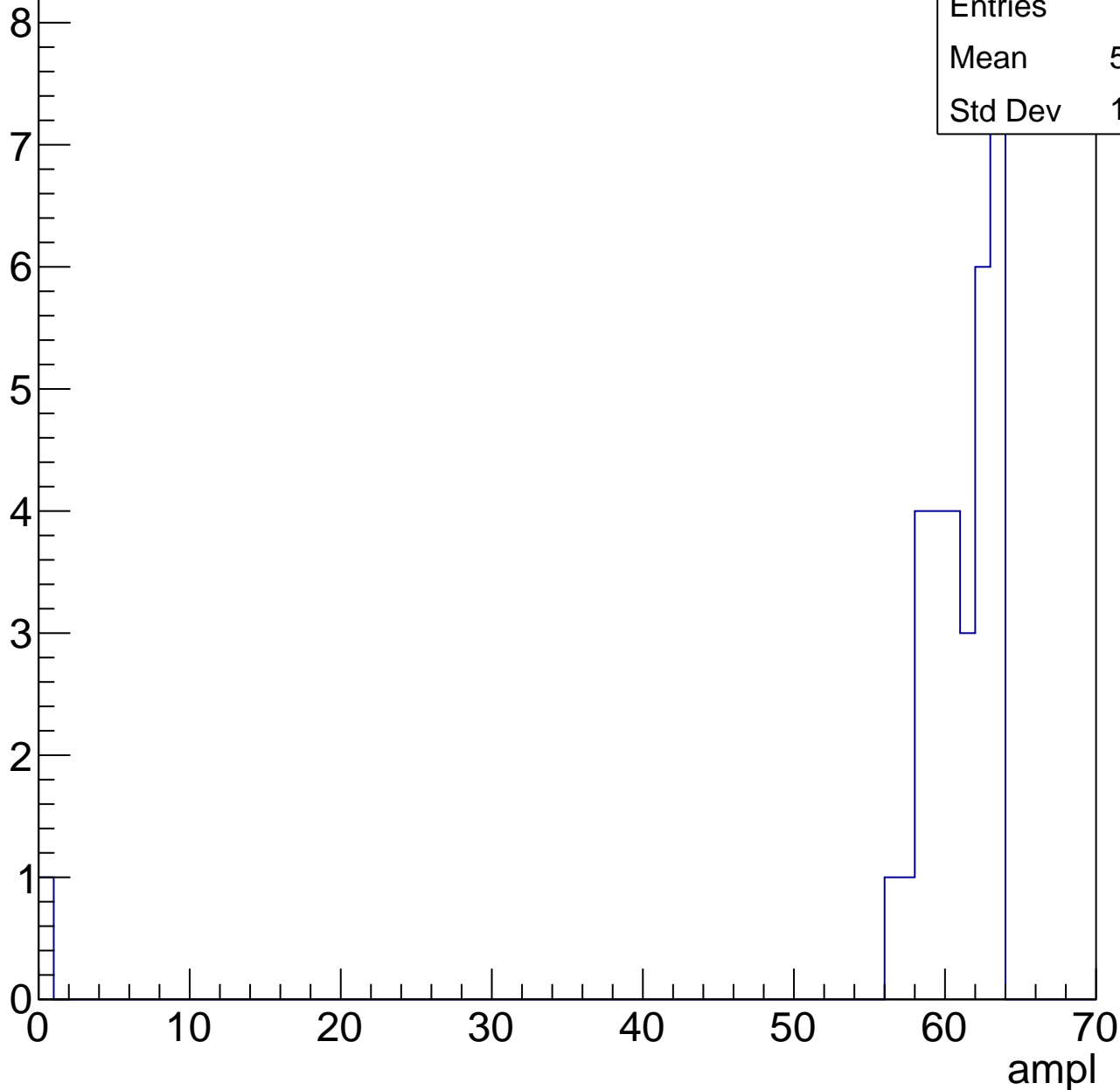


B1L100S, U9-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.75 |
| Std Dev | 10.74 |



B1L100S, U9-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

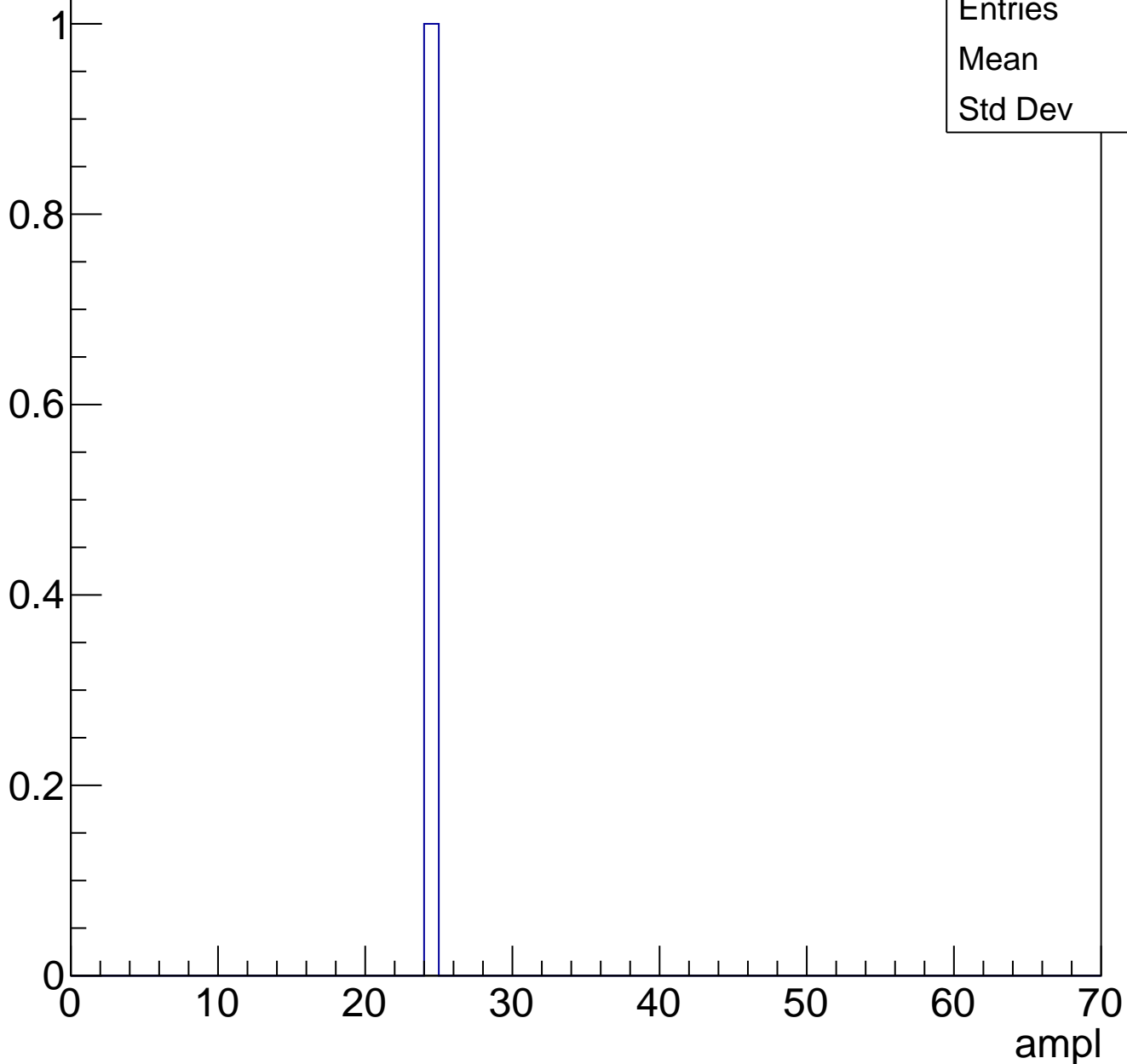
Entry



B1L100S, U9-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

B1L100S, U9-ch29, adc0

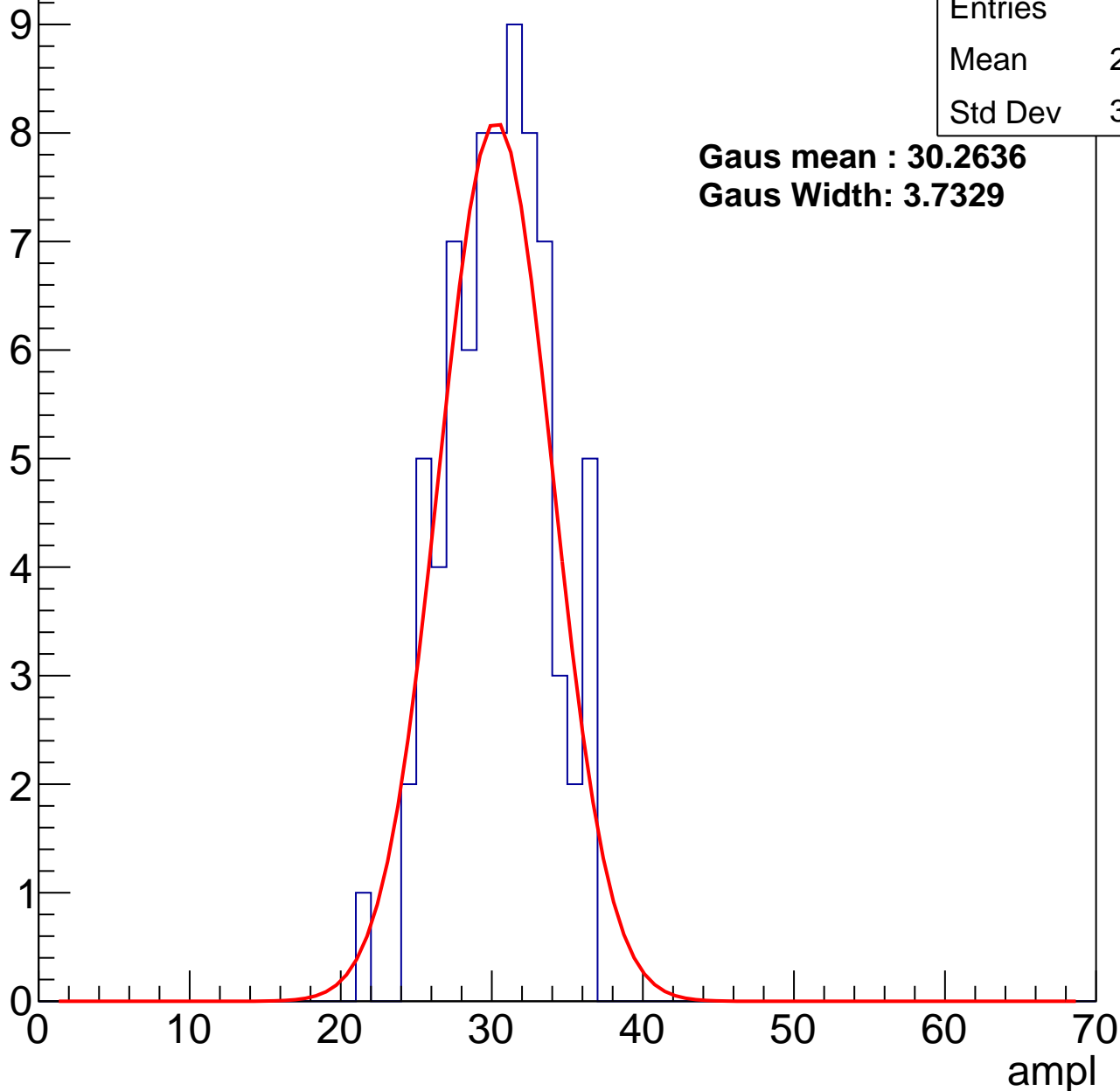
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.93 |
| Std Dev | 3.304 |

Gaus mean : 30.2636

Gaus Width: 3.7329



B1L100S, U9-ch29, adc1

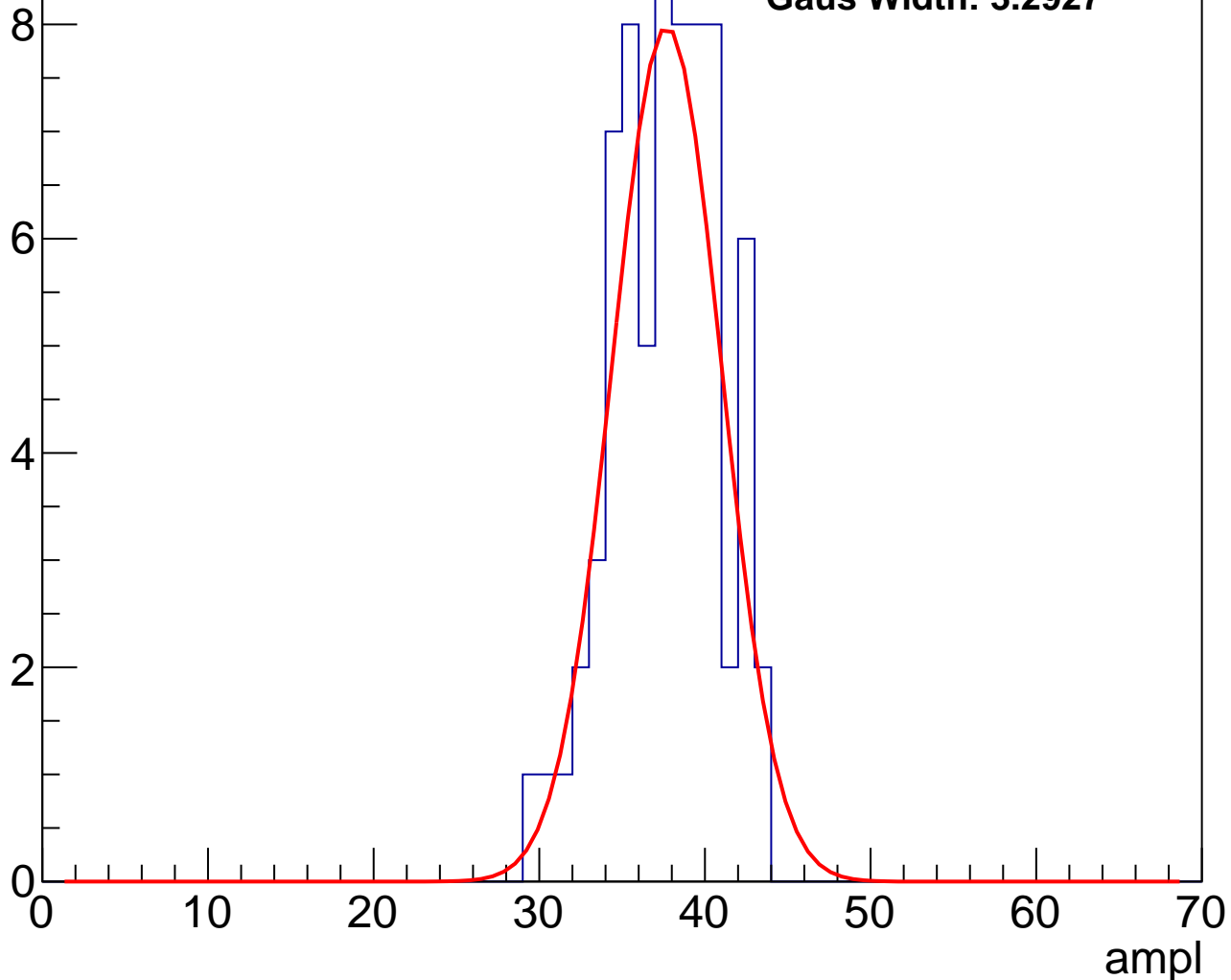
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.18 |
| Std Dev | 3.142 |

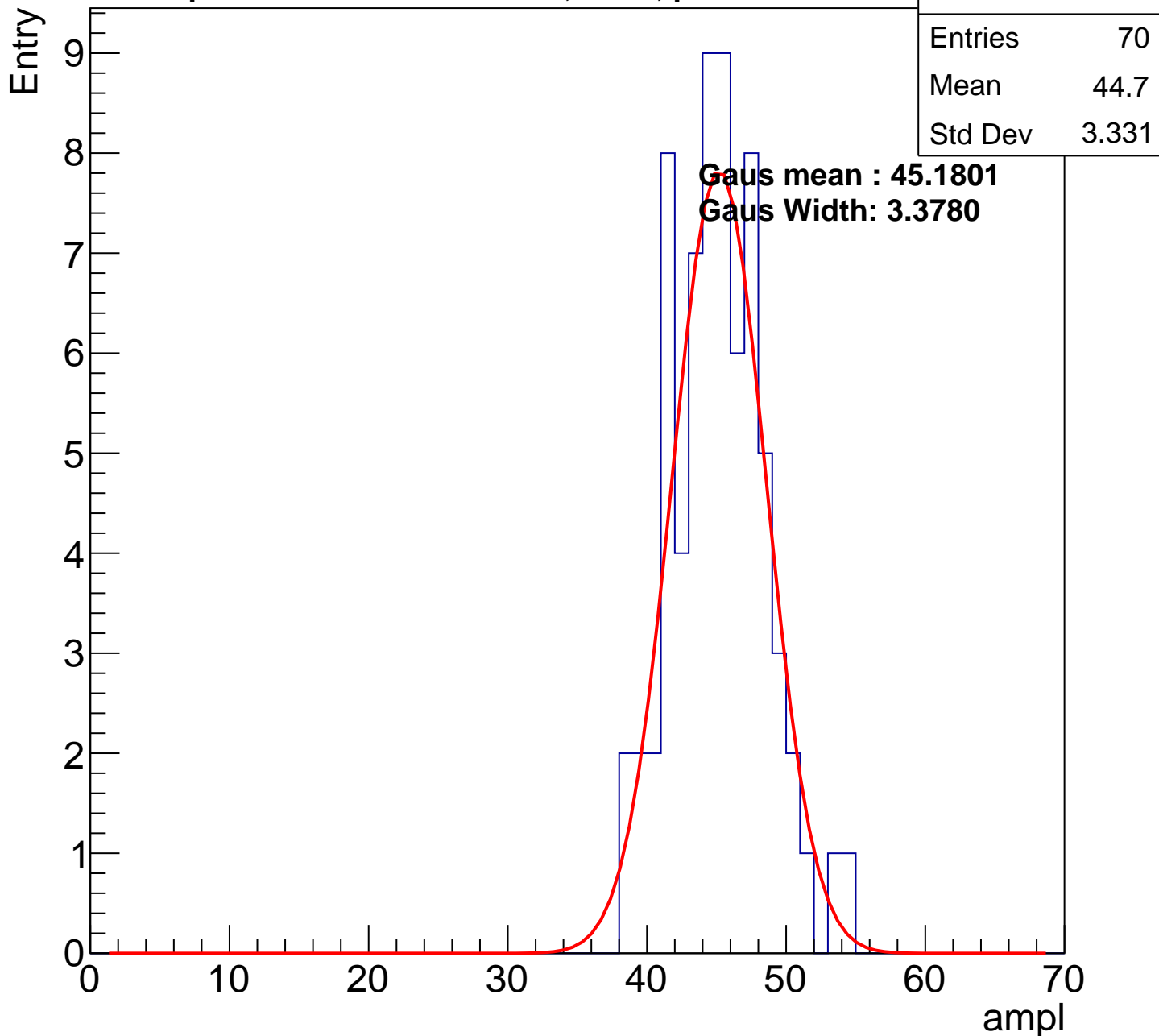
Gaus mean : 37.6949

Gaus Width: 3.2927



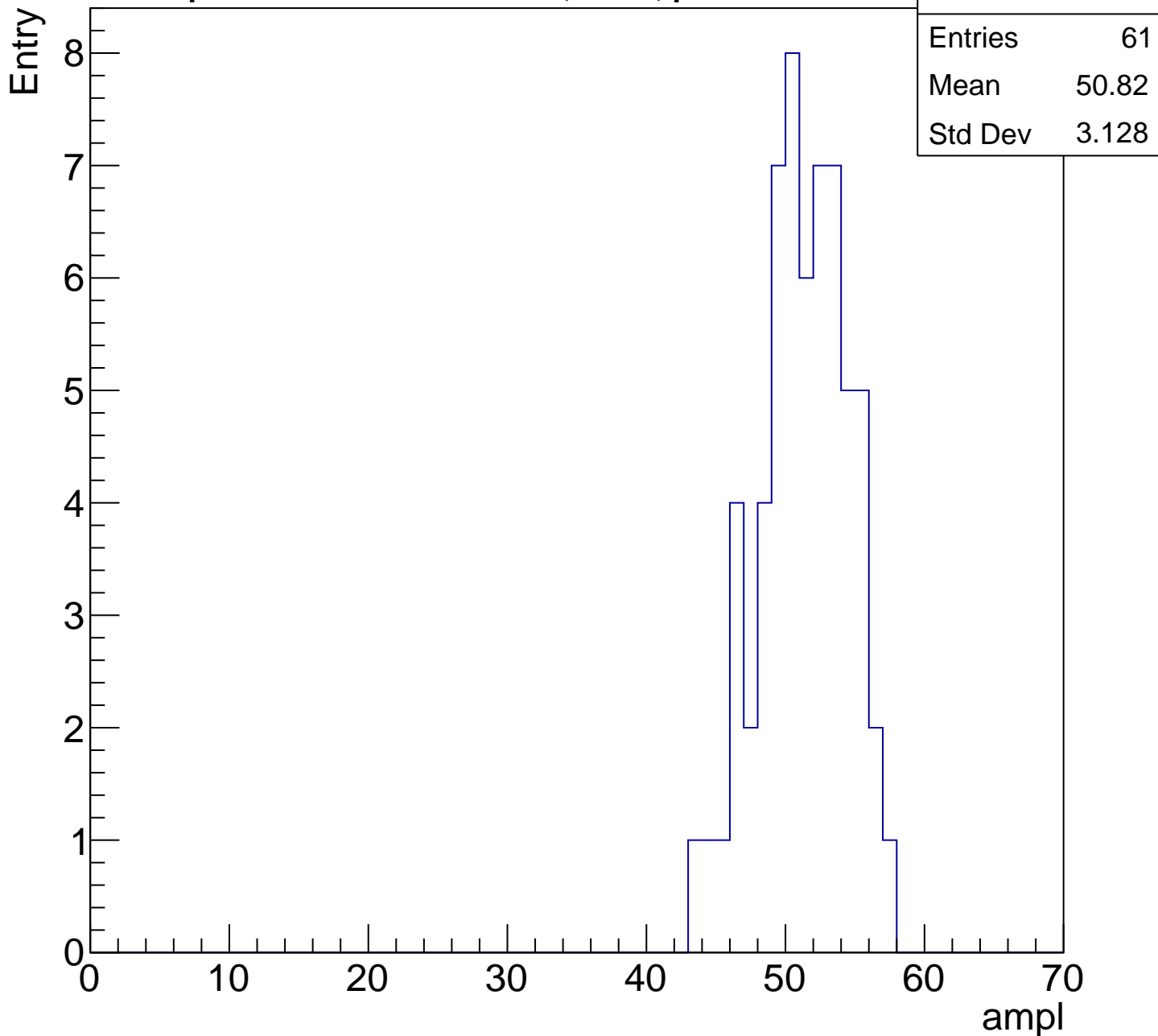
B1L100S, U9-ch29, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

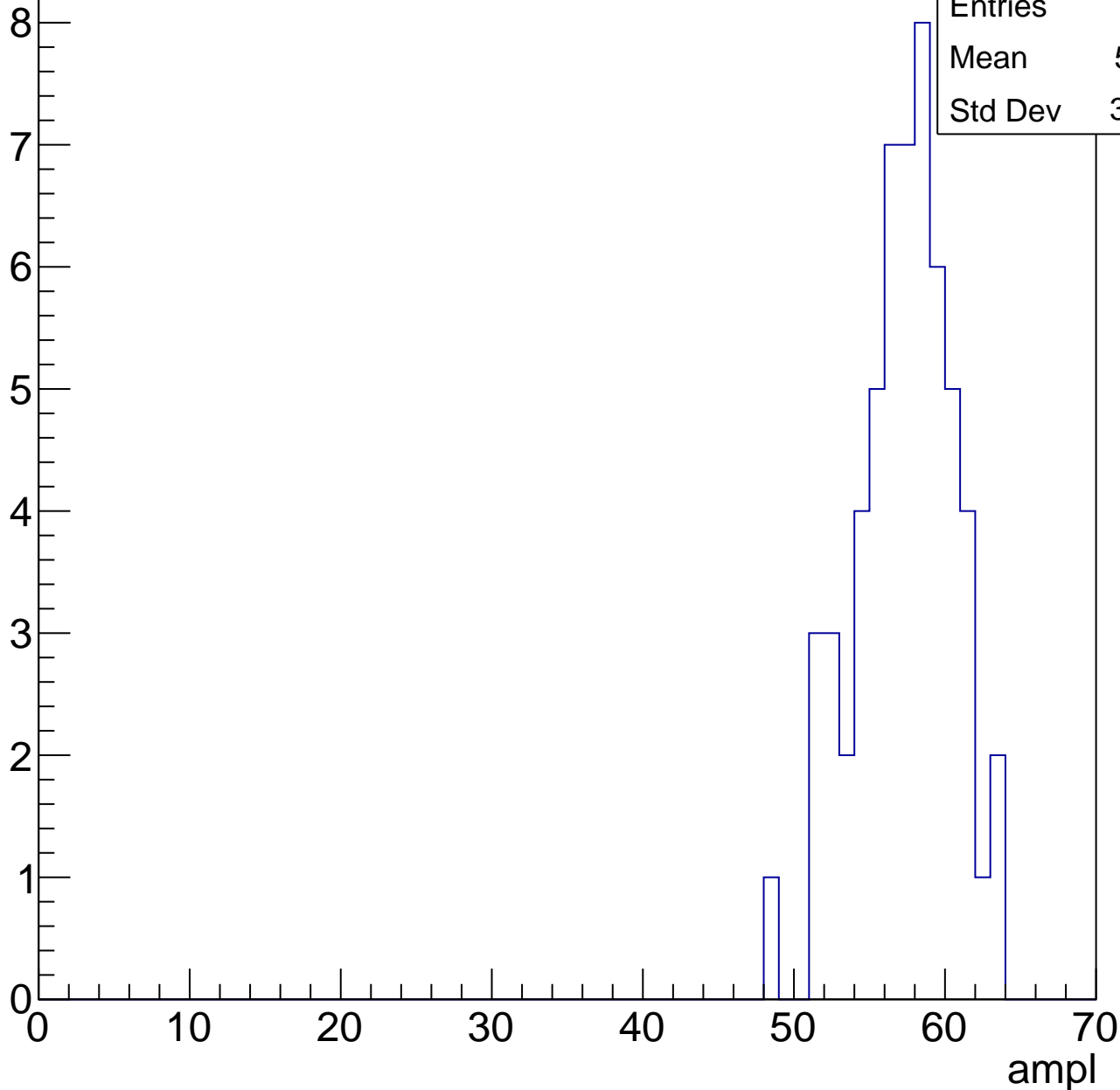


B1L100S, U9-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 56.81 |
| Std Dev | 3.203 |



B1L100S, U9-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 38 |
| Mean | 59.21 |
| Std Dev | 9.916 |

ampl

0

10

20

30

40

50

60

70

B1L100S, U9-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch30, adc0

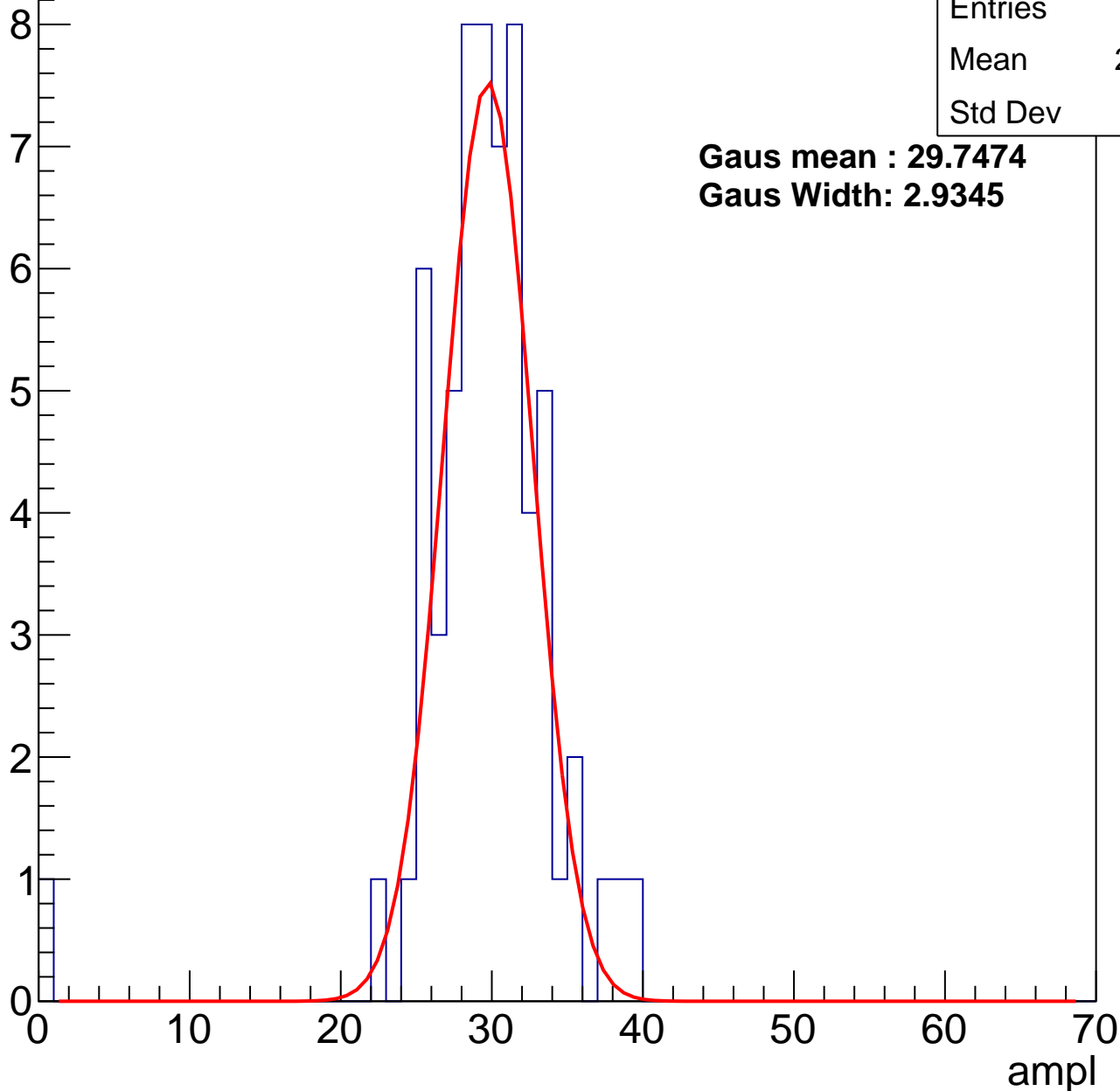
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 29.11 |
| Std Dev | 4.98 |

Gaus mean : 29.7474

Gaus Width: 2.9345



B1L100S, U9-ch30, adc1

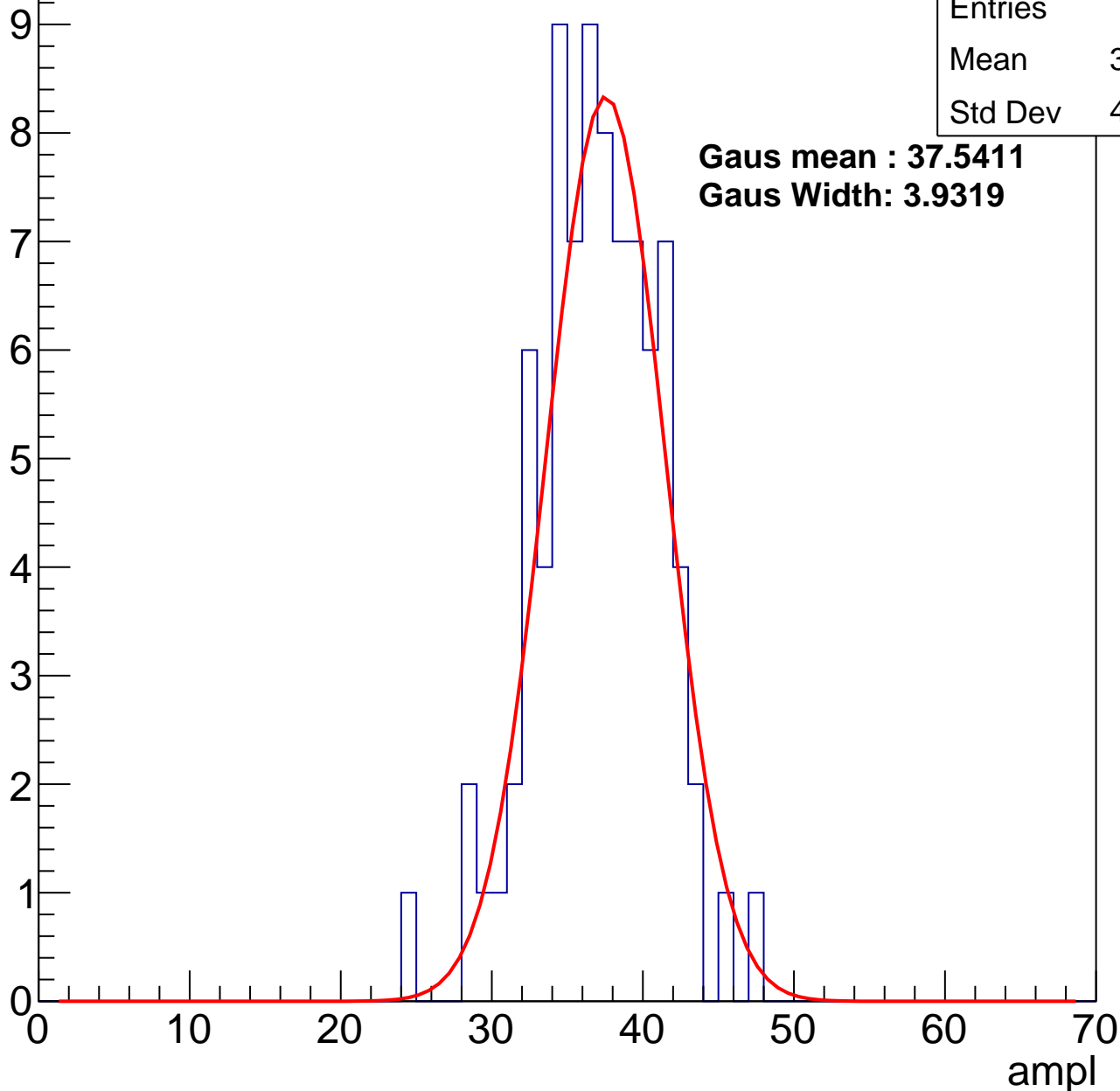
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 36.56 |
| Std Dev | 4.013 |

Gaus mean : 37.5411

Gaus Width: 3.9319



B1L100S, U9-ch30, adc2

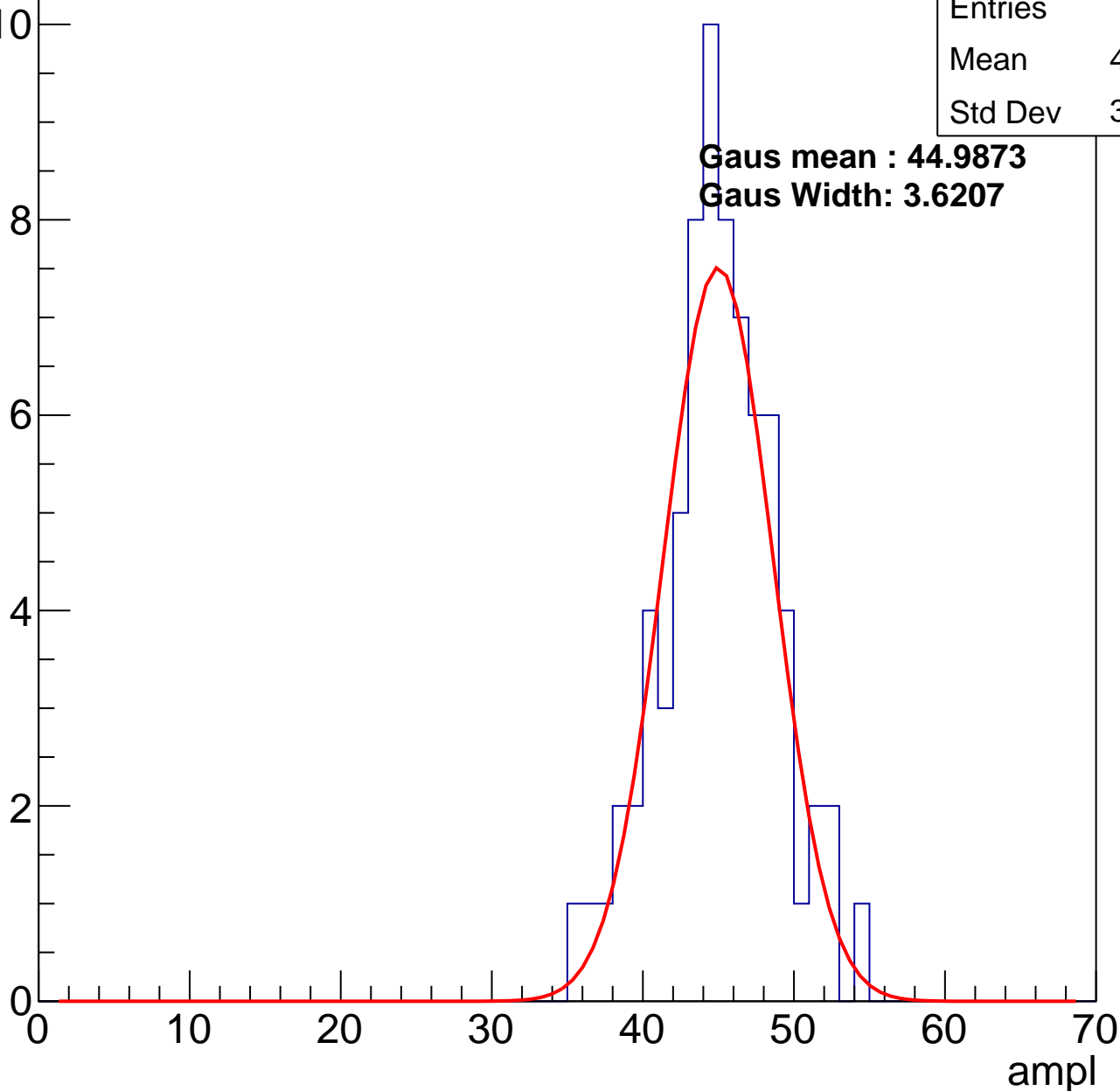
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 44.55 |
| Std Dev | 3.785 |

Gaus mean : 44.9873

Gaus Width: 3.6207

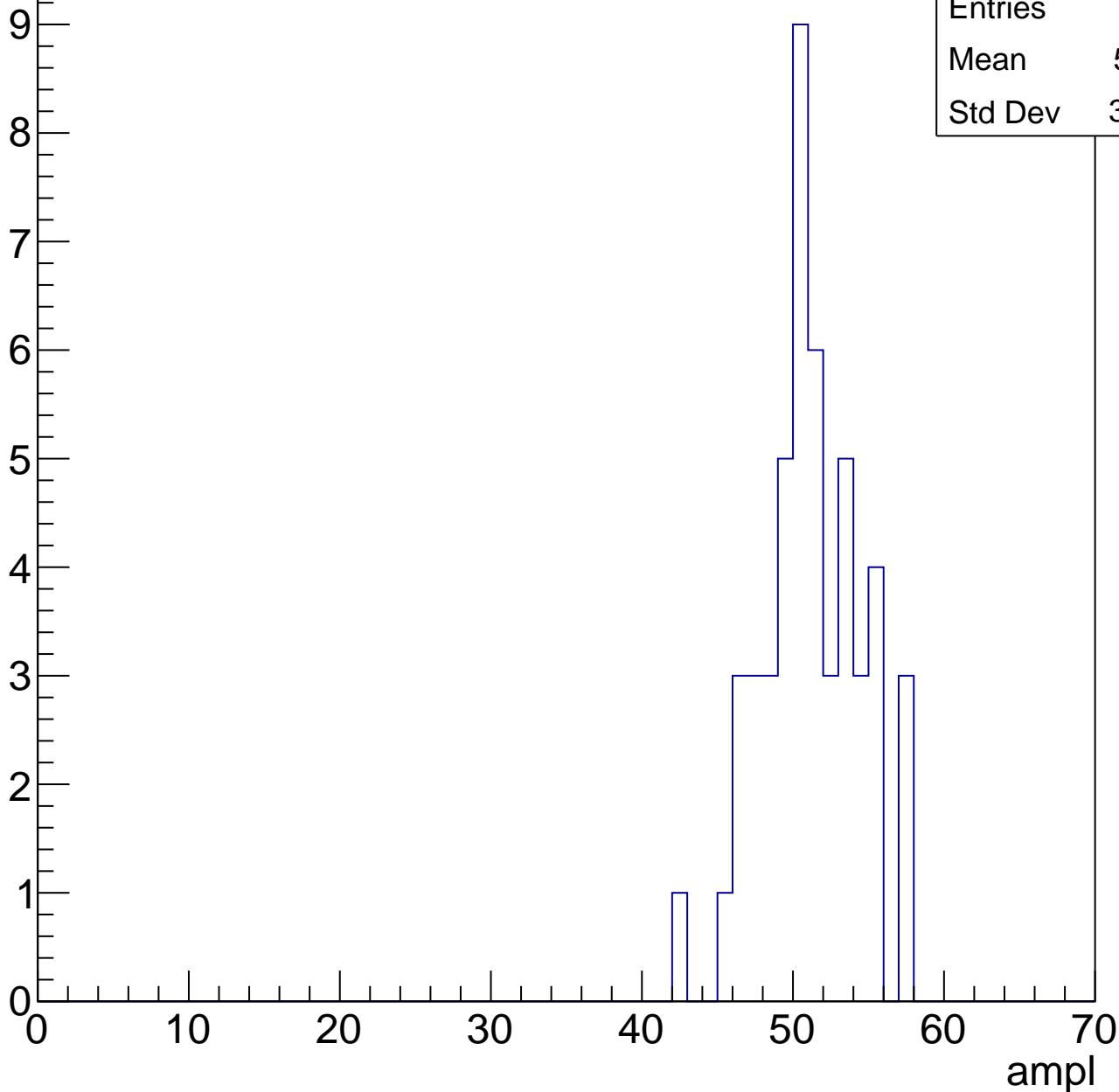


B1L100S, U9-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 50.71 |
| Std Dev | 3.239 |

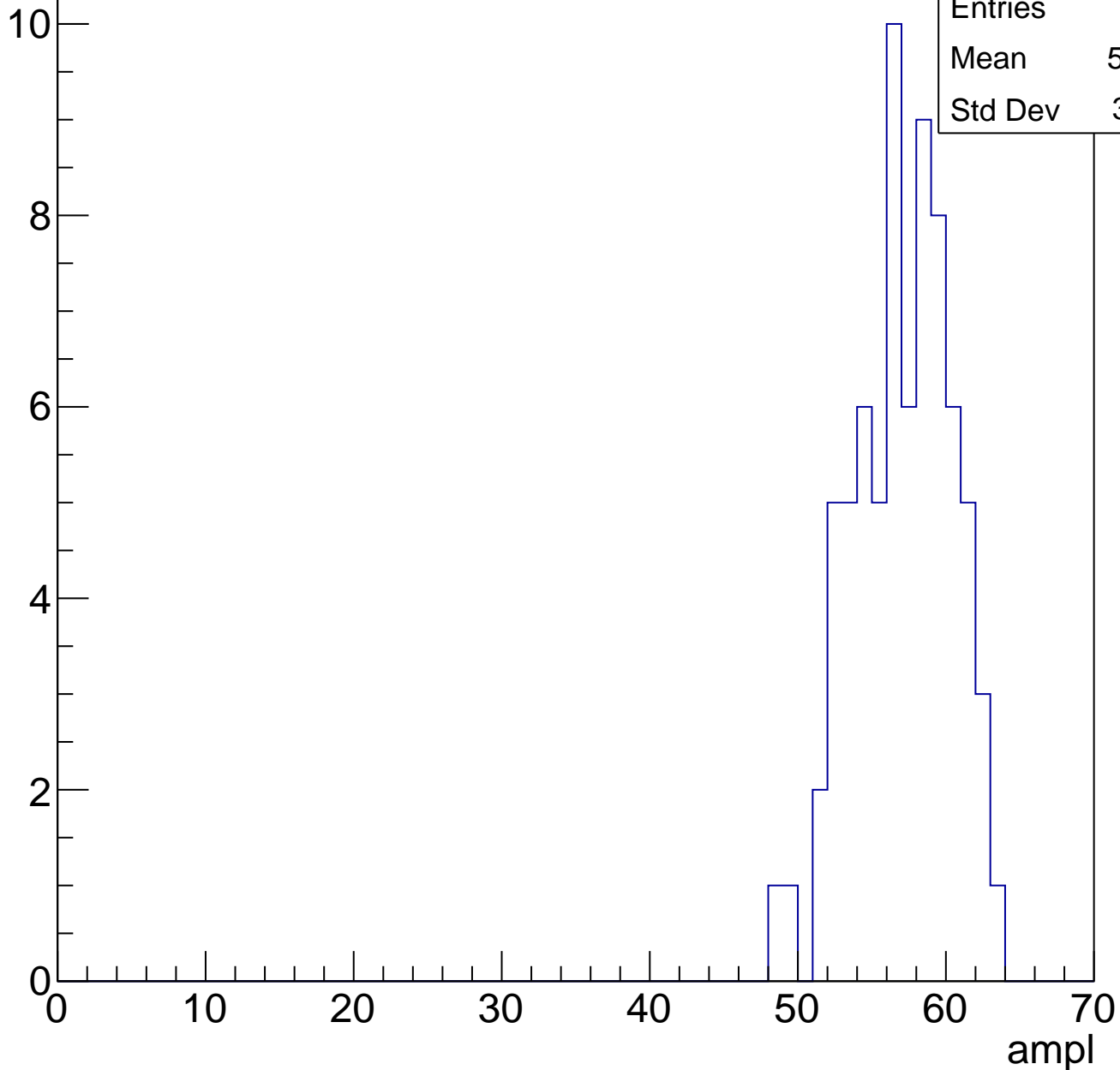


B1L100S, U9-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 56.62 |
| Std Dev | 3.271 |

Entry

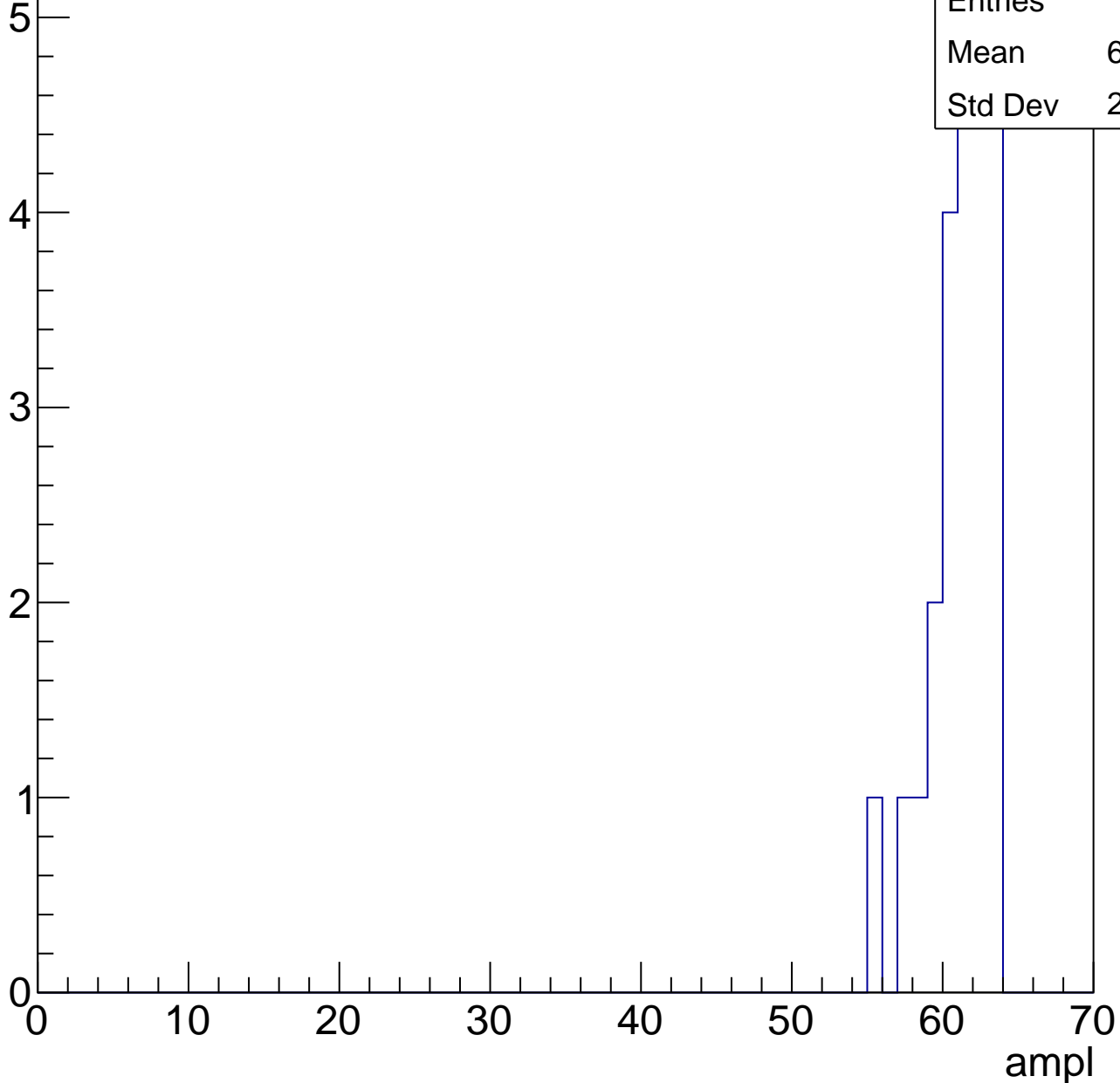


B1L100S, U9-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

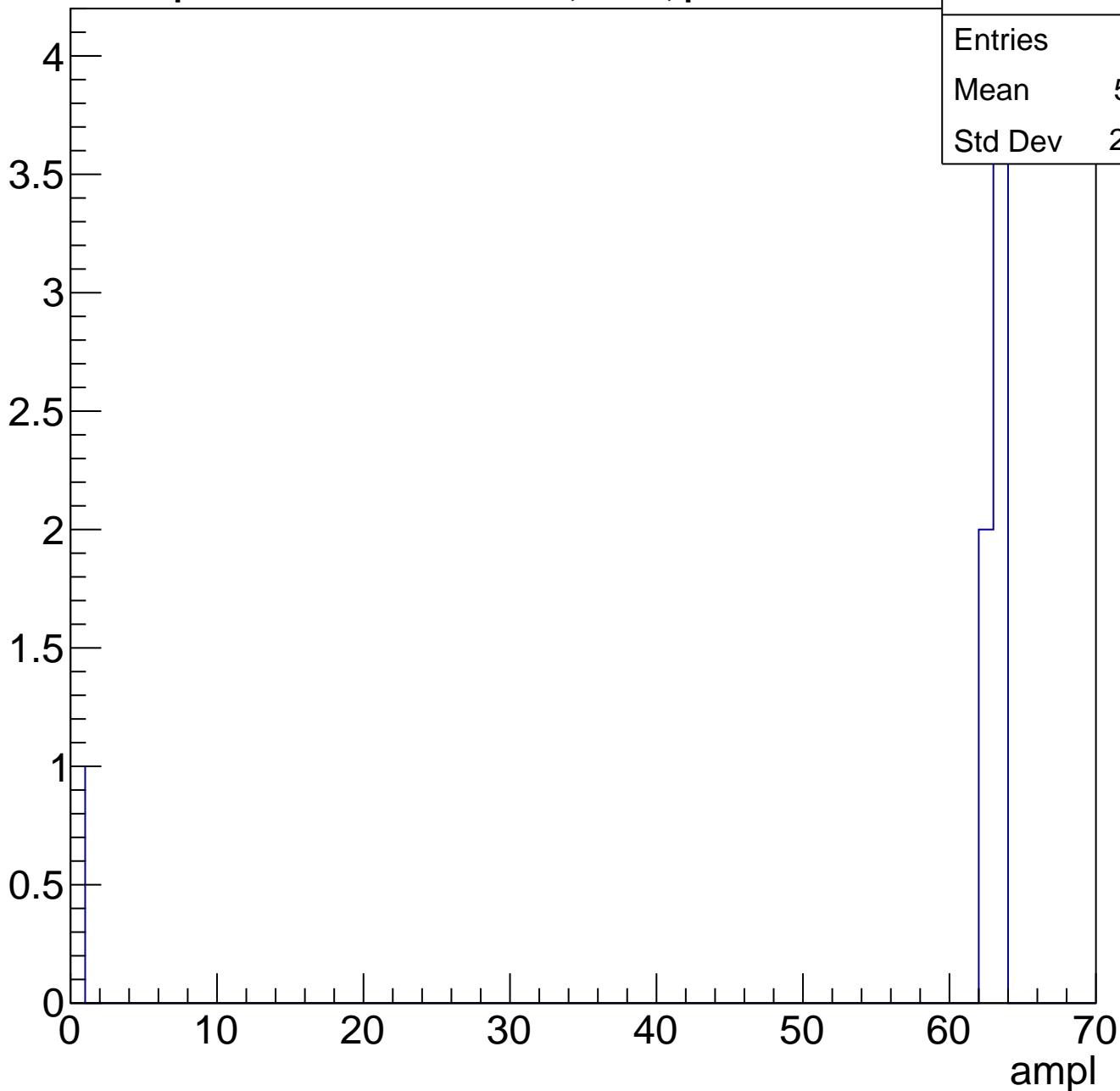
| | |
|---------|-------|
| Entries | 24 |
| Mean | 60.75 |
| Std Dev | 2.005 |



B1L100S, U9-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch31, adc0

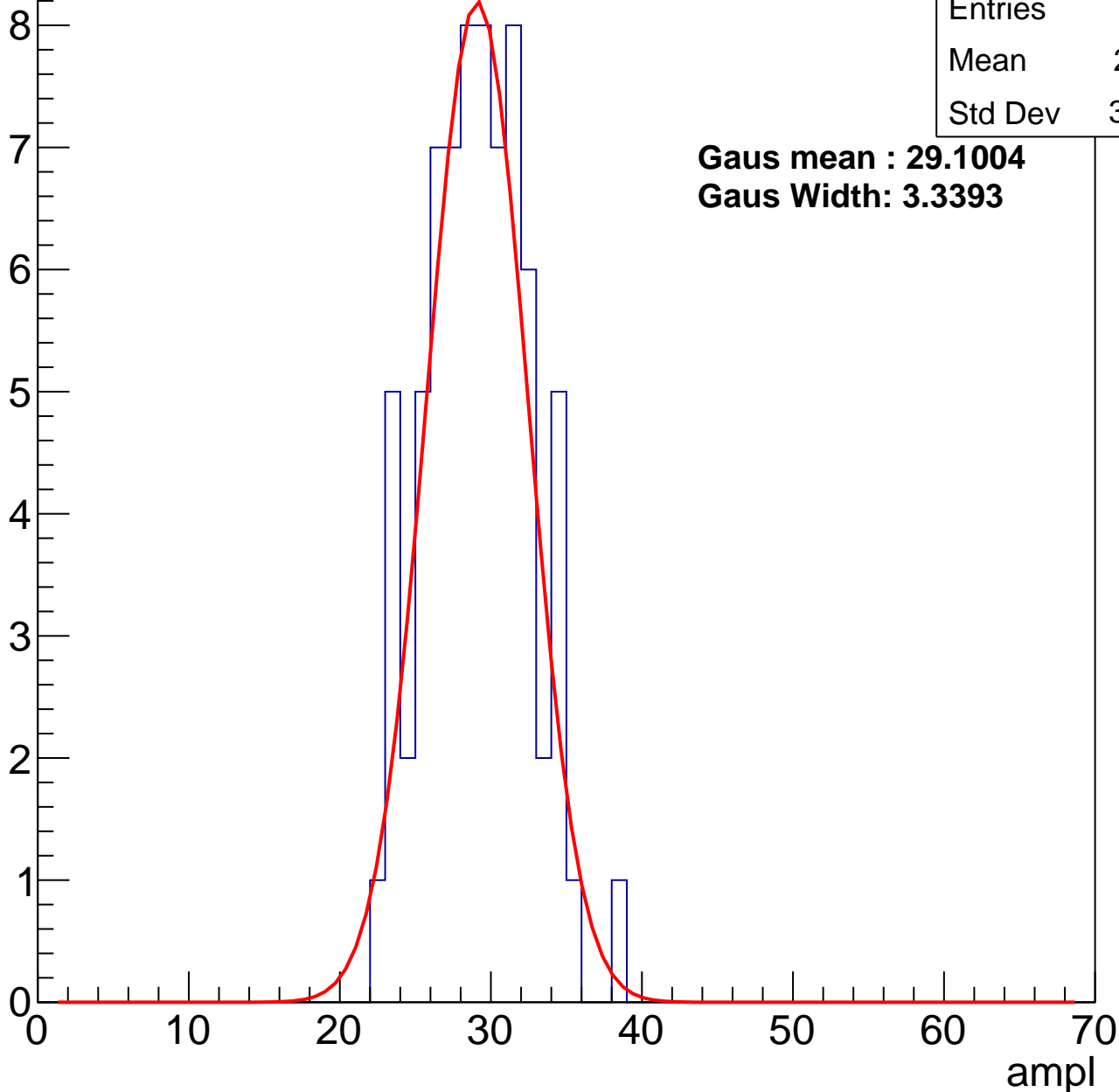
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 28.71 |
| Std Dev | 3.349 |

Gaus mean : 29.1004

Gaus Width: 3.3393



B1L100S, U9-ch31, adc1

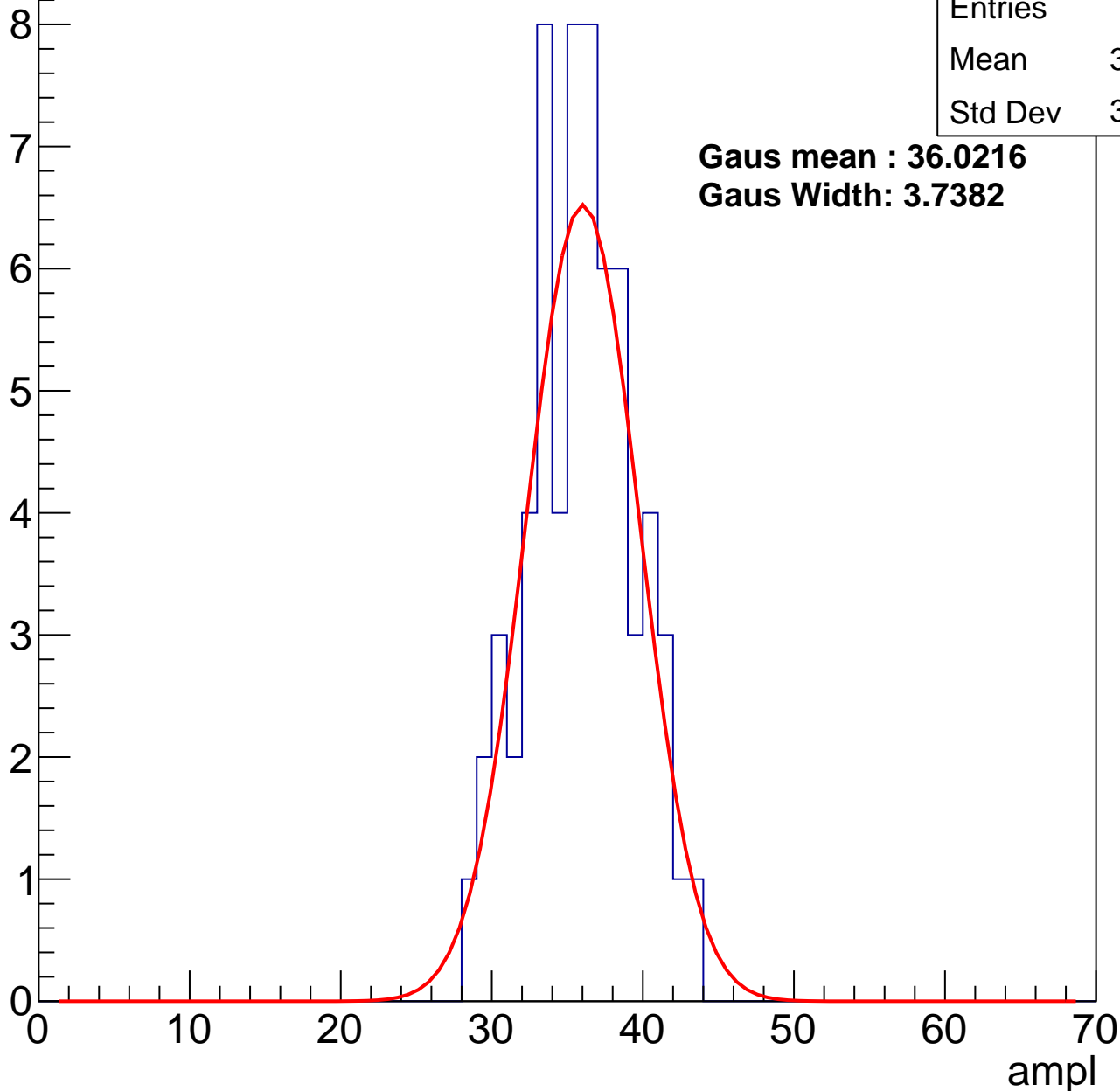
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.45 |
| Std Dev | 3.405 |

Gaus mean : 36.0216

Gaus Width: 3.7382



B1L100S, U9-ch31, adc2

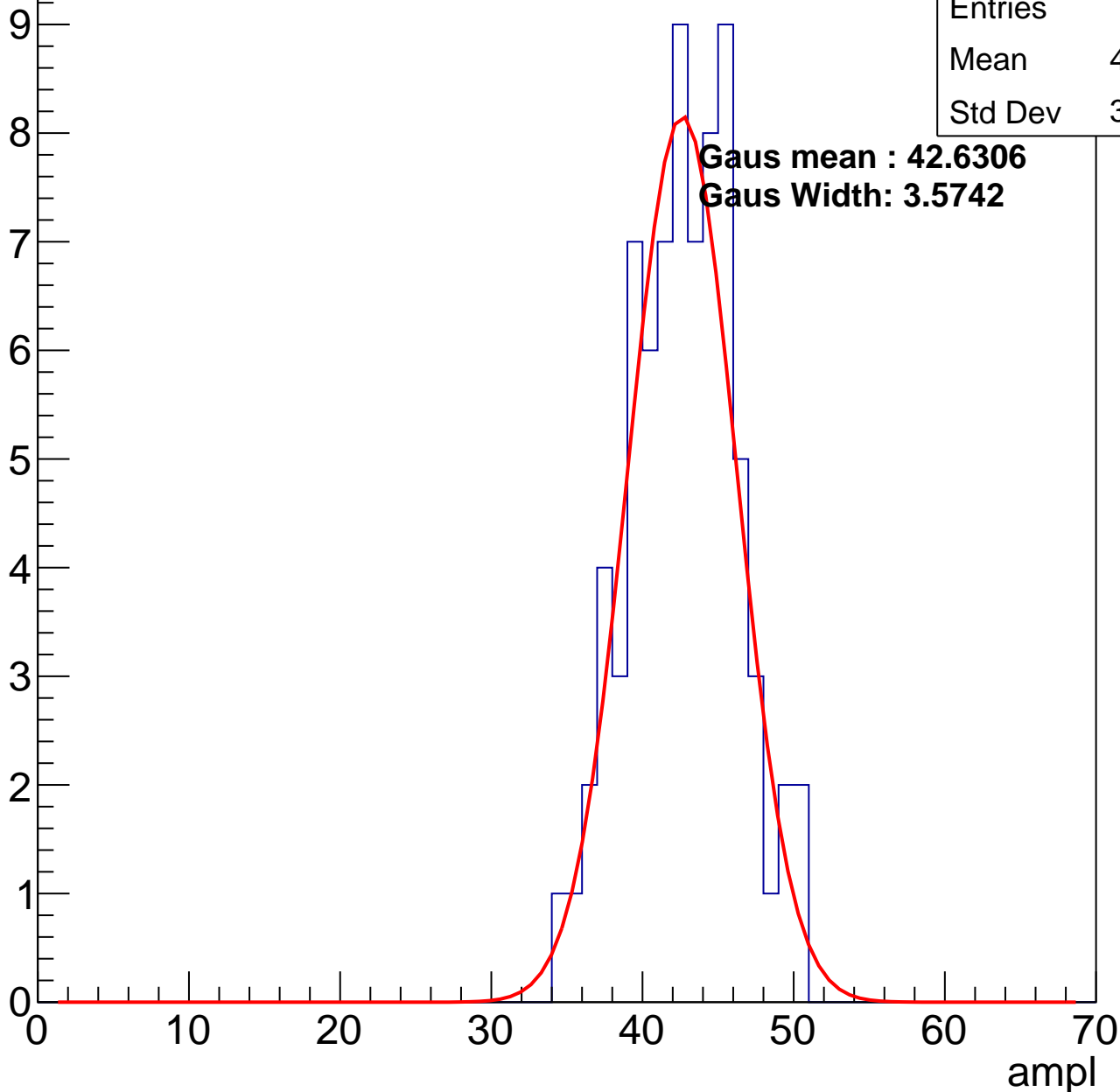
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 42.29 |
| Std Dev | 3.527 |

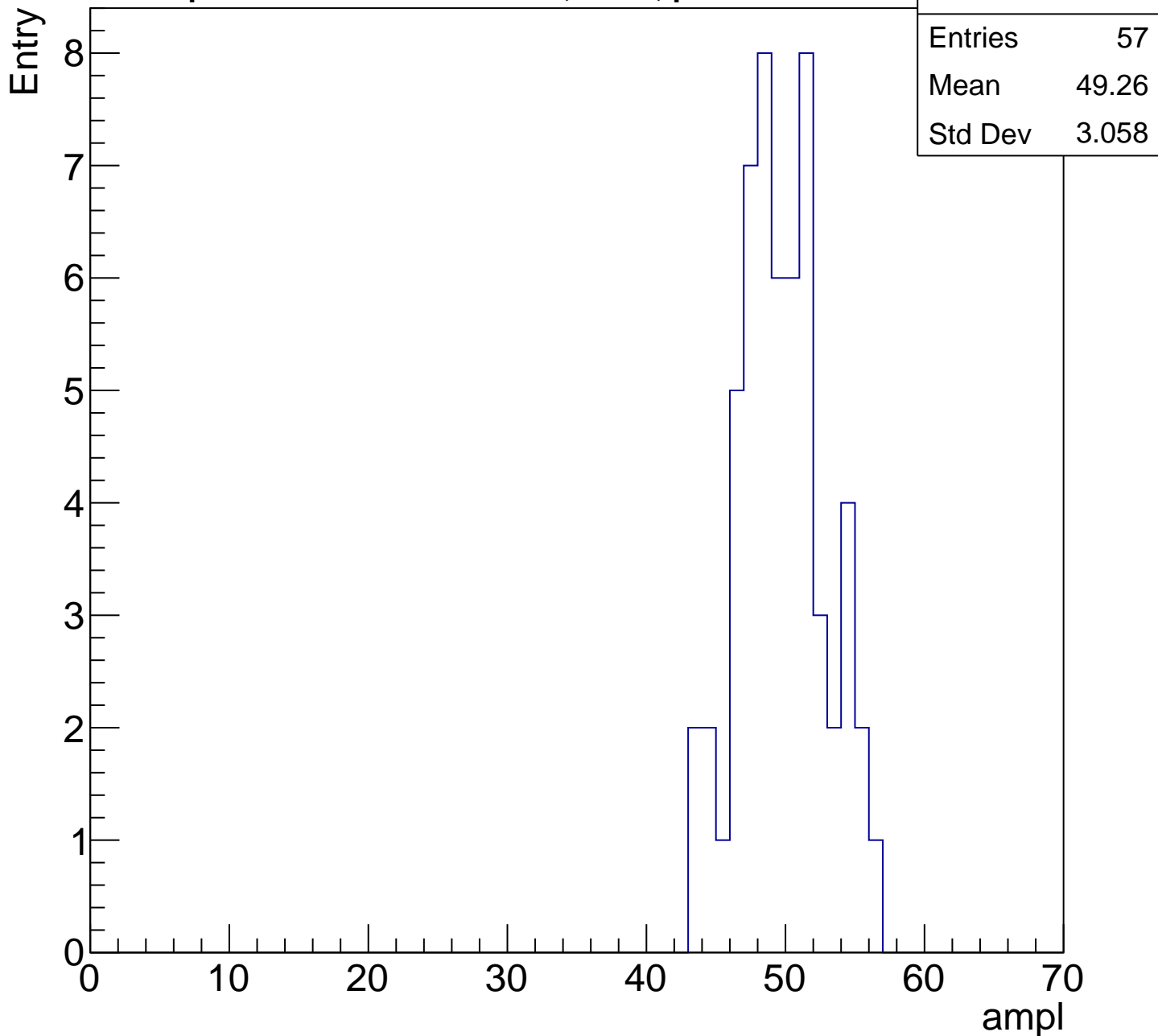
Gaus mean : 42.6306

Gaus Width: 3.5742



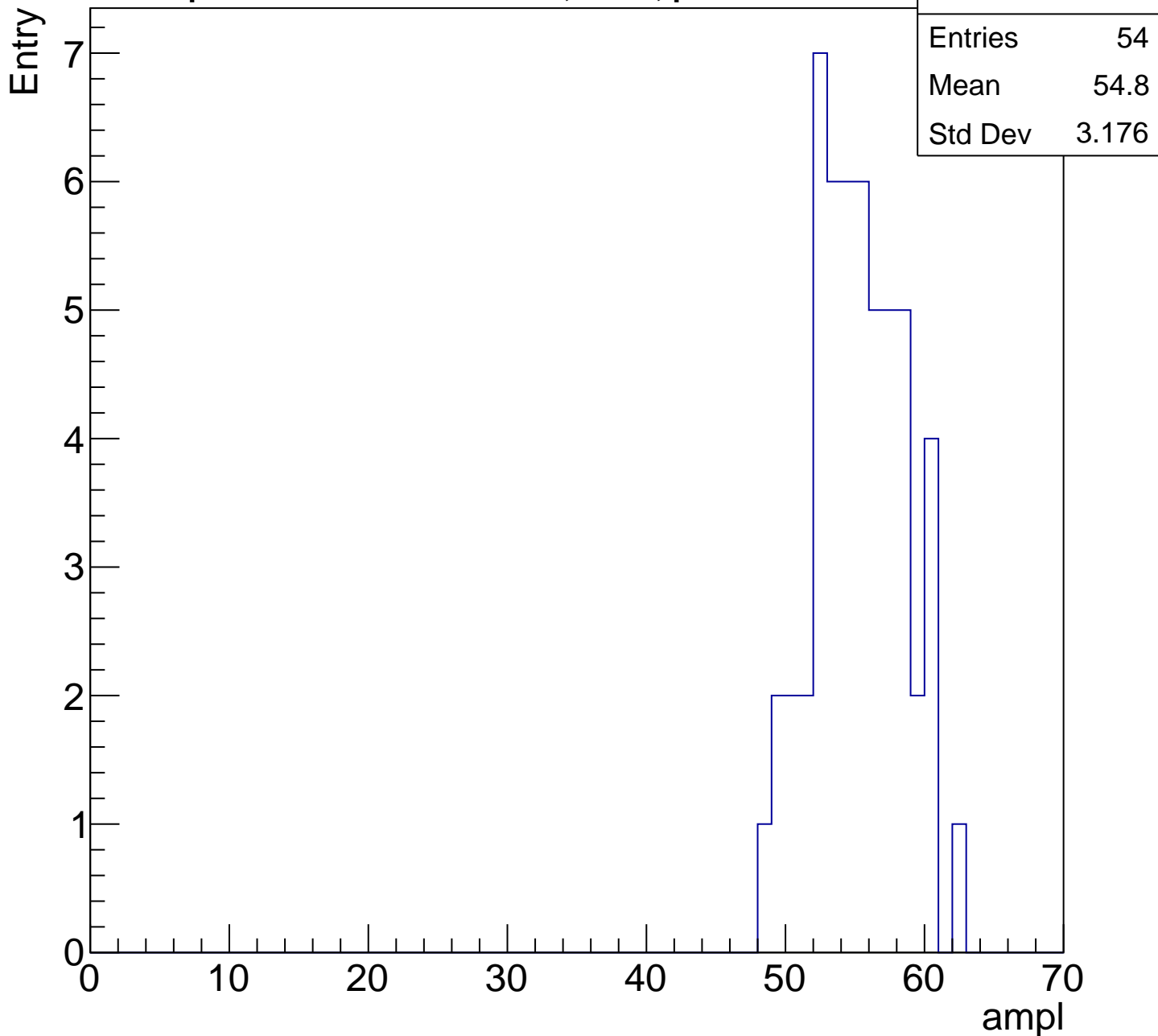
B1L100S, U9-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch31, adc5

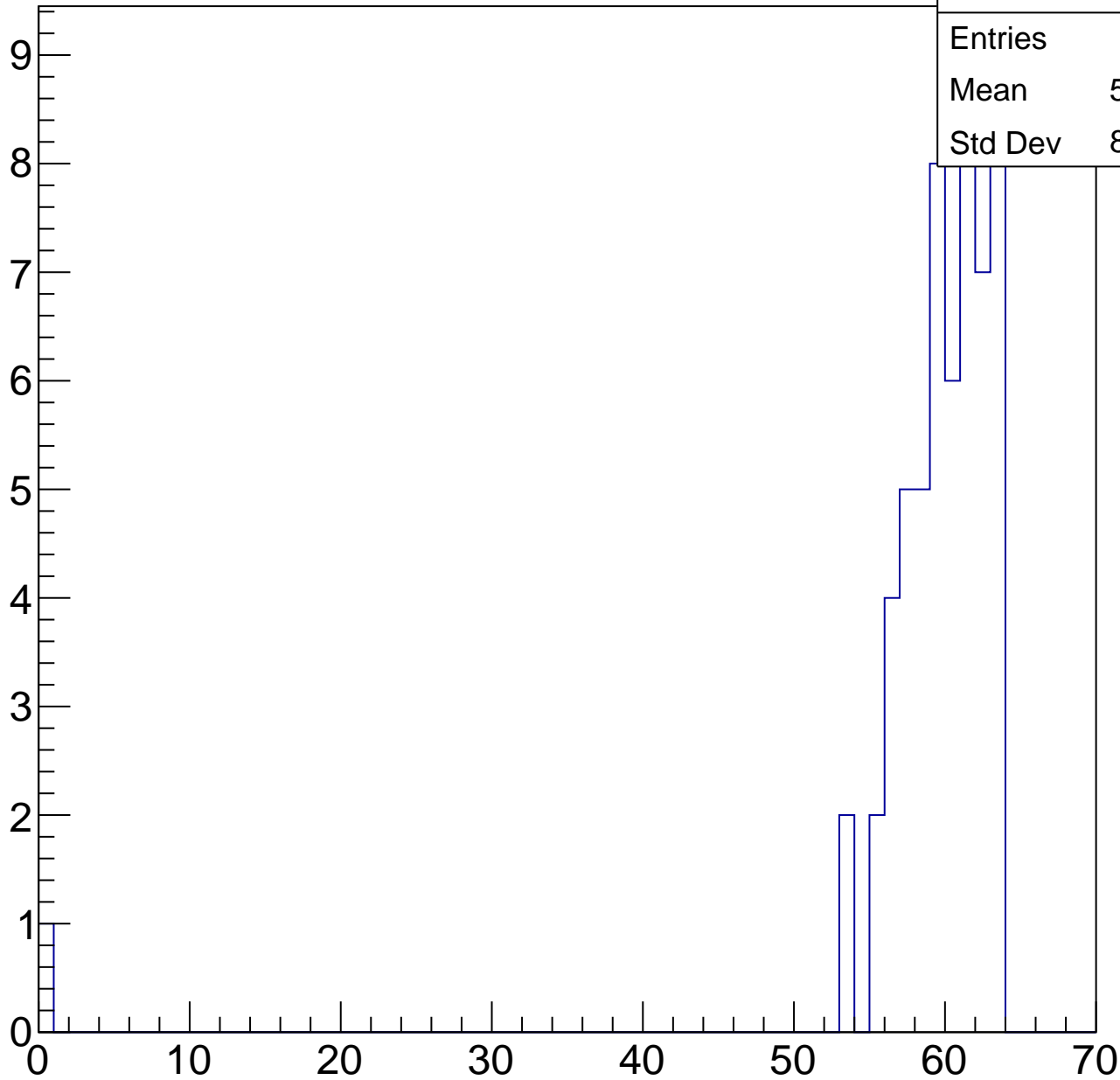
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 57 |
| Mean | 58.53 |
| Std Dev | 8.249 |

ampl



B1L100S, U9-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 61.8 |
| Std Dev | 0.7483 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch32, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 61 |
| Mean | 28.49 |
| Std Dev | 4.928 |

Gaus mean : 29.5017

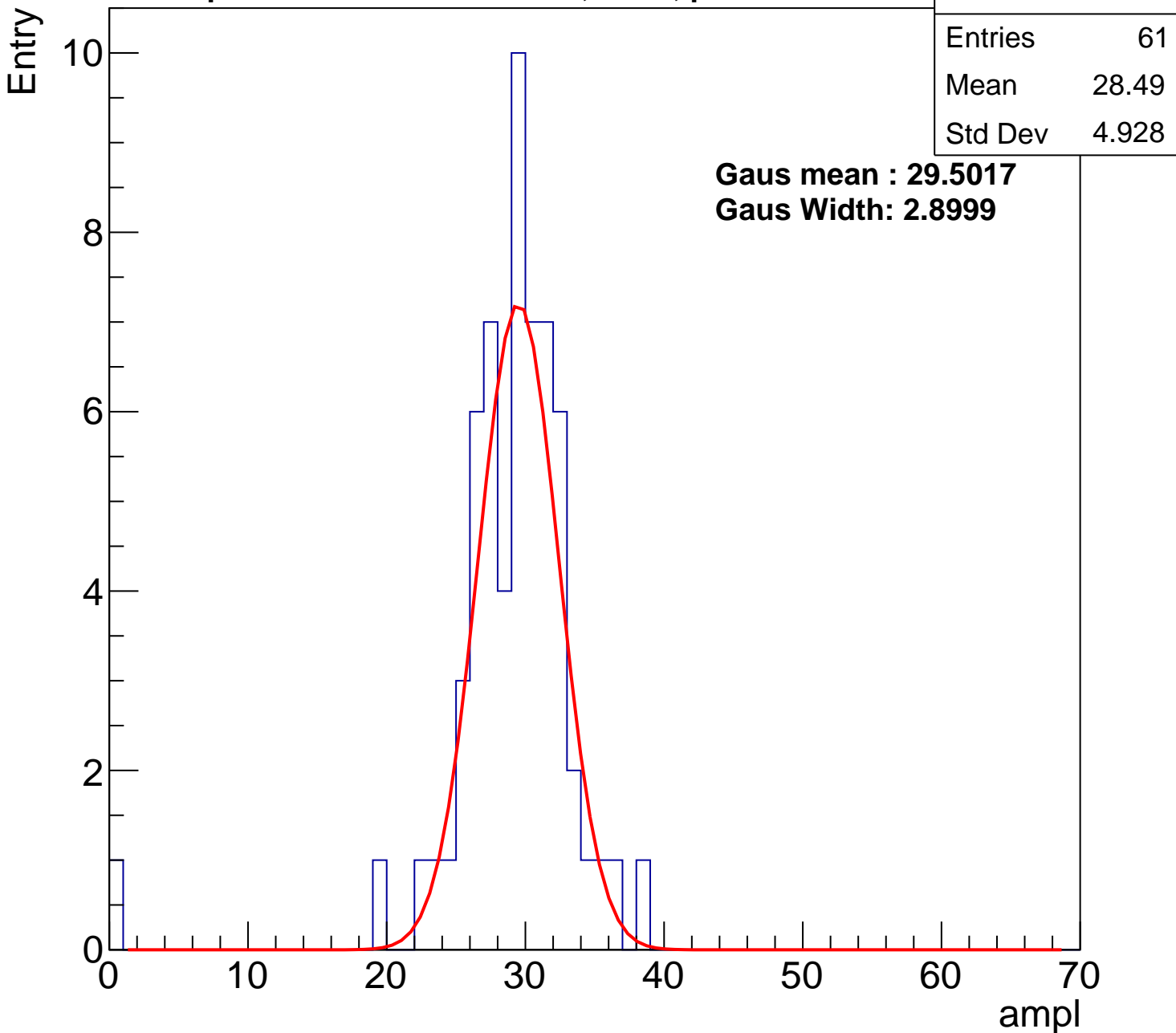
Gaus Width: 2.8999

Entry

10
8
6
4
2
0

ampl

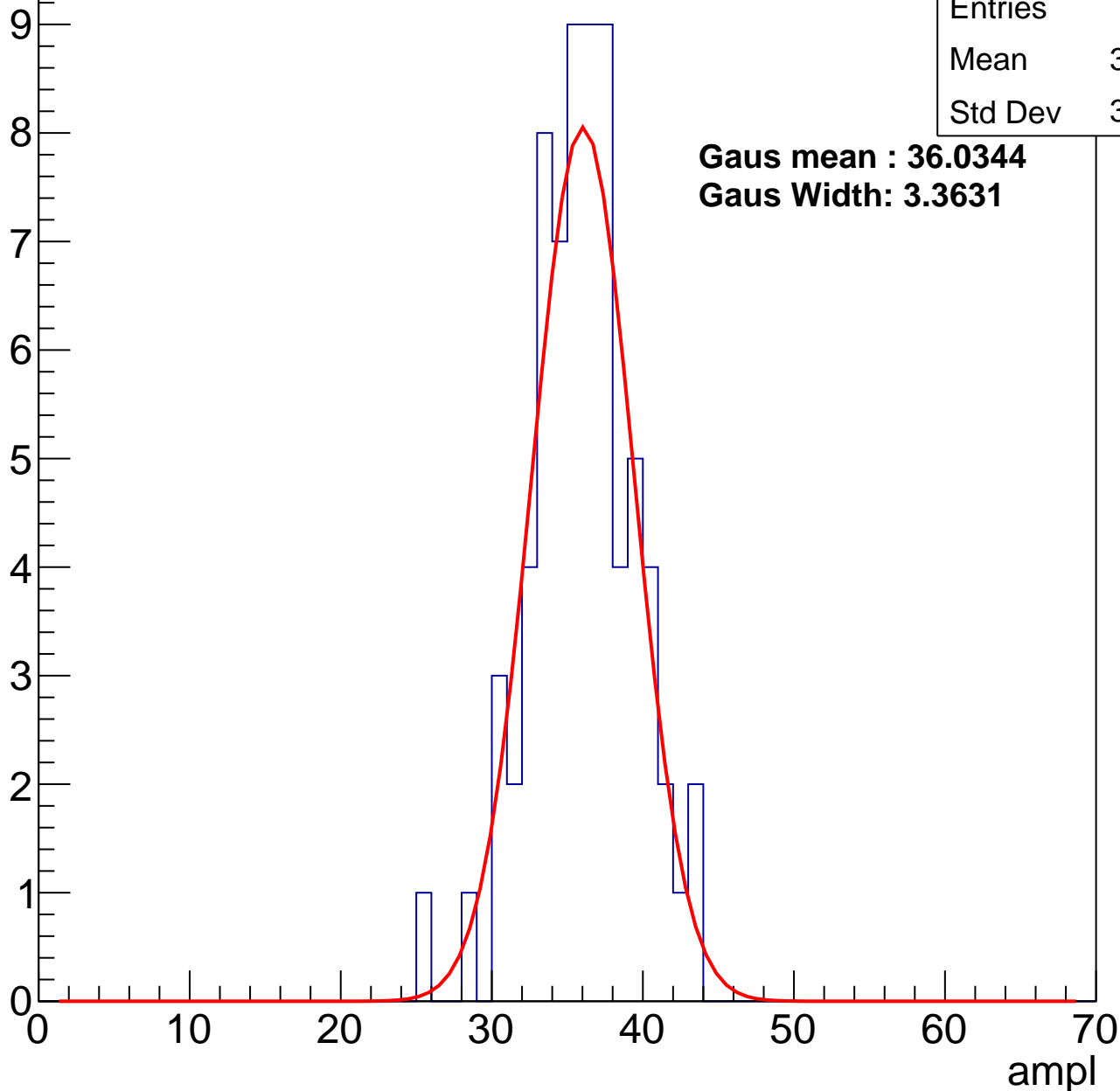
0 10 20 30 40 50 60 70



B1L100S, U9-ch32, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch32, adc2

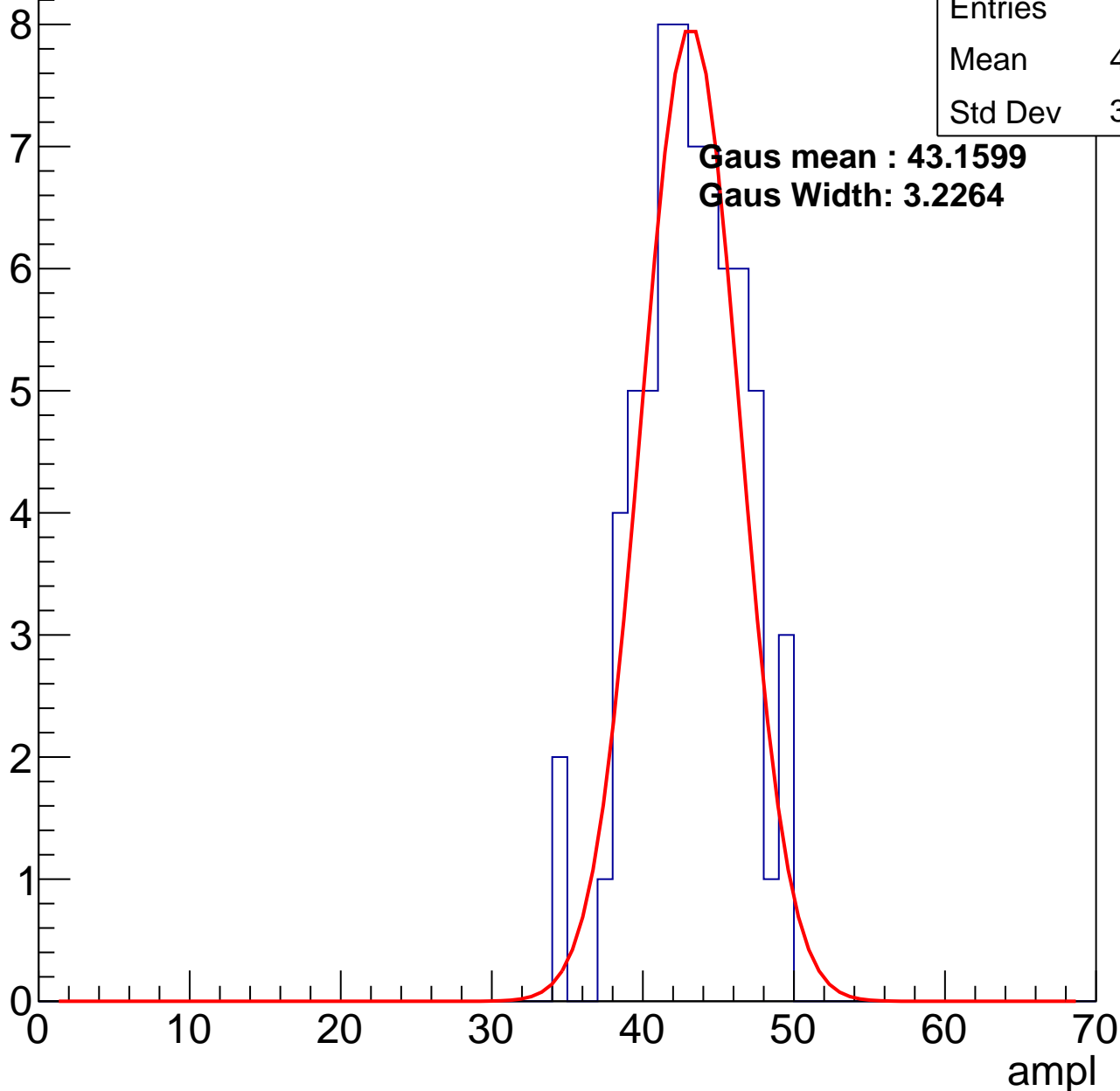
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.66 |
| Std Dev | 3.328 |

Gaus mean : 43.1599

Gaus Width: 3.2264

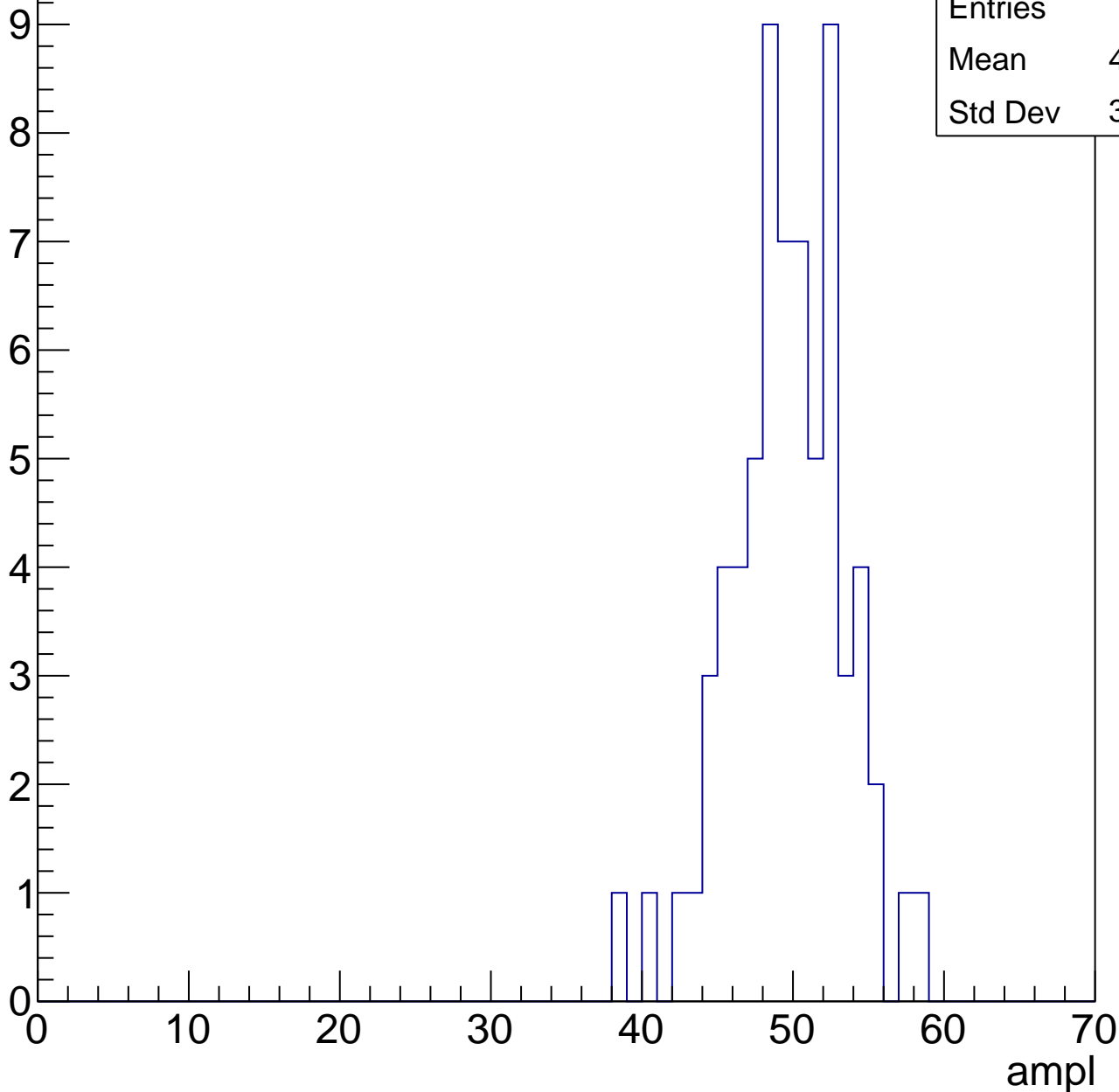


B1L100S, U9-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

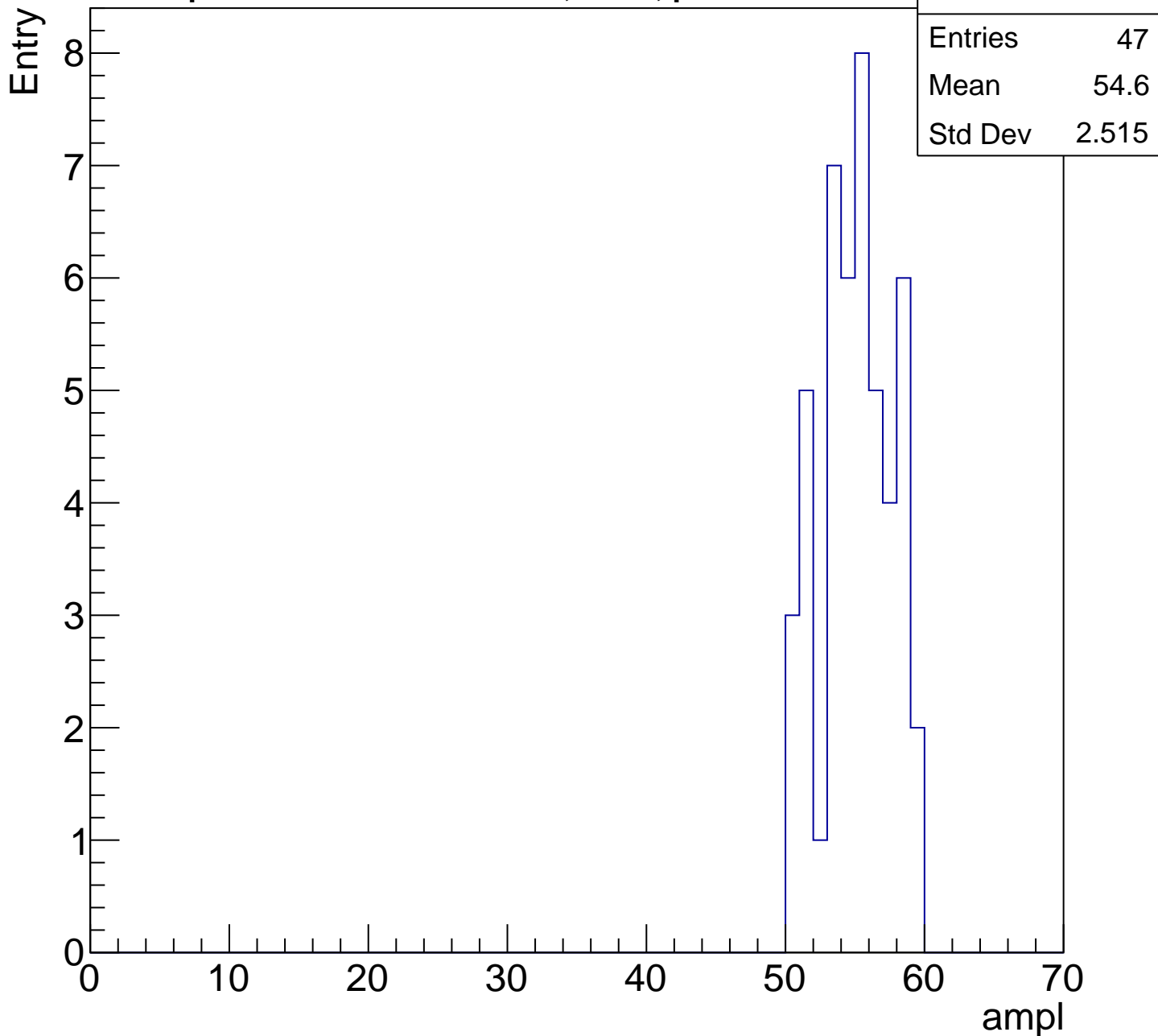
Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 49.15 |
| Std Dev | 3.762 |



B1L100S, U9-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 56

Mean 58.89

Std Dev 8.222

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

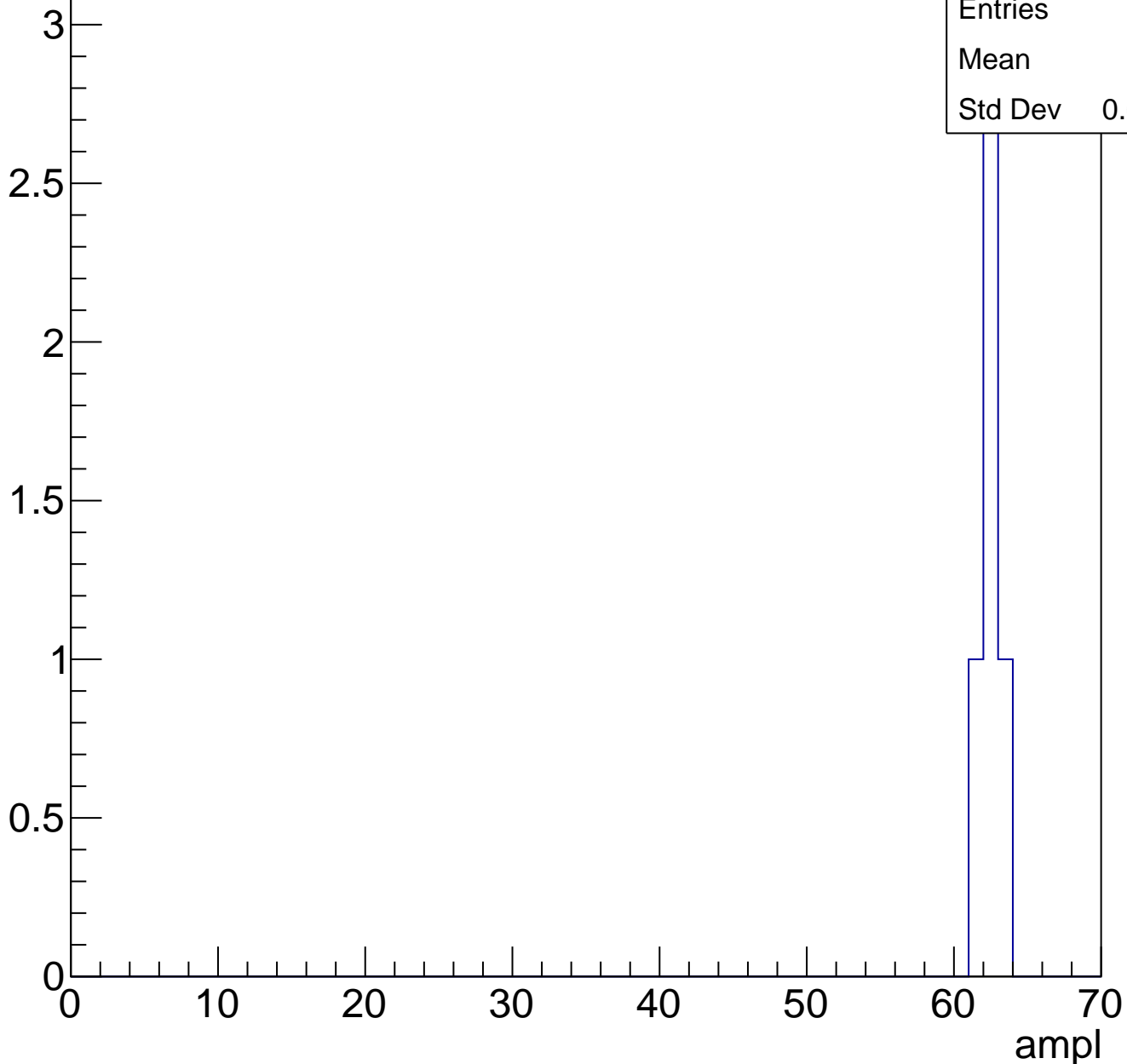
8

10

B1L100S, U9-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch33, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 66 |
| Mean | 30.06 |
| Std Dev | 3.104 |

Gaus mean : 30.9357

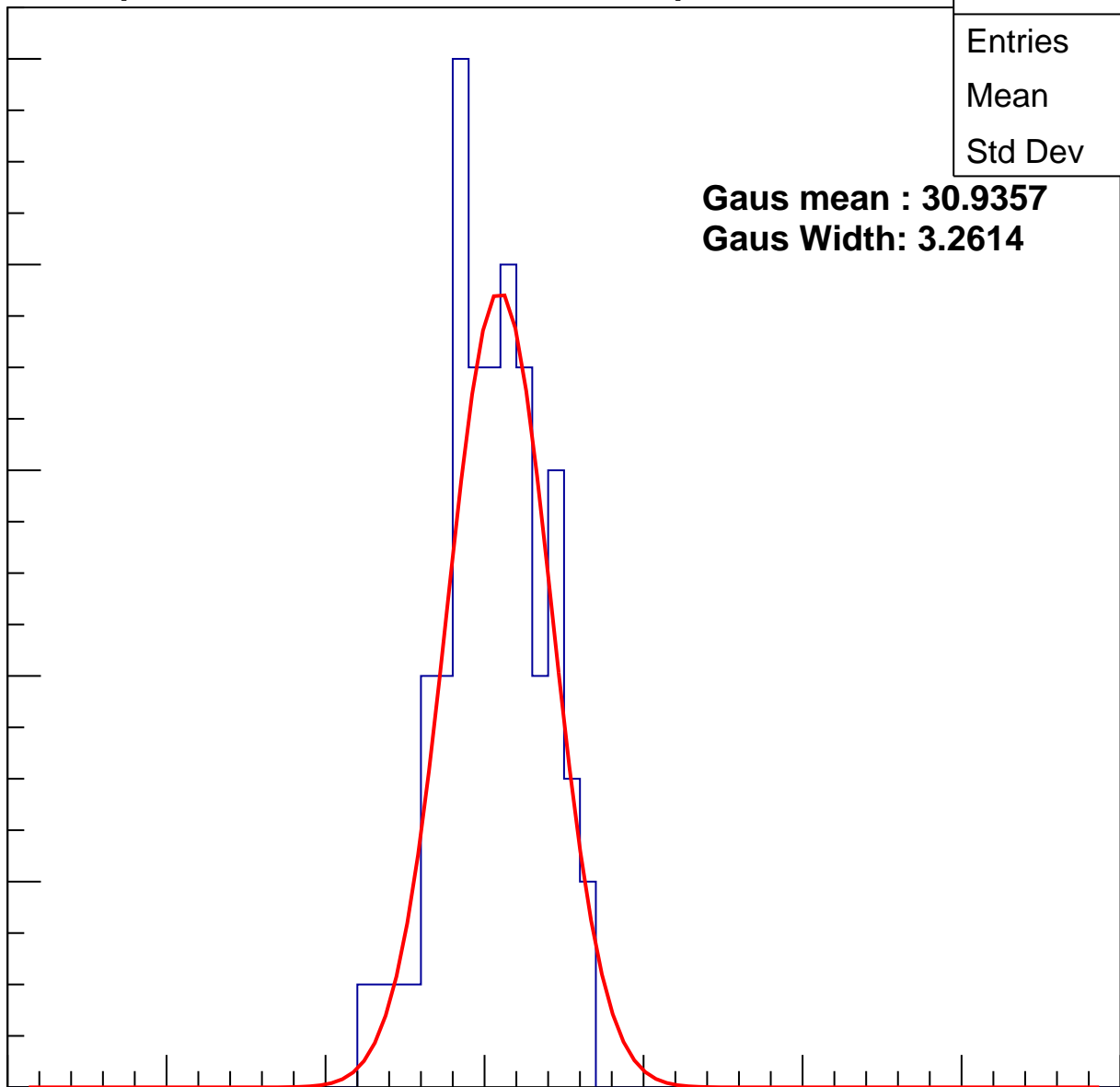
Gaus Width: 3.2614

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch33, adc1

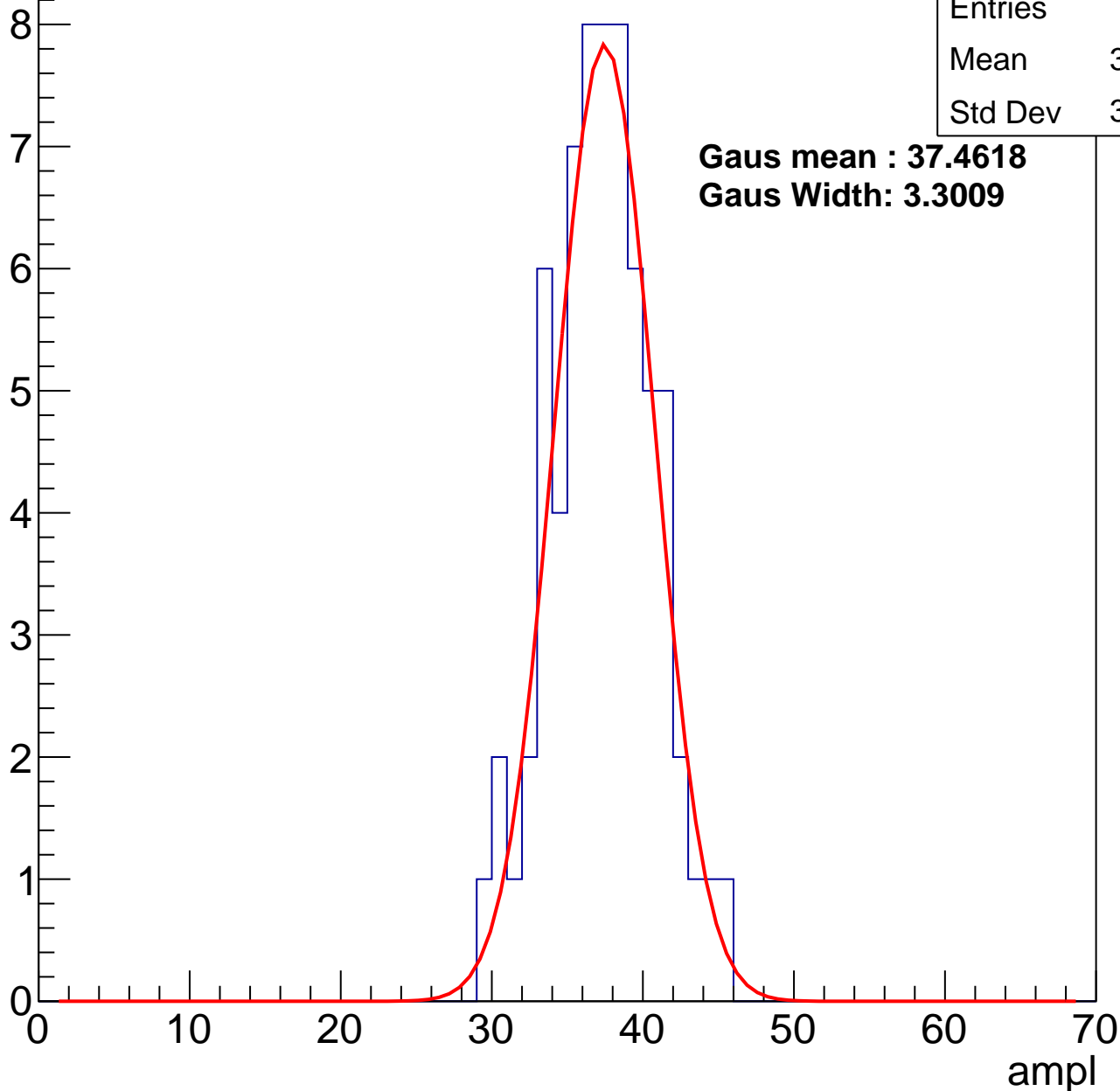
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.85 |
| Std Dev | 3.366 |

Gaus mean : 37.4618

Gaus Width: 3.3009



B1L100S, U9-ch33, adc2

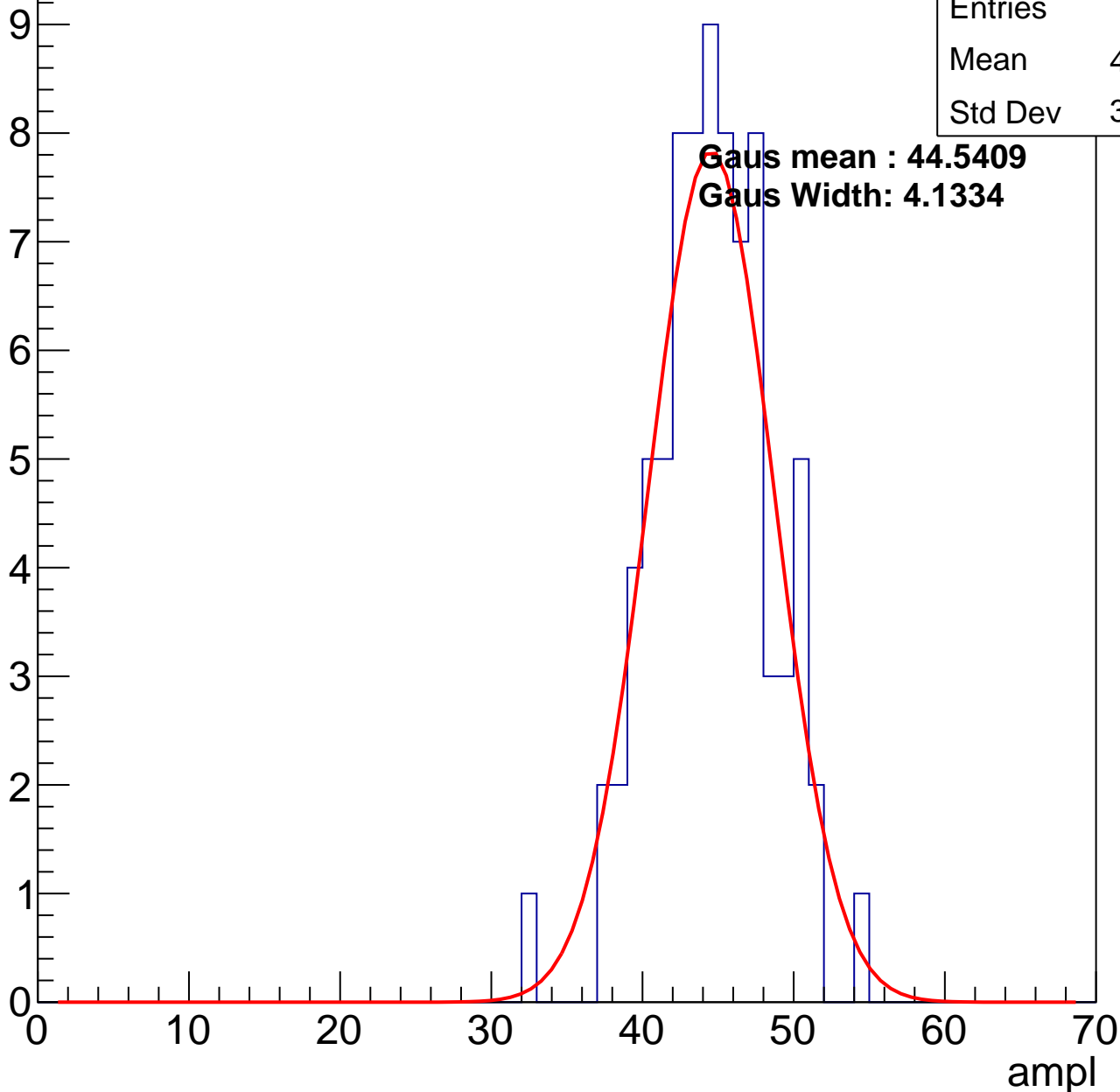
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 44.12 |
| Std Dev | 3.828 |

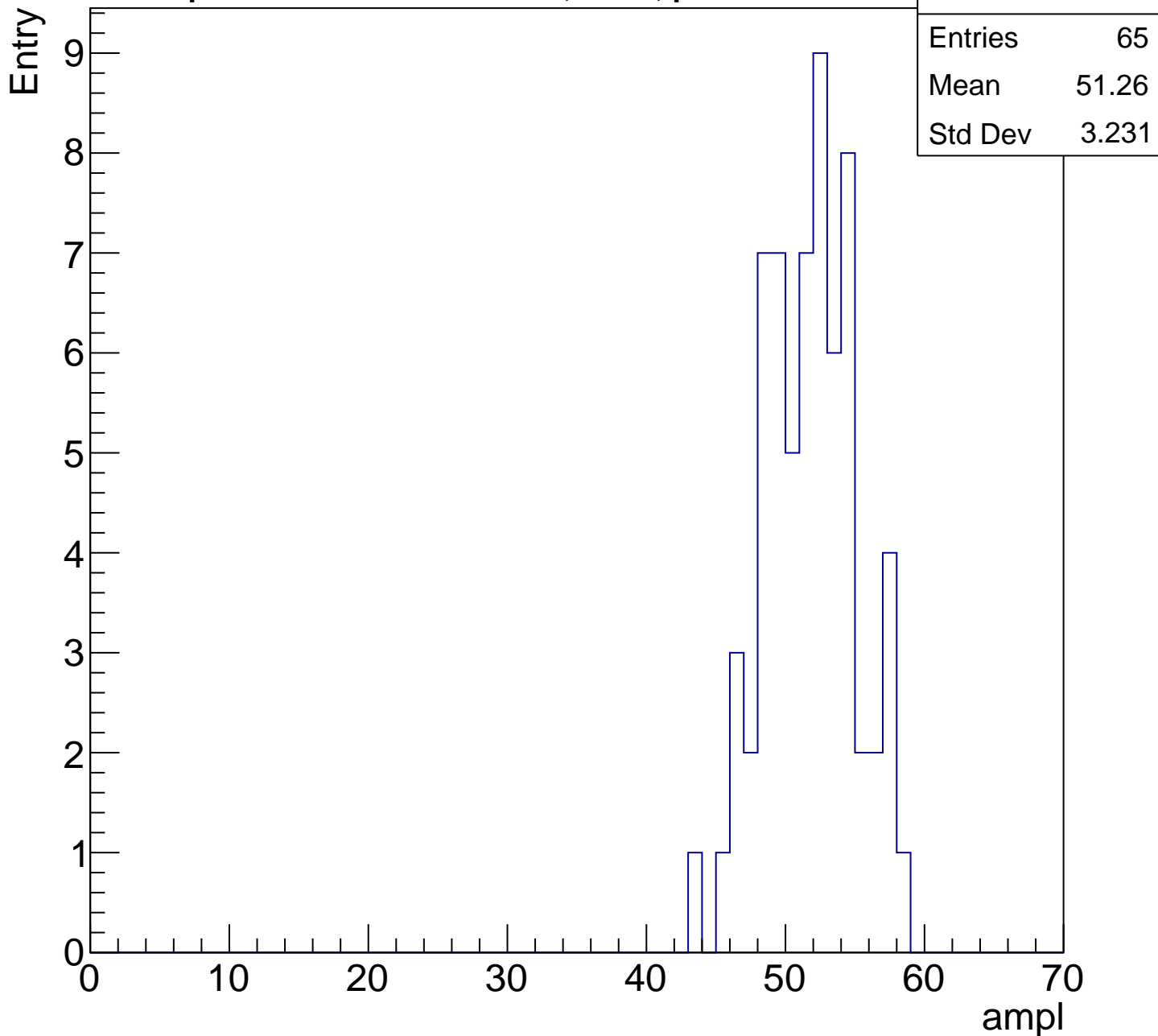
Gaus mean : 44.5409

Gaus Width: 4.1334



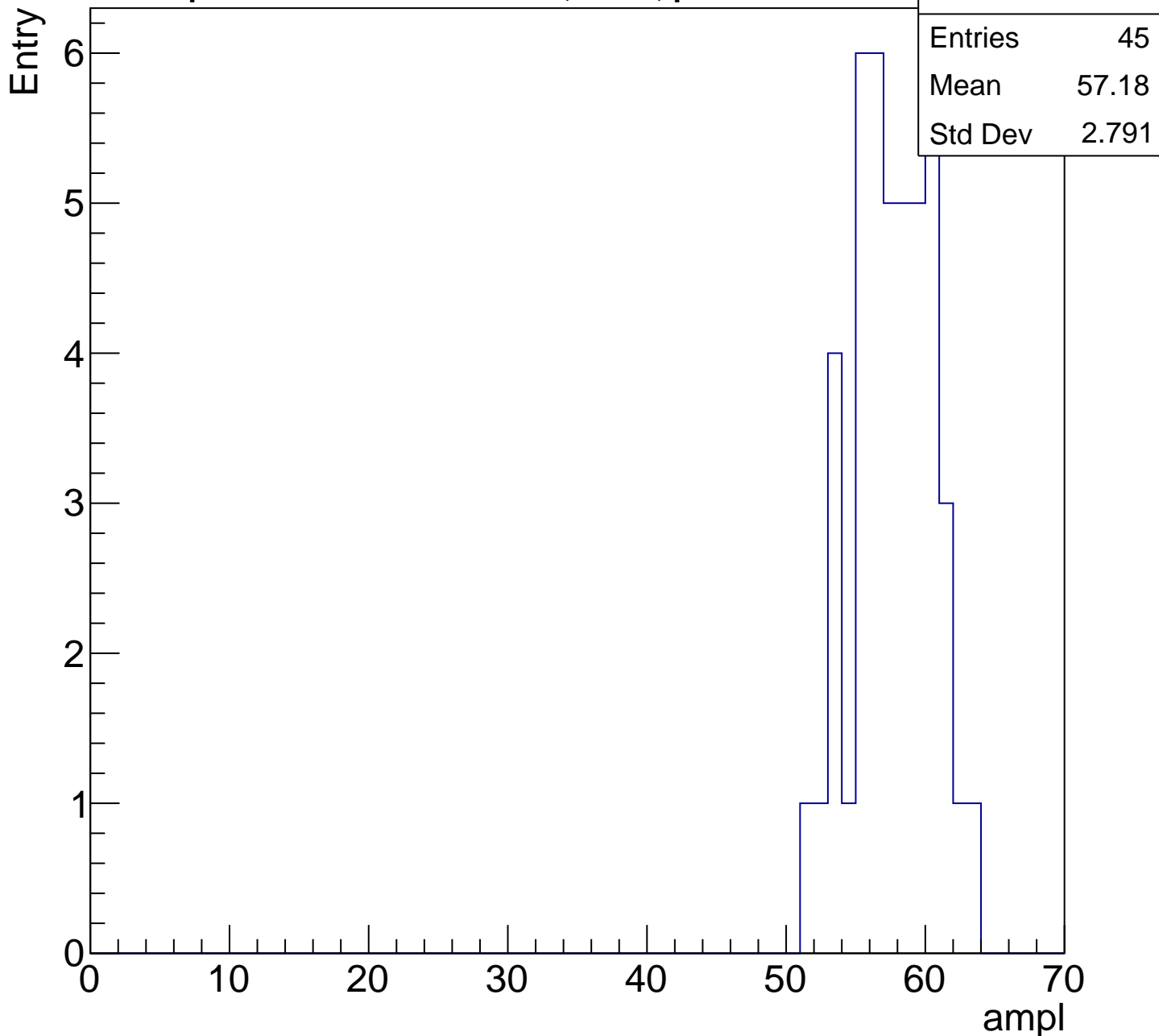
B1L100S, U9-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

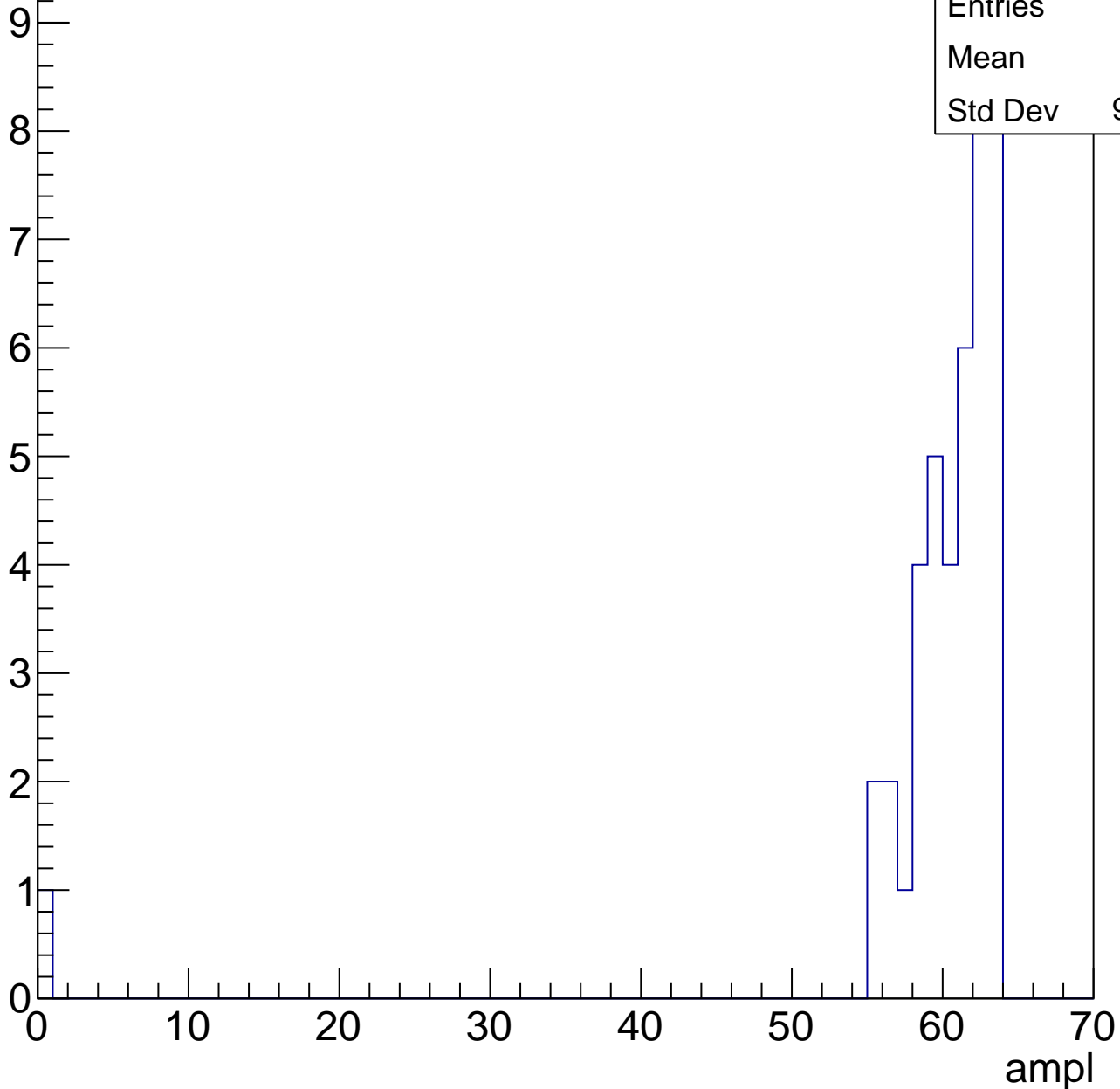


B1L100S, U9-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59 |
| Std Dev | 9.391 |



B1L100S, U9-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch34, adc0

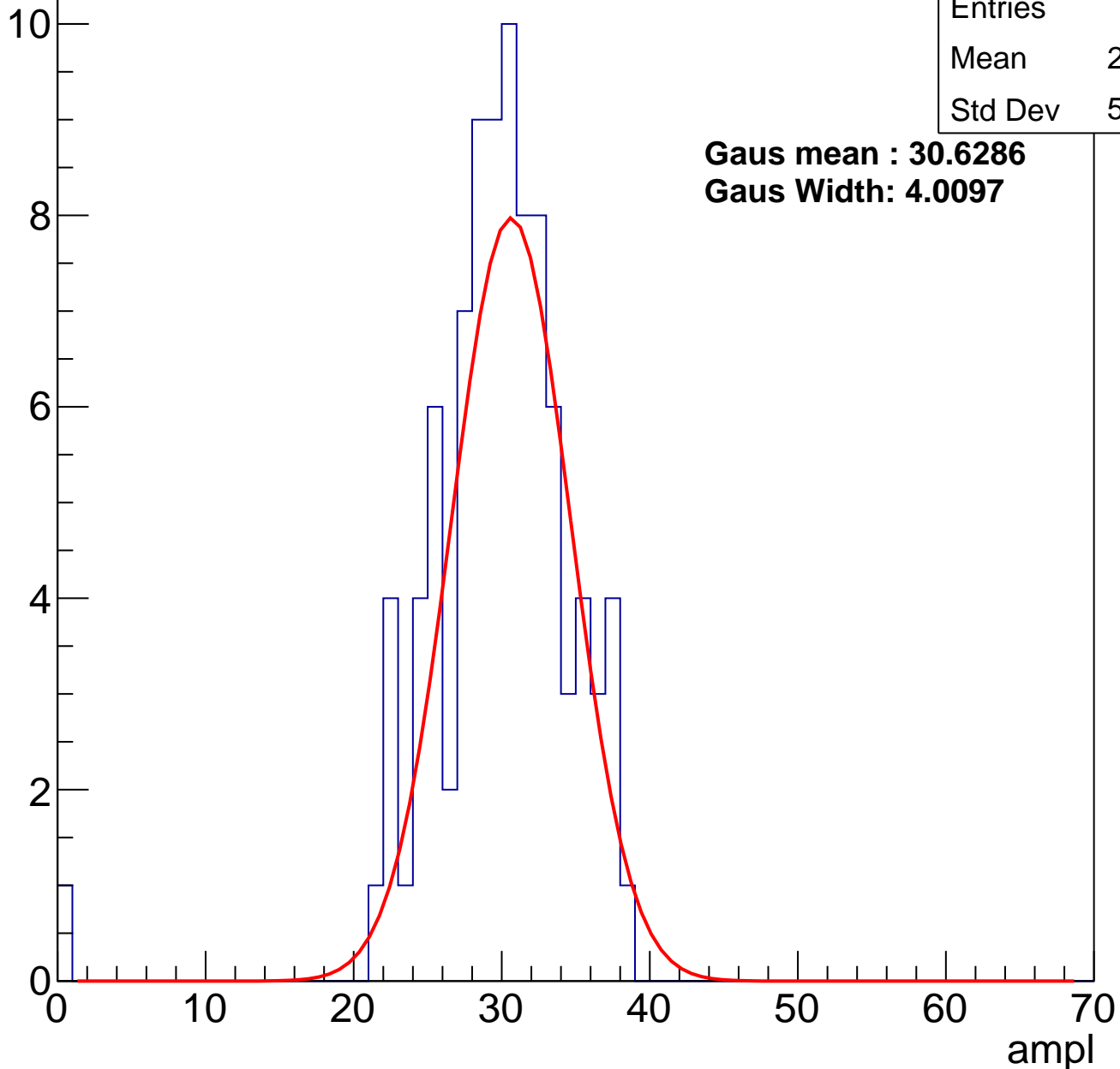
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 29.34 |
| Std Dev | 5.017 |

Gaus mean : 30.6286

Gaus Width: 4.0097

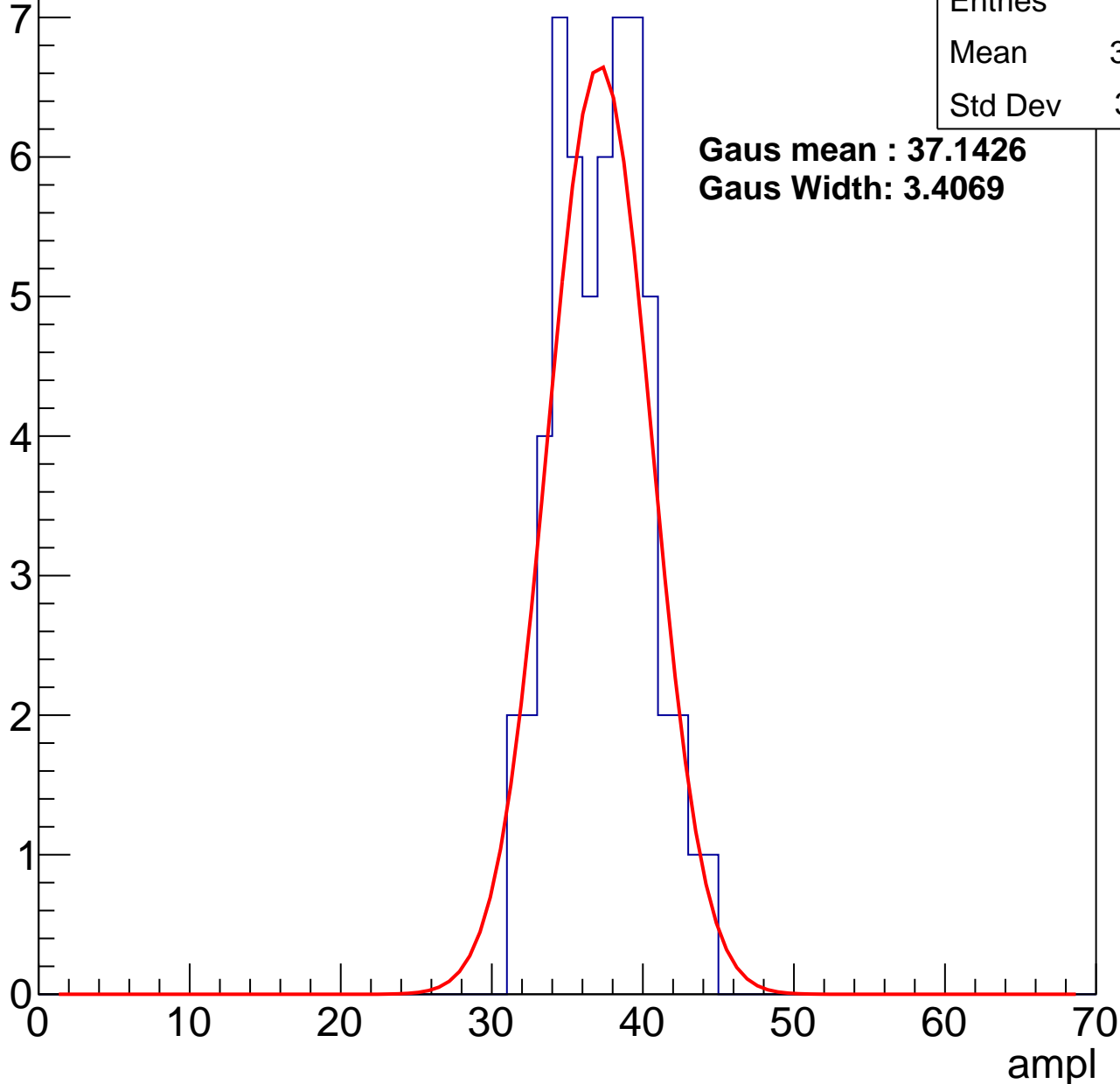
Entry



B1L100S, U9-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch34, adc2

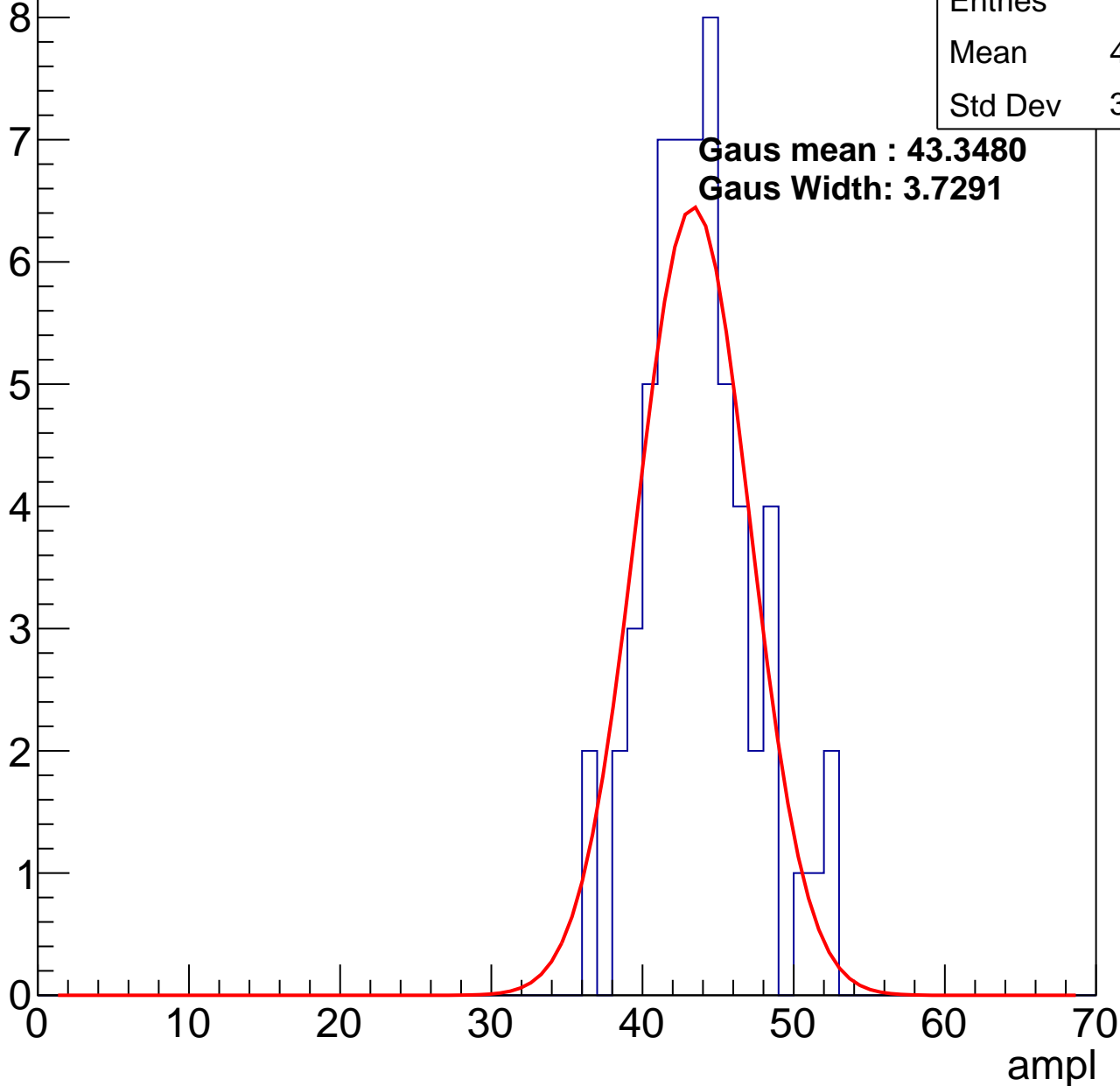
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 43.32 |
| Std Dev | 3.514 |

Gaus mean : 43.3480

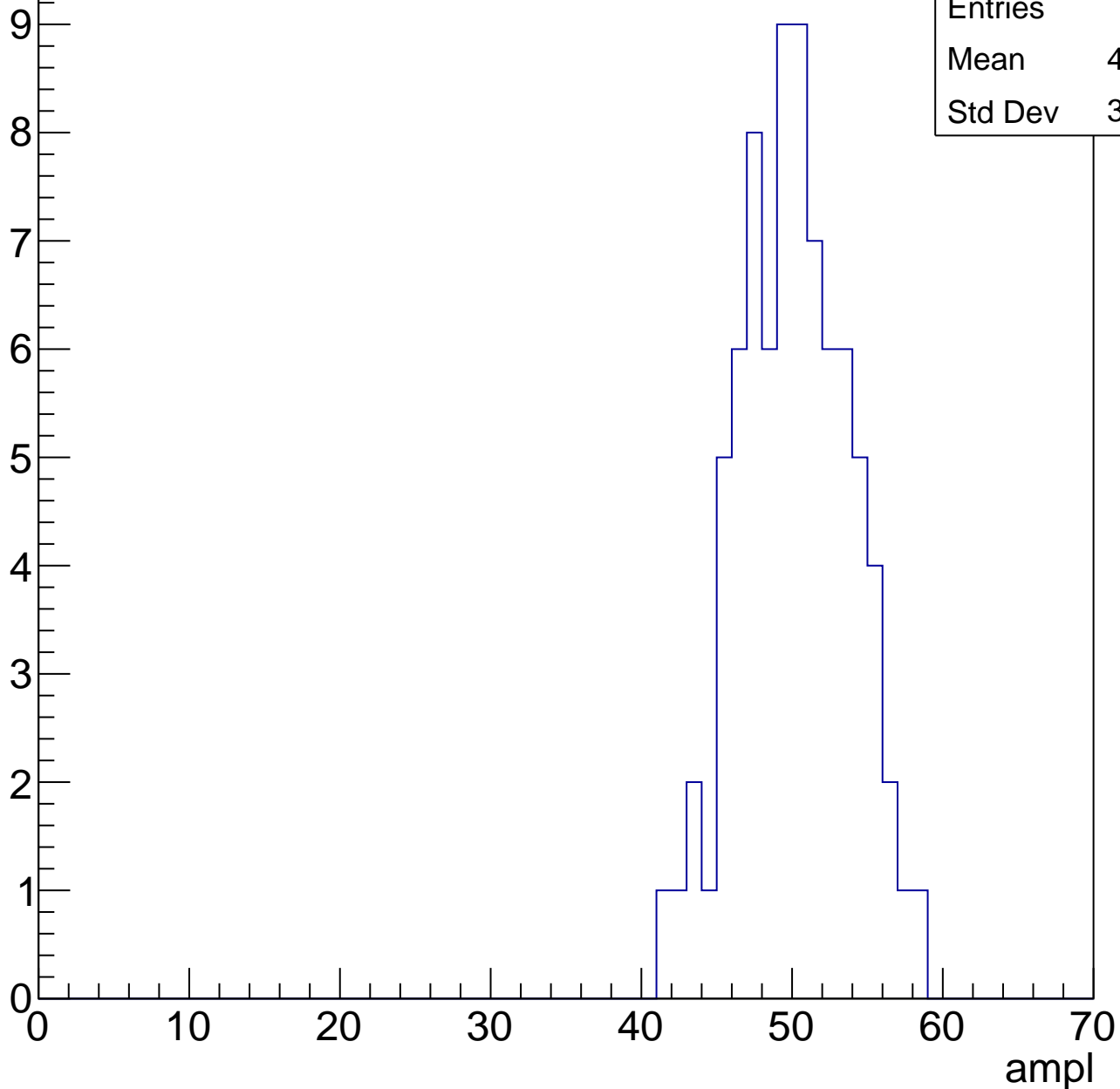
Gaus Width: 3.7291



B1L100S, U9-ch34, adc3

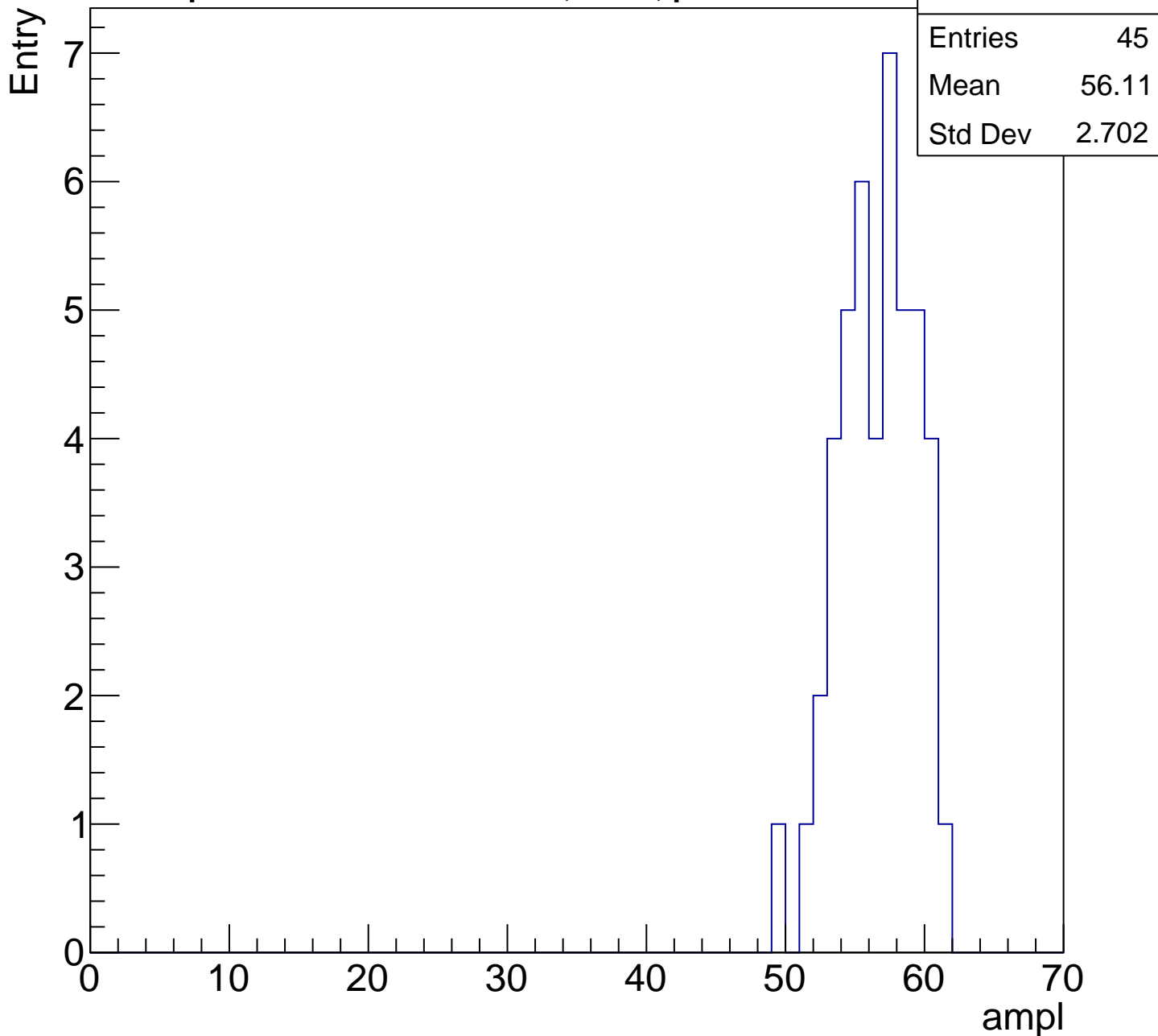
calib_packv5_042523_0143.root, FC#4, port A2

Entry



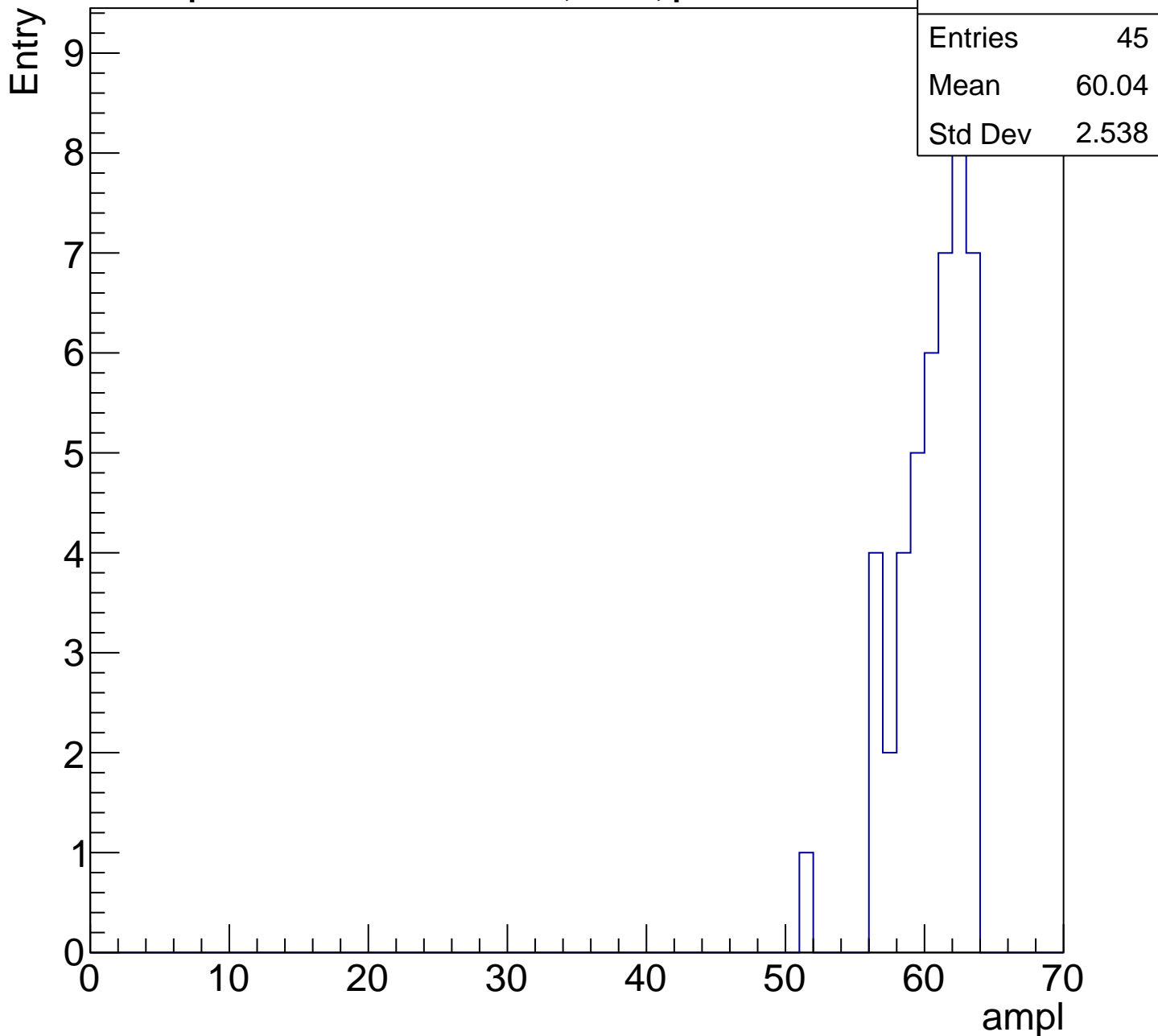
B1L100S, U9-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch34, adc5

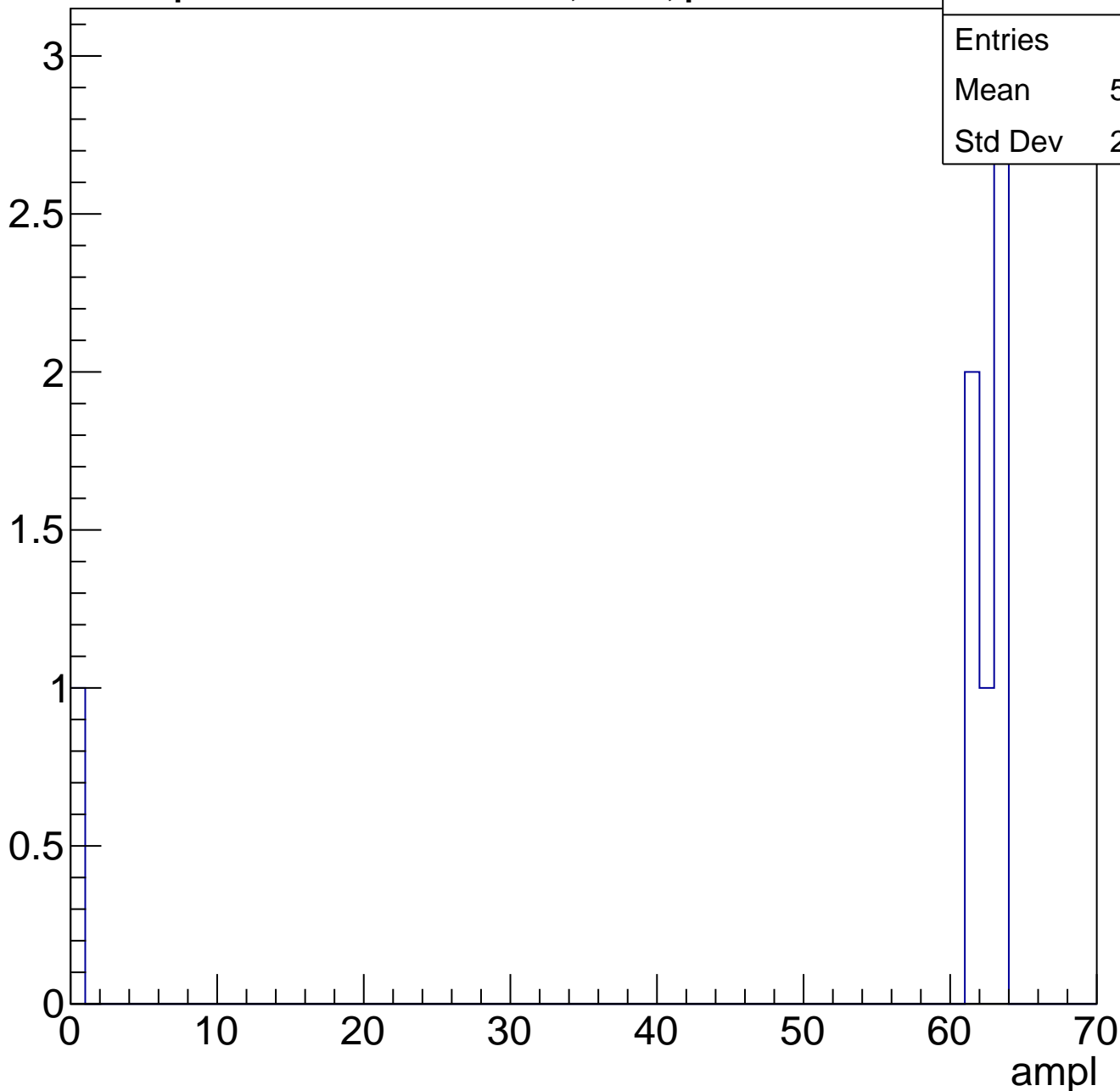
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 7 |
| Mean | 53.29 |
| Std Dev | 21.77 |

B1L100S, U9-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch35, adc0

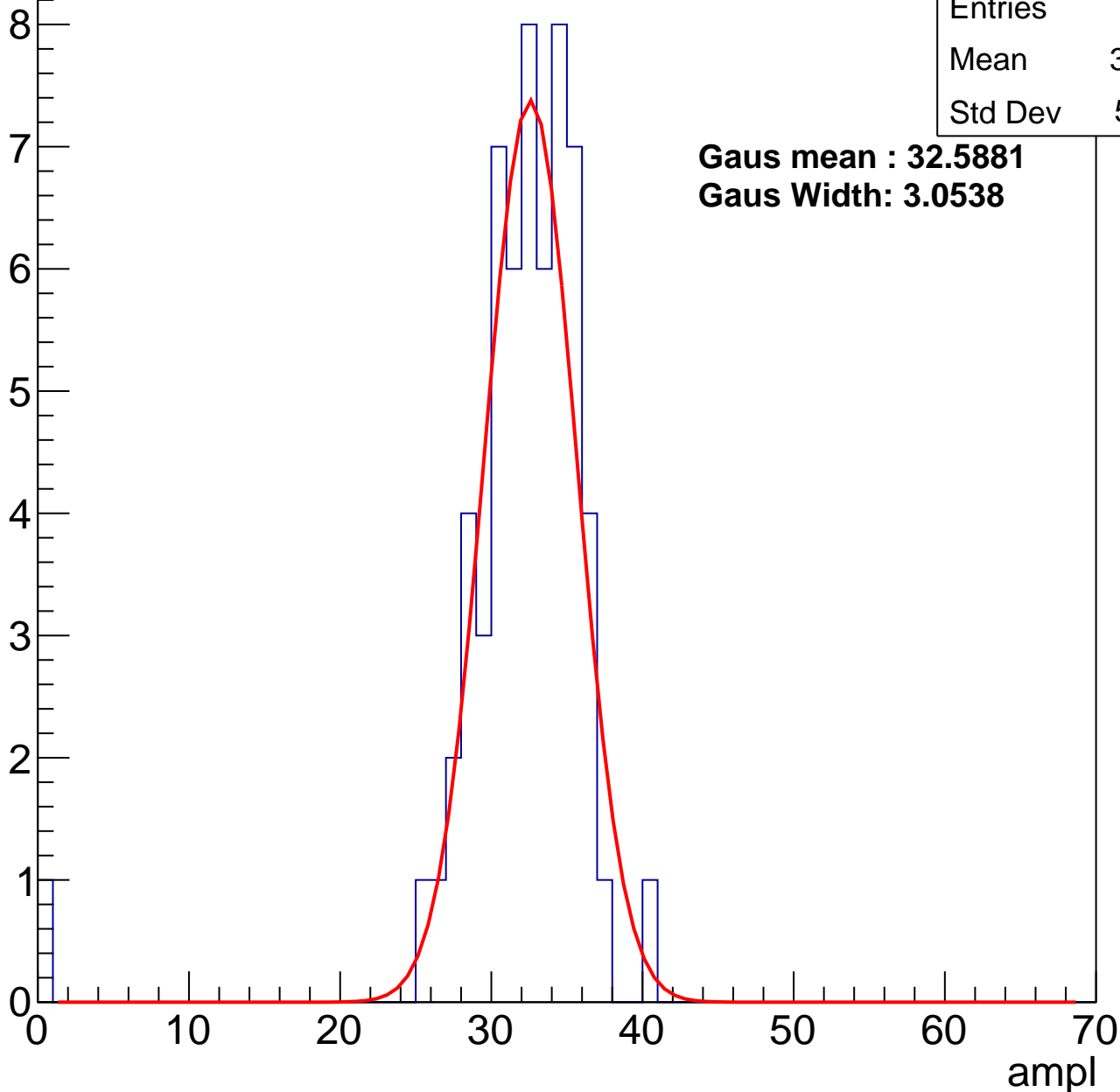
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 31.53 |
| Std Dev | 5.041 |

Gaus mean : 32.5881

Gaus Width: 3.0538



B1L100S, U9-ch35, adc1

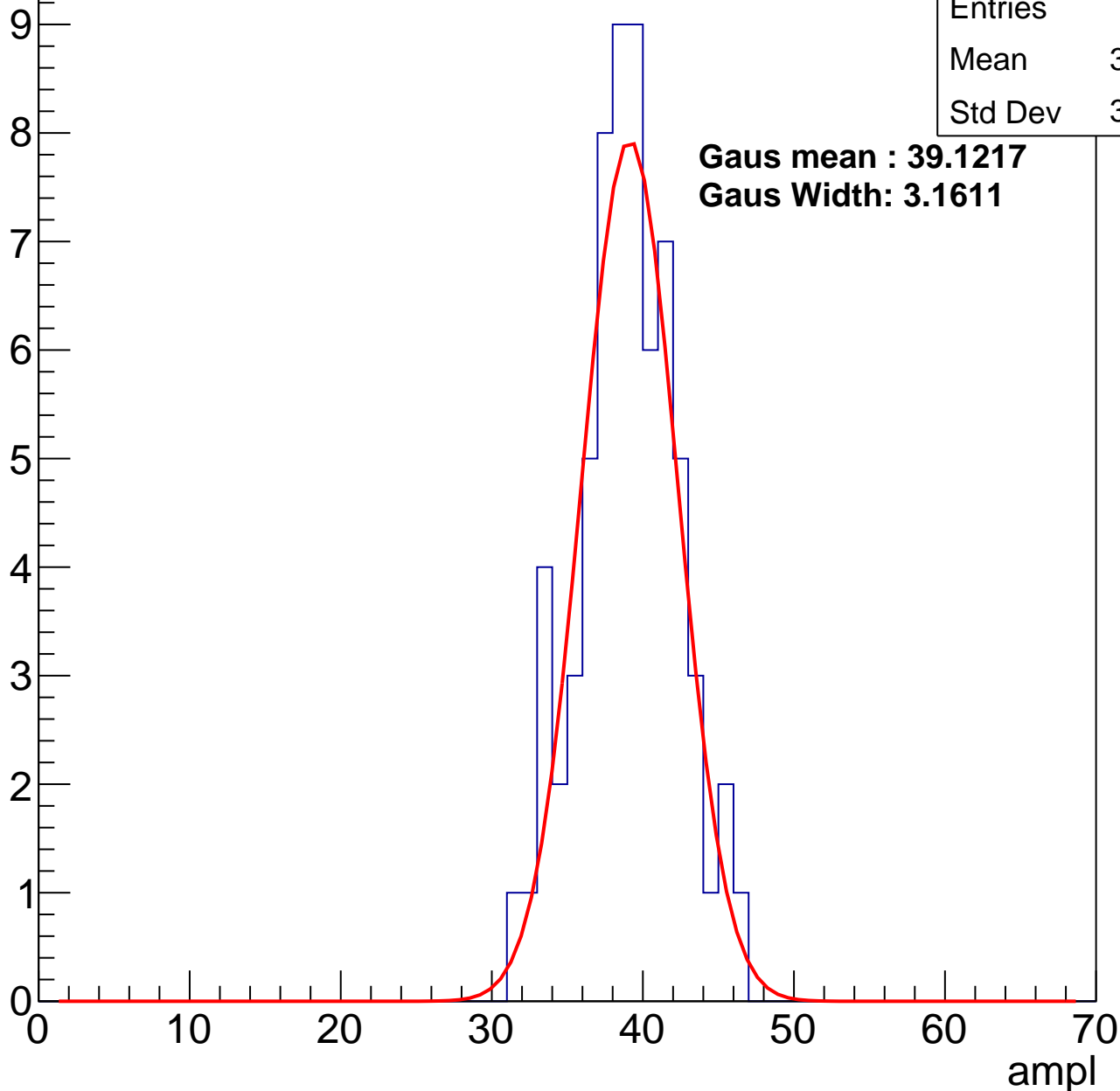
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 38.55 |
| Std Dev | 3.234 |

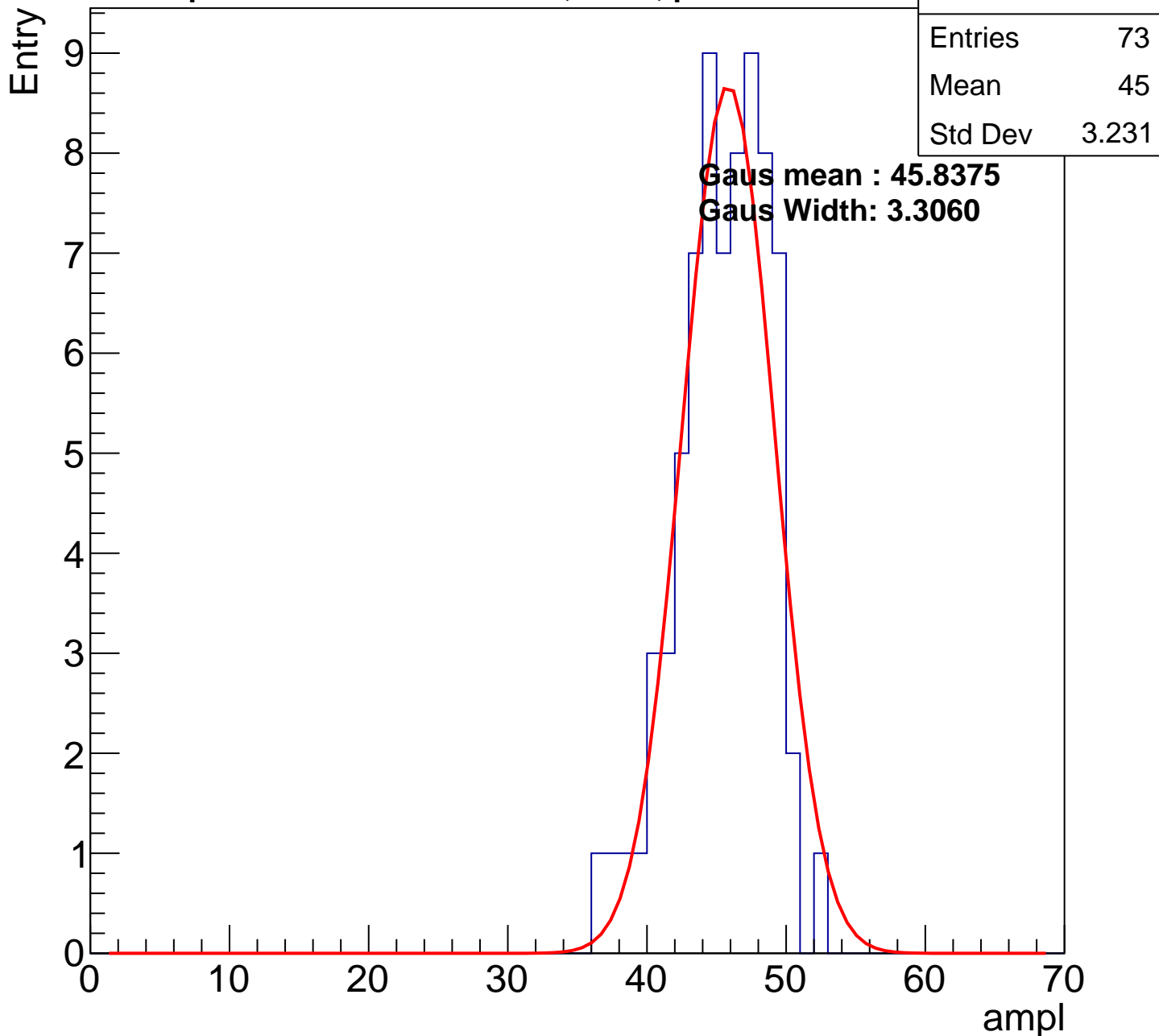
Gaus mean : 39.1217

Gaus Width: 3.1611



B1L100S, U9-ch35, adc2

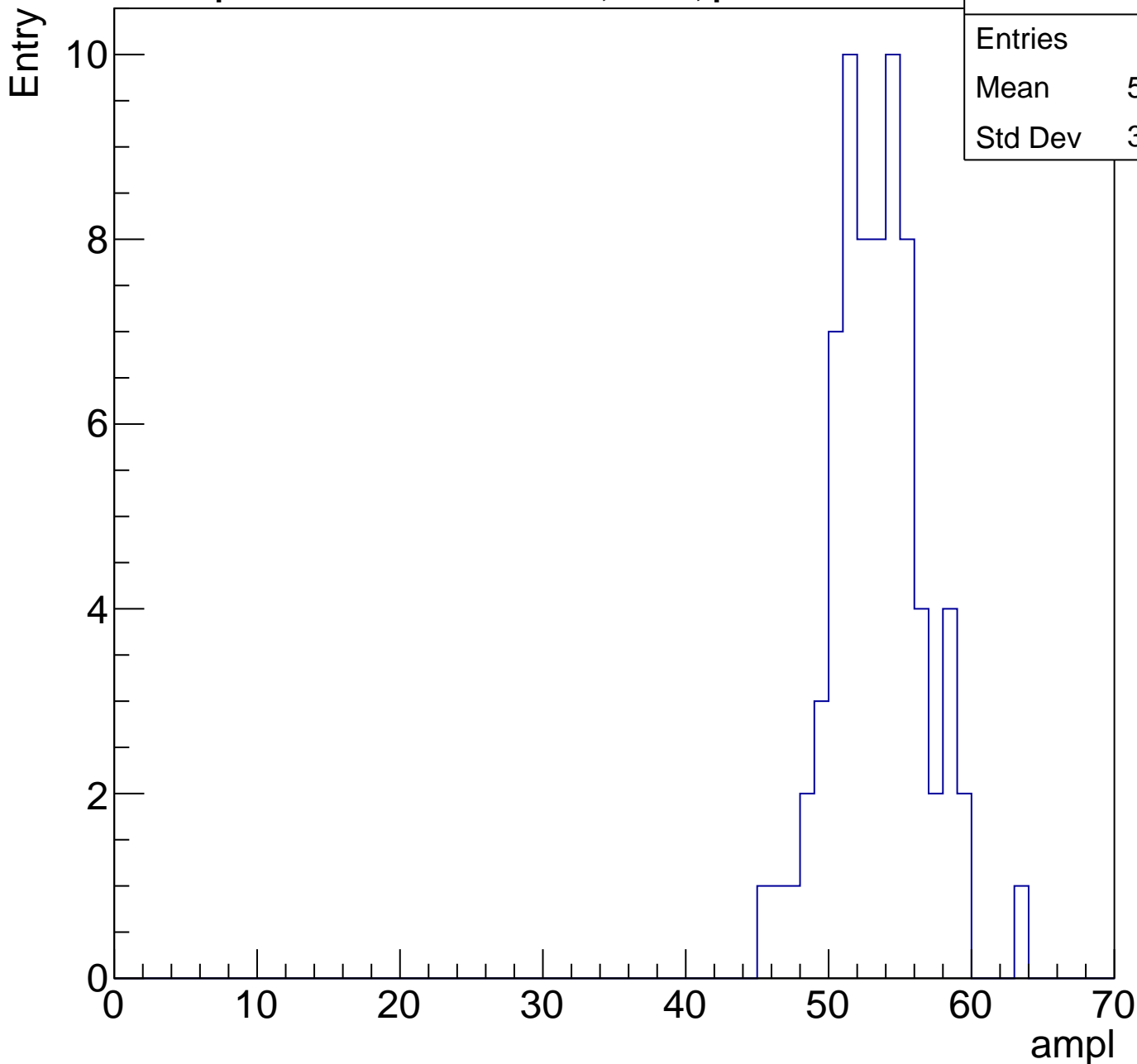
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 52.94 |
| Std Dev | 3.205 |



B1L100S, U9-ch35, adc4

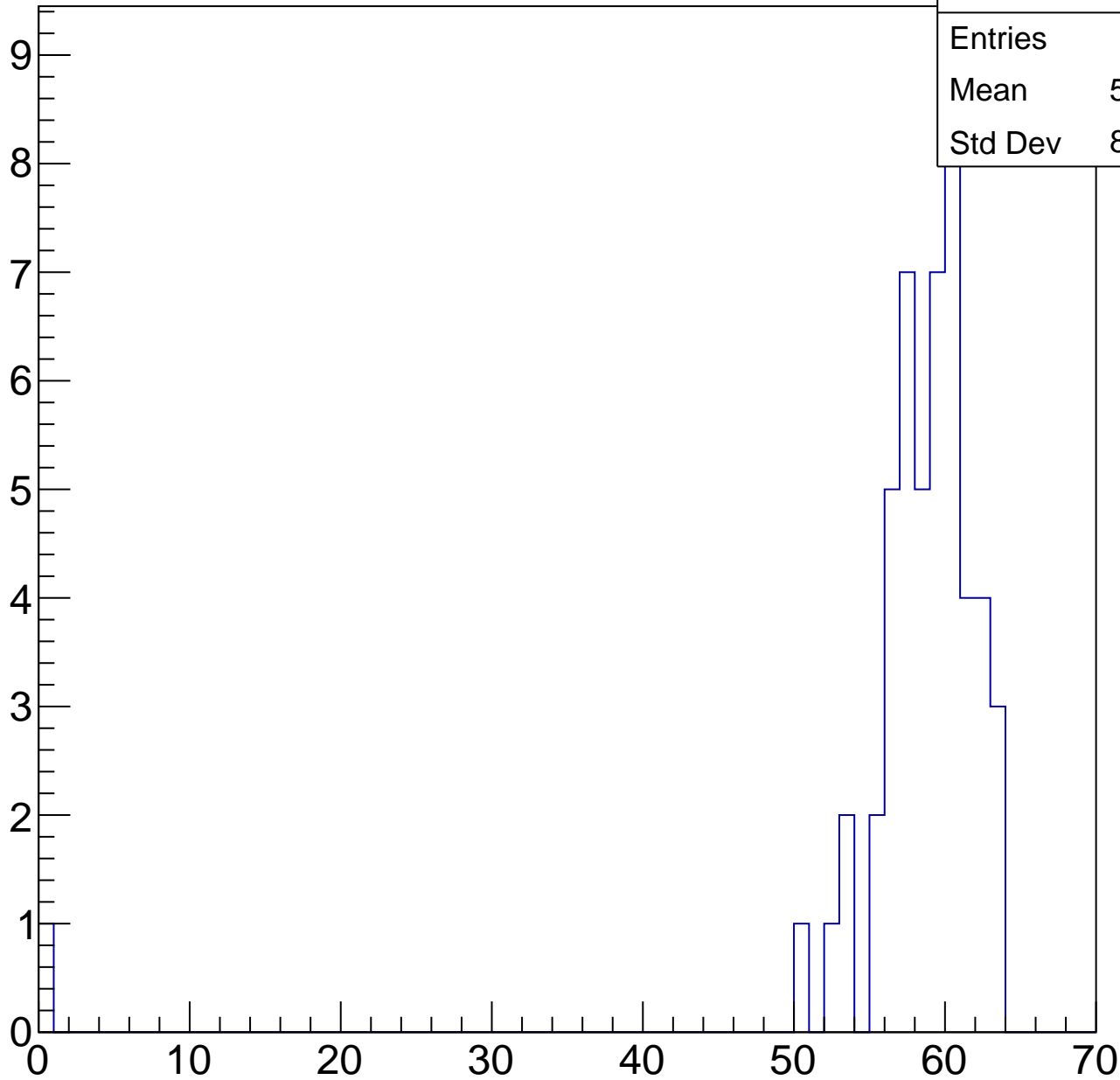
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.27 |
| Std Dev | 8.575 |

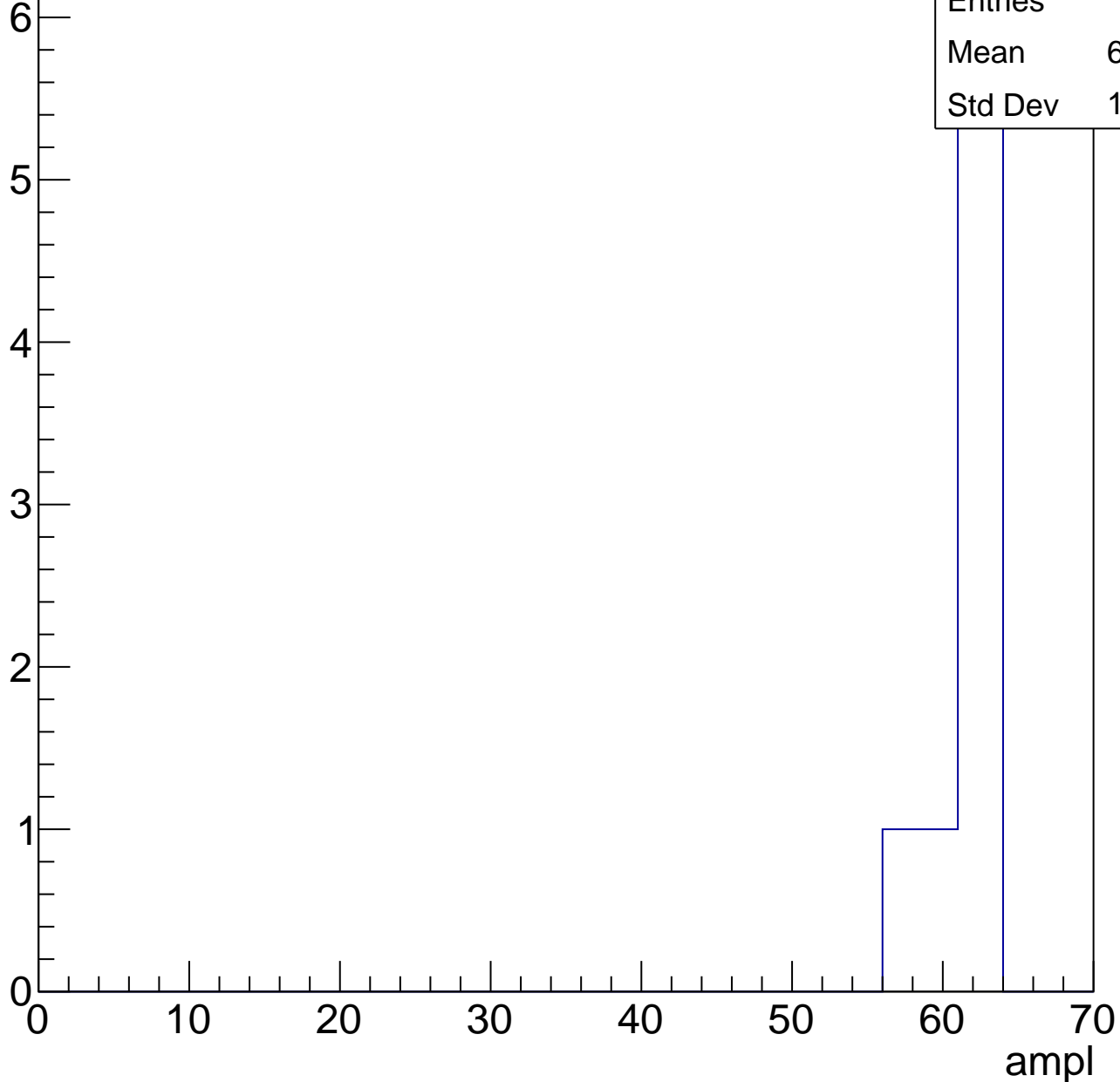
ampl



B1L100S, U9-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch36, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 93 |
| Mean | 27.8 |
| Std Dev | 3.926 |

Gaus mean : 28.3740

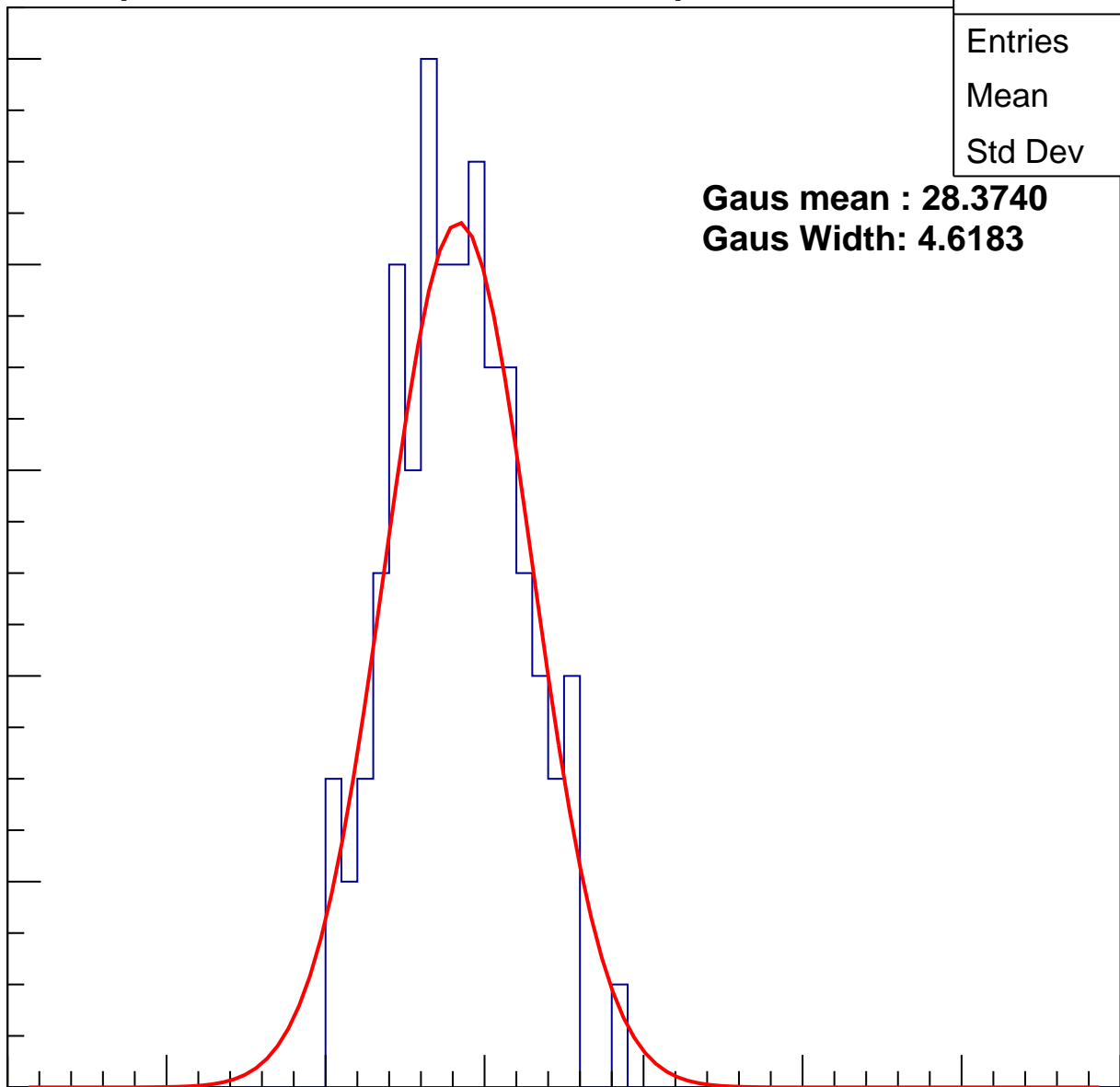
Gaus Width: 4.6183

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



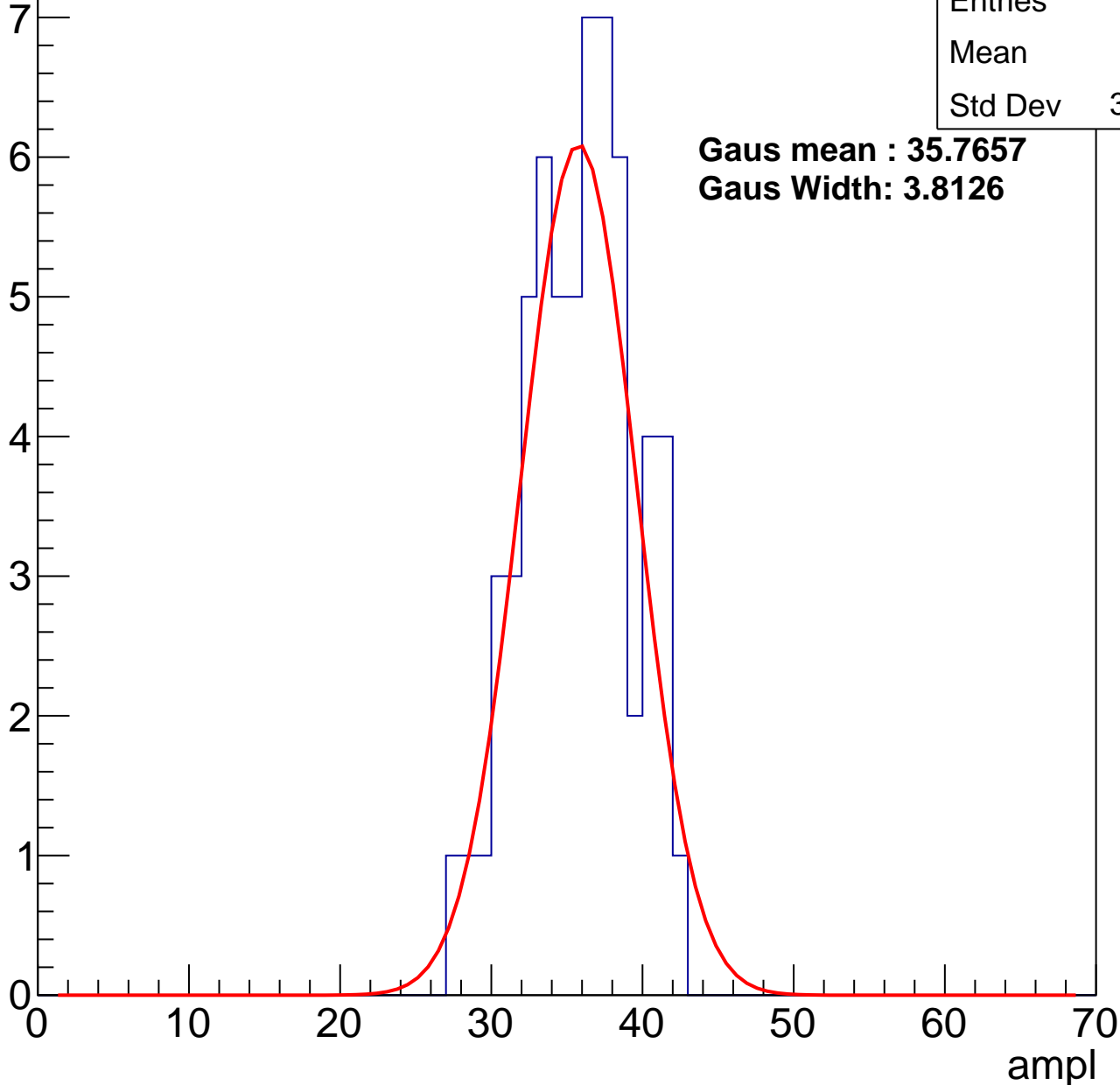
B1L100S, U9-ch36, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

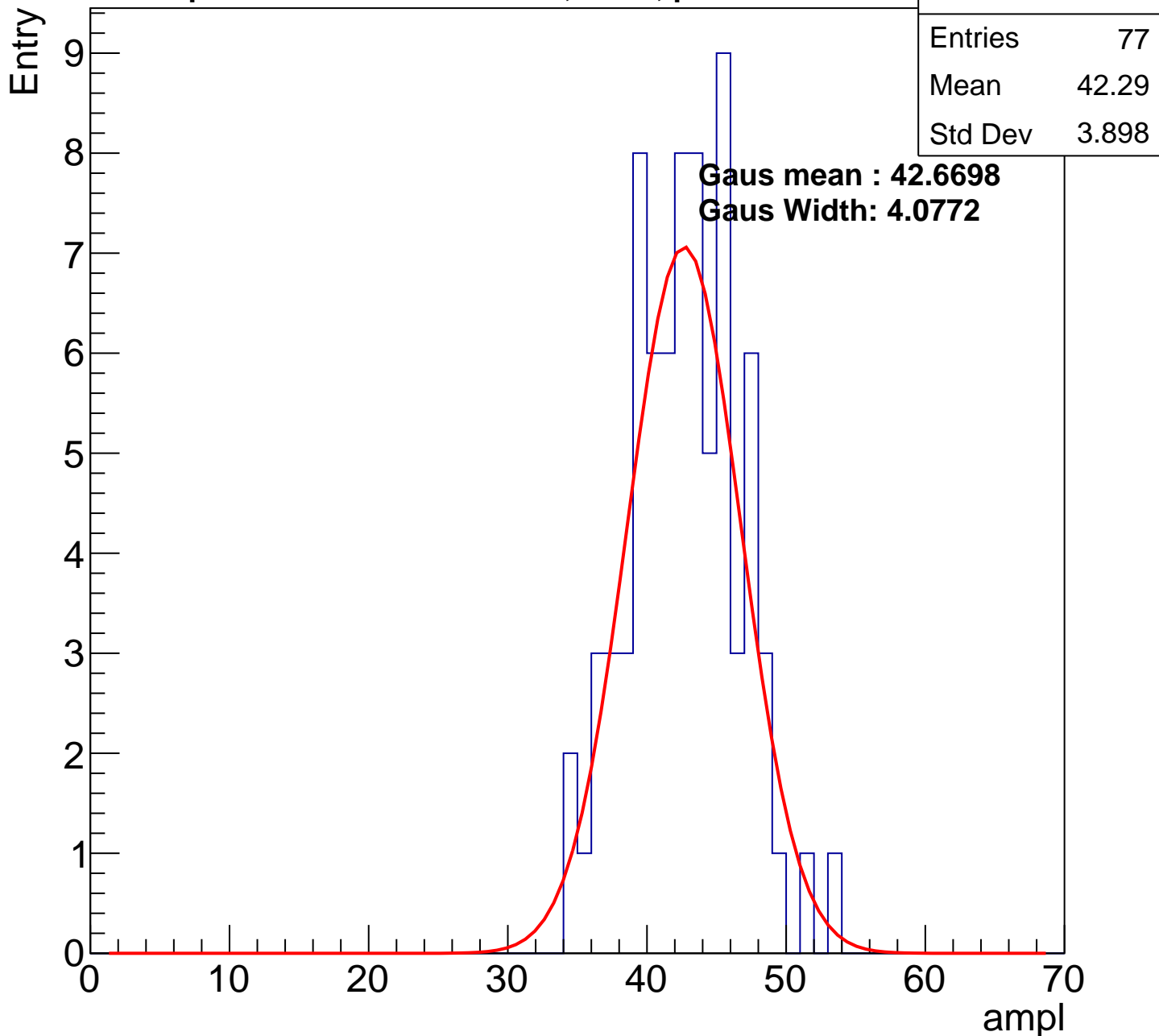
| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.3 |
| Std Dev | 3.517 |

Gaus mean : 35.7657
Gaus Width: 3.8126



B1L100S, U9-ch36, adc2

calib_packv5_042523_0143.root, FC#4, port A2

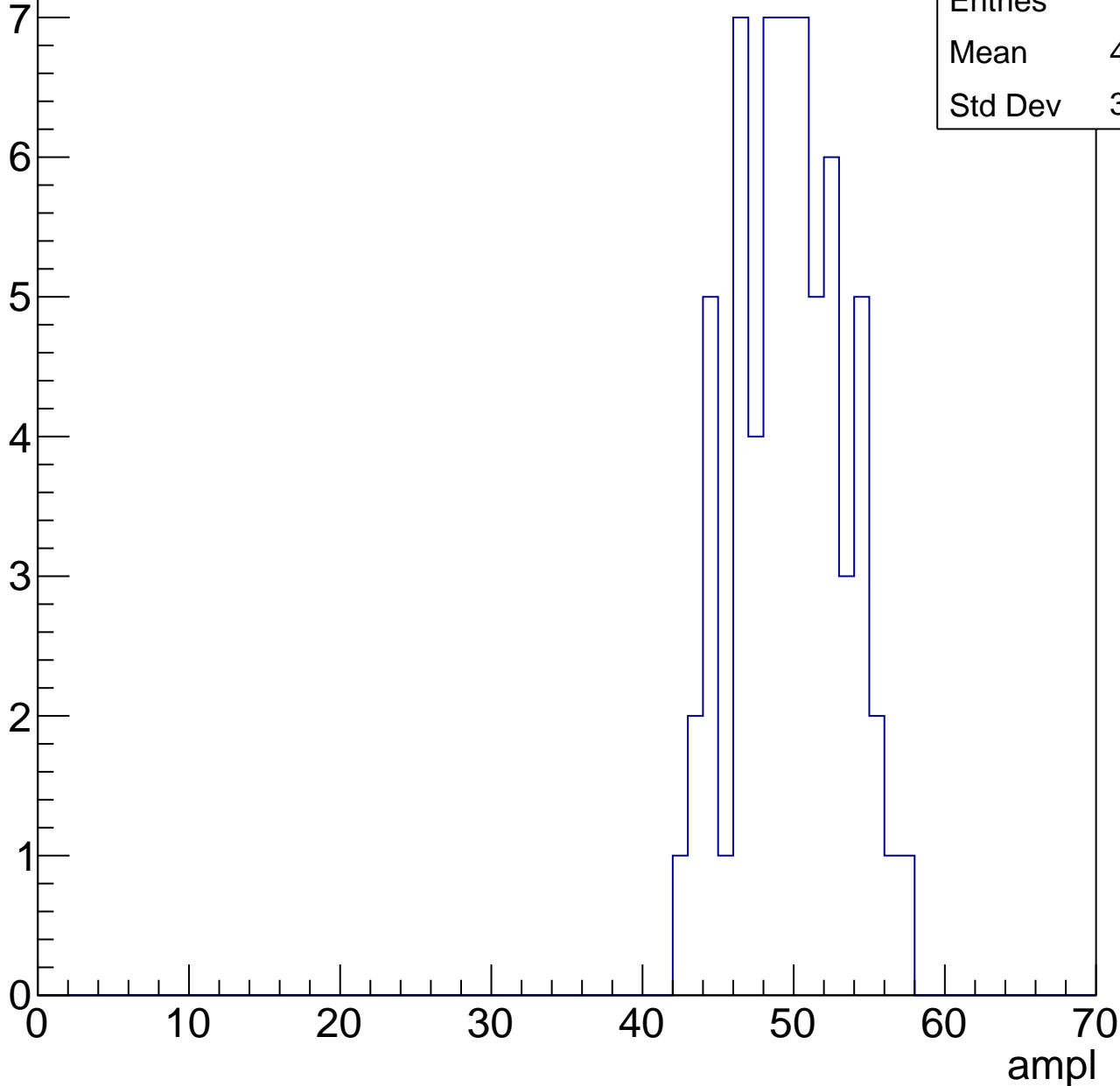


B1L100S, U9-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

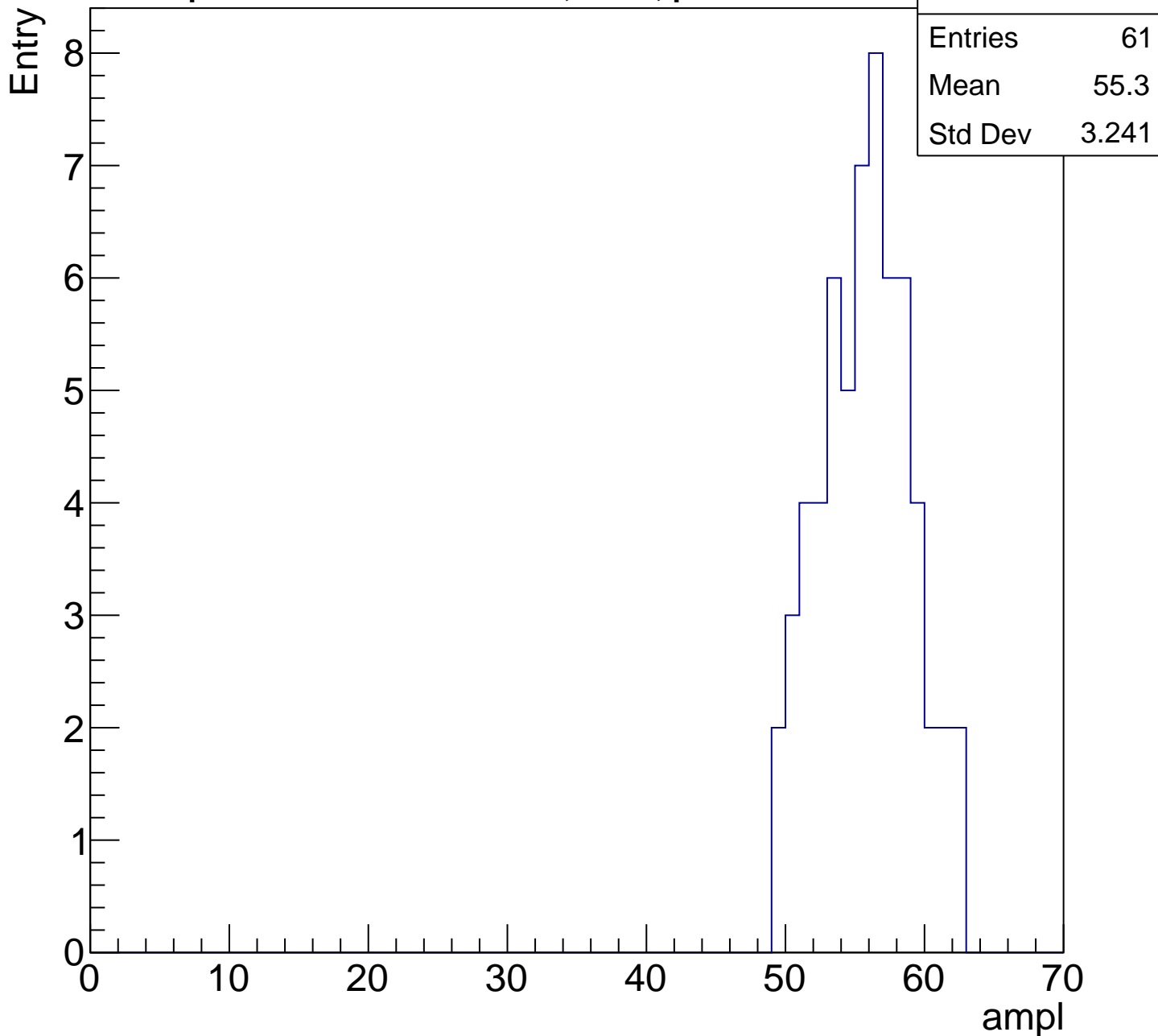
Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.23 |
| Std Dev | 3.499 |



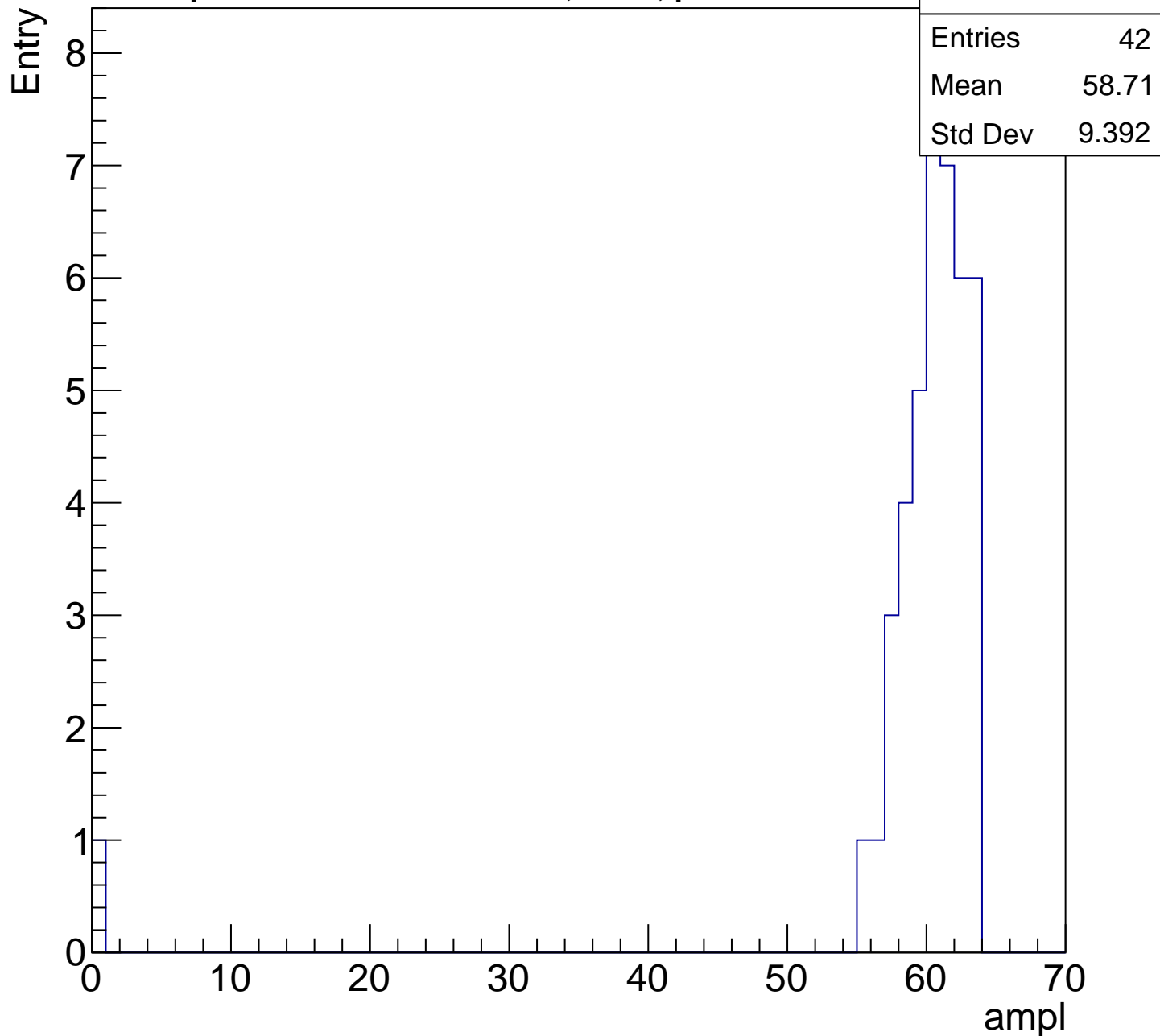
B1L100S, U9-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch36, adc5

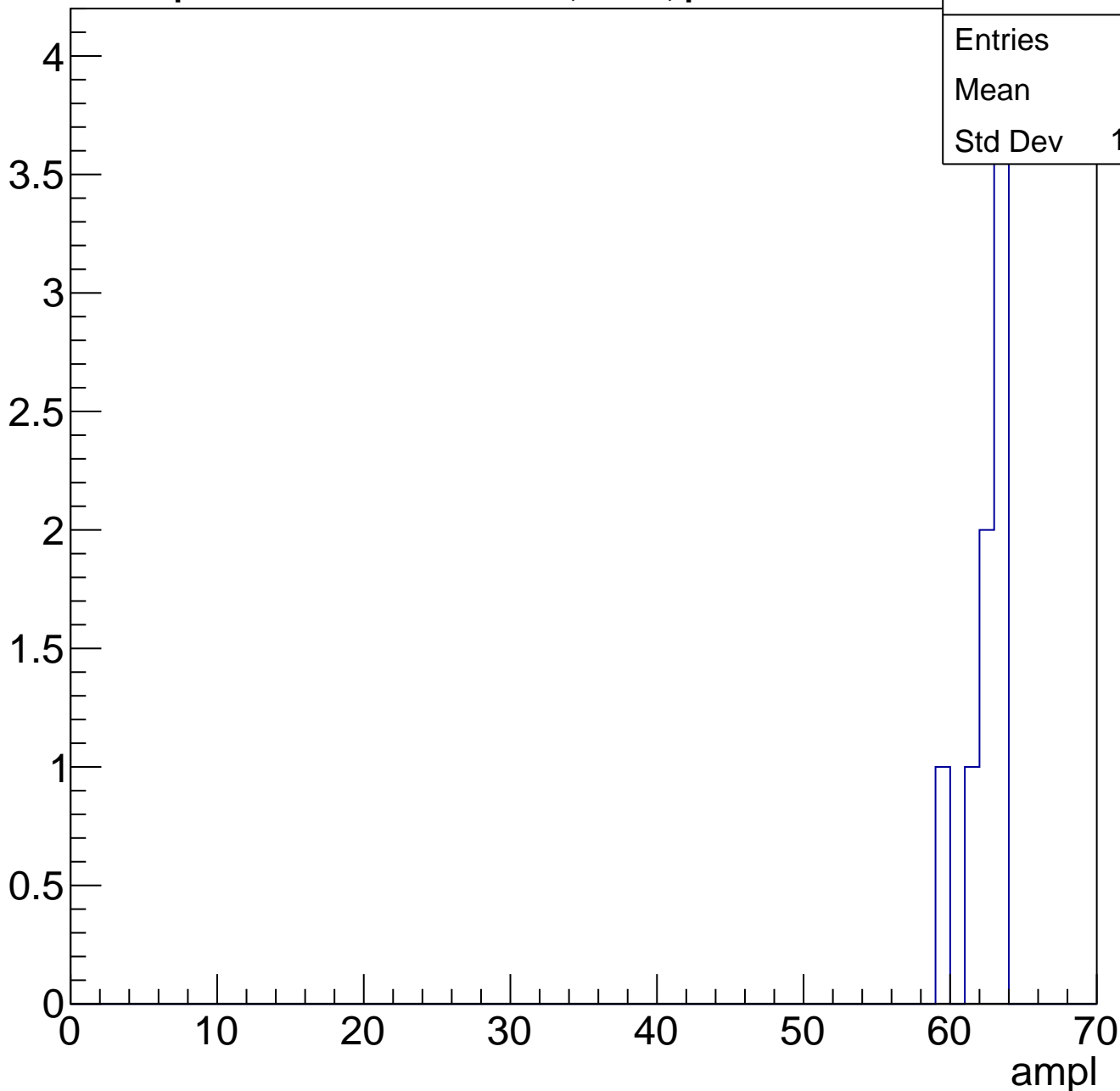
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch37, adc0

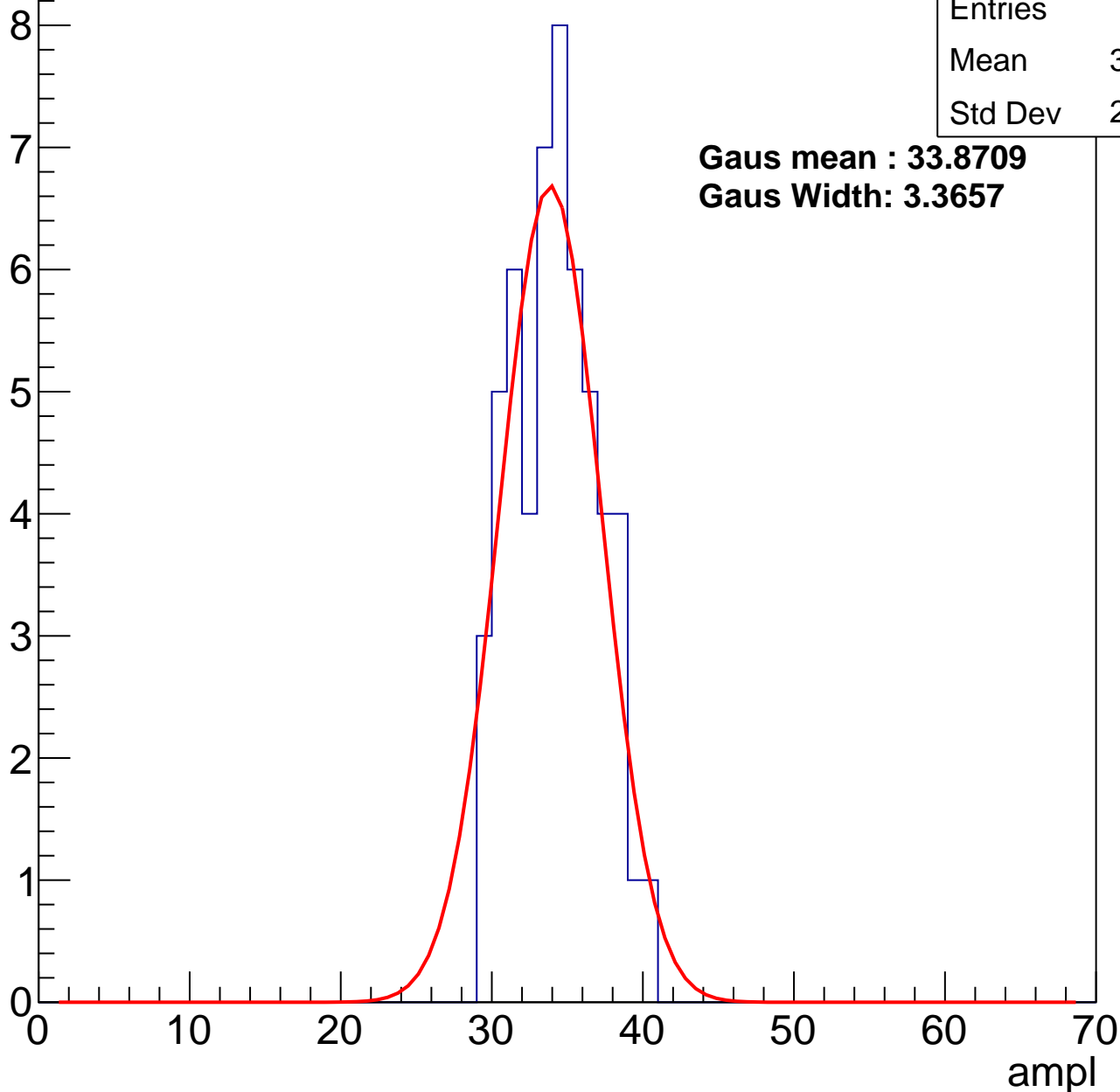
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 33.76 |
| Std Dev | 2.775 |

Gaus mean : 33.8709

Gaus Width: 3.3657



B1L100S, U9-ch37, adc1

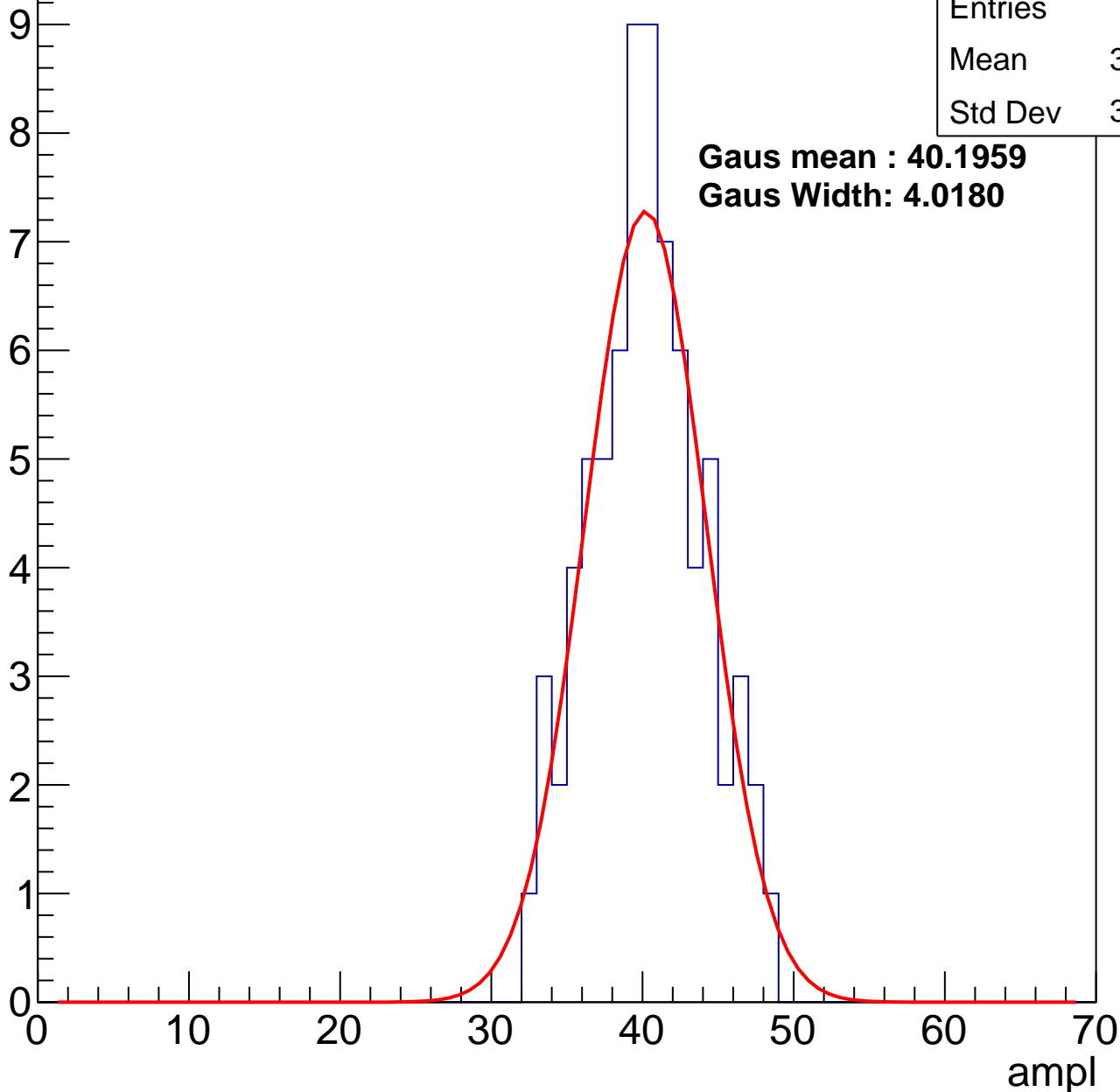
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 39.78 |
| Std Dev | 3.684 |

Gaus mean : 40.1959

Gaus Width: 4.0180



B1L100S, U9-ch37, adc2

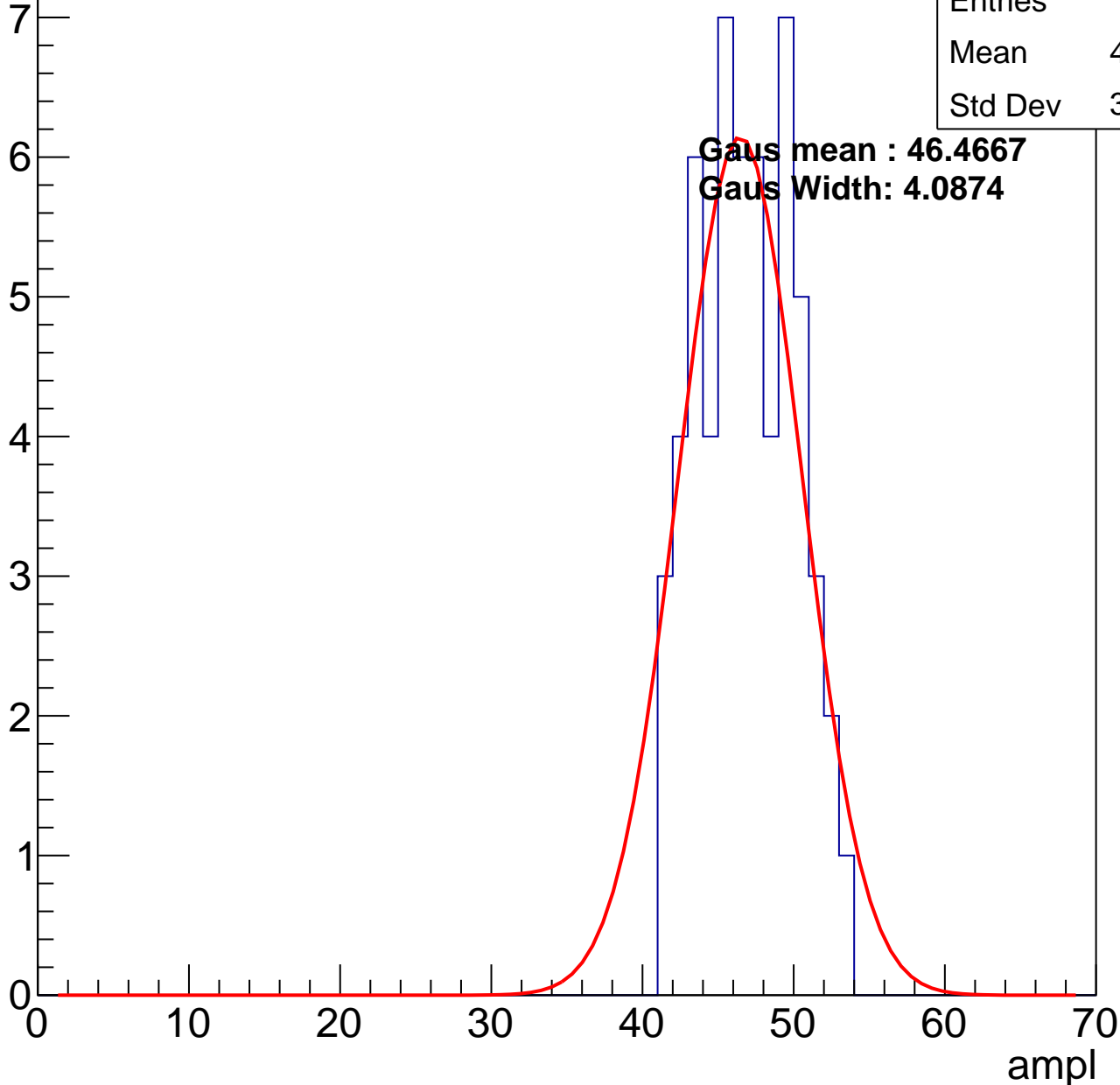
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 46.43 |
| Std Dev | 3.124 |

Gaus mean : 46.4667

Gaus Width: 4.0874

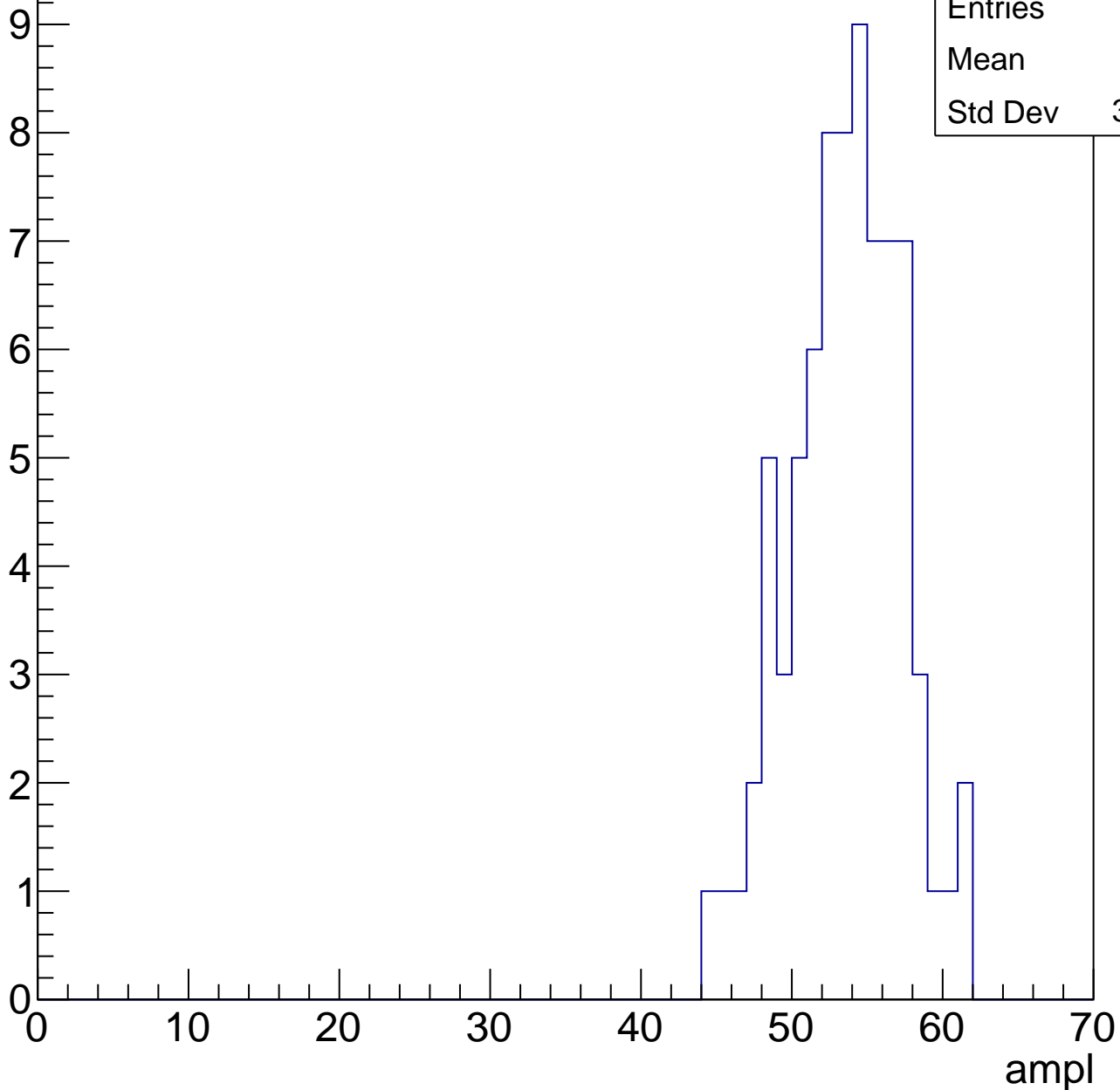


B1L100S, U9-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 53.1 |
| Std Dev | 3.631 |



B1L100S, U9-ch37, adc4

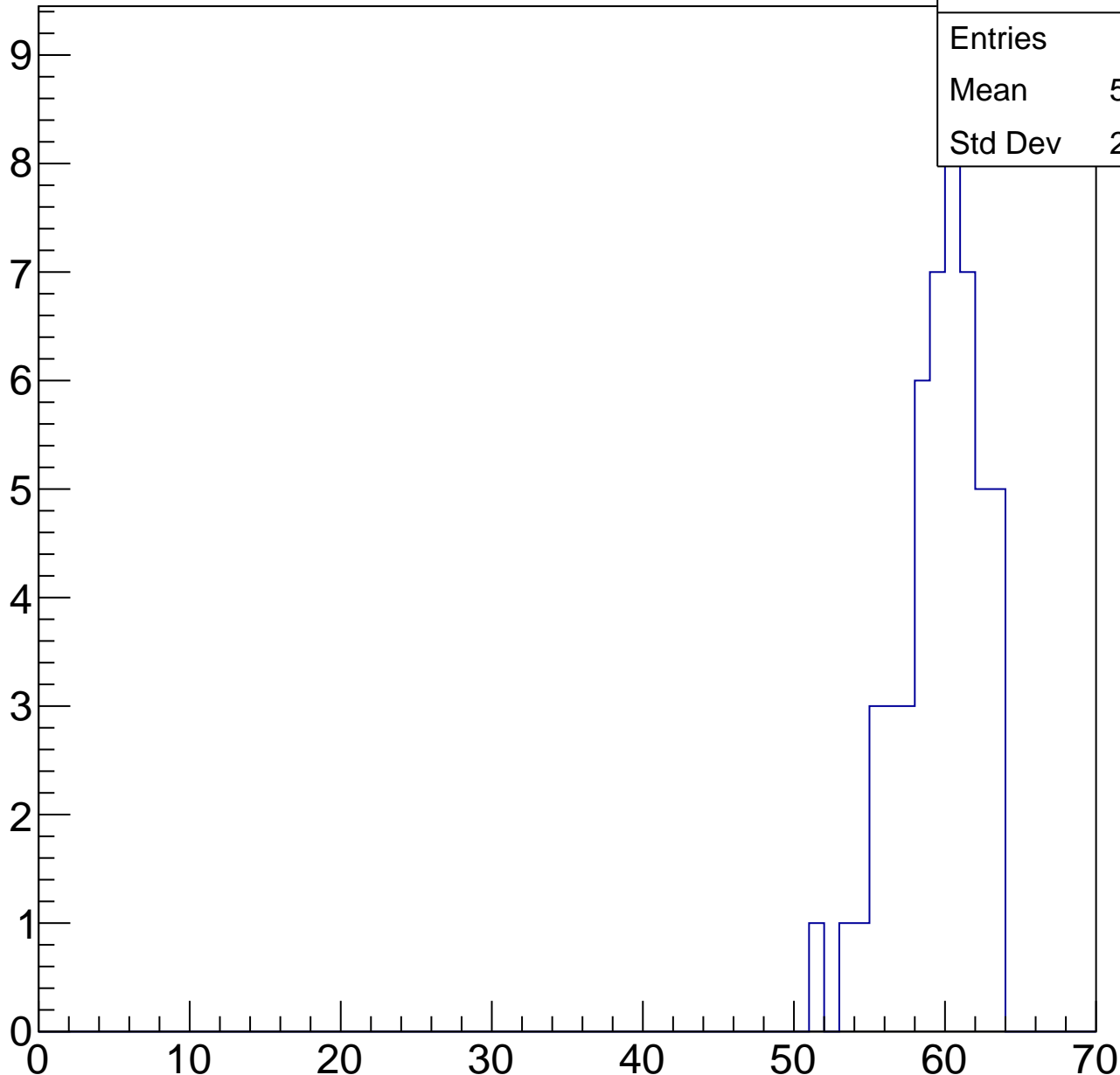
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.12 |
| Std Dev | 2.727 |

ampl

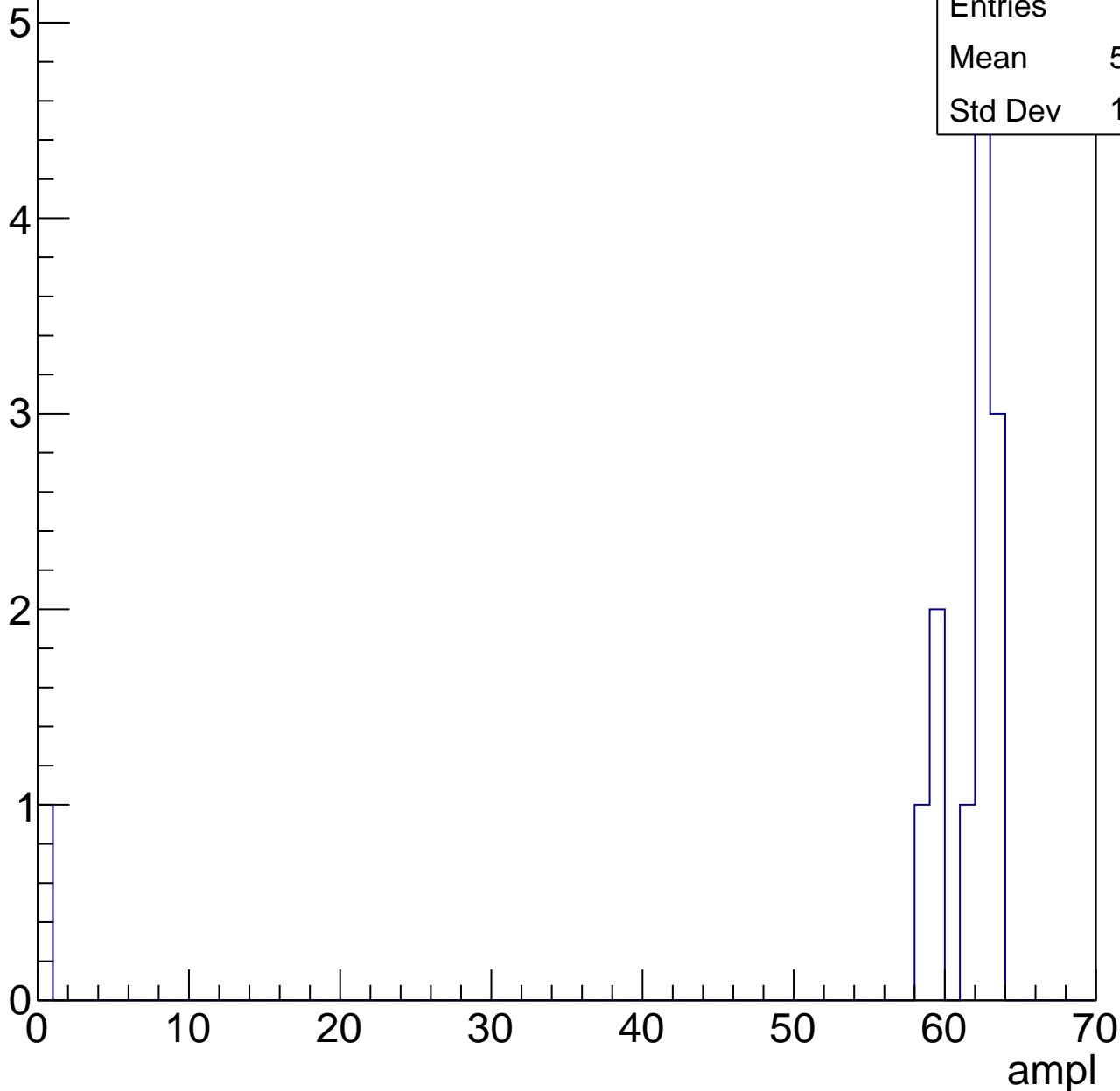


B1L100S, U9-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

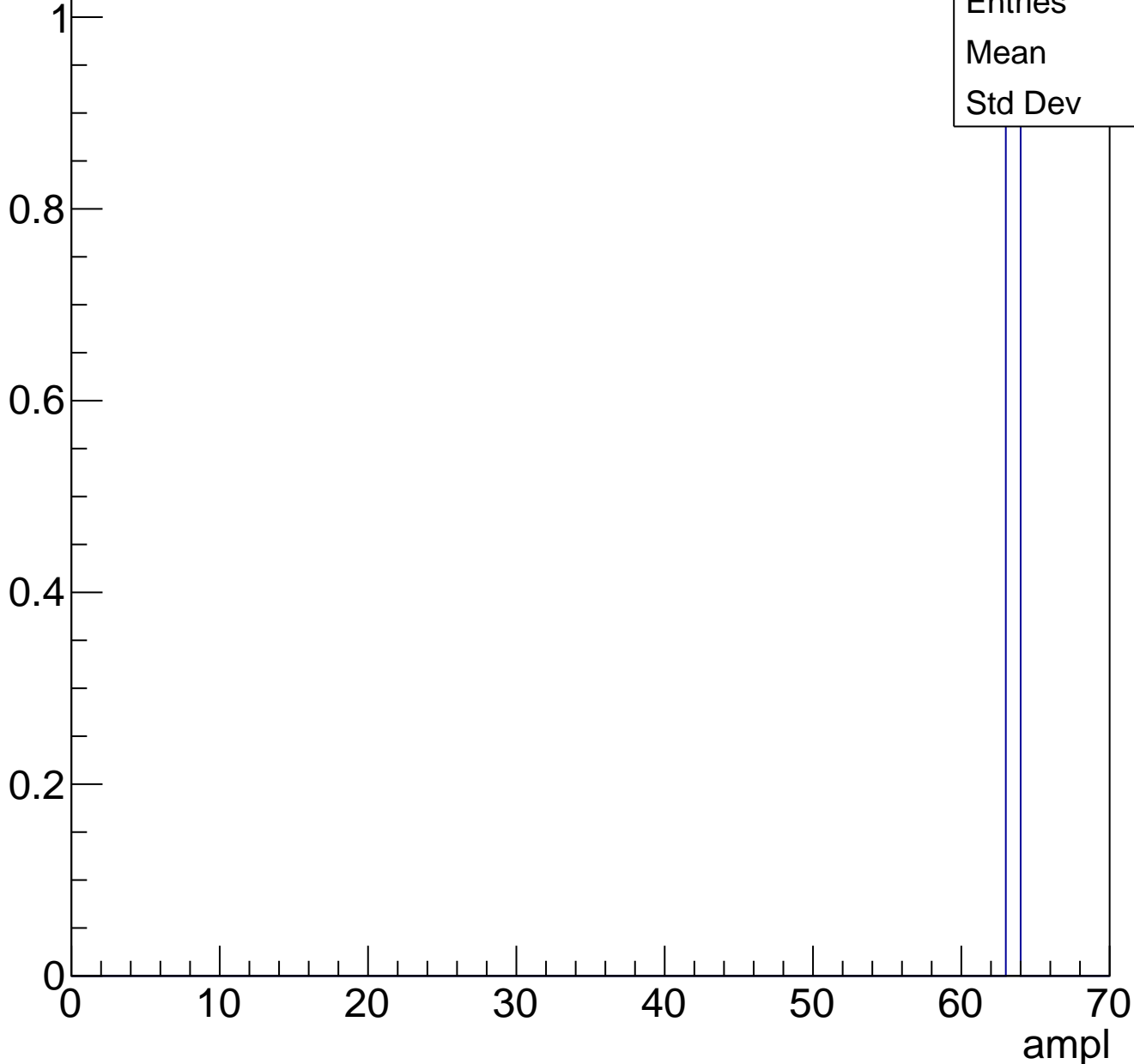
| | |
|---------|-------|
| Entries | 13 |
| Mean | 56.62 |
| Std Dev | 16.42 |



B1L100S, U9-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch38, adc0

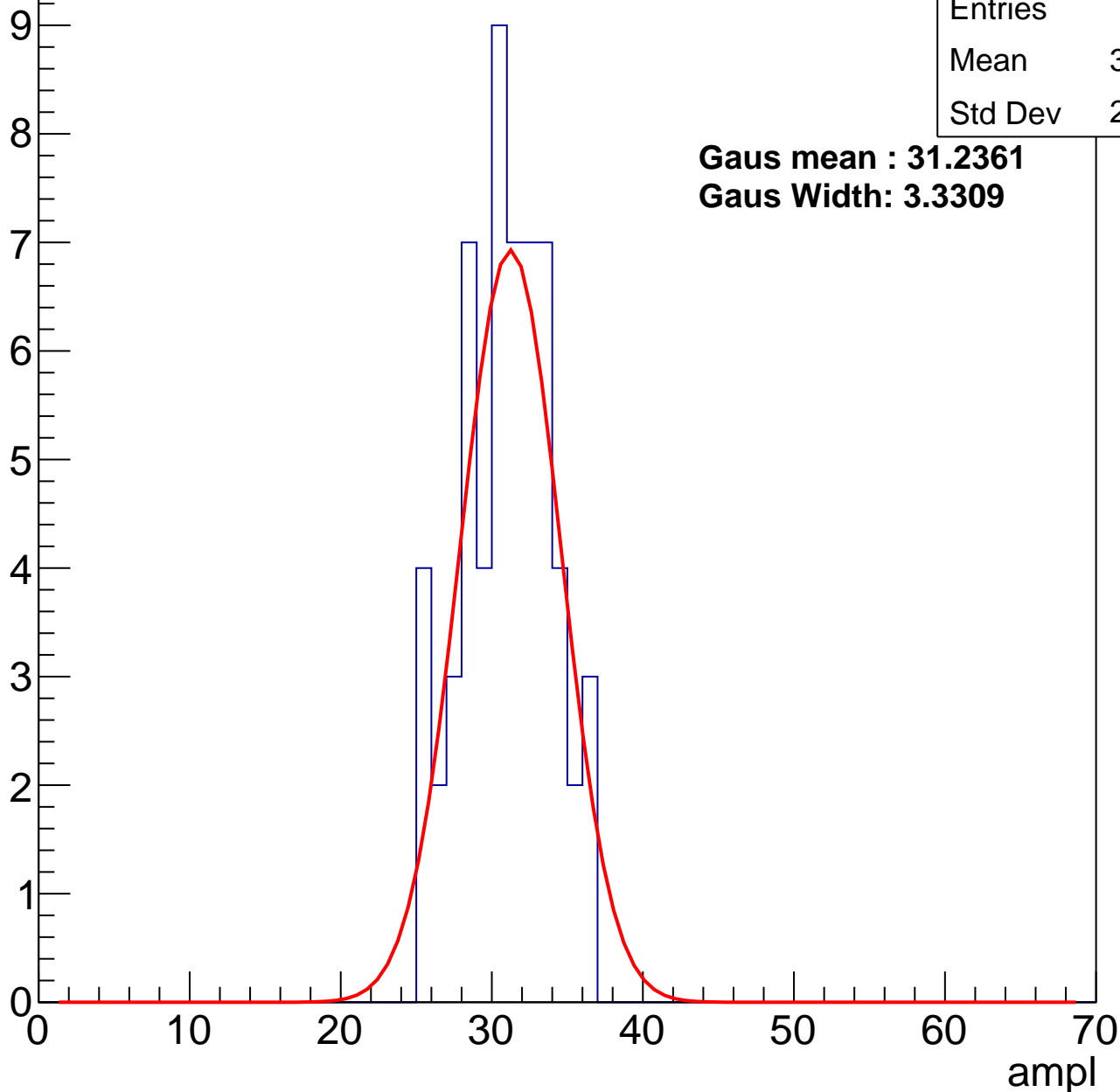
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 30.53 |
| Std Dev | 2.896 |

Gaus mean : 31.2361

Gaus Width: 3.3309



B1L100S, U9-ch38, adc1

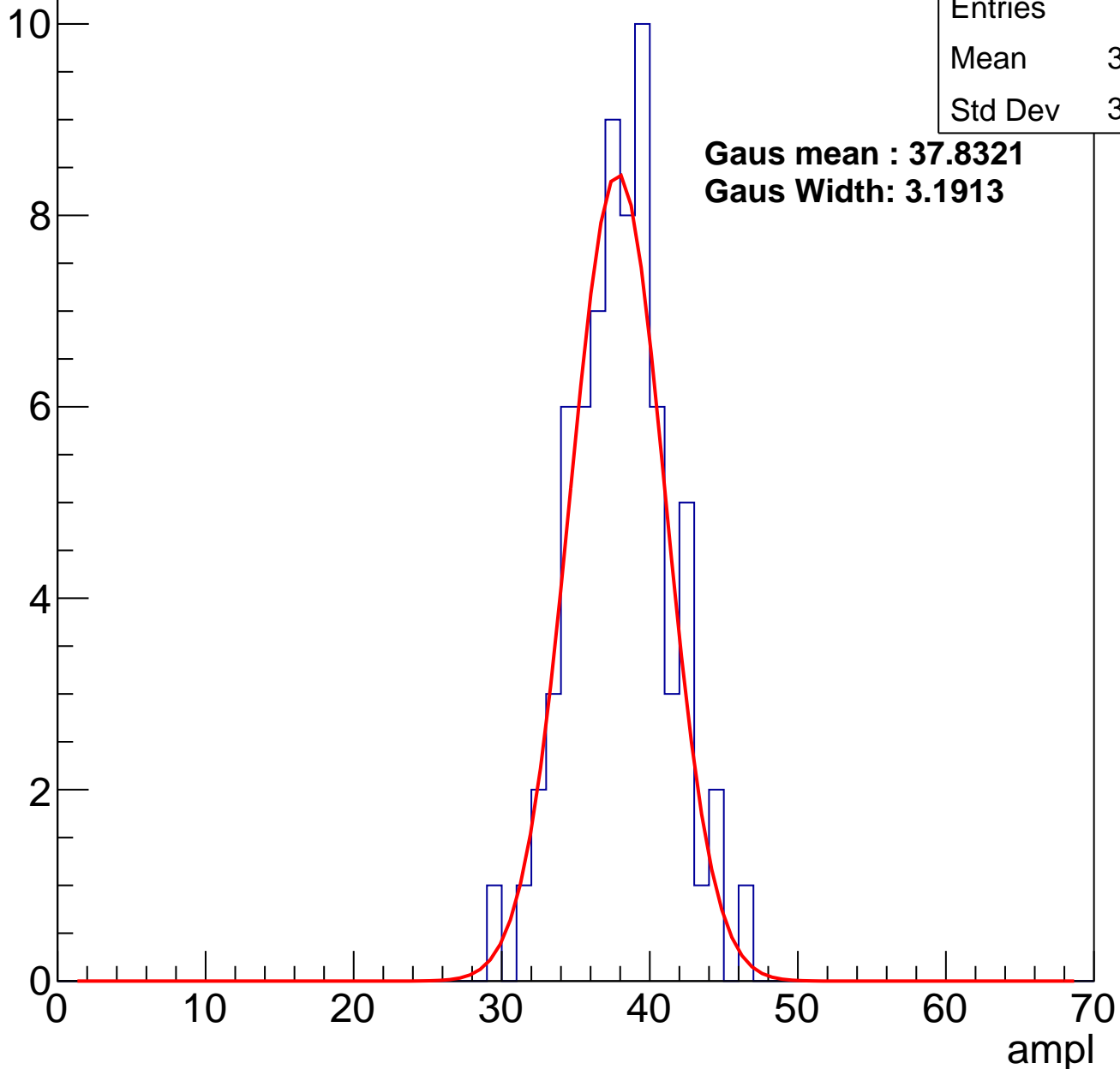
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.55 |
| Std Dev | 3.254 |

Gaus mean : 37.8321

Gaus Width: 3.1913

Entry



B1L100S, U9-ch38, adc2

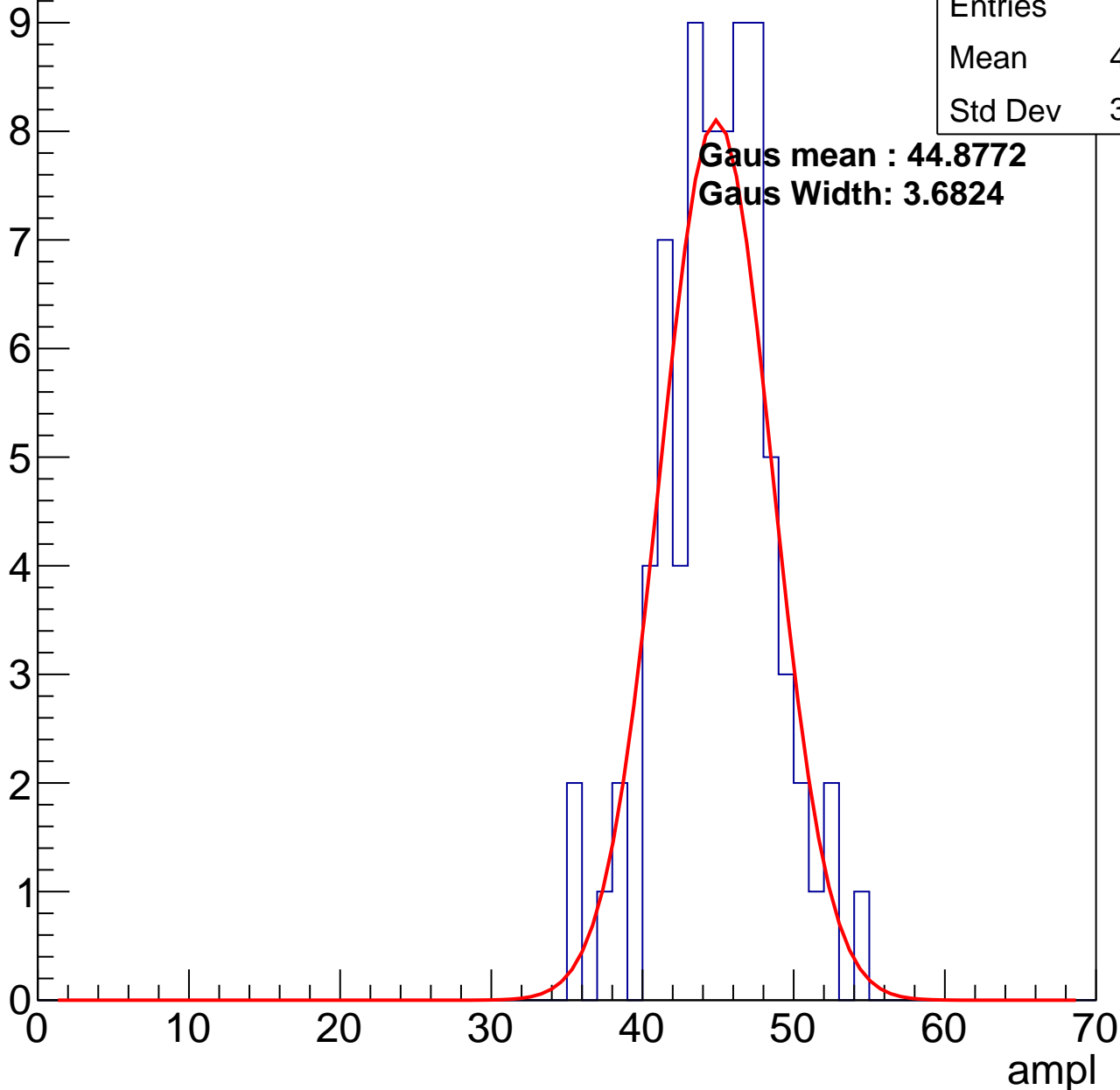
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 44.55 |
| Std Dev | 3.677 |

Gaus mean : 44.8772

Gaus Width: 3.6824

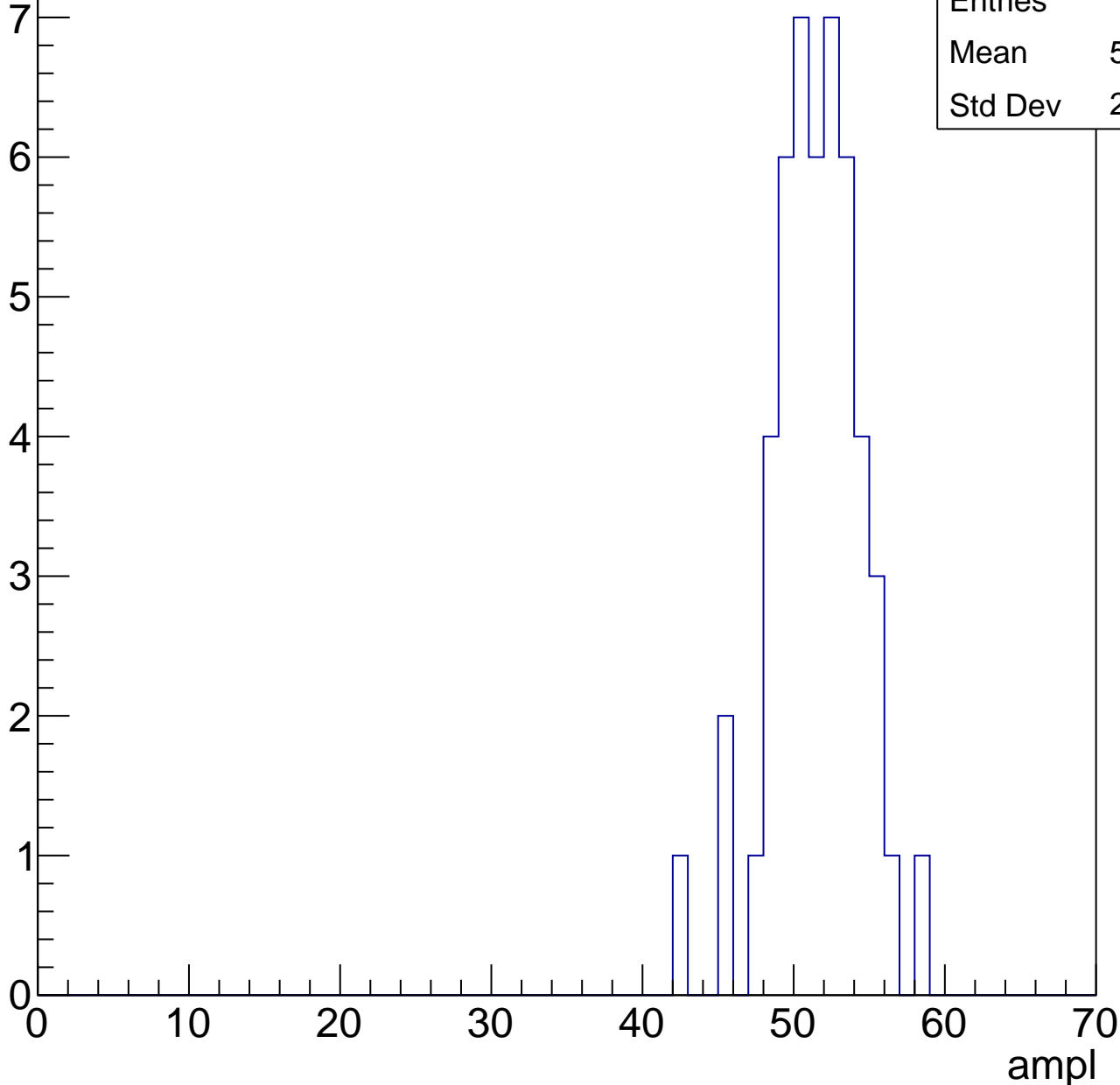


B1L100S, U9-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

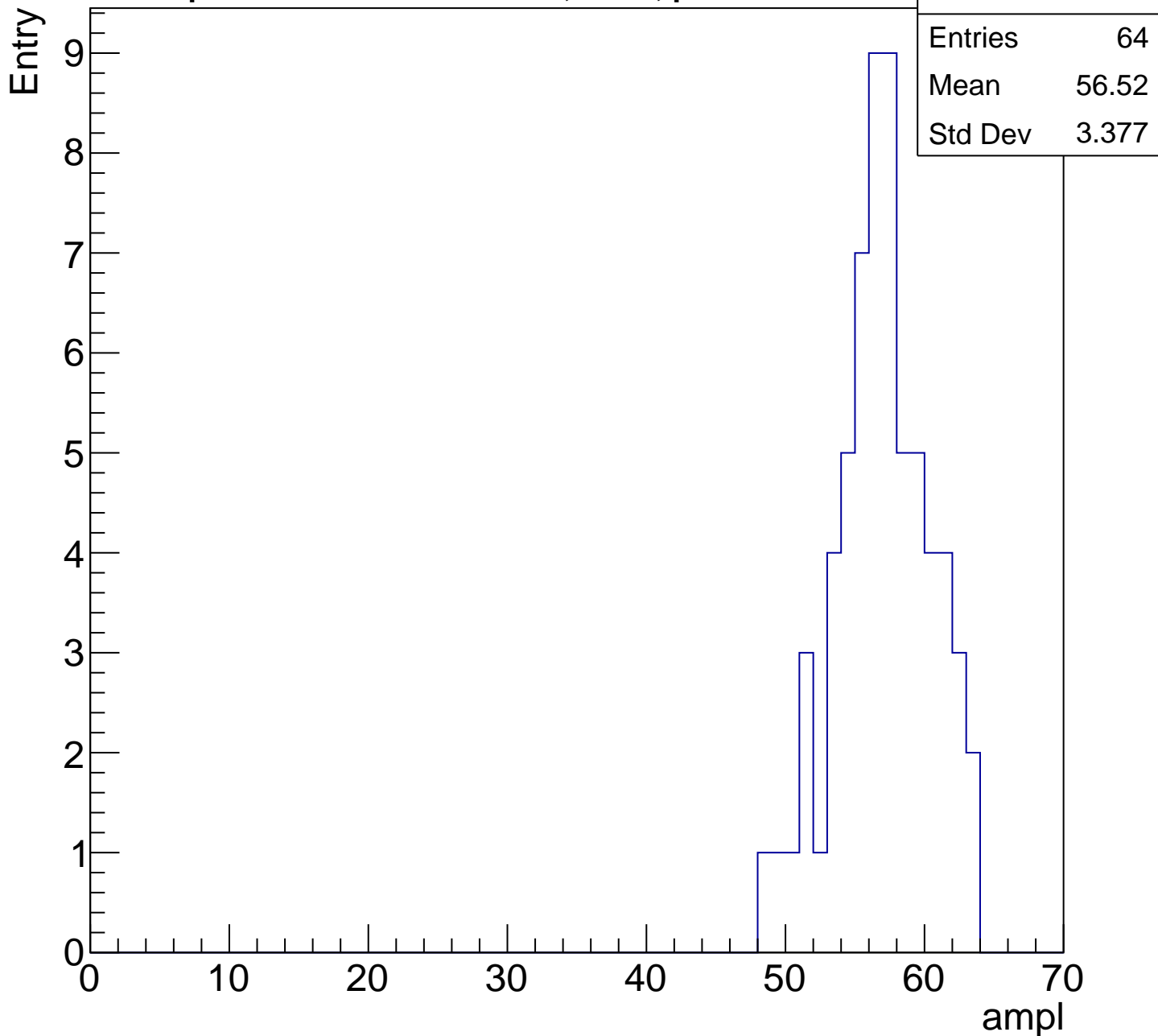
Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 50.98 |
| Std Dev | 2.945 |



B1L100S, U9-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

6

5

4

3

2

1

0

Entries 33

Mean 58.73

Std Dev 10.53

ampl

0

10

20

30

40

50

60

70

B1L100S, U9-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch39, adc0

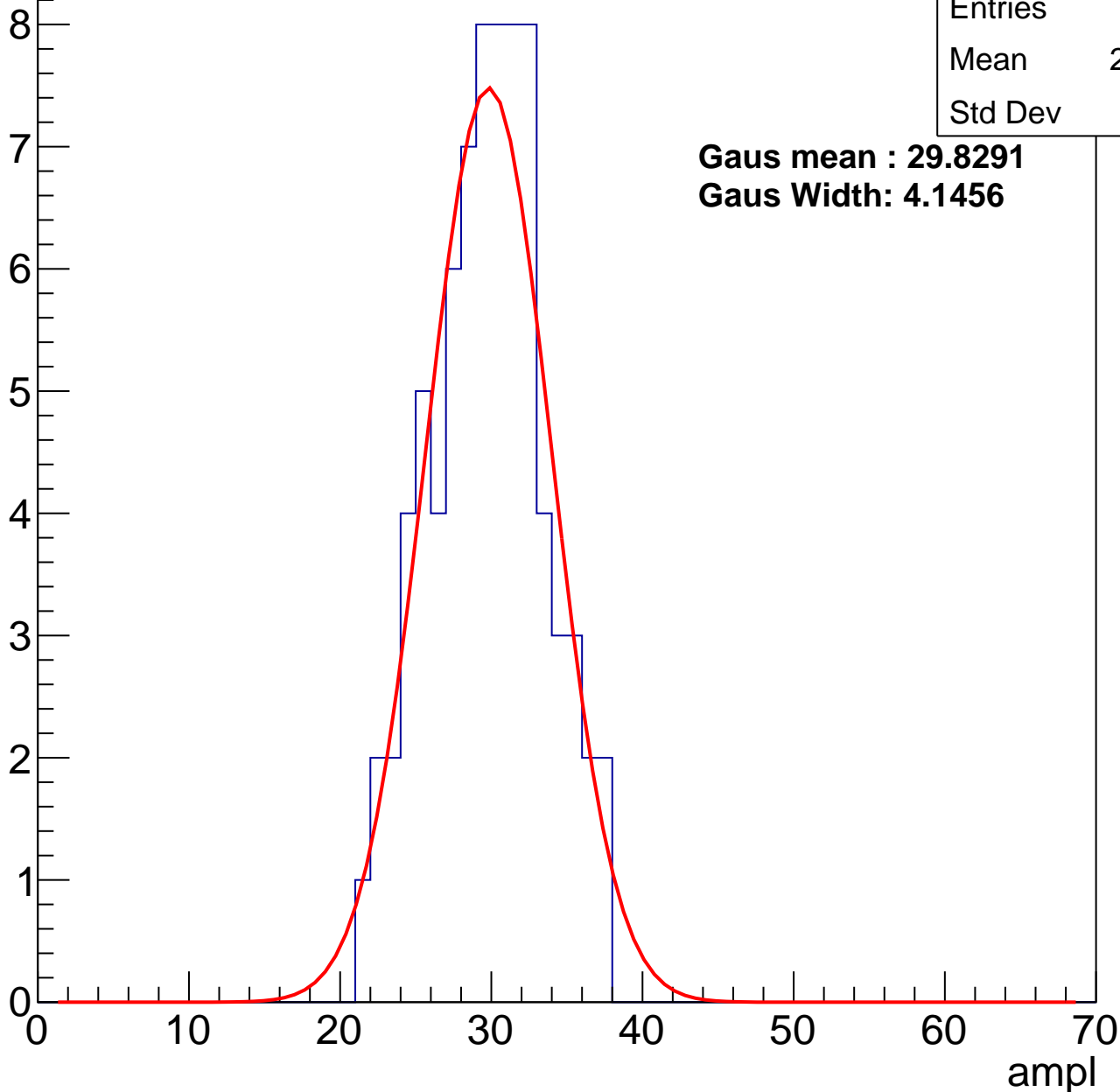
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 29.29 |
| Std Dev | 3.71 |

Gaus mean : 29.8291

Gaus Width: 4.1456



B1L100S, U9-ch39, adc1

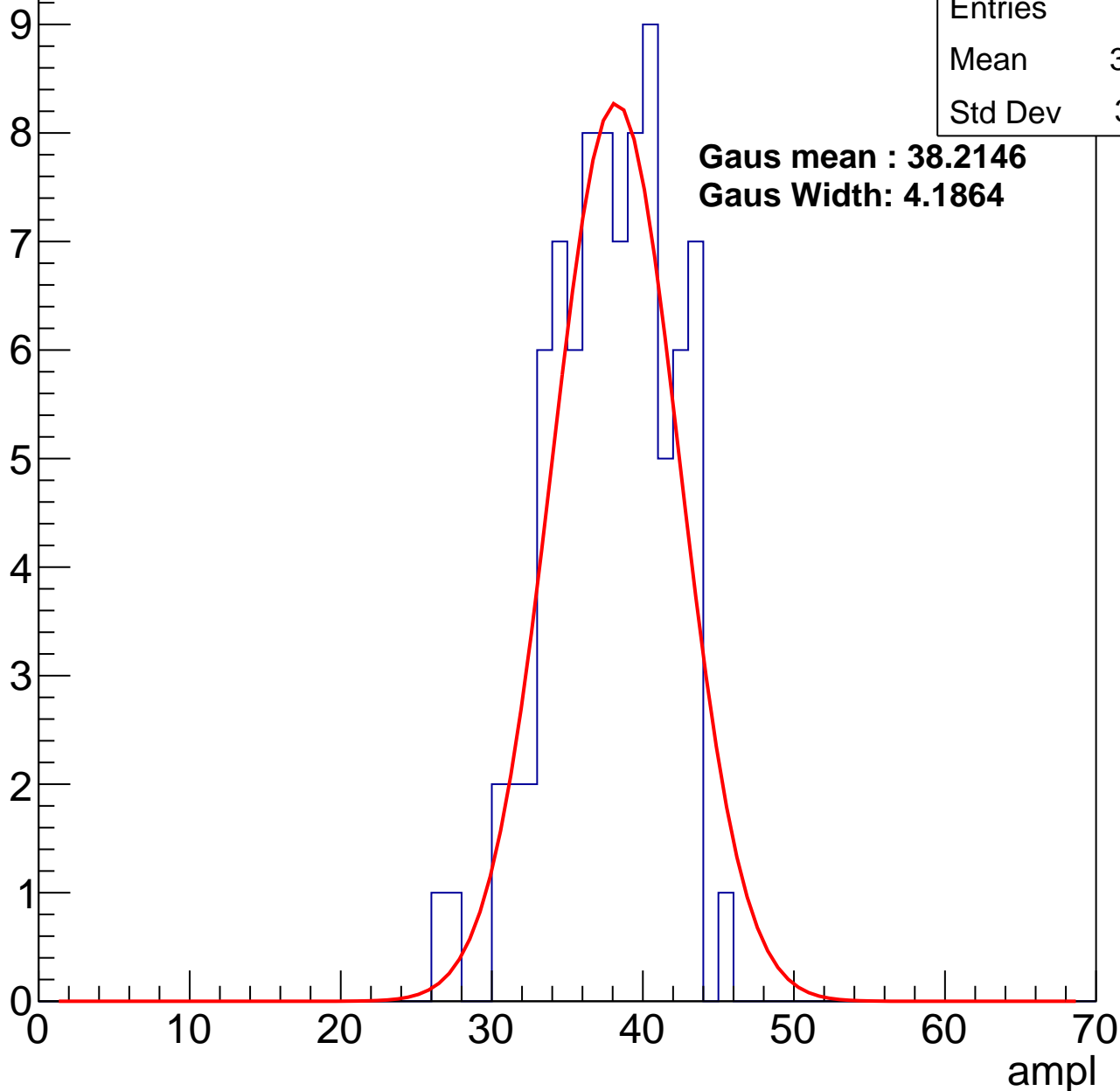
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 37.33 |
| Std Dev | 3.871 |

Gaus mean : 38.2146

Gaus Width: 4.1864



B1L100S, U9-ch39, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 51 |
| Mean | 43.94 |
| Std Dev | 3.102 |

Gaus mean : 44.9889

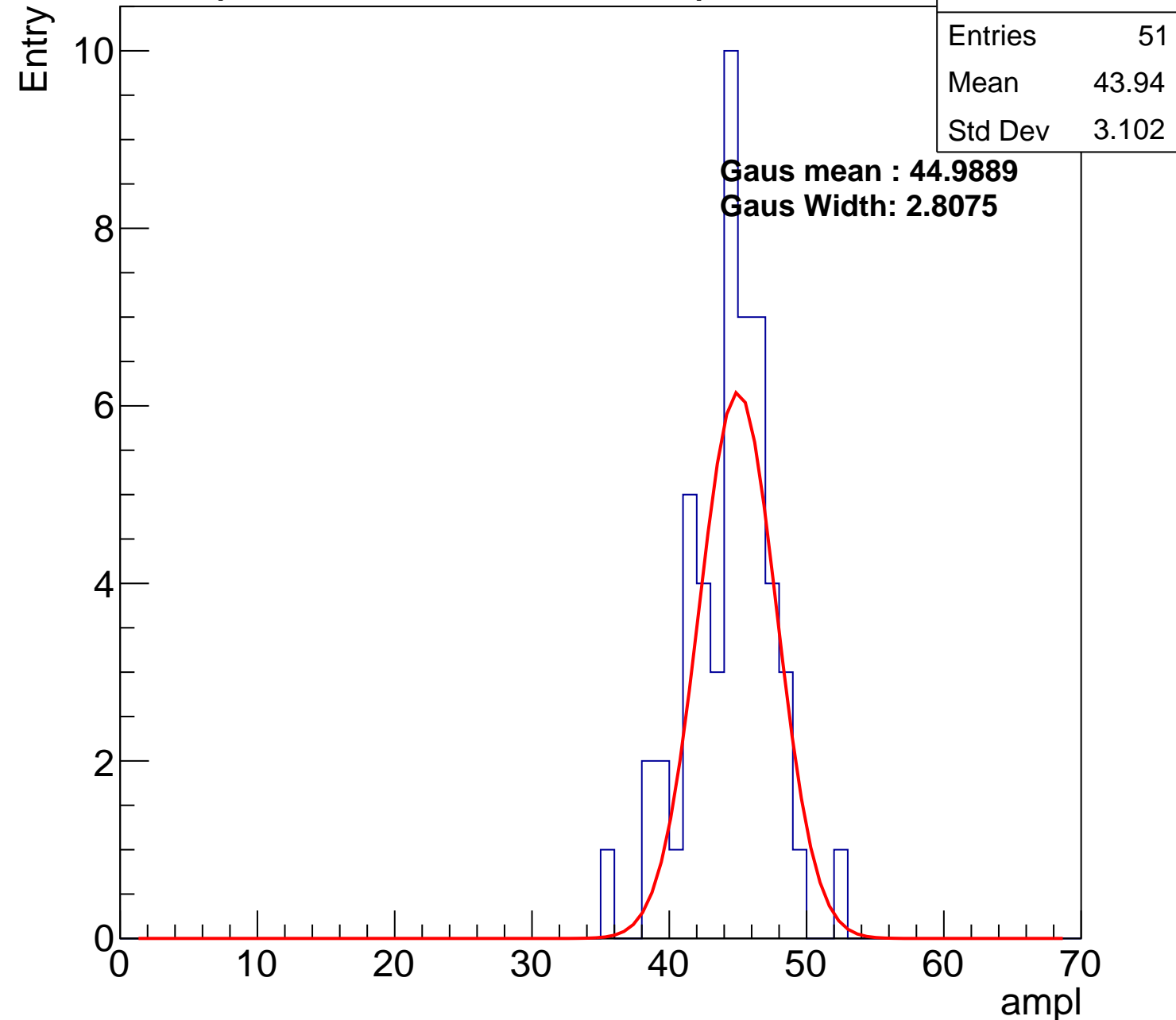
Gaus Width: 2.8075

Entry

10
8
6
4
2
0

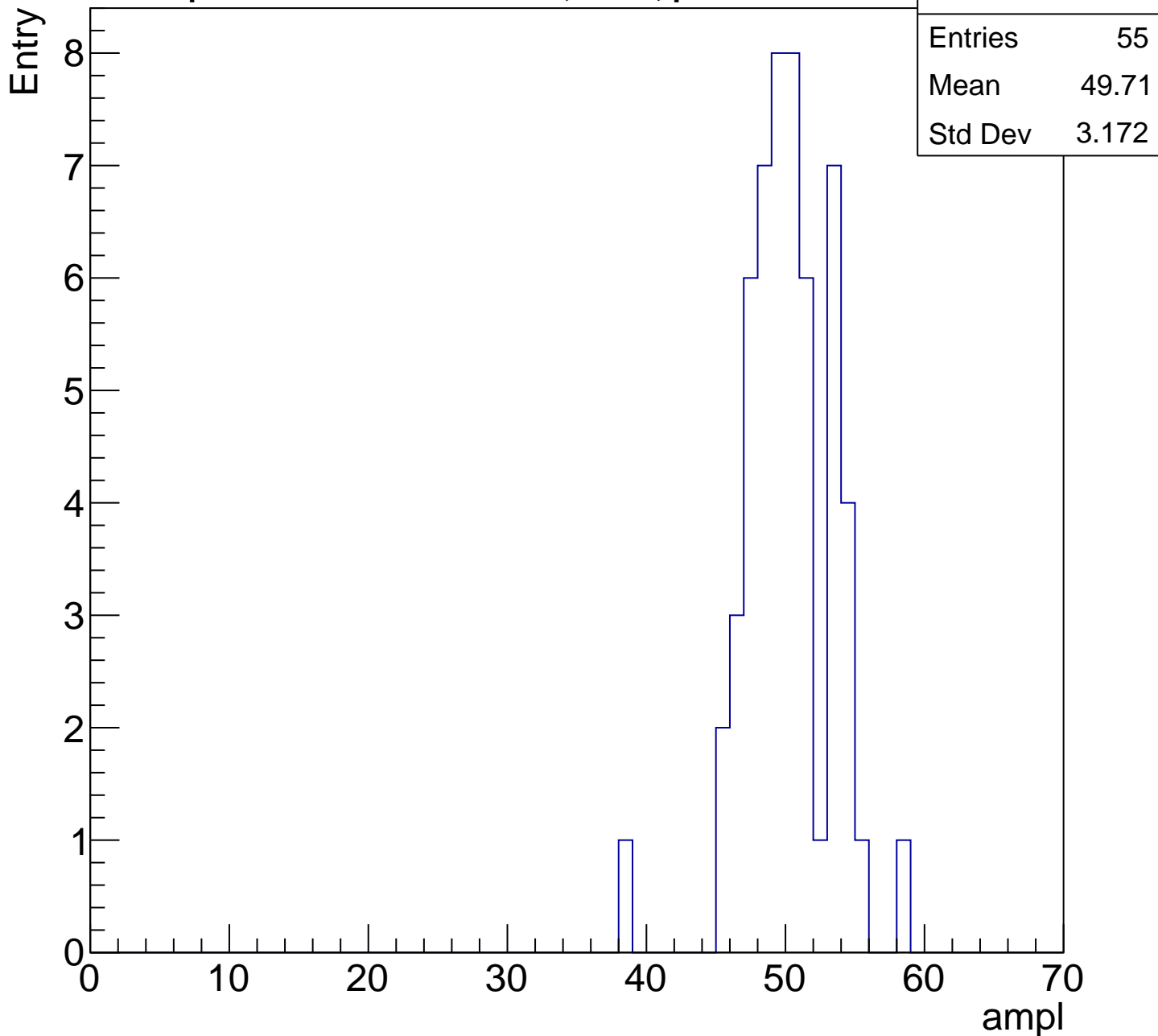
ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

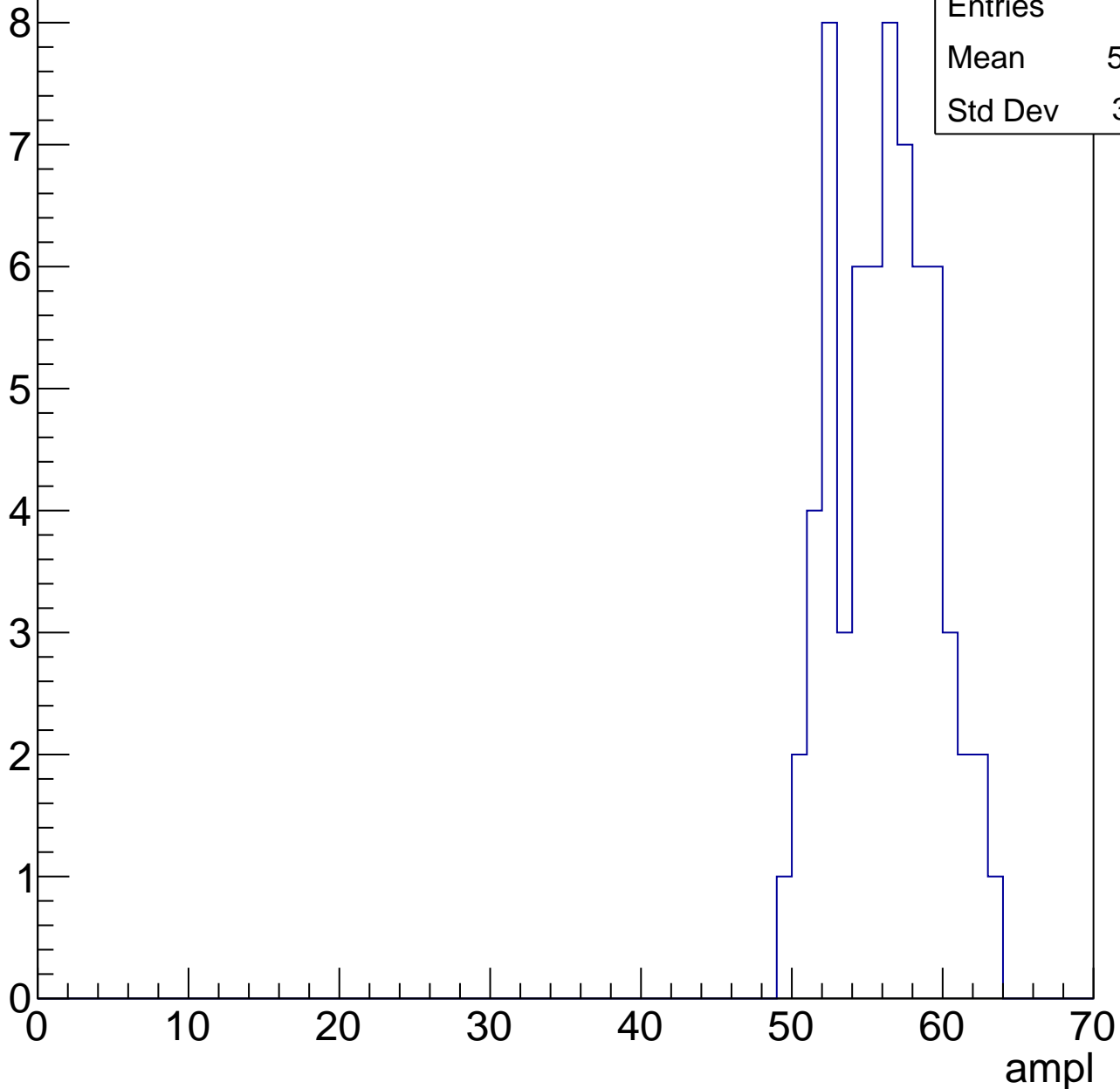


B1L100S, U9-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 55.69 |
| Std Dev | 3.291 |



B1L100S, U9-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.85 |
| Std Dev | 8.992 |

ampl

0

10

20

30

40

50

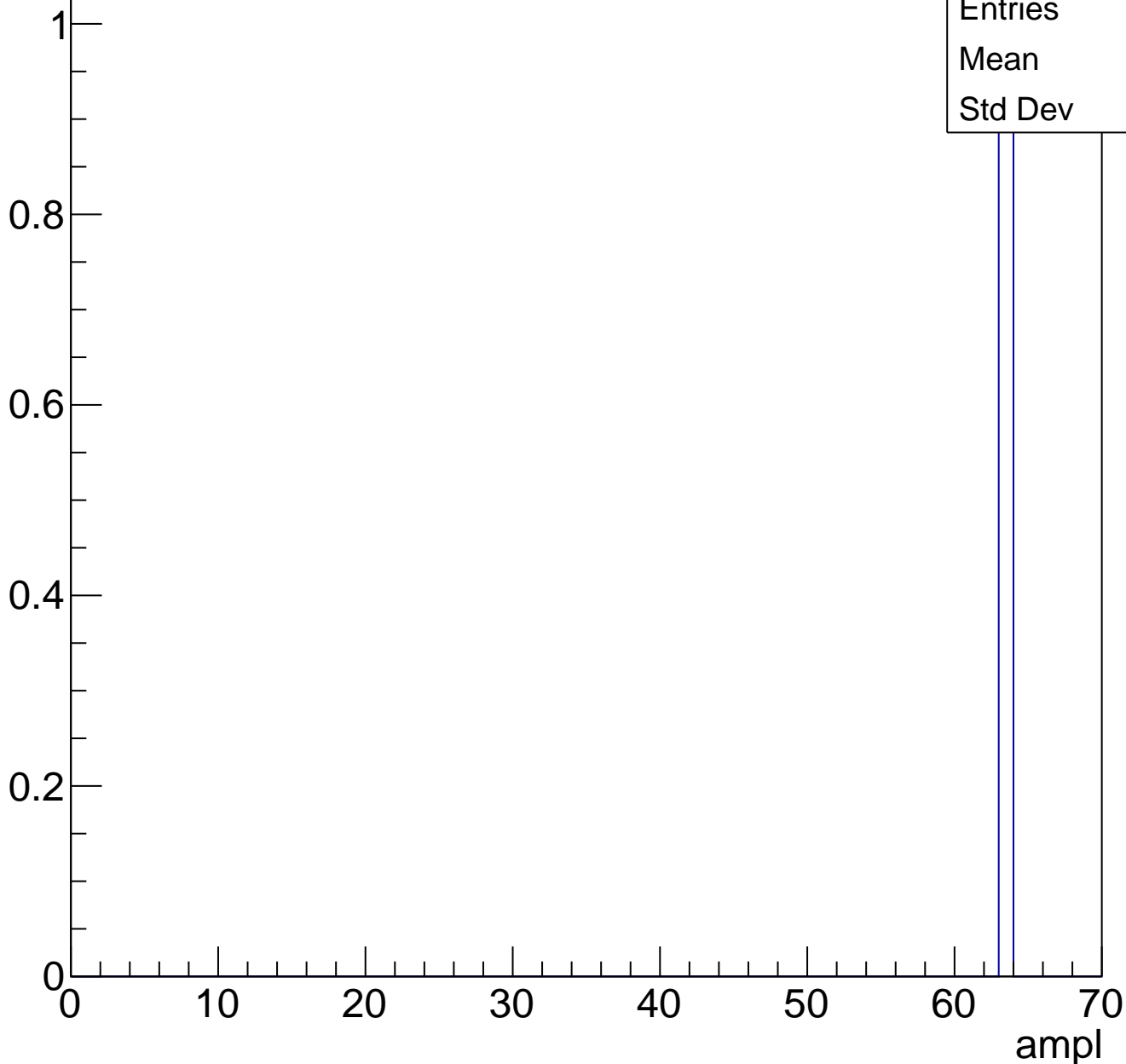
60

70

B1L100S, U9-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch40, adc0

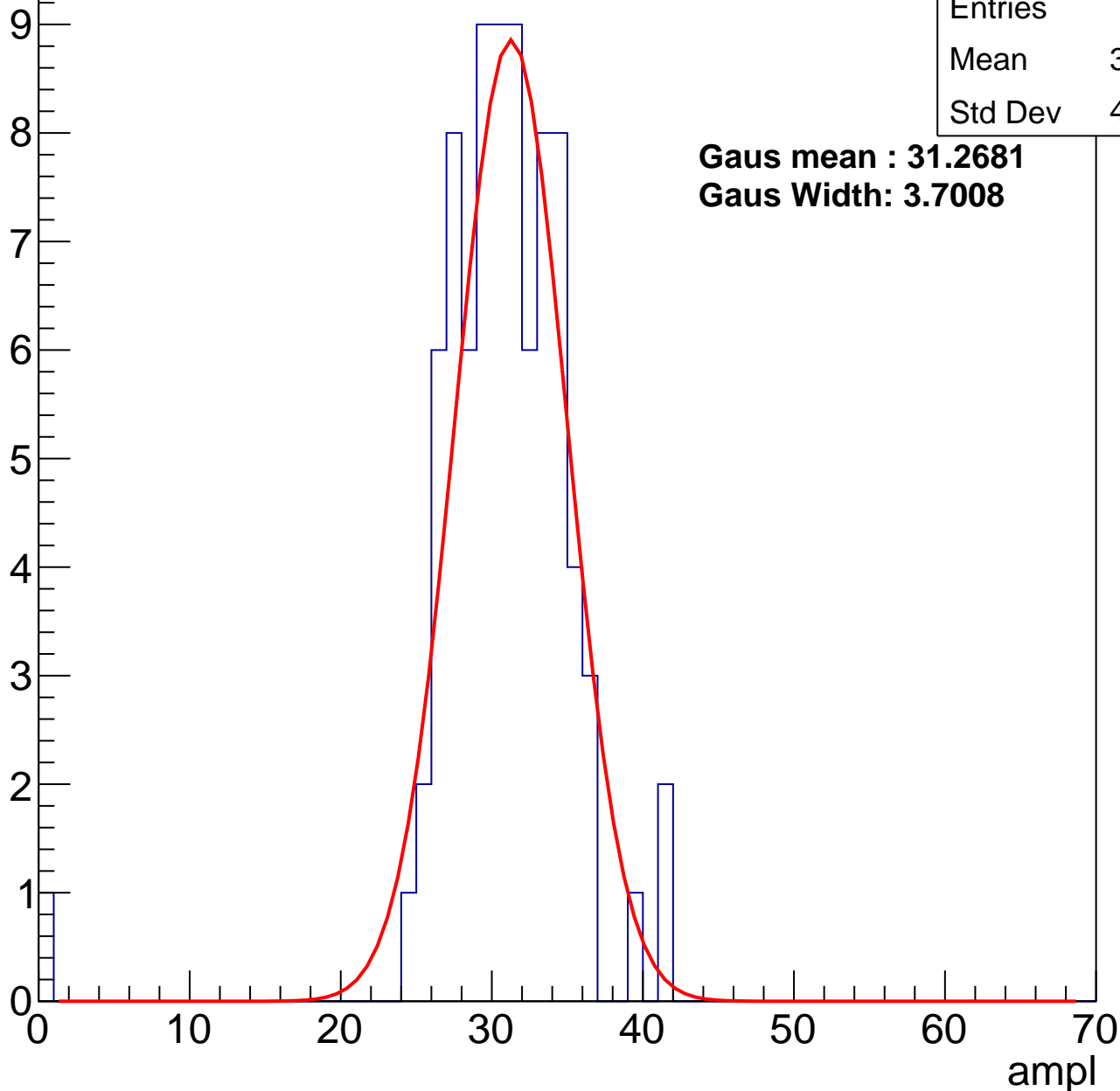
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 30.37 |
| Std Dev | 4.834 |

Gaus mean : 31.2681

Gaus Width: 3.7008



B1L100S, U9-ch40, adc1

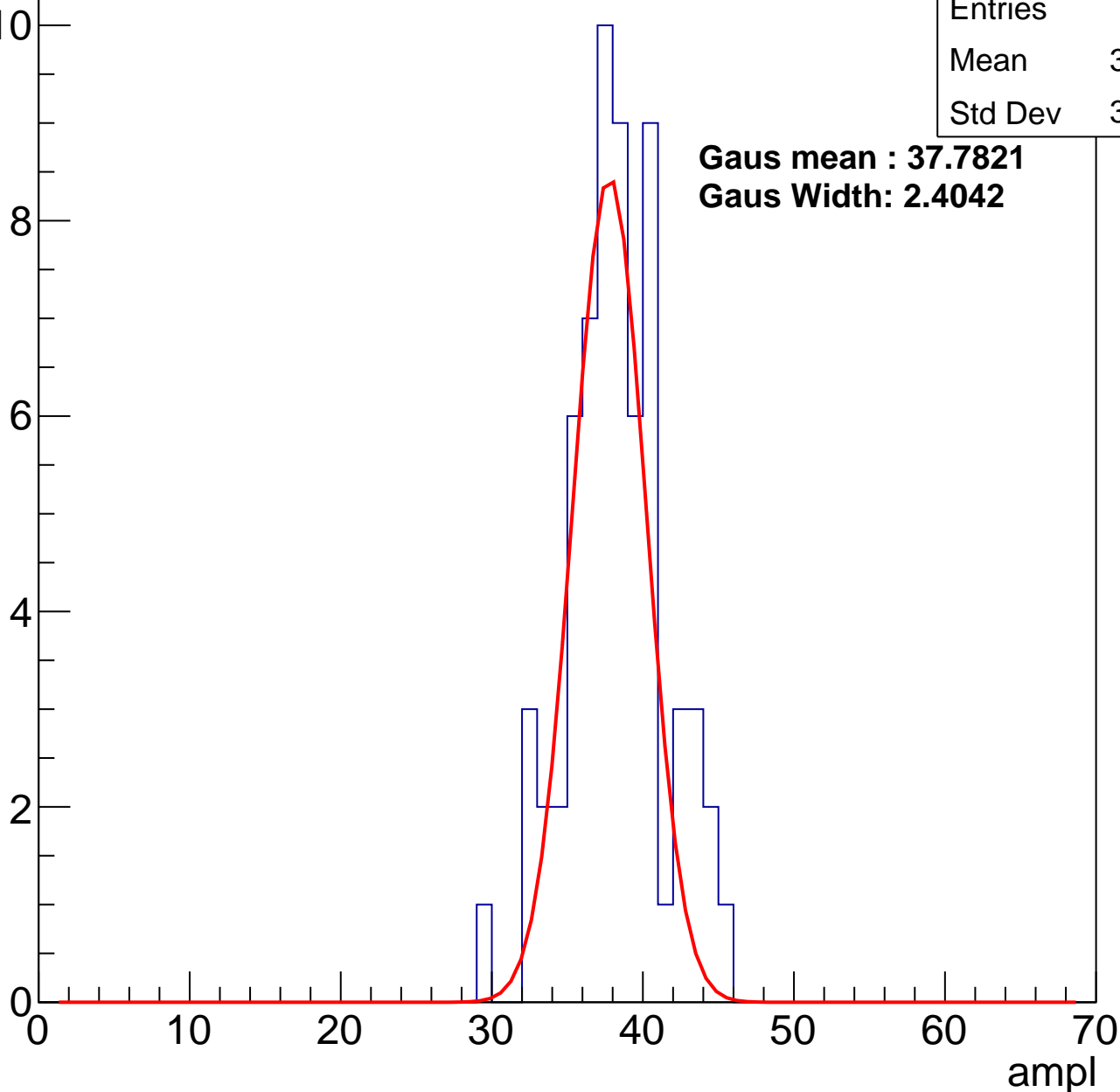
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 37.78 |
| Std Dev | 3.174 |

Gaus mean : 37.7821

Gaus Width: 2.4042

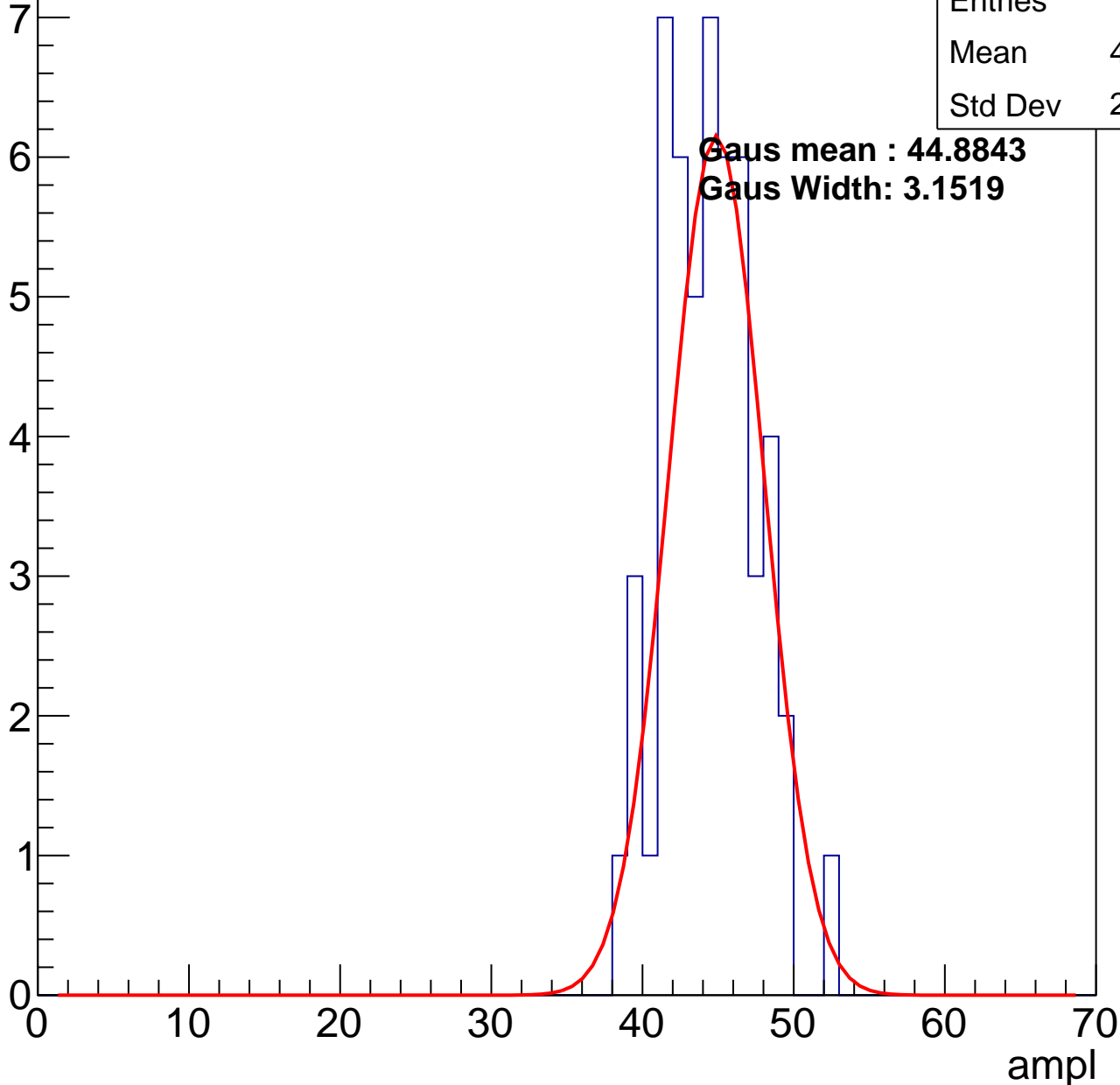


B1L100S, U9-ch40, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 43.96 |
| Std Dev | 2.955 |



B1L100S, U9-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

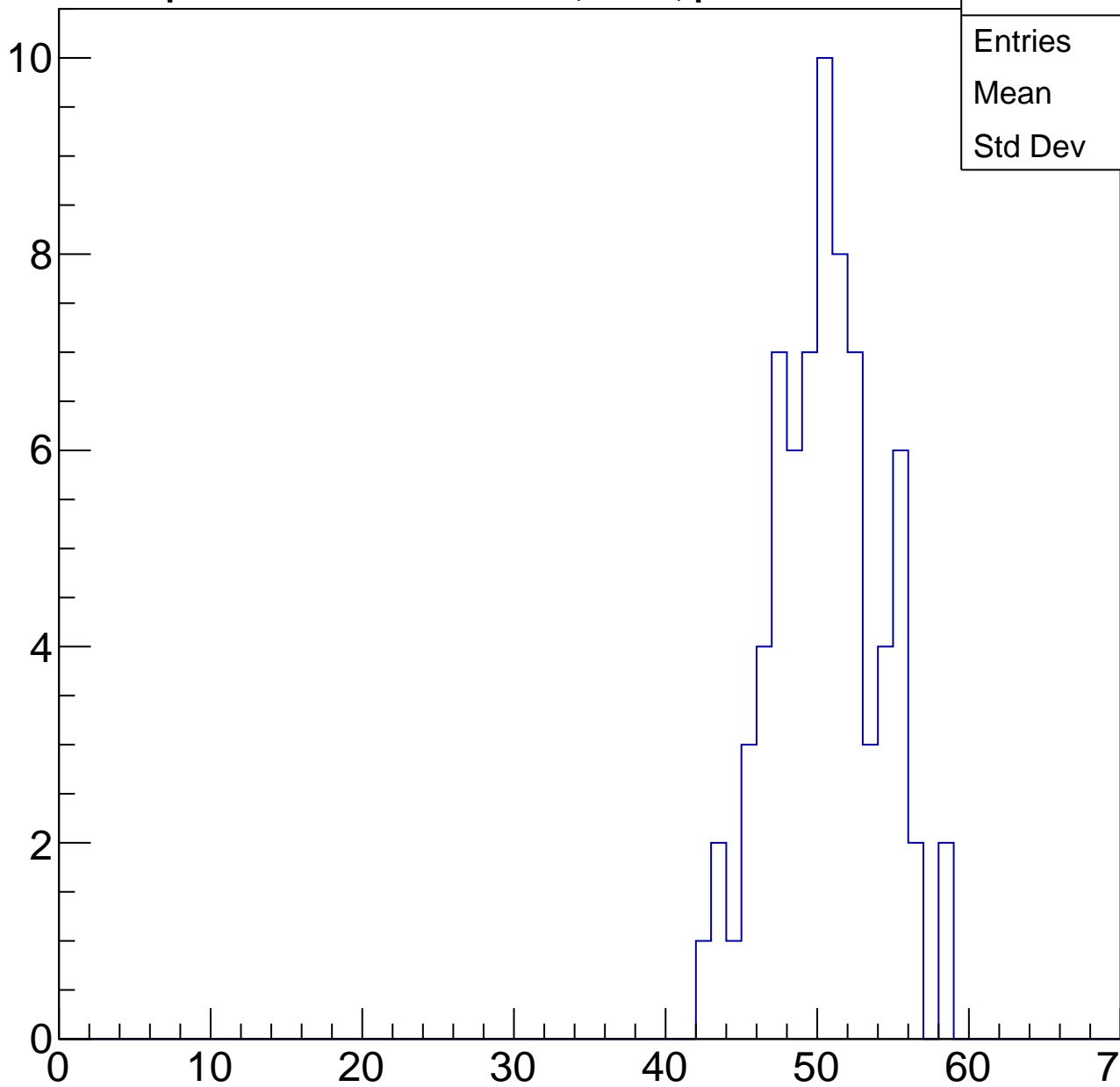
| | |
|---------|-------|
| Entries | 73 |
| Mean | 50.08 |
| Std Dev | 3.526 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

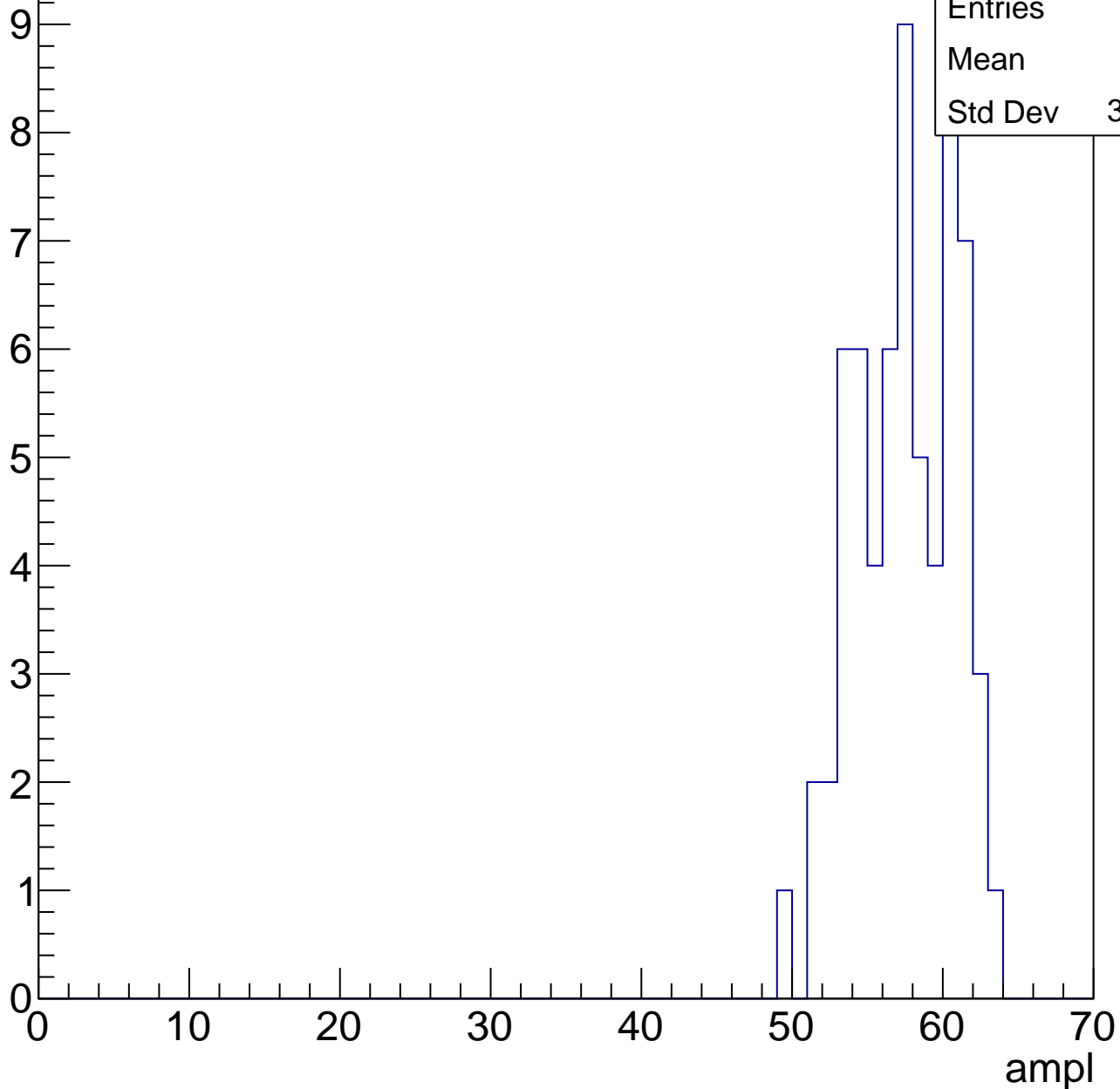
ampl



B1L100S, U9-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

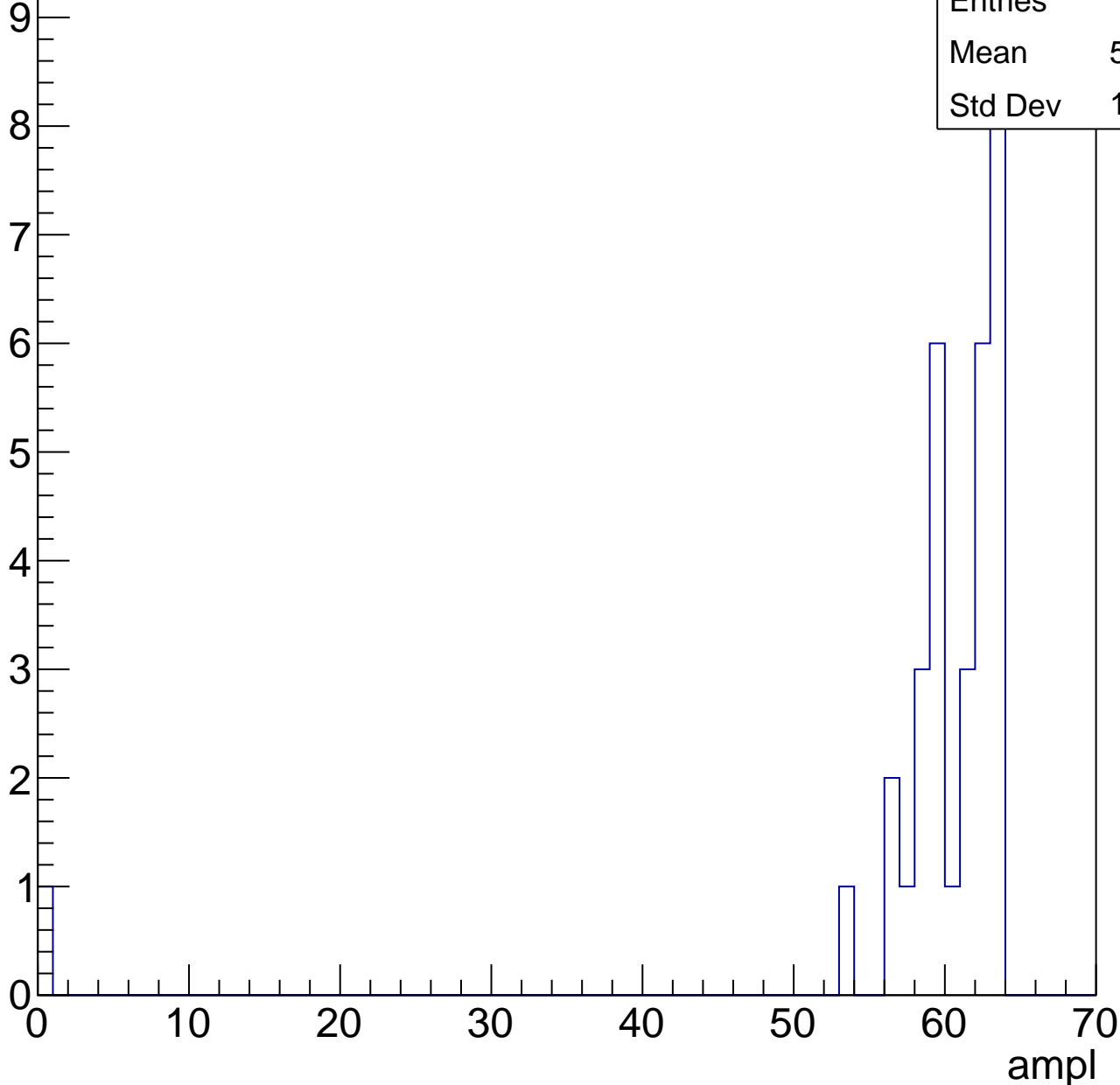


B1L100S, U9-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 33 |
| Mean | 58.55 |
| Std Dev | 10.65 |



B1L100S, U9-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch41, adc0

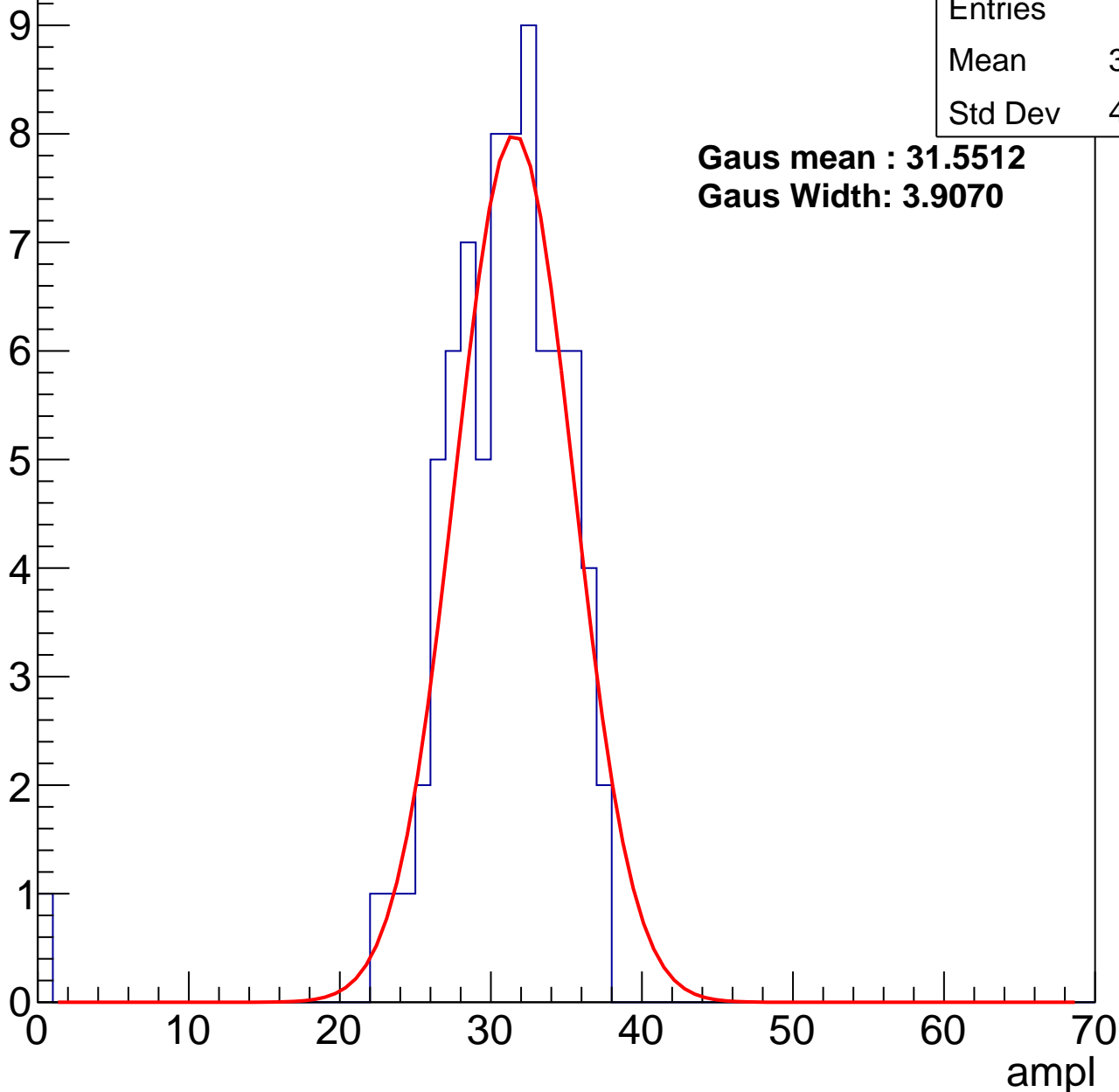
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 30.23 |
| Std Dev | 4.867 |

Gaus mean : 31.5512

Gaus Width: 3.9070



B1L100S, U9-ch41, adc1

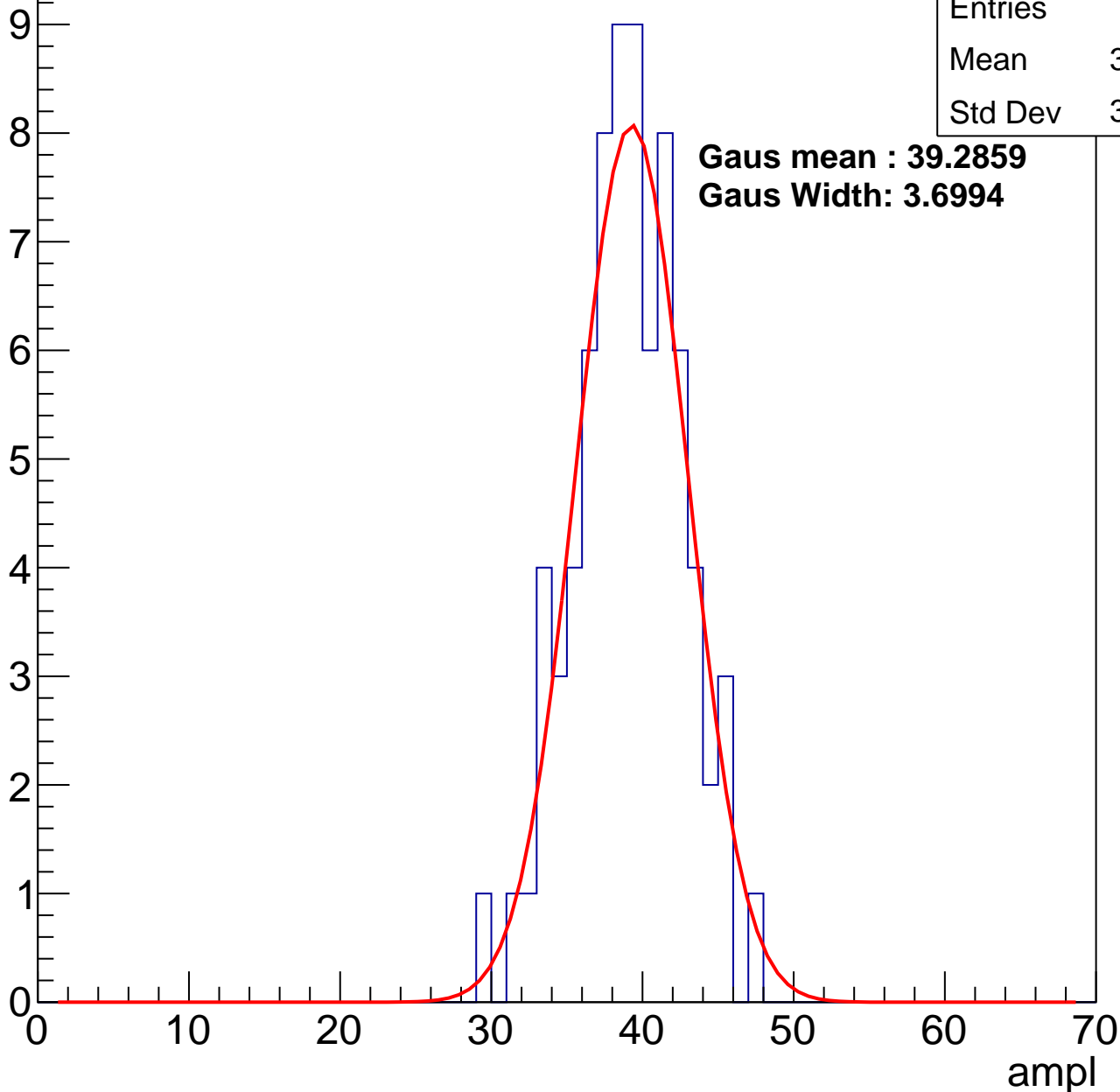
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 38.59 |
| Std Dev | 3.547 |

Gaus mean : 39.2859

Gaus Width: 3.6994



B1L100S, U9-ch41, adc2

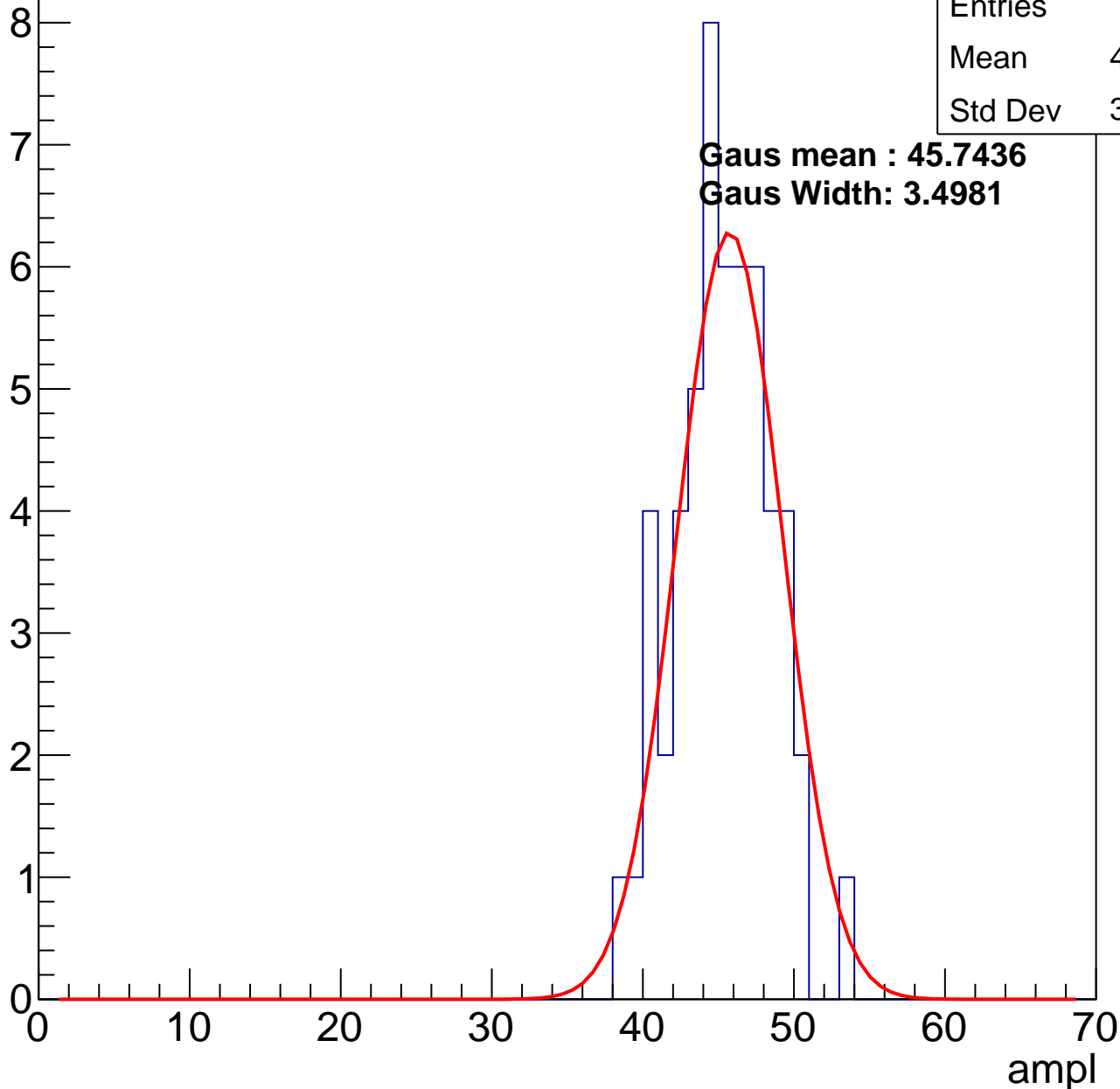
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 44.87 |
| Std Dev | 3.115 |

Gaus mean : 45.7436

Gaus Width: 3.4981

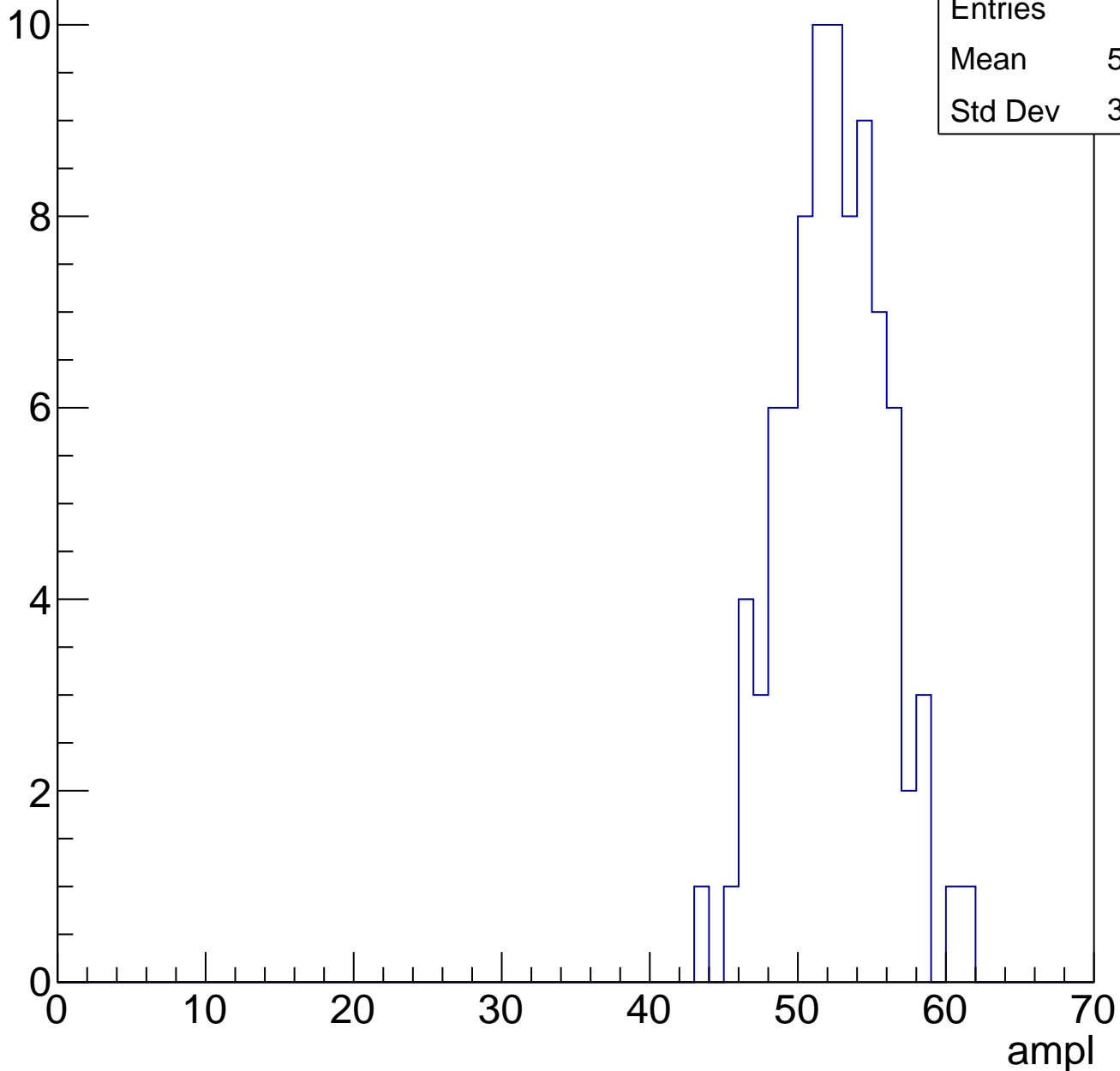


B1L100S, U9-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 51.92 |
| Std Dev | 3.488 |



B1L100S, U9-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

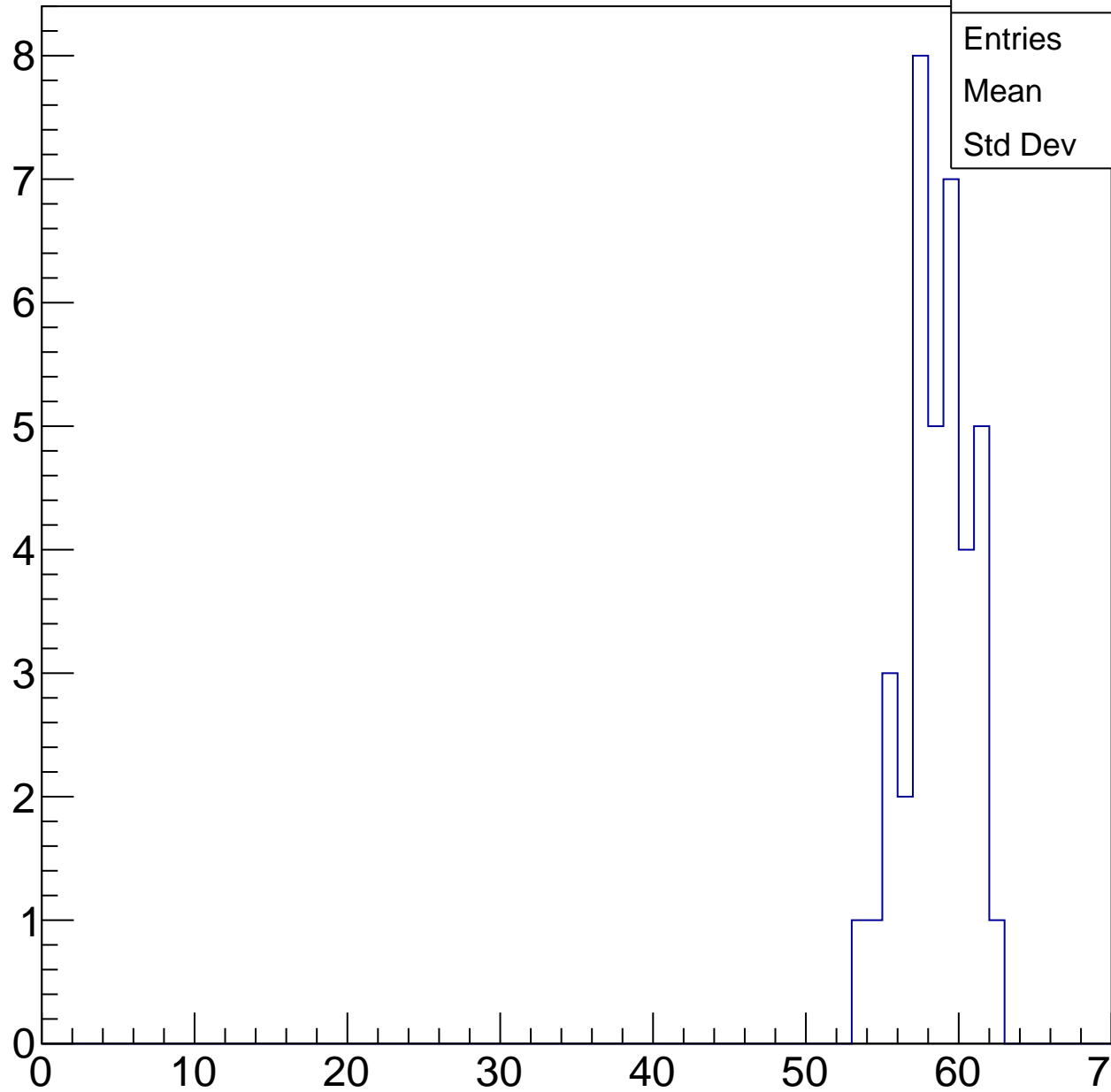
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 37 |
| Mean | 58.11 |
| Std Dev | 2.128 |

ampl

0 10 20 30 40 50 60 70

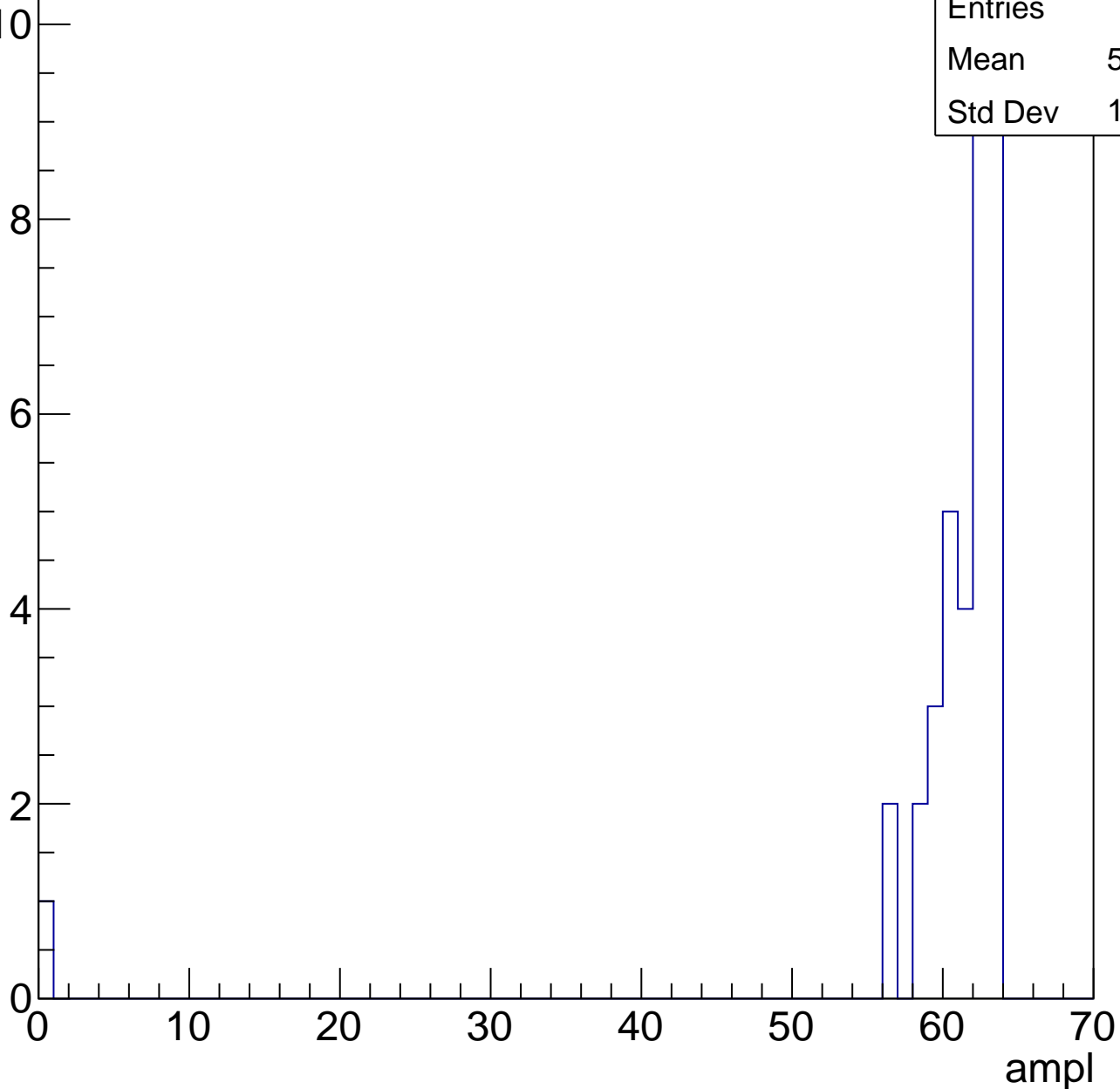


B1L100S, U9-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 36 |
| Mean | 59.36 |
| Std Dev | 10.22 |



B1L100S, U9-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch42, adc0

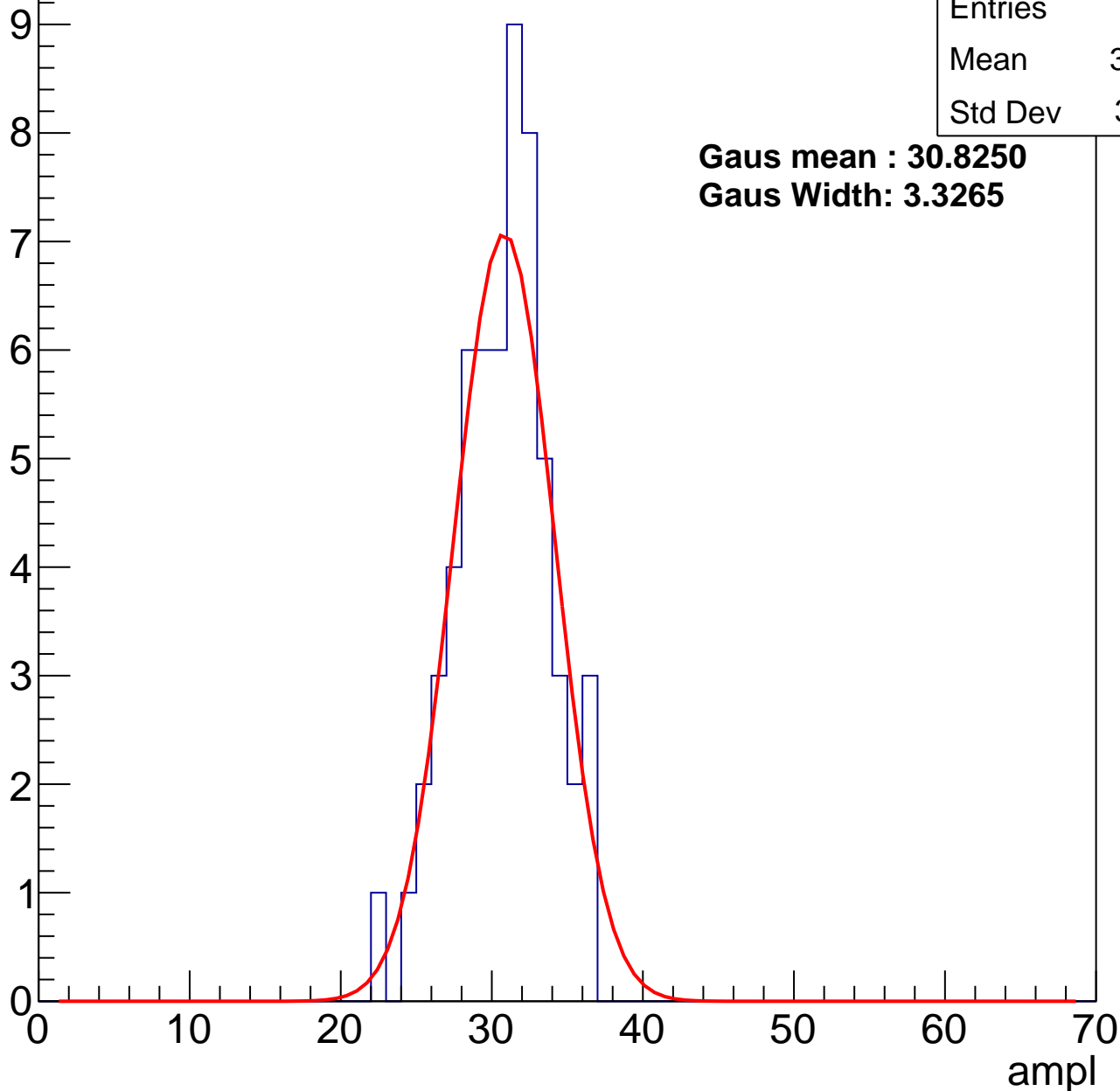
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 30.24 |
| Std Dev | 3.061 |

Gaus mean : 30.8250

Gaus Width: 3.3265



B1L100S, U9-ch42, adc1

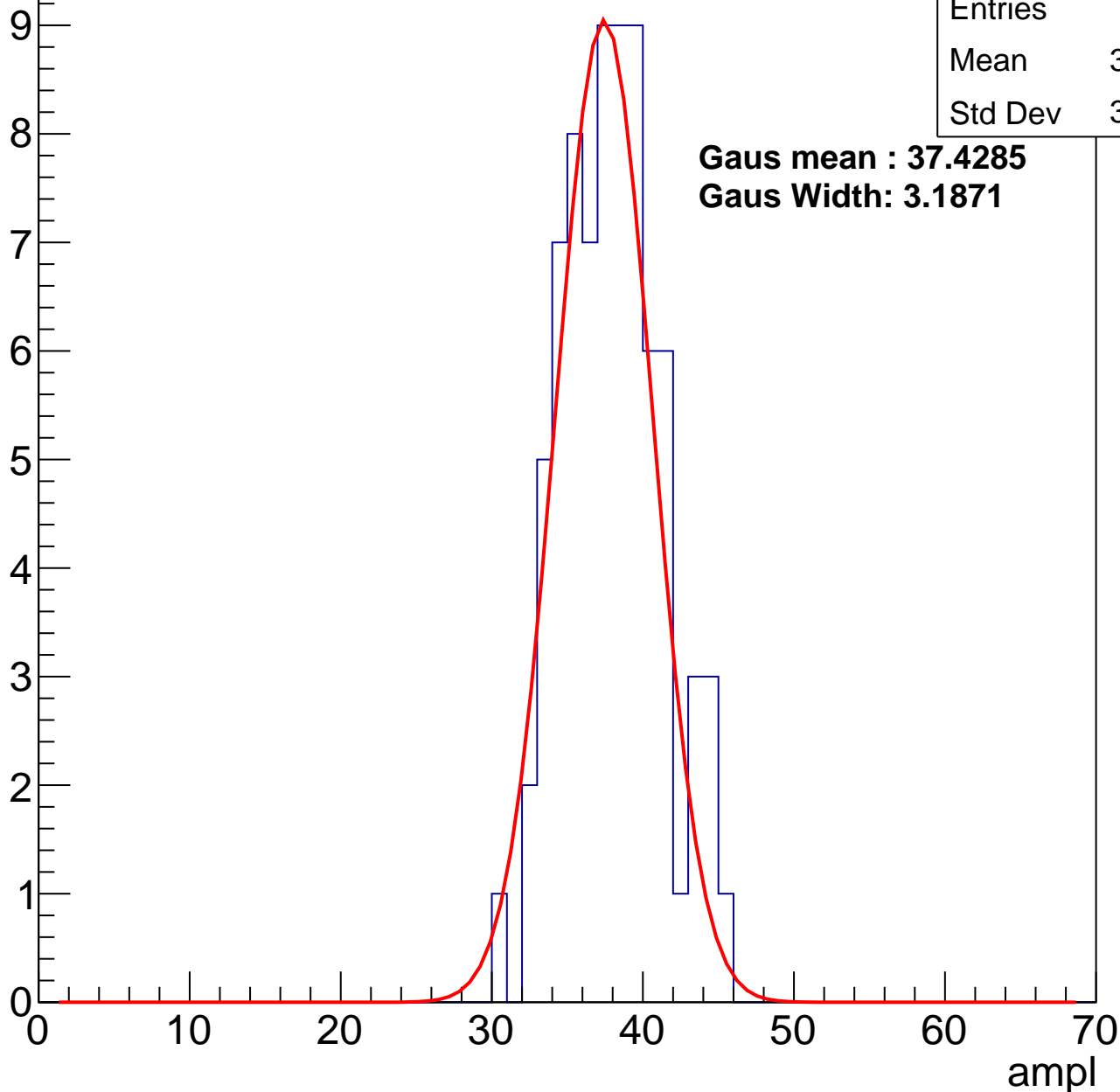
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.52 |
| Std Dev | 3.222 |

Gaus mean : 37.4285

Gaus Width: 3.1871



B1L100S, U9-ch42, adc2

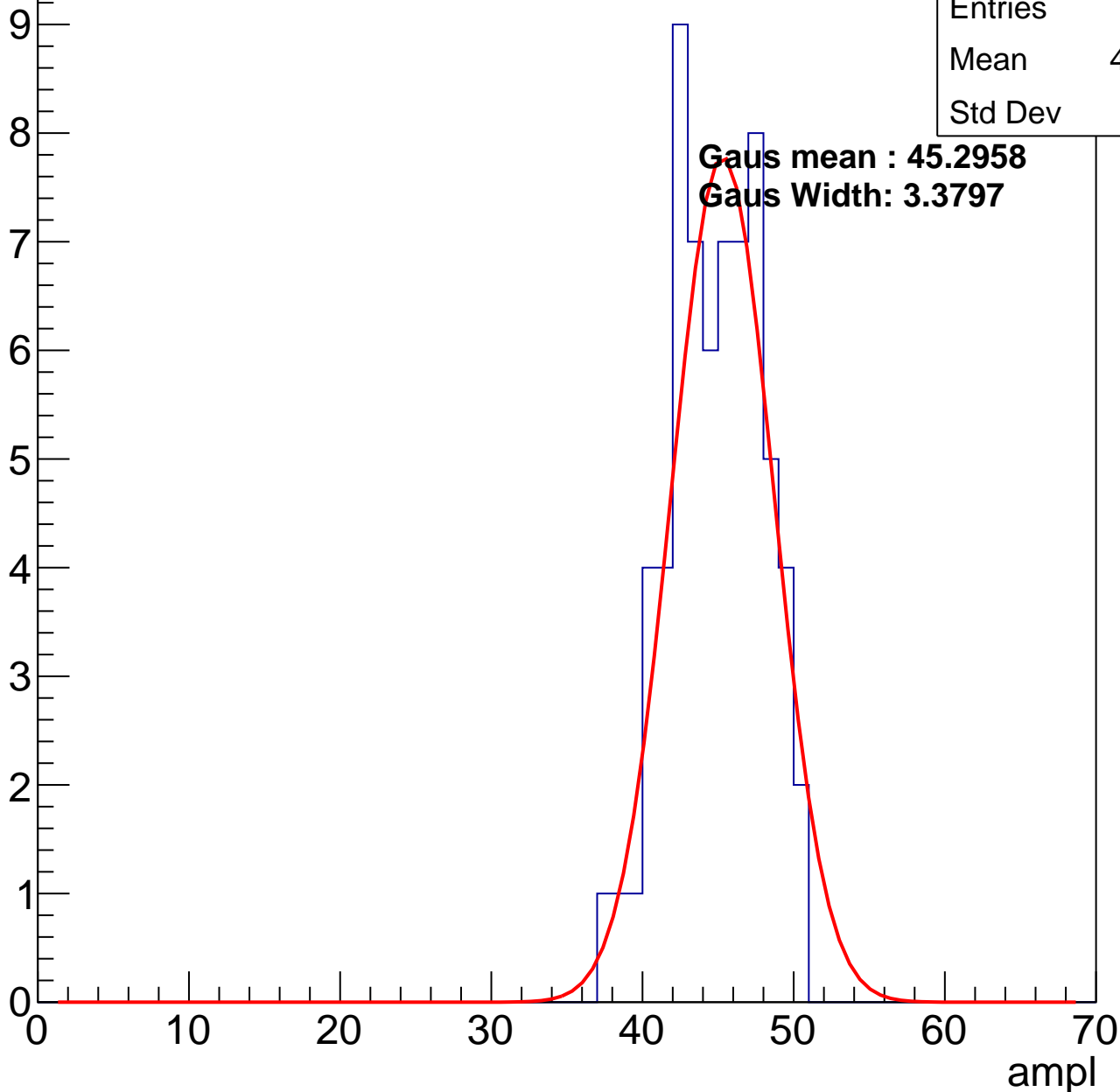
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44.39 |
| Std Dev | 3.02 |

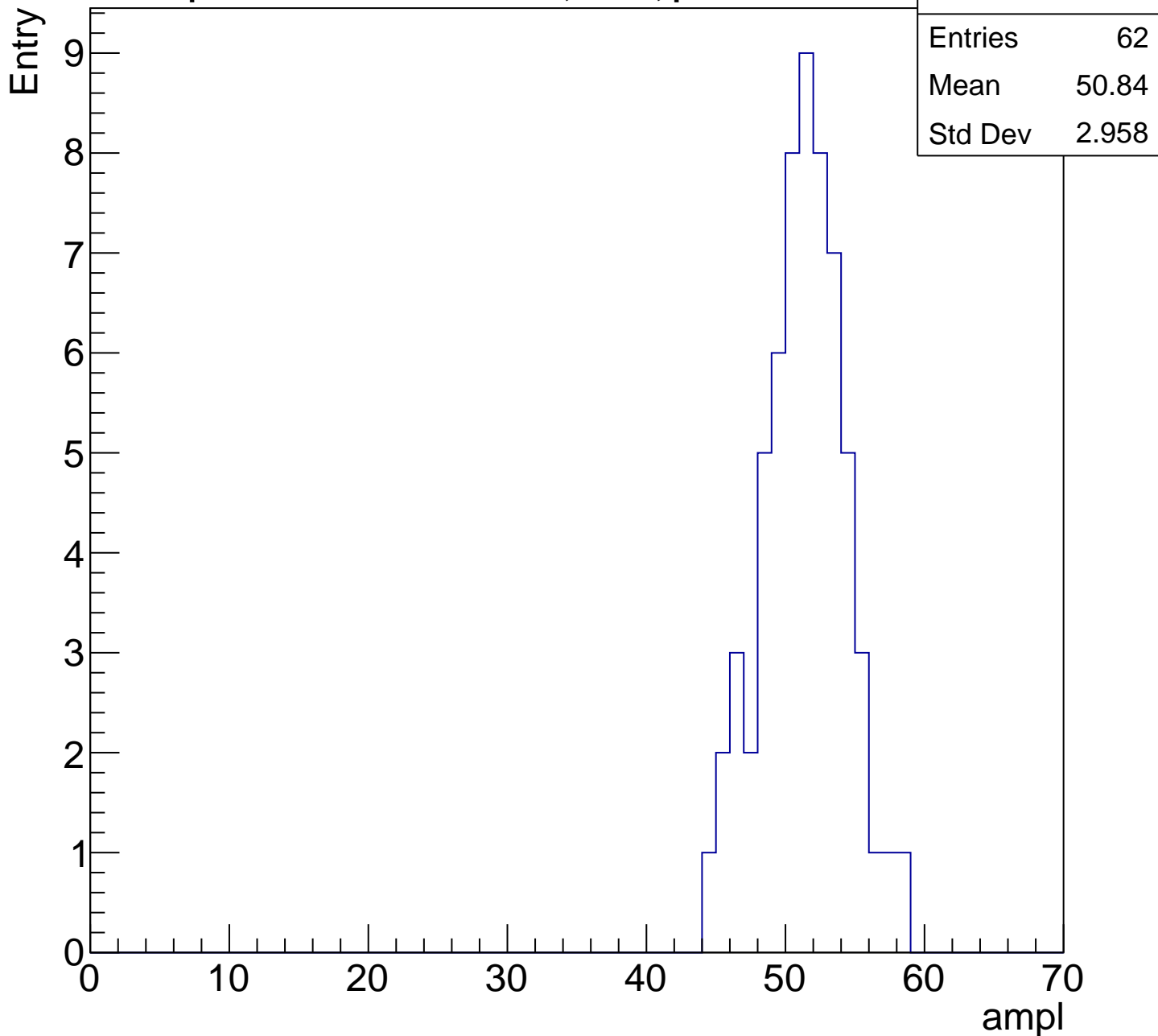
Gaus mean : 45.2958

Gaus Width: 3.3797



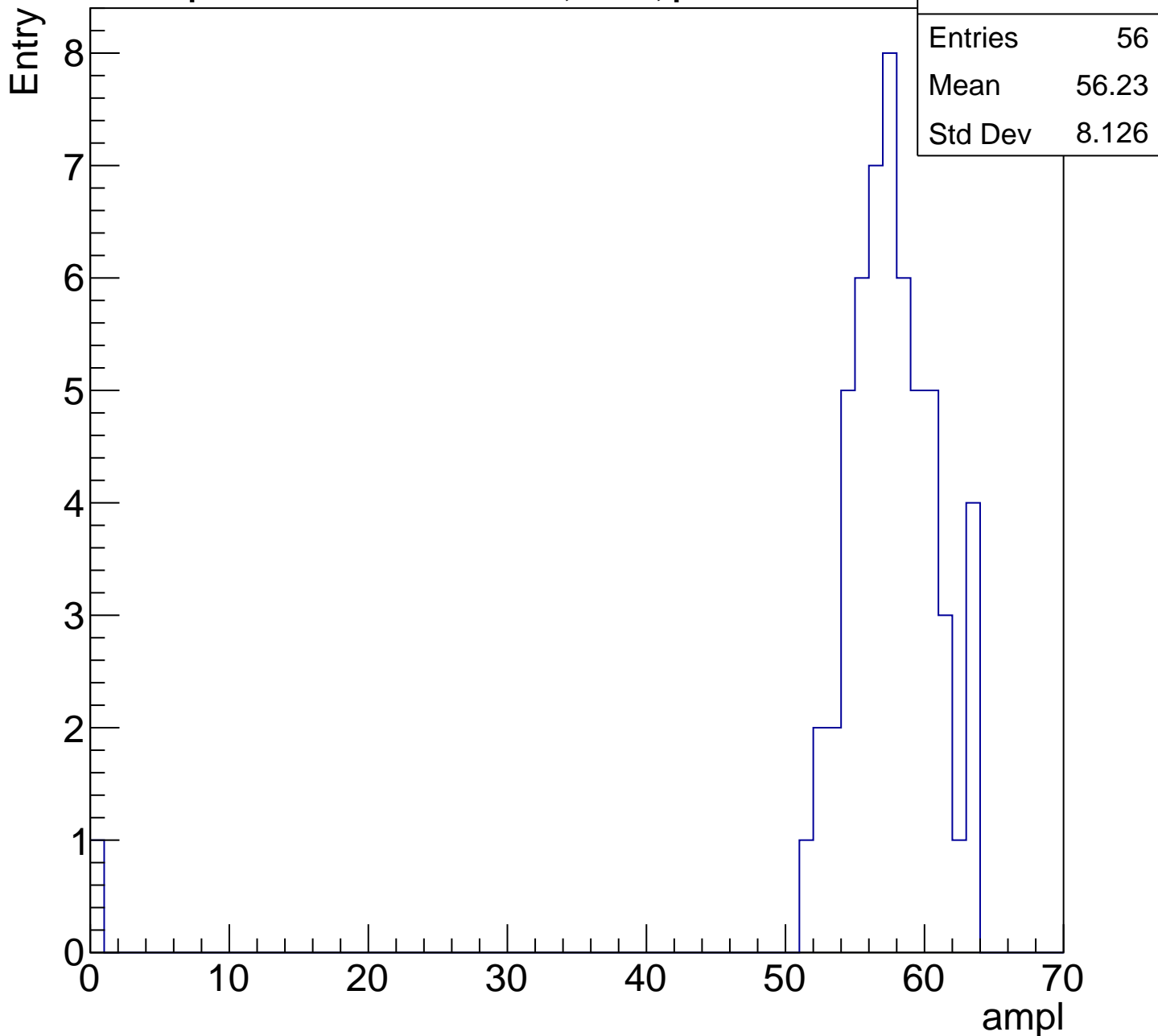
B1L100S, U9-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

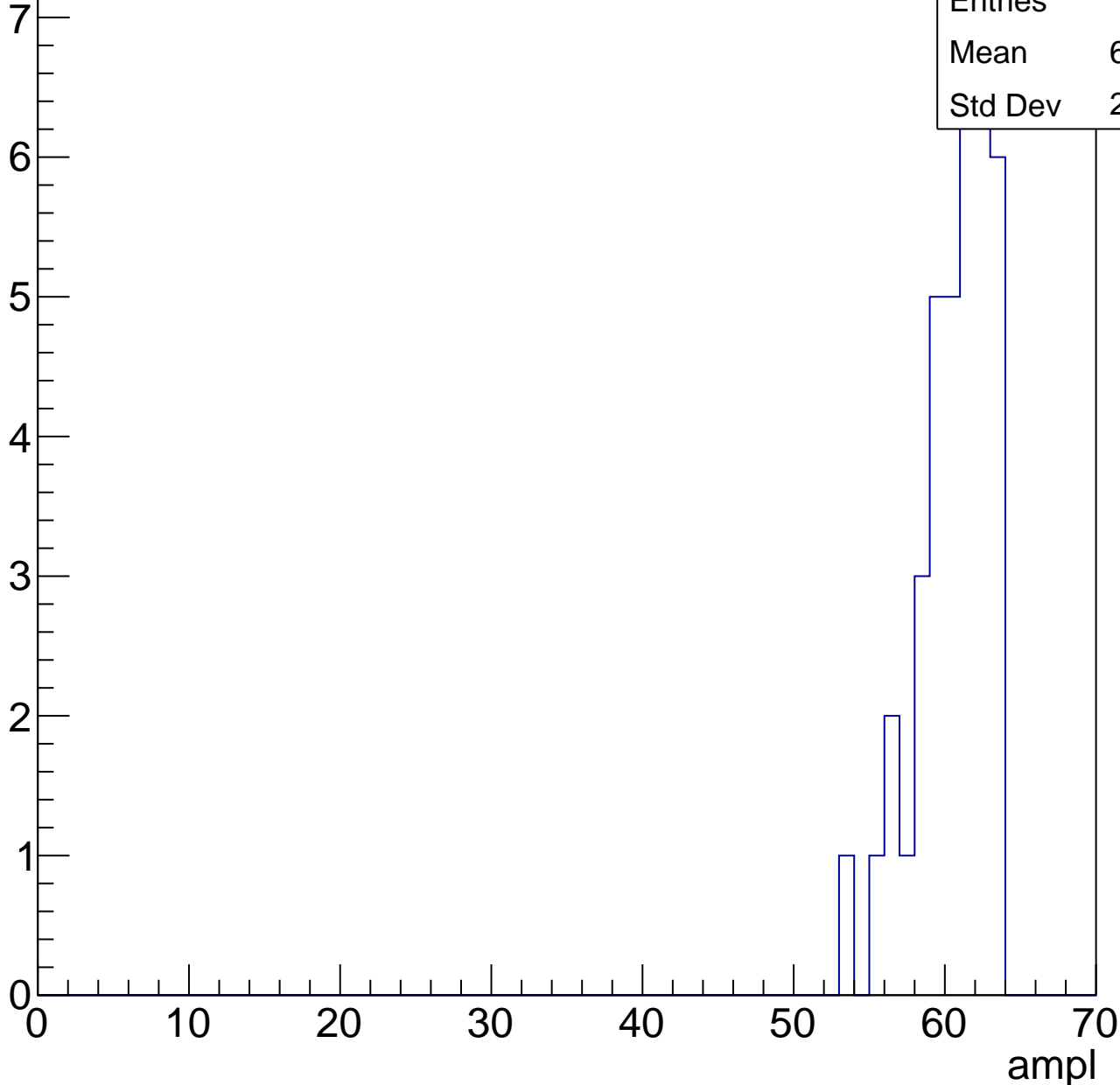


B1L100S, U9-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 60.13 |
| Std Dev | 2.408 |



B1L100S, U9-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch43, adc0

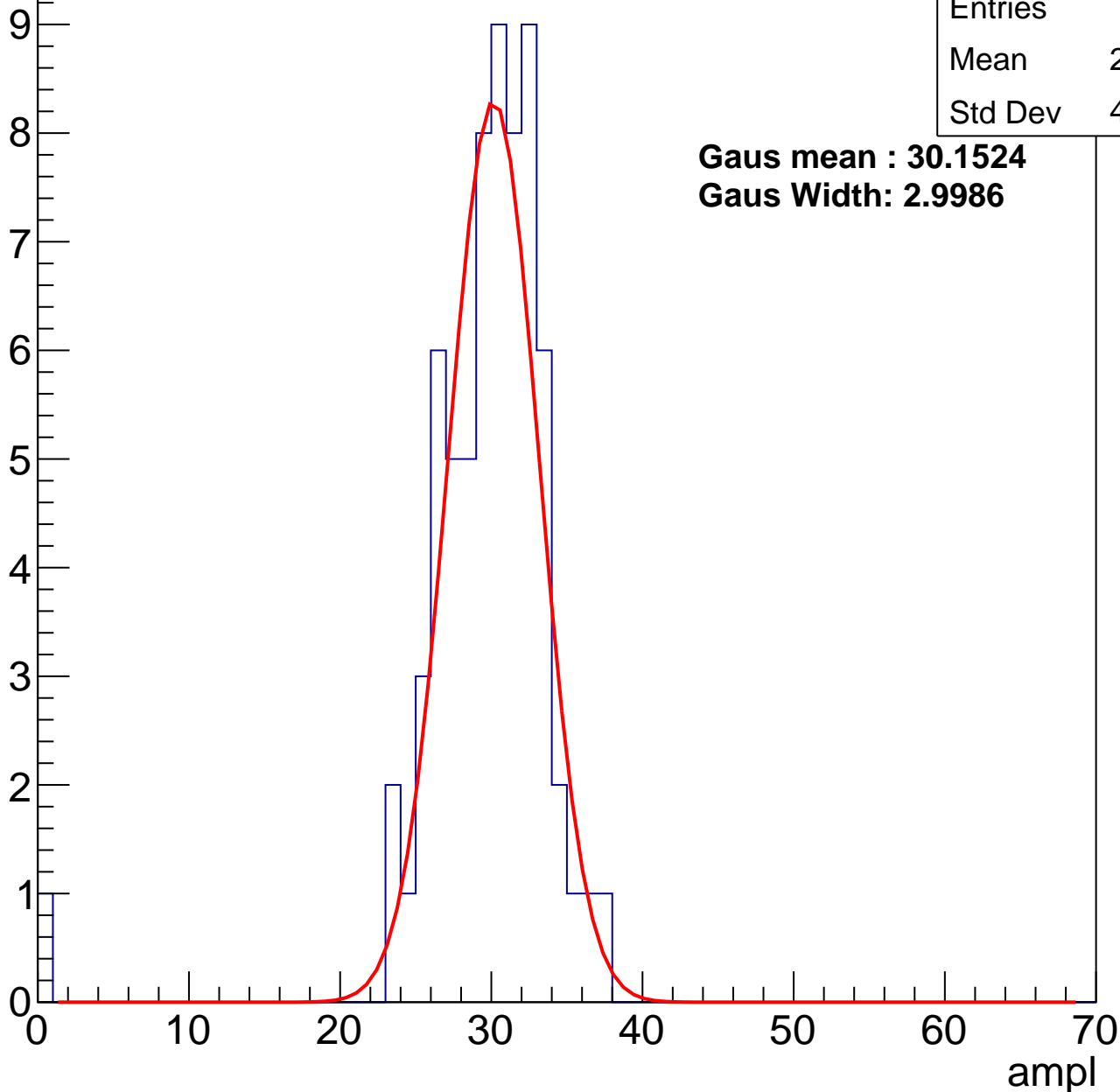
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.24 |
| Std Dev | 4.662 |

Gaus mean : 30.1524

Gaus Width: 2.9986



B1L100S, U9-ch43, adc1

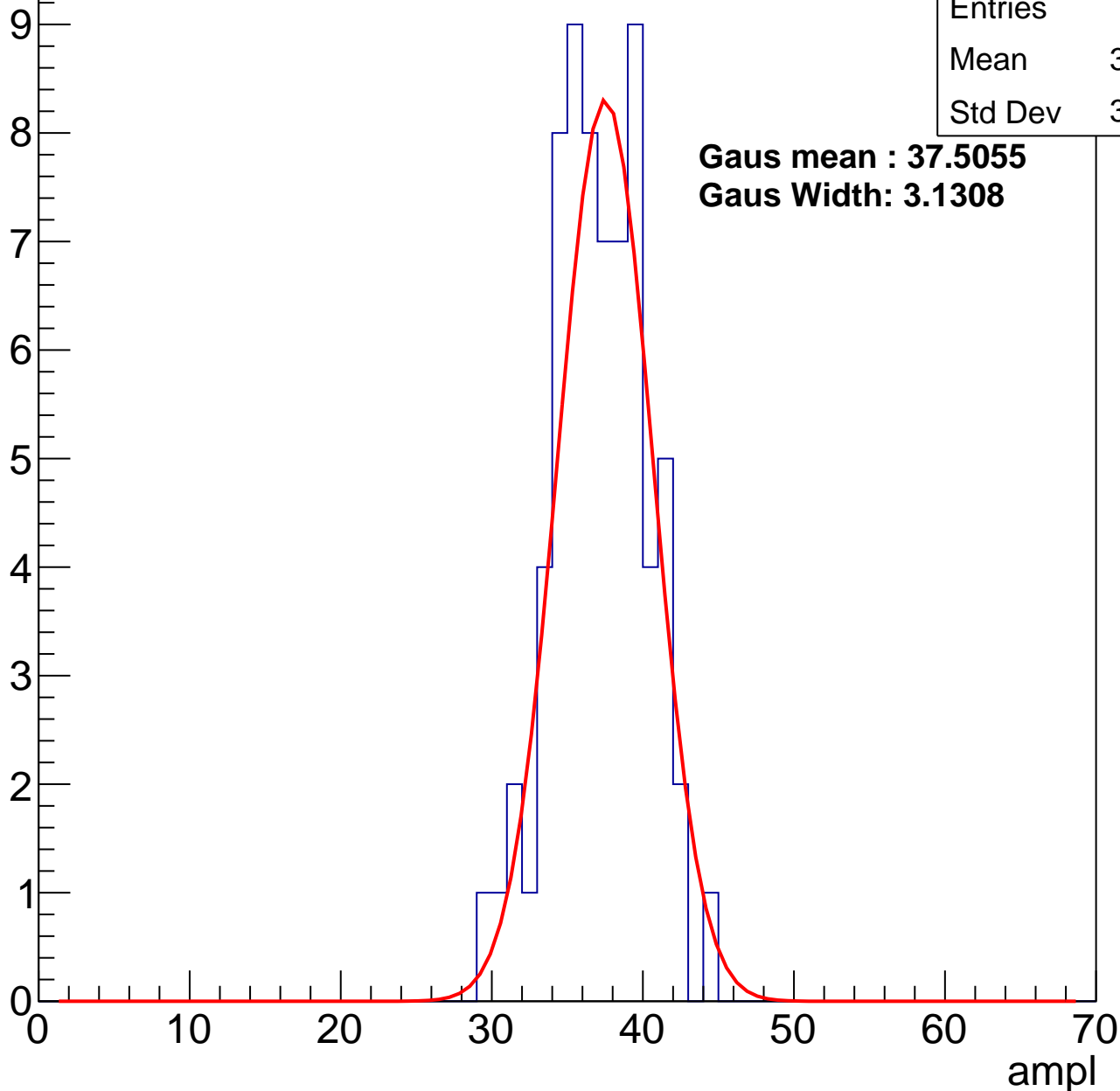
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.65 |
| Std Dev | 3.045 |

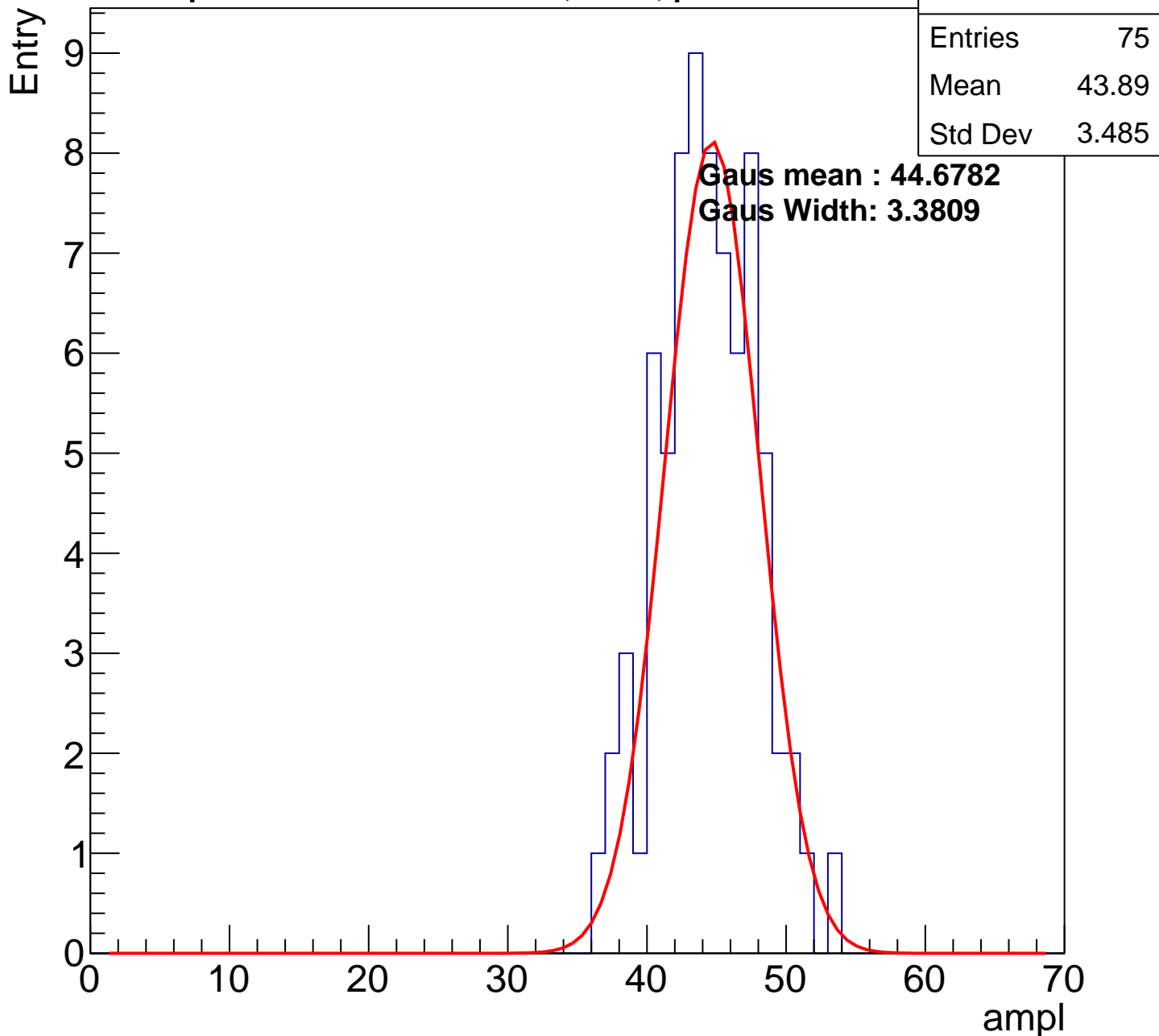
Gaus mean : 37.5055

Gaus Width: 3.1308



B1L100S, U9-ch43, adc2

calib_packv5_042523_0143.root, FC#4, port A2

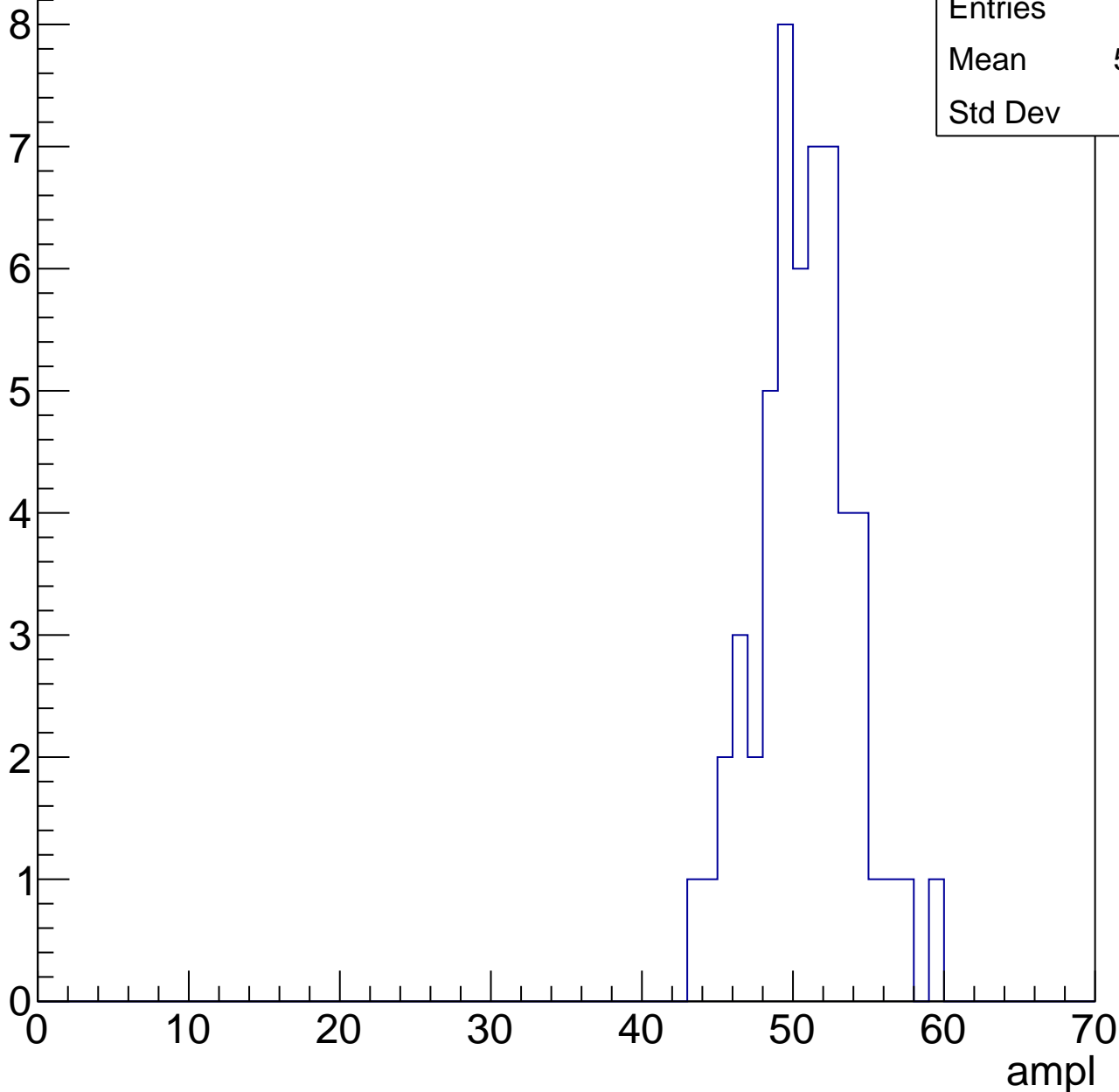


B1L100S, U9-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 50.31 |
| Std Dev | 3.19 |

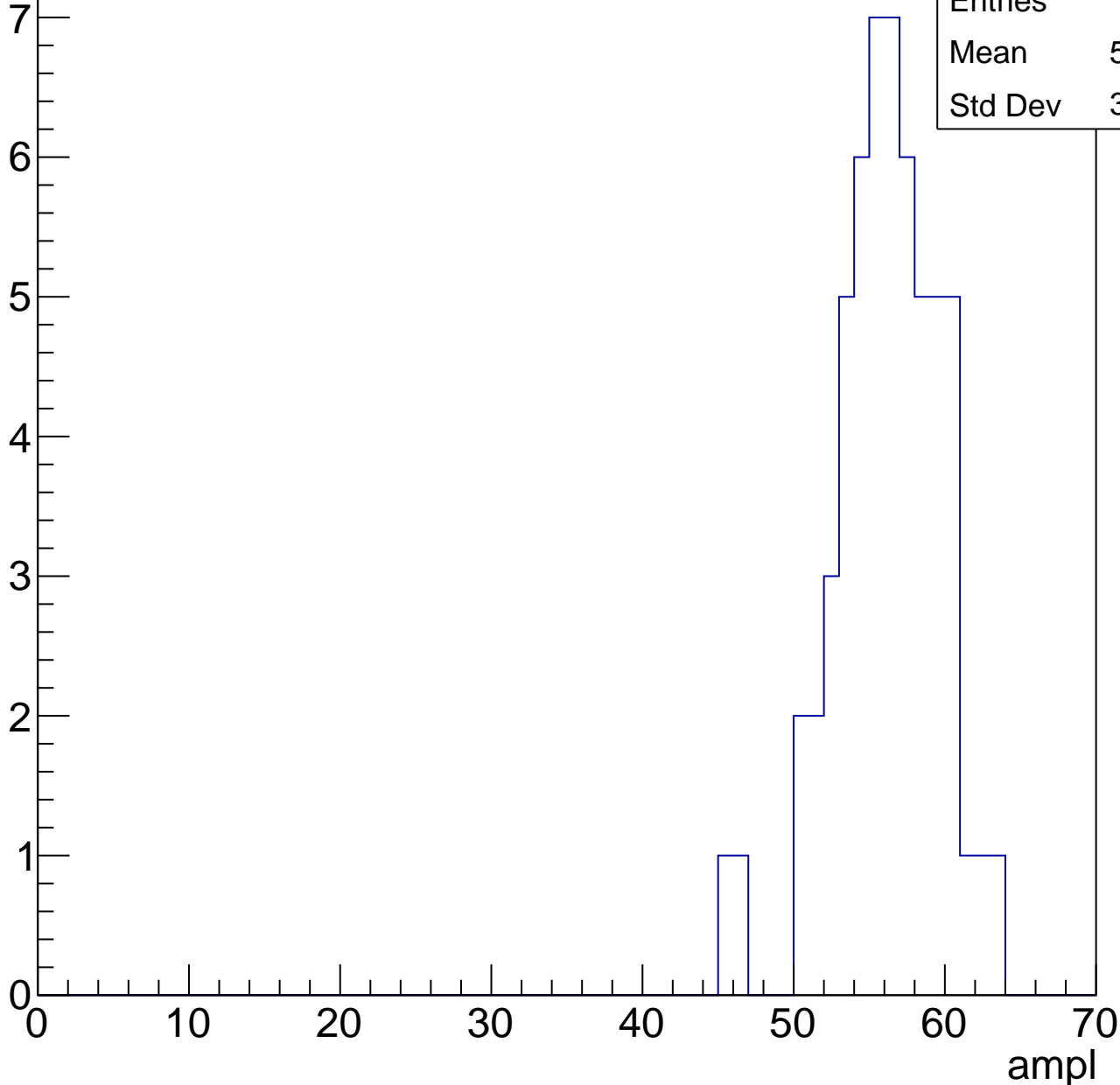


B1L100S, U9-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

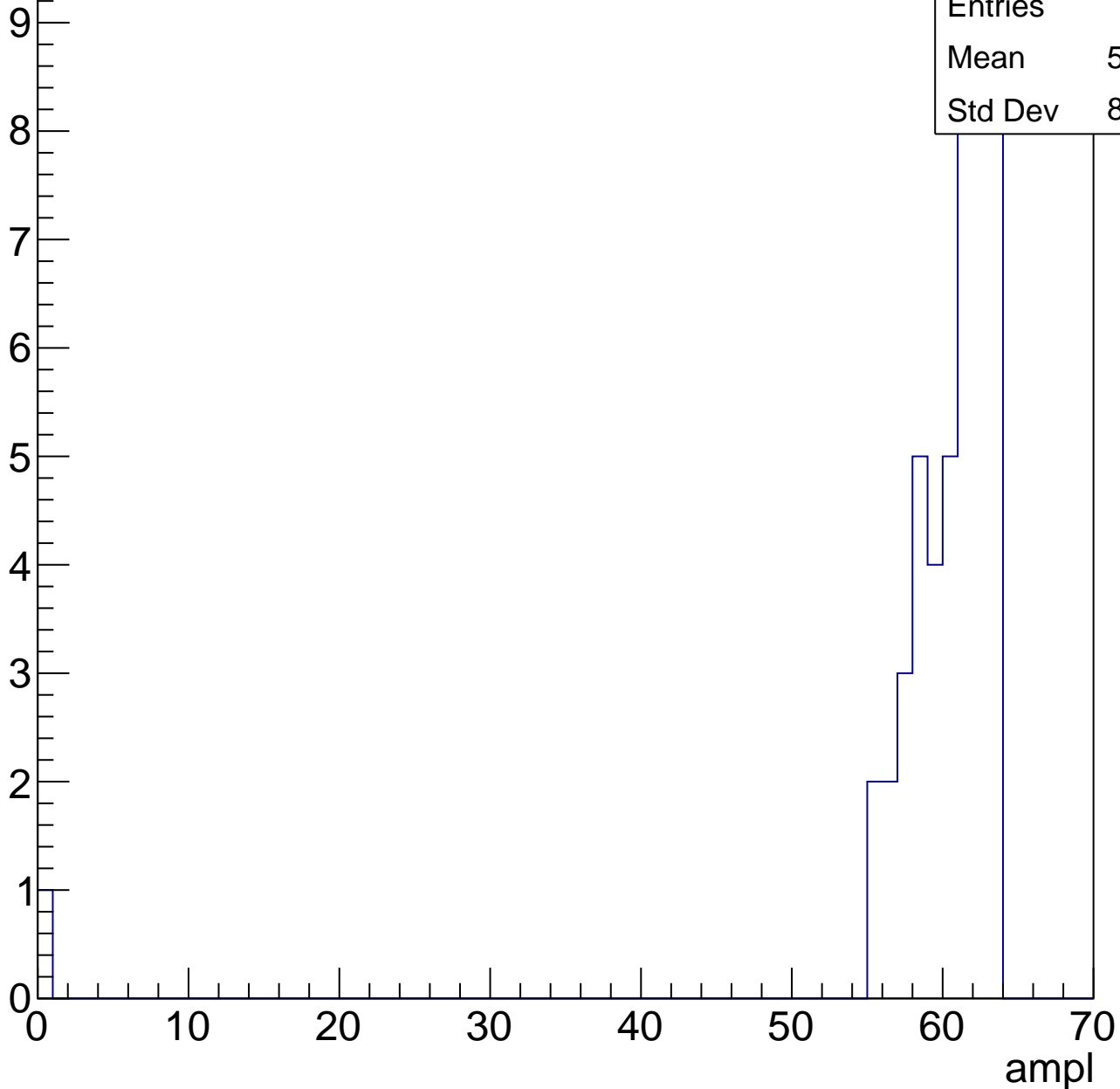
| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.66 |
| Std Dev | 3.526 |



B1L100S, U9-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch44, adc0

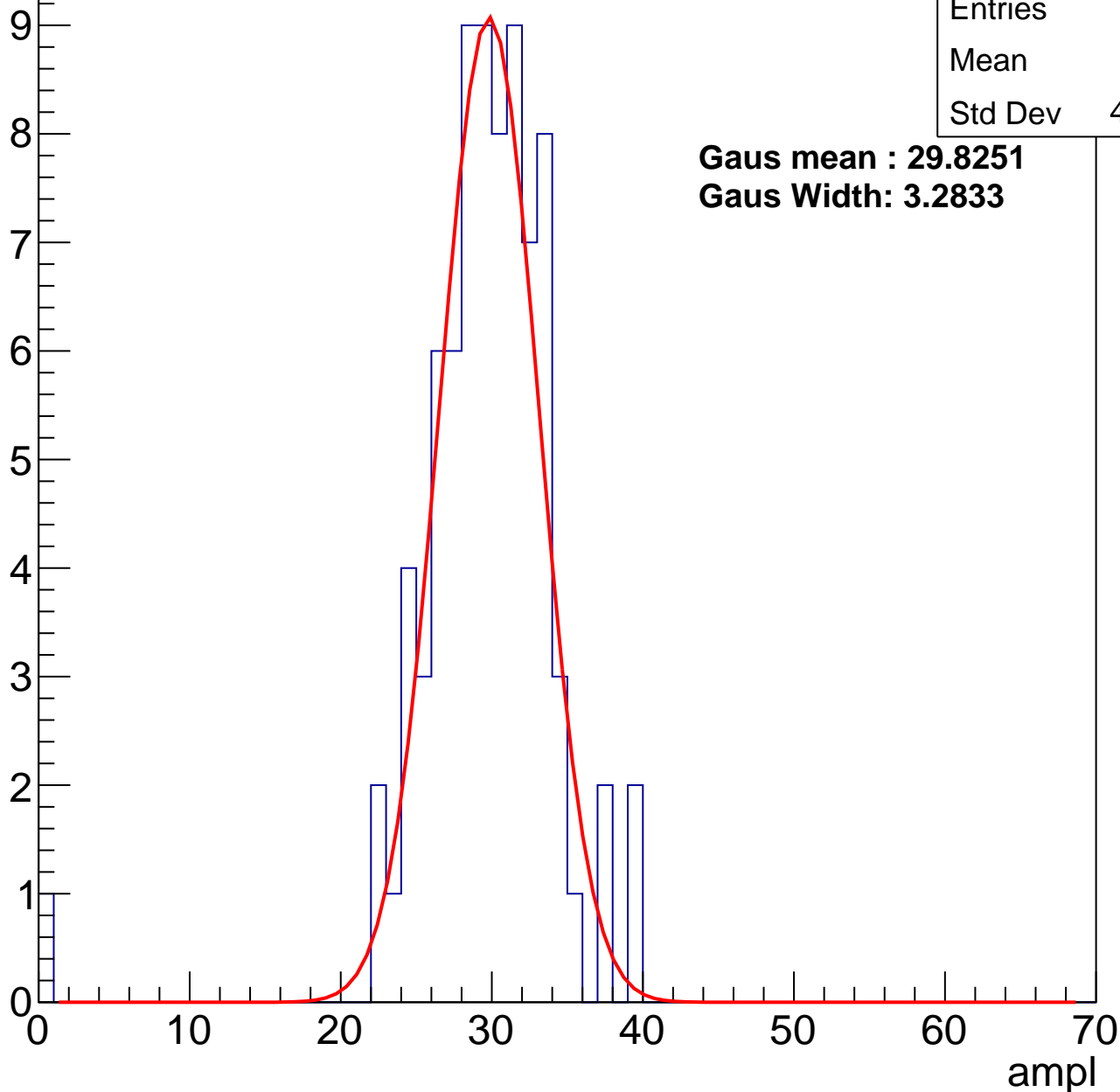
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 29.2 |
| Std Dev | 4.819 |

Gaus mean : 29.8251

Gaus Width: 3.2833



B1L100S, U9-ch44, adc1

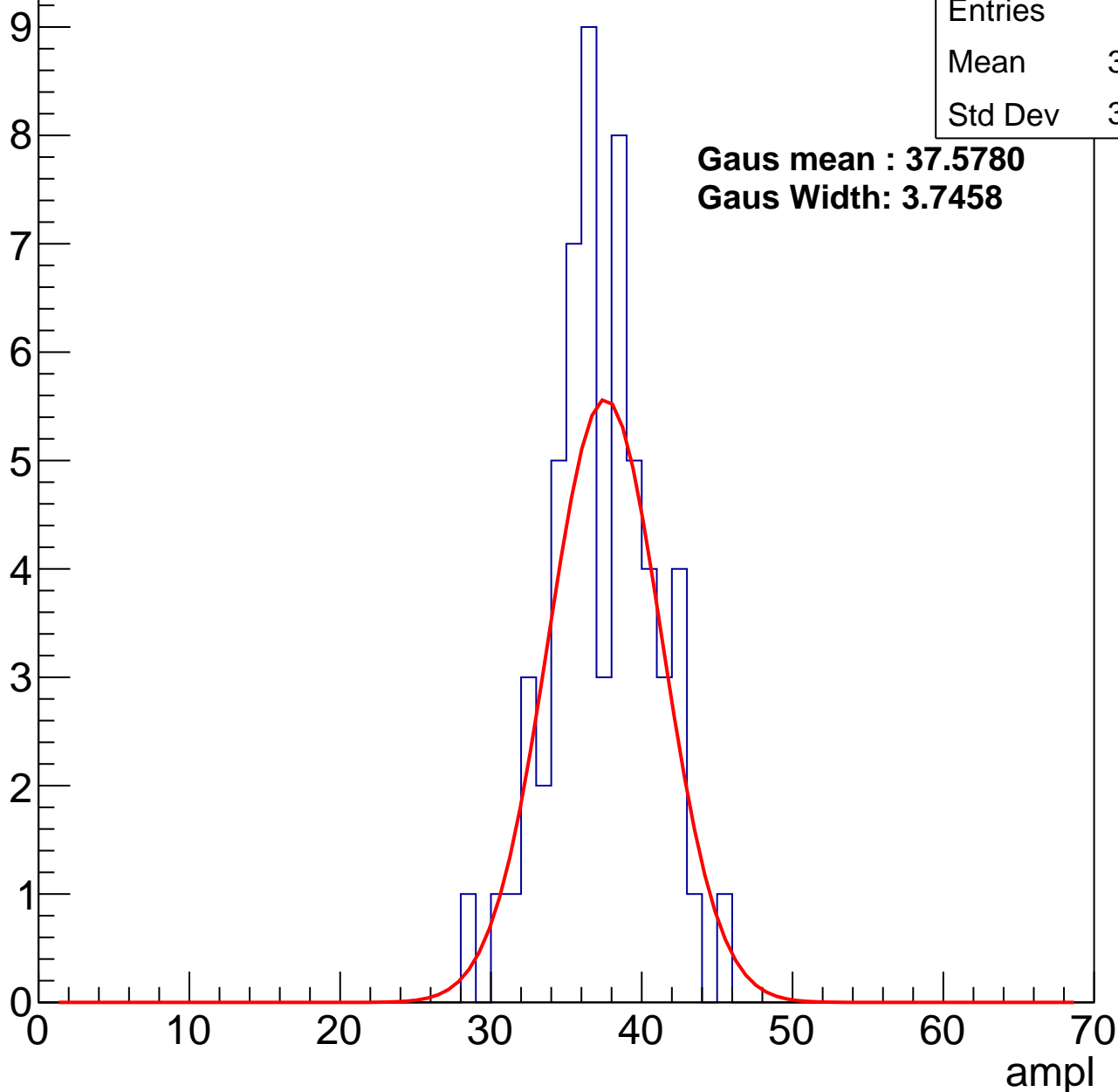
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 36.88 |
| Std Dev | 3.389 |

Gaus mean : 37.5780

Gaus Width: 3.7458



B1L100S, U9-ch44, adc2

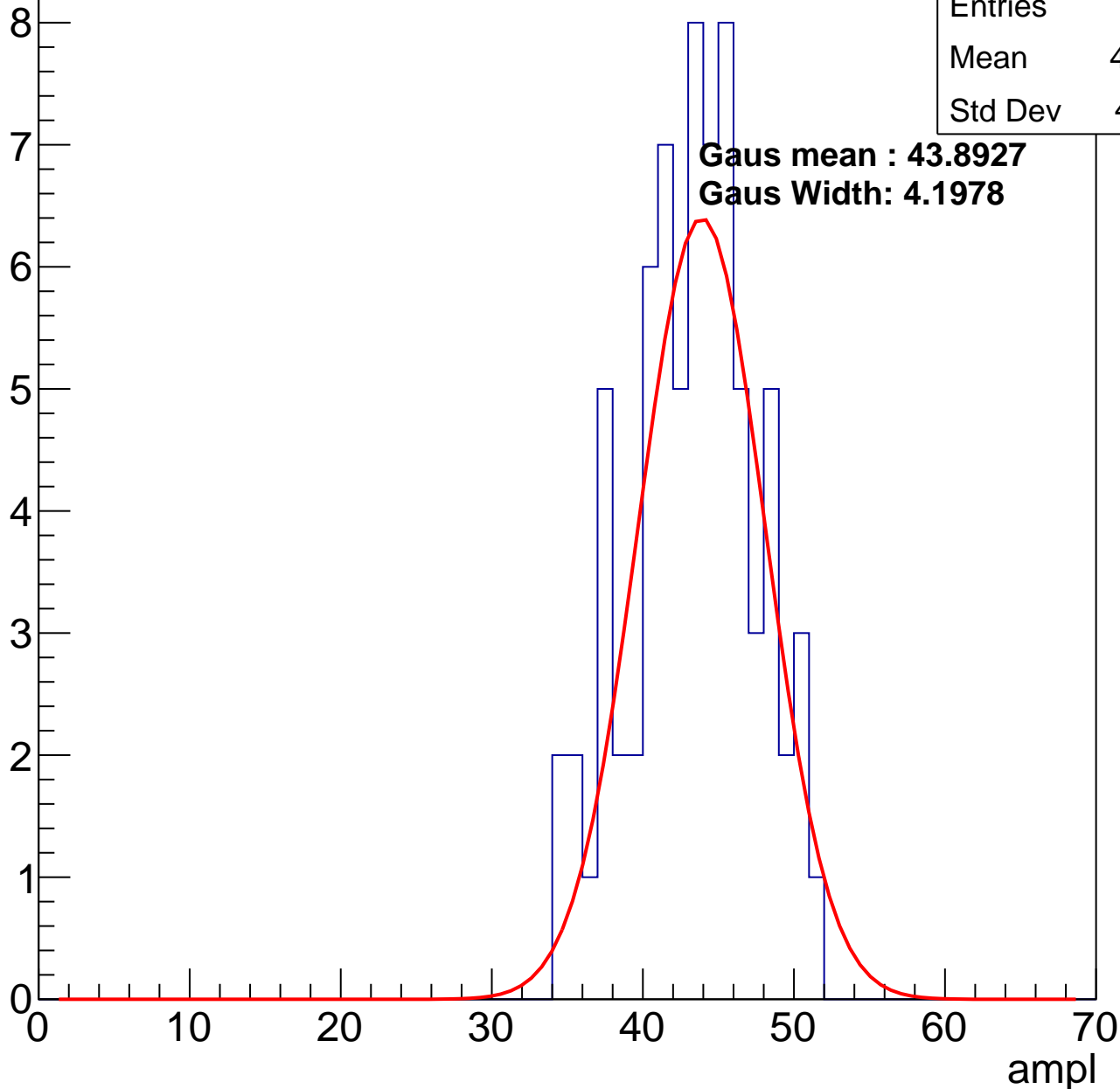
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 42.86 |
| Std Dev | 4.071 |

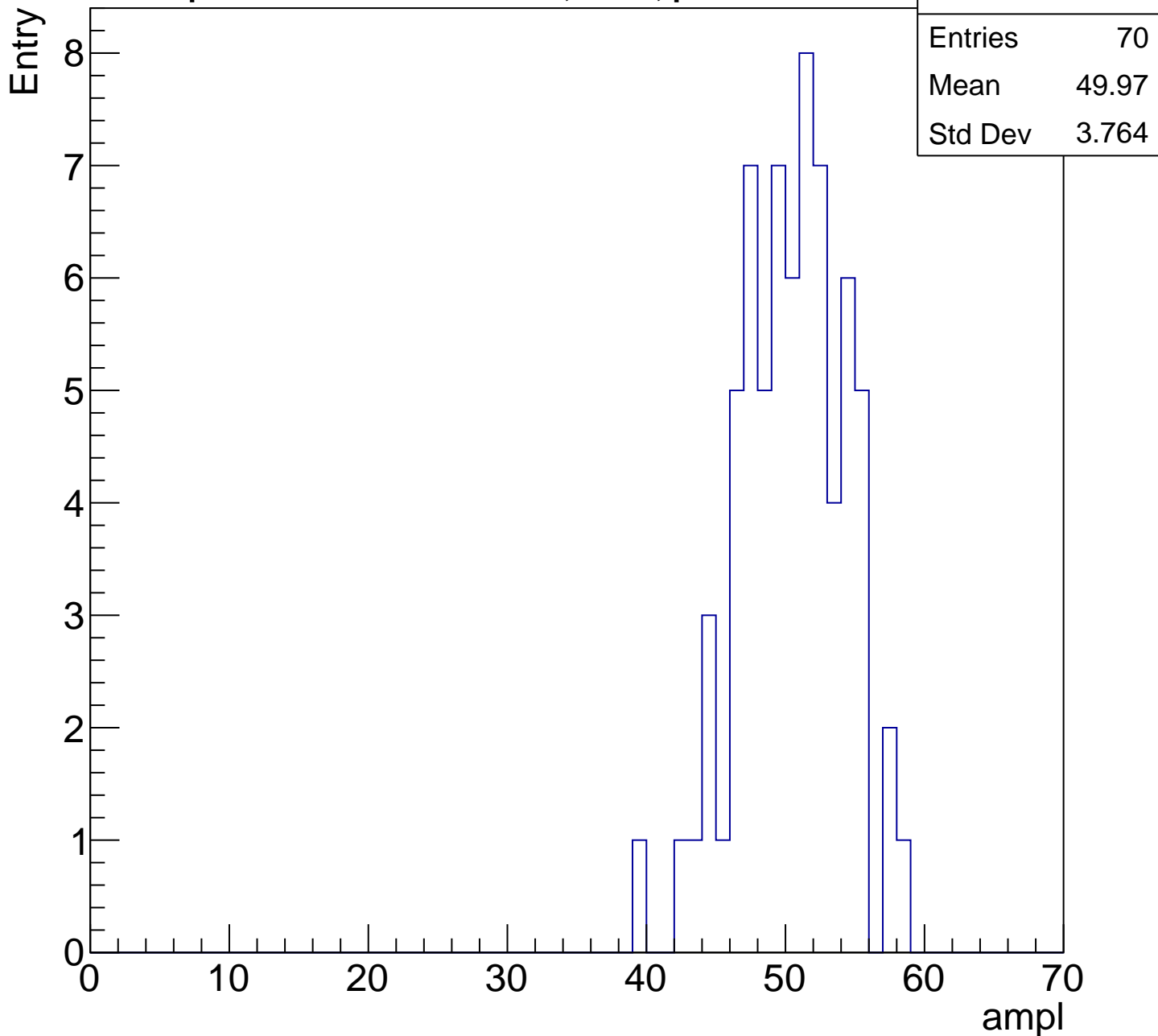
Gaus mean : 43.8927

Gaus Width: 4.1978



B1L100S, U9-ch44, adc3

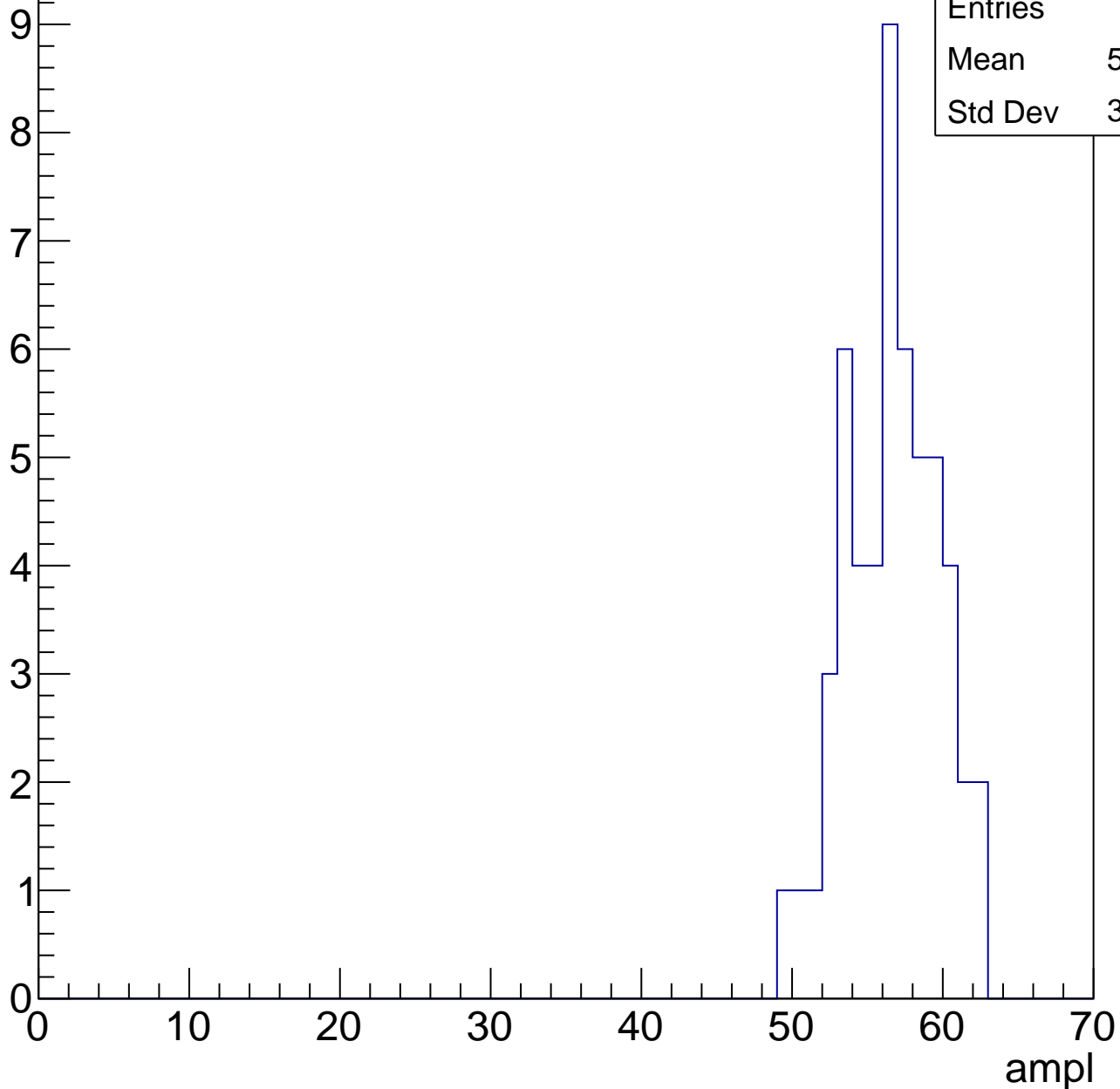
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



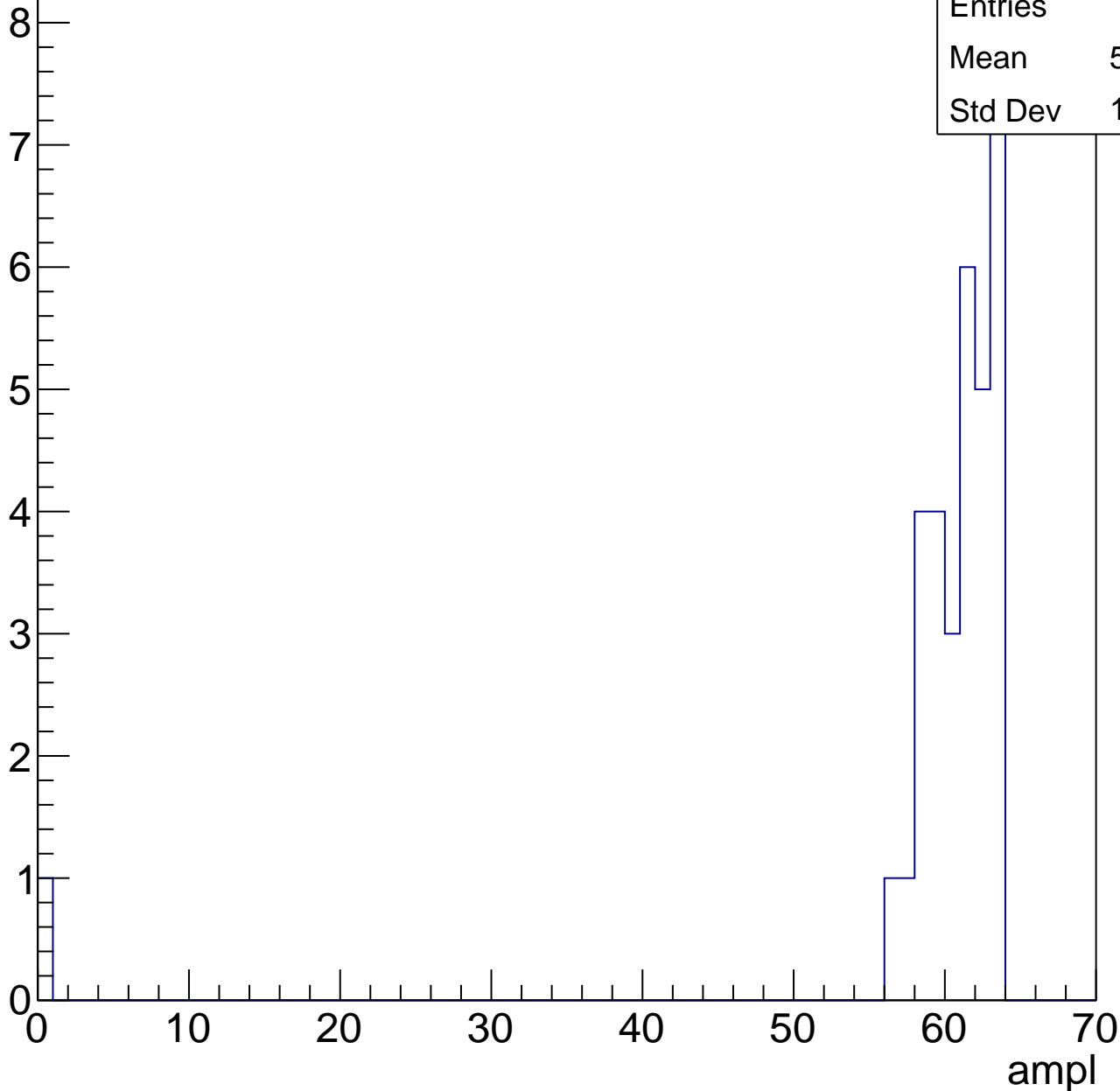
| | |
|---------|-------|
| Entries | 53 |
| Mean | 56.17 |
| Std Dev | 3.033 |

B1L100S, U9-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

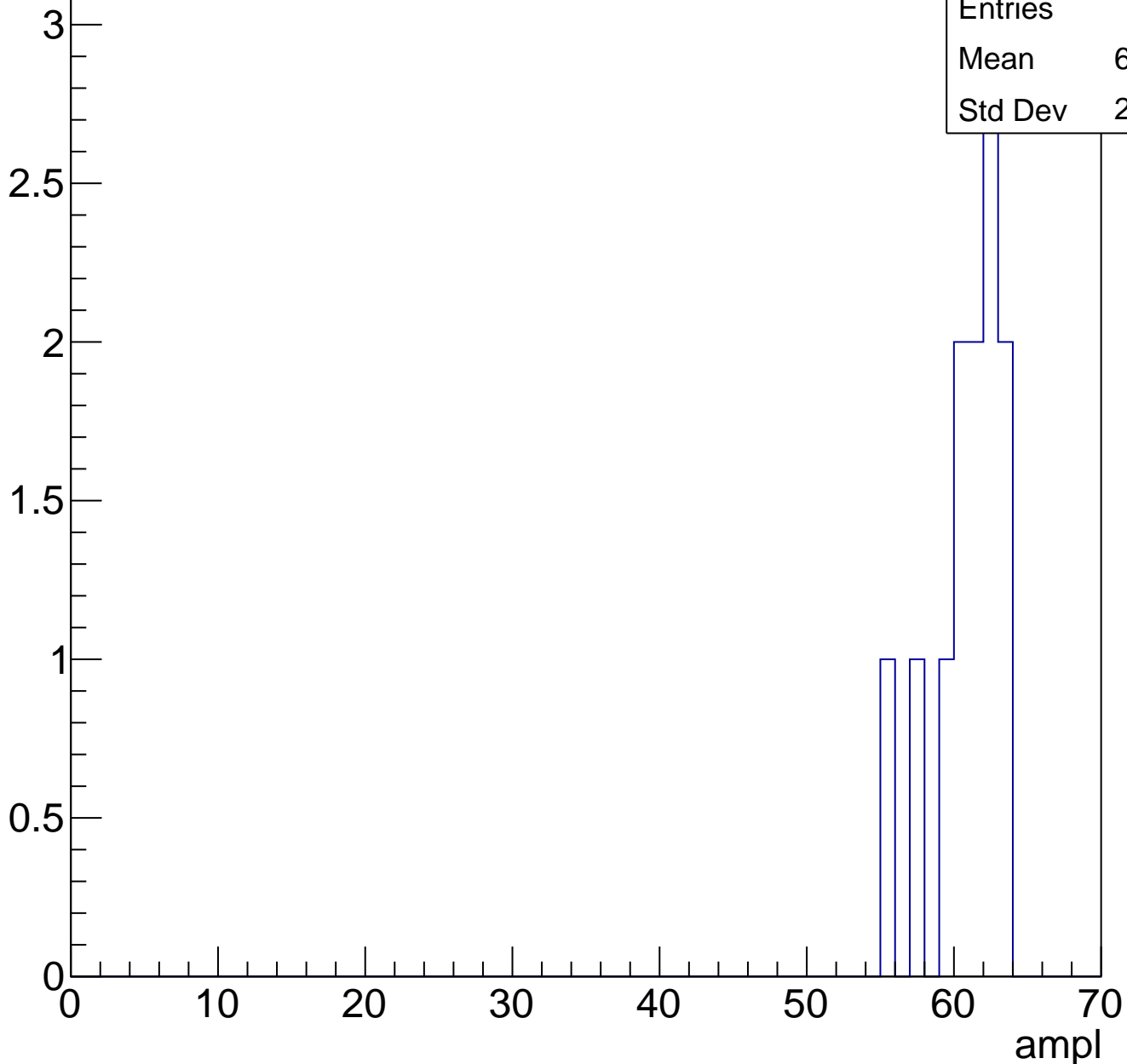
| | |
|---------|-------|
| Entries | 33 |
| Mean | 58.82 |
| Std Dev | 10.58 |



B1L100S, U9-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 12 |
| Mean | 60.42 |
| Std Dev | 2.326 |

B1L100S, U9-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch45, adc0

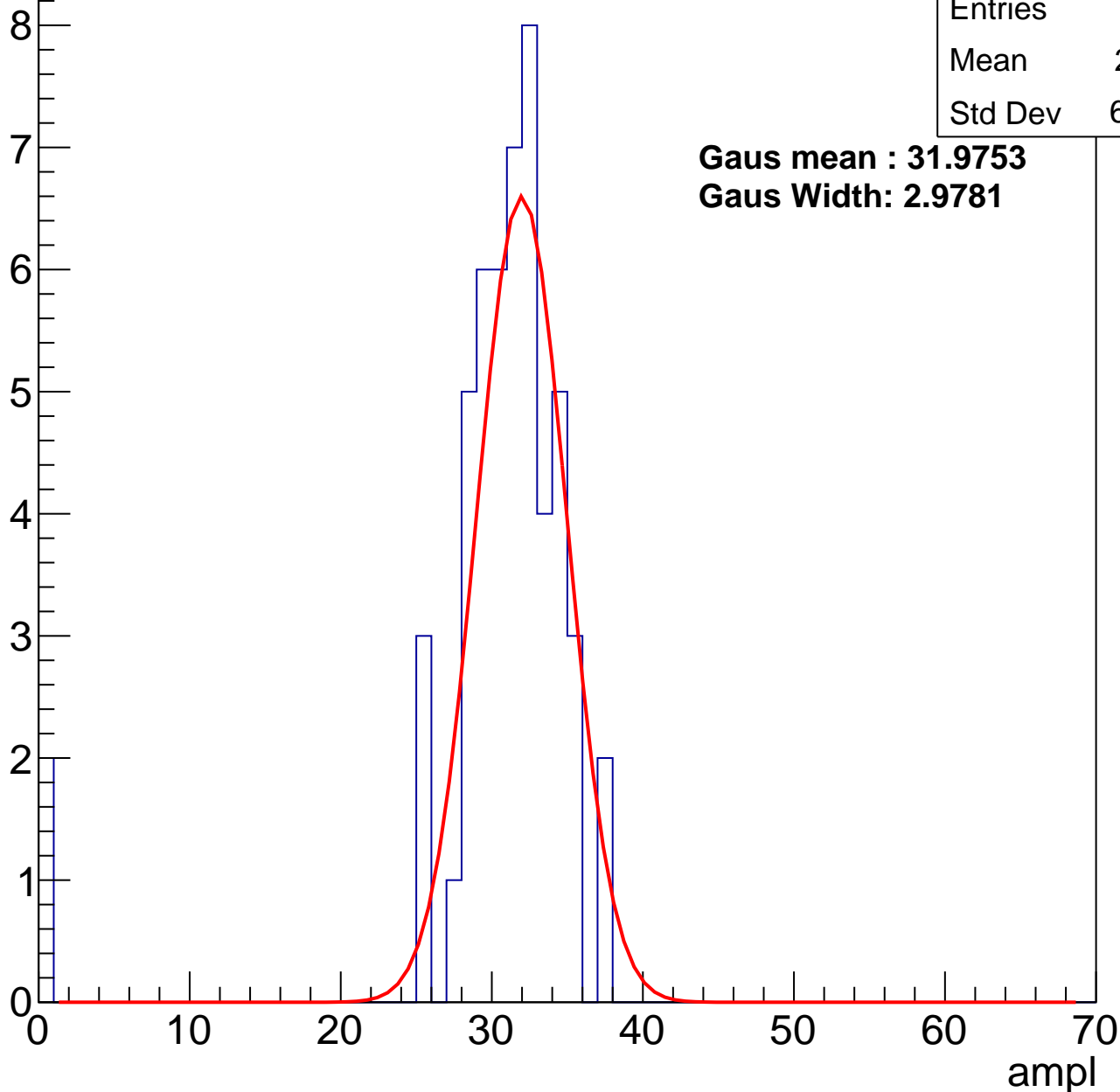
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 29.81 |
| Std Dev | 6.558 |

Gaus mean : 31.9753

Gaus Width: 2.9781



B1L100S, U9-ch45, adc1

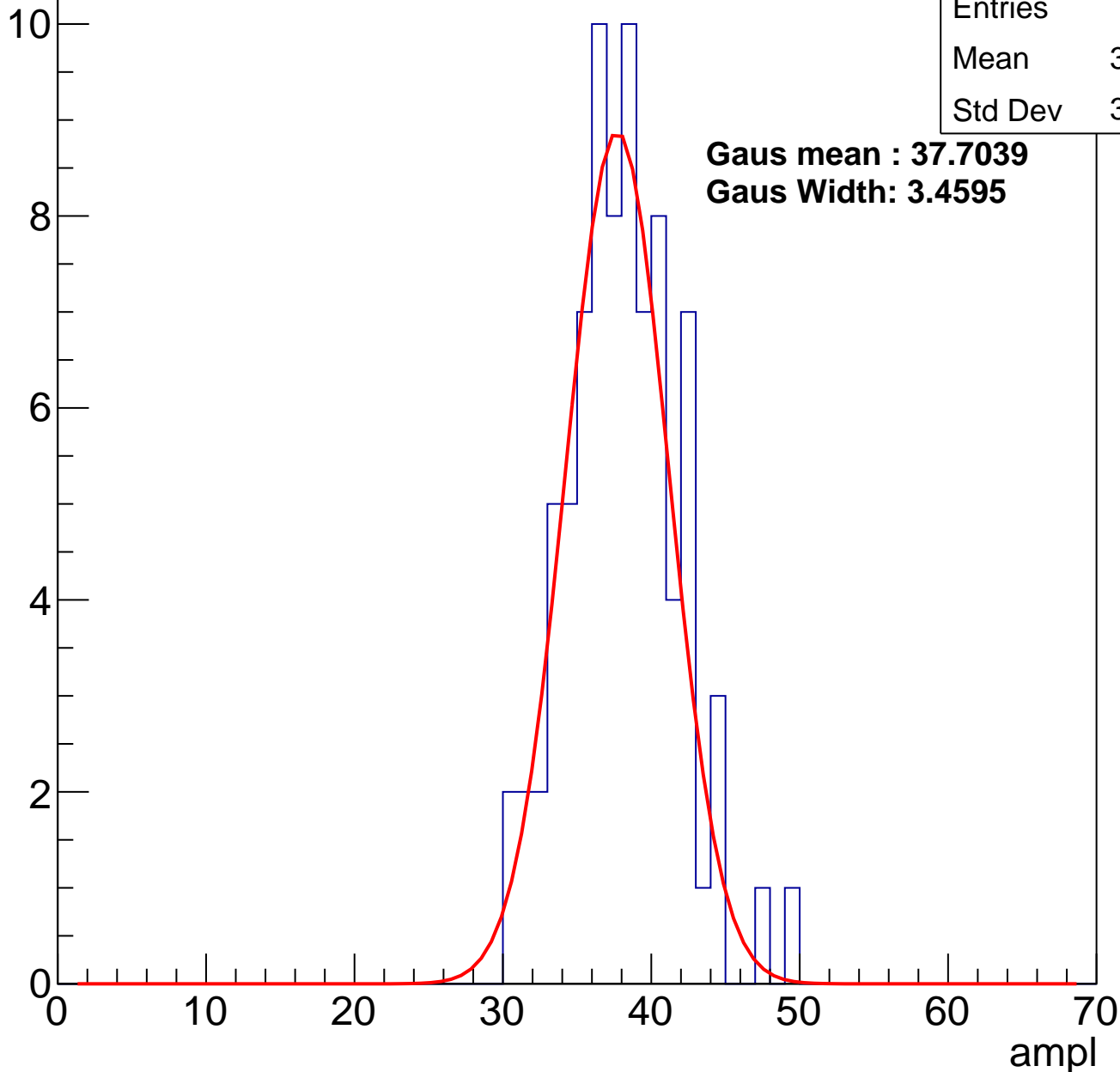
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 37.64 |
| Std Dev | 3.682 |

Gaus mean : 37.7039

Gaus Width: 3.4595

Entry



B1L100S, U9-ch45, adc2

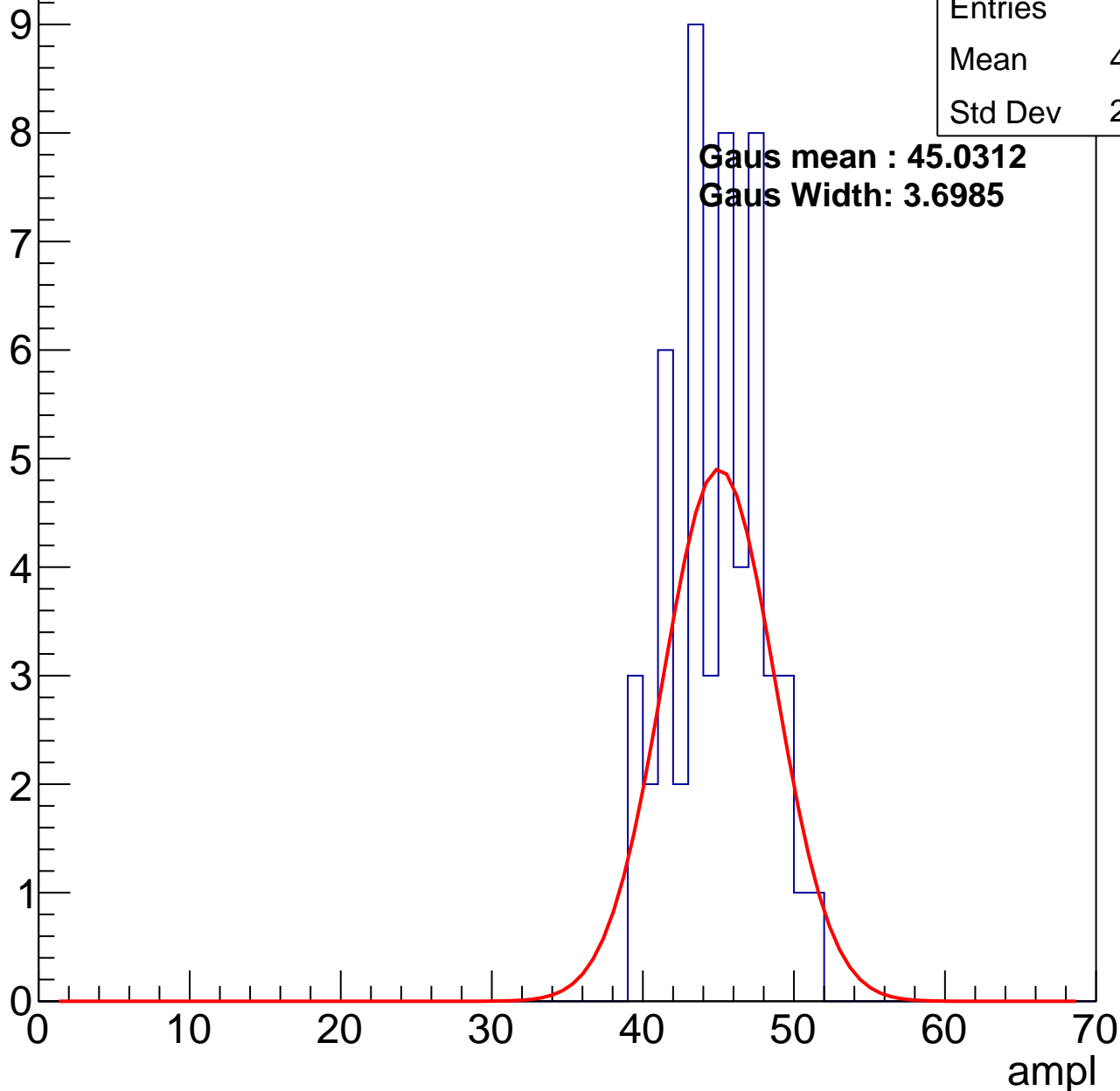
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 44.49 |
| Std Dev | 2.976 |

Gaus mean : 45.0312

Gaus Width: 3.6985



B1L100S, U9-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

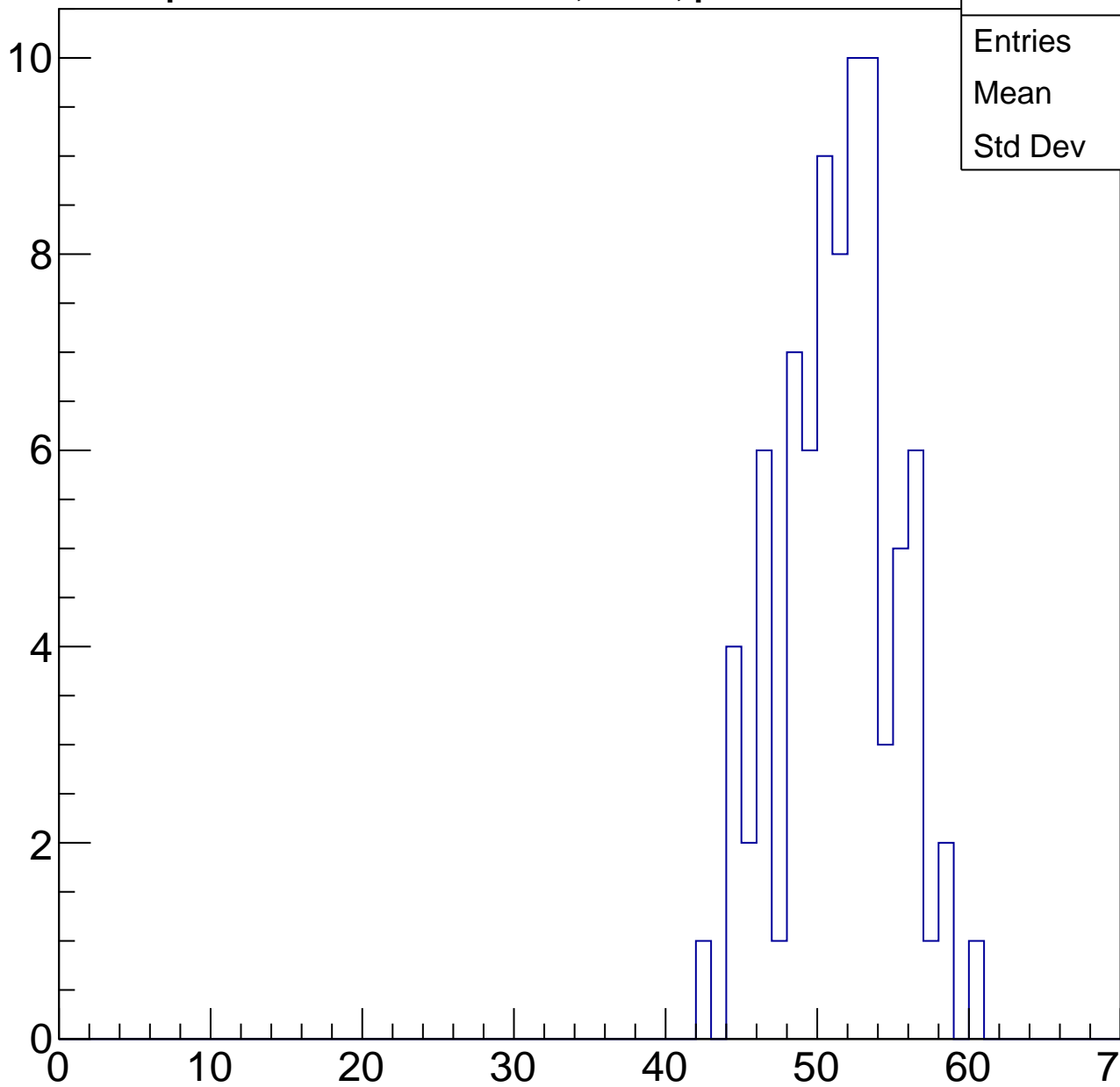
| | |
|---------|-------|
| Entries | 82 |
| Mean | 50.91 |
| Std Dev | 3.716 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch45, adc4

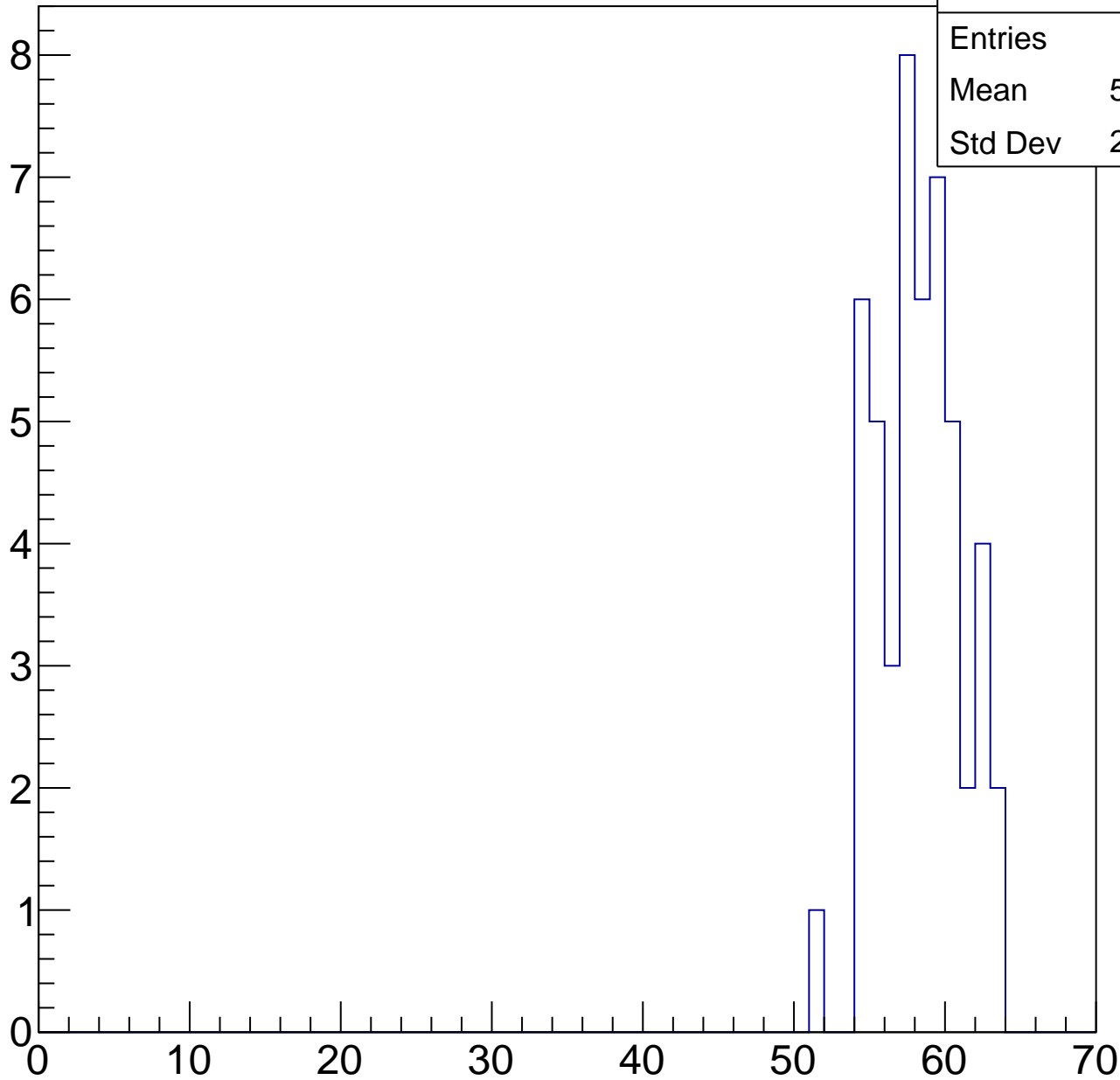
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

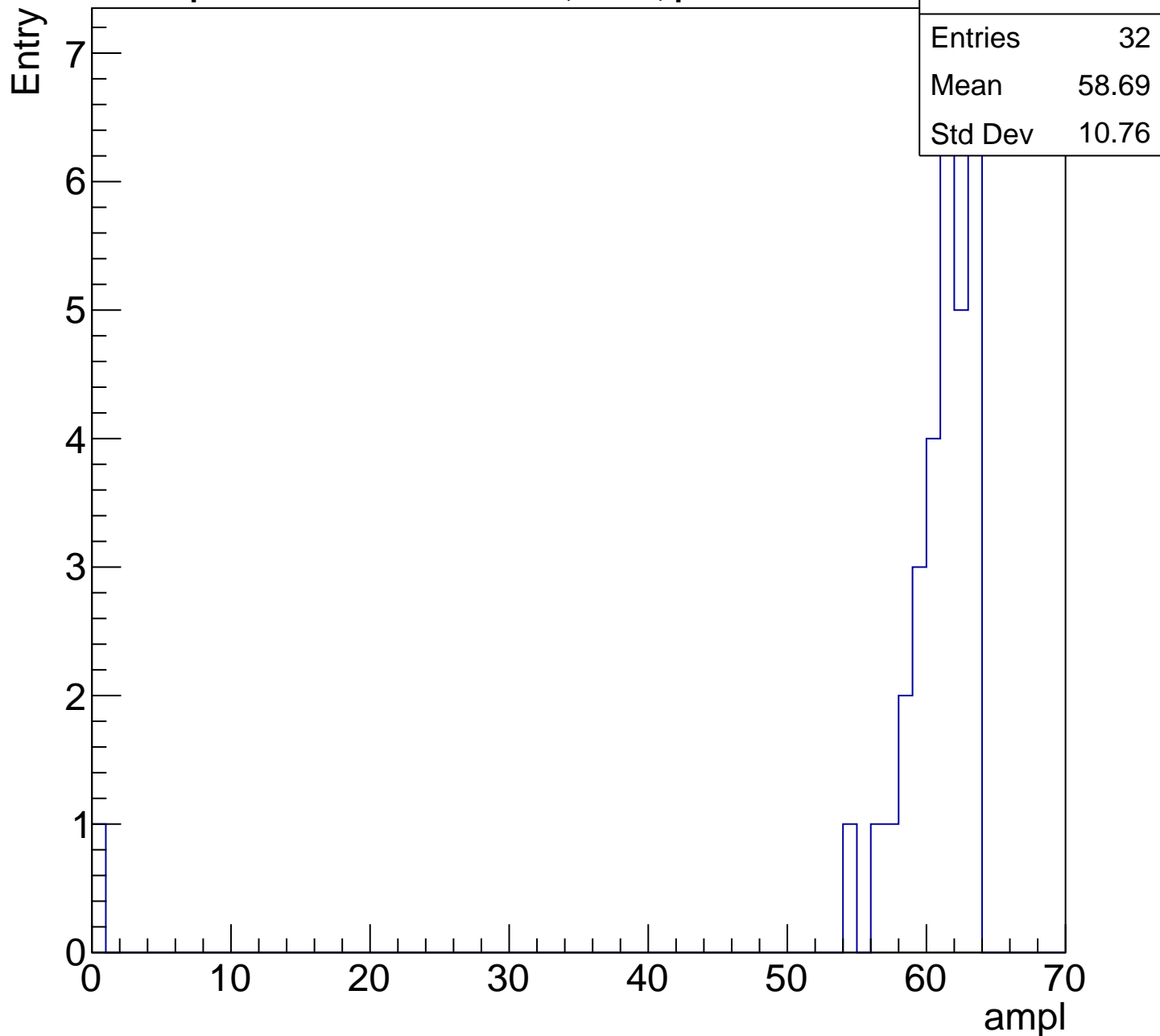
| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.78 |
| Std Dev | 2.735 |

ampl



B1L100S, U9-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch46, adc0

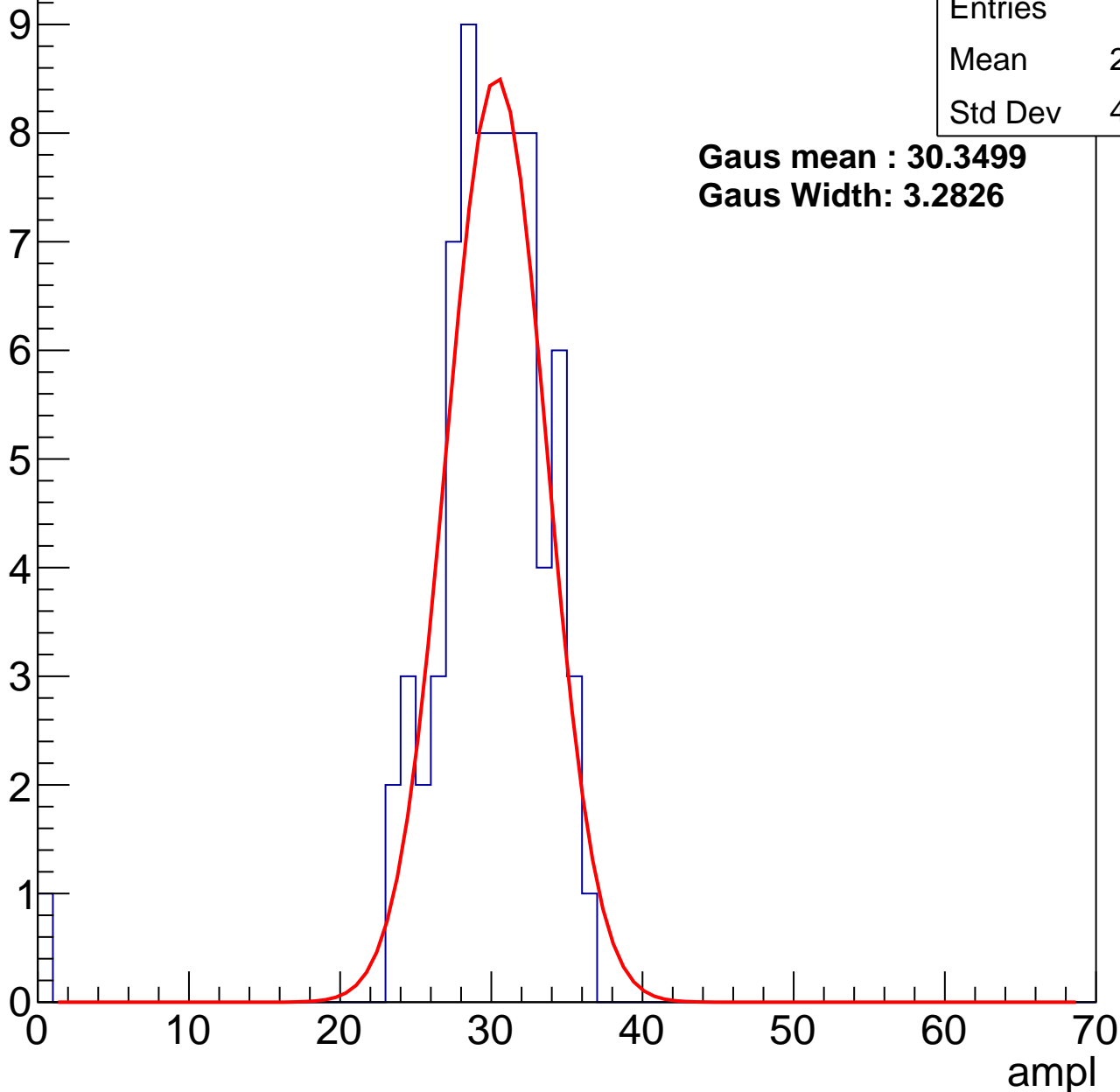
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.32 |
| Std Dev | 4.622 |

Gaus mean : 30.3499

Gaus Width: 3.2826



B1L100S, U9-ch46, adc1

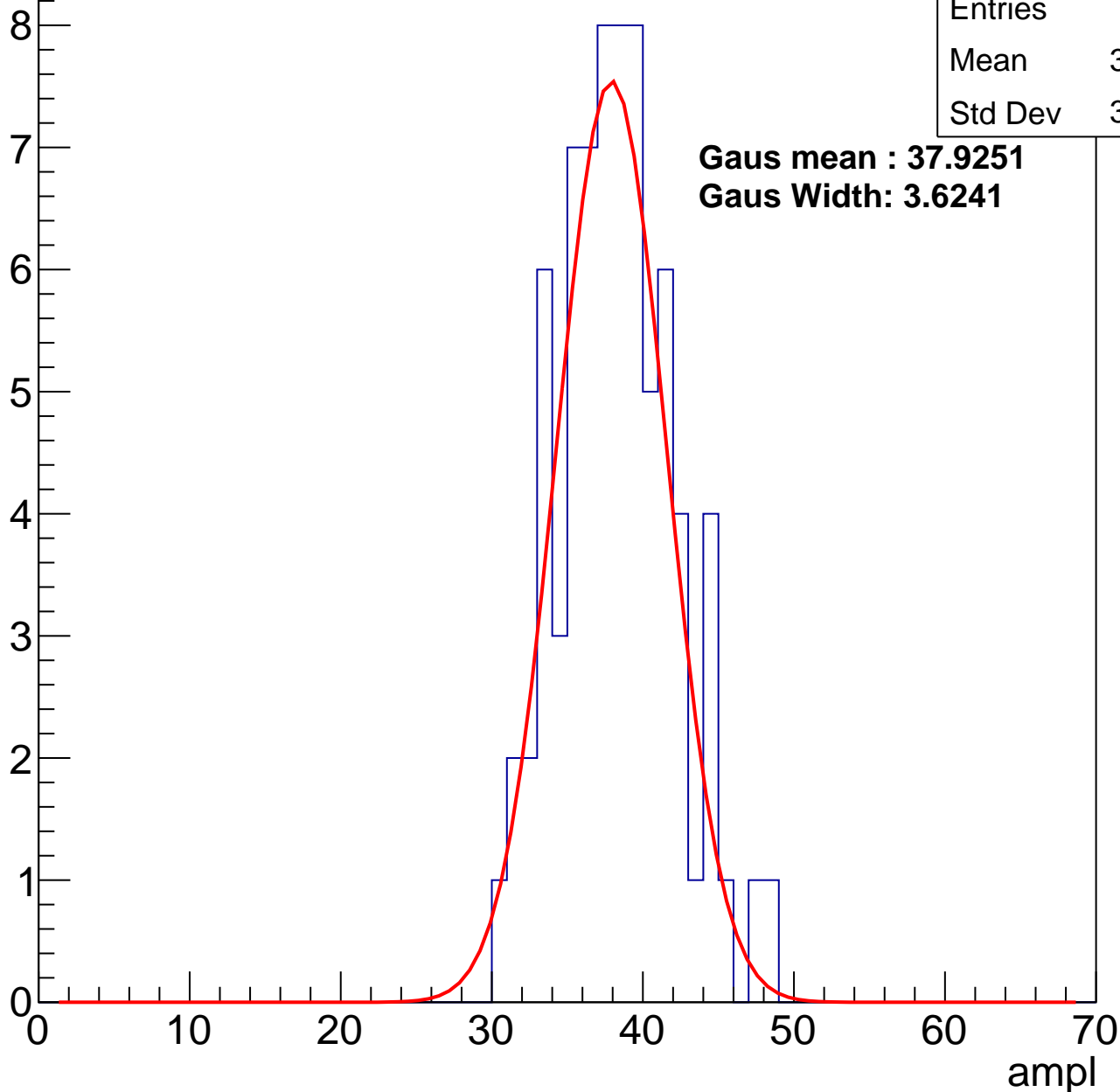
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 37.84 |
| Std Dev | 3.784 |

Gaus mean : 37.9251

Gaus Width: 3.6241



B1L100S, U9-ch46, adc2

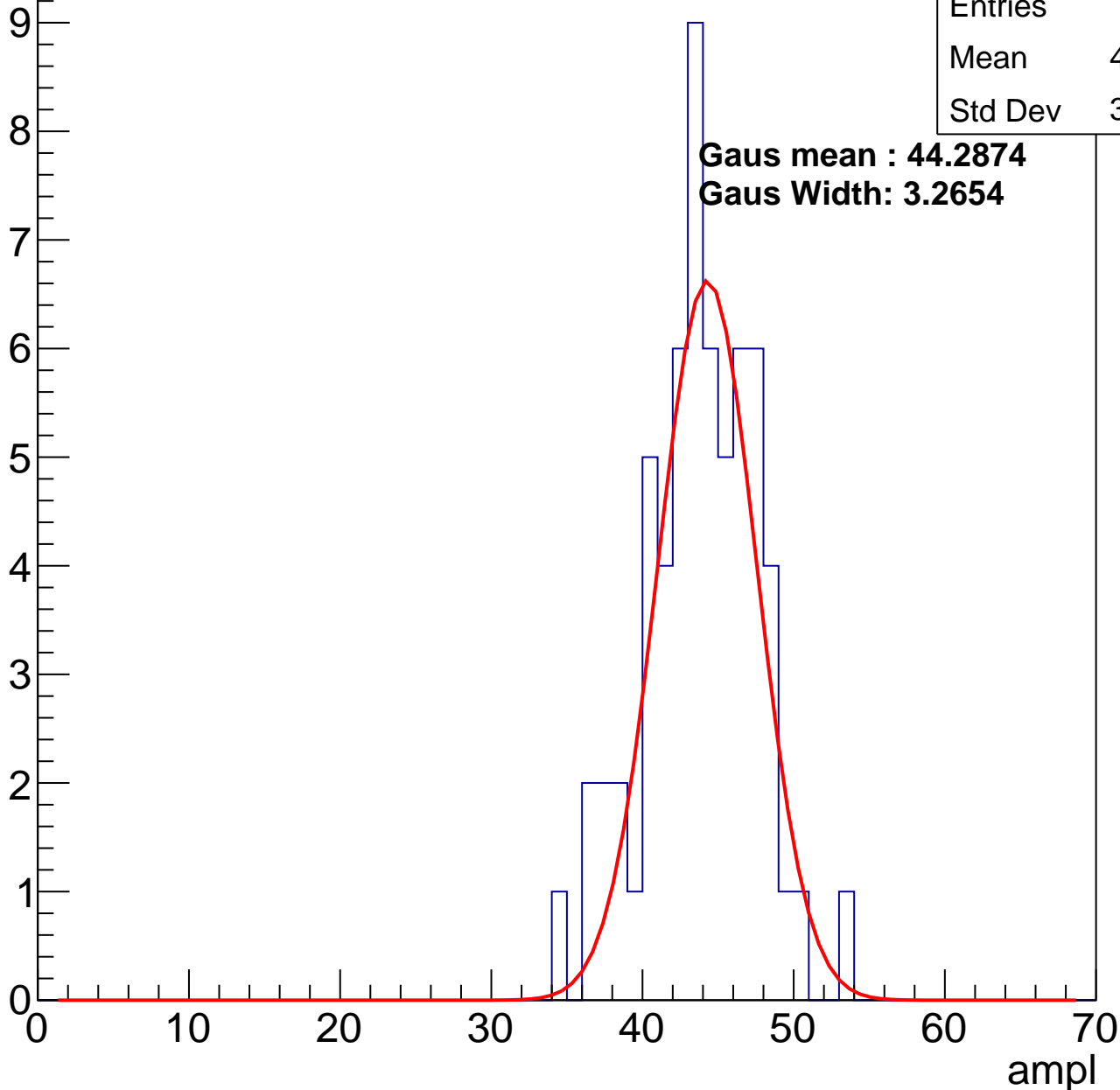
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.37 |
| Std Dev | 3.673 |

Gaus mean : 44.2874

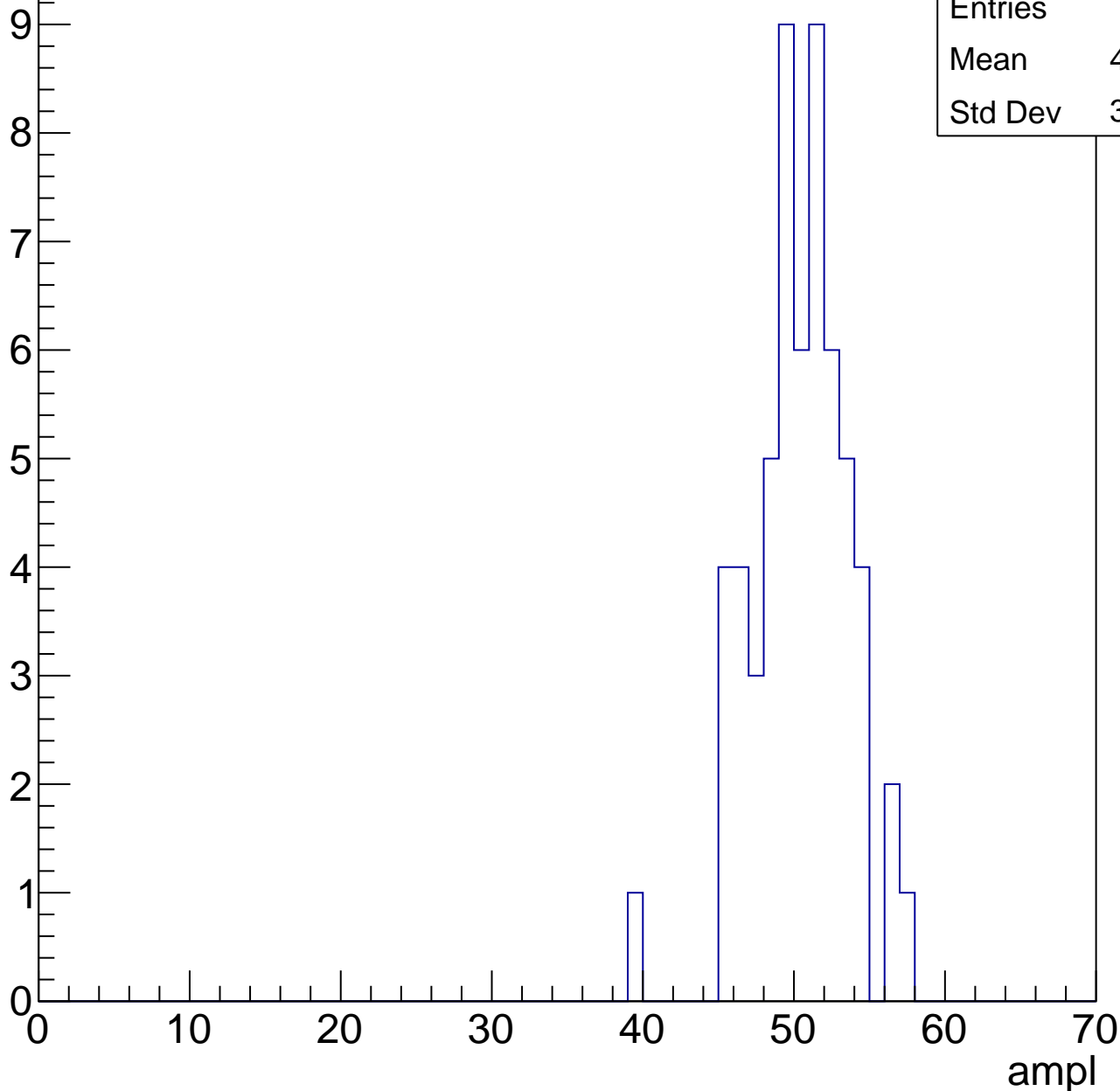
Gaus Width: 3.2654



B1L100S, U9-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

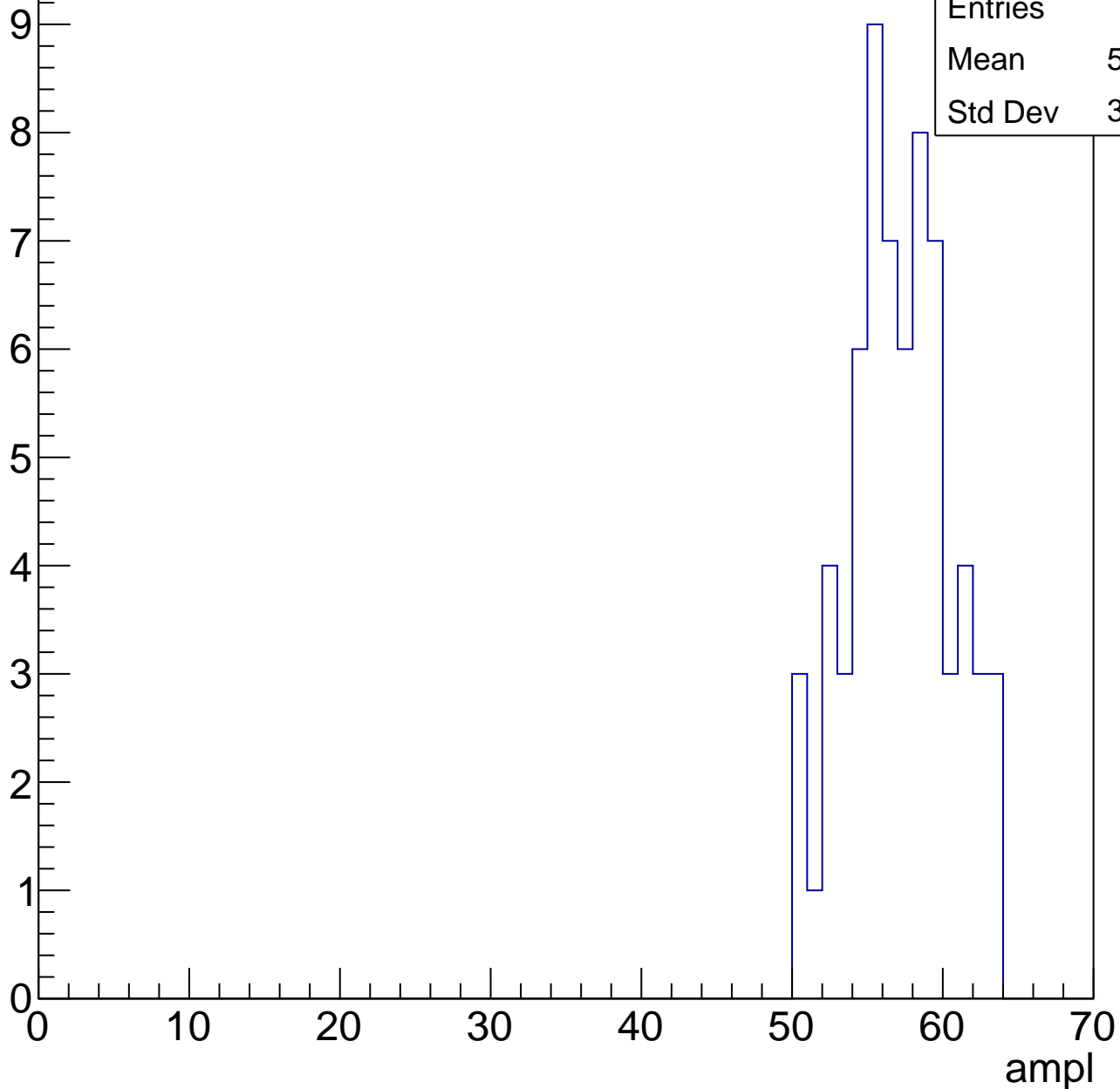


| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.93 |
| Std Dev | 3.199 |

B1L100S, U9-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

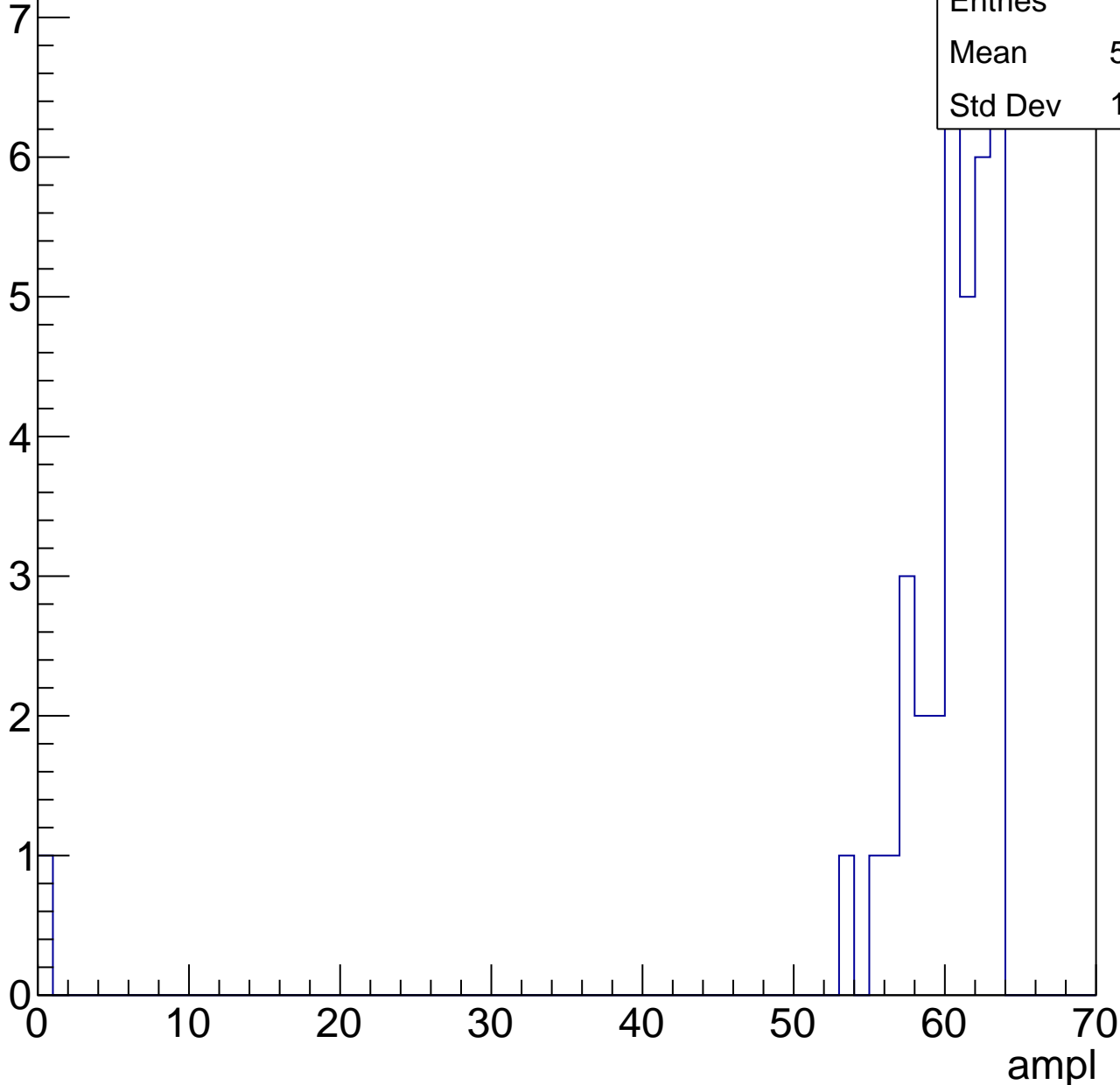


B1L100S, U9-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

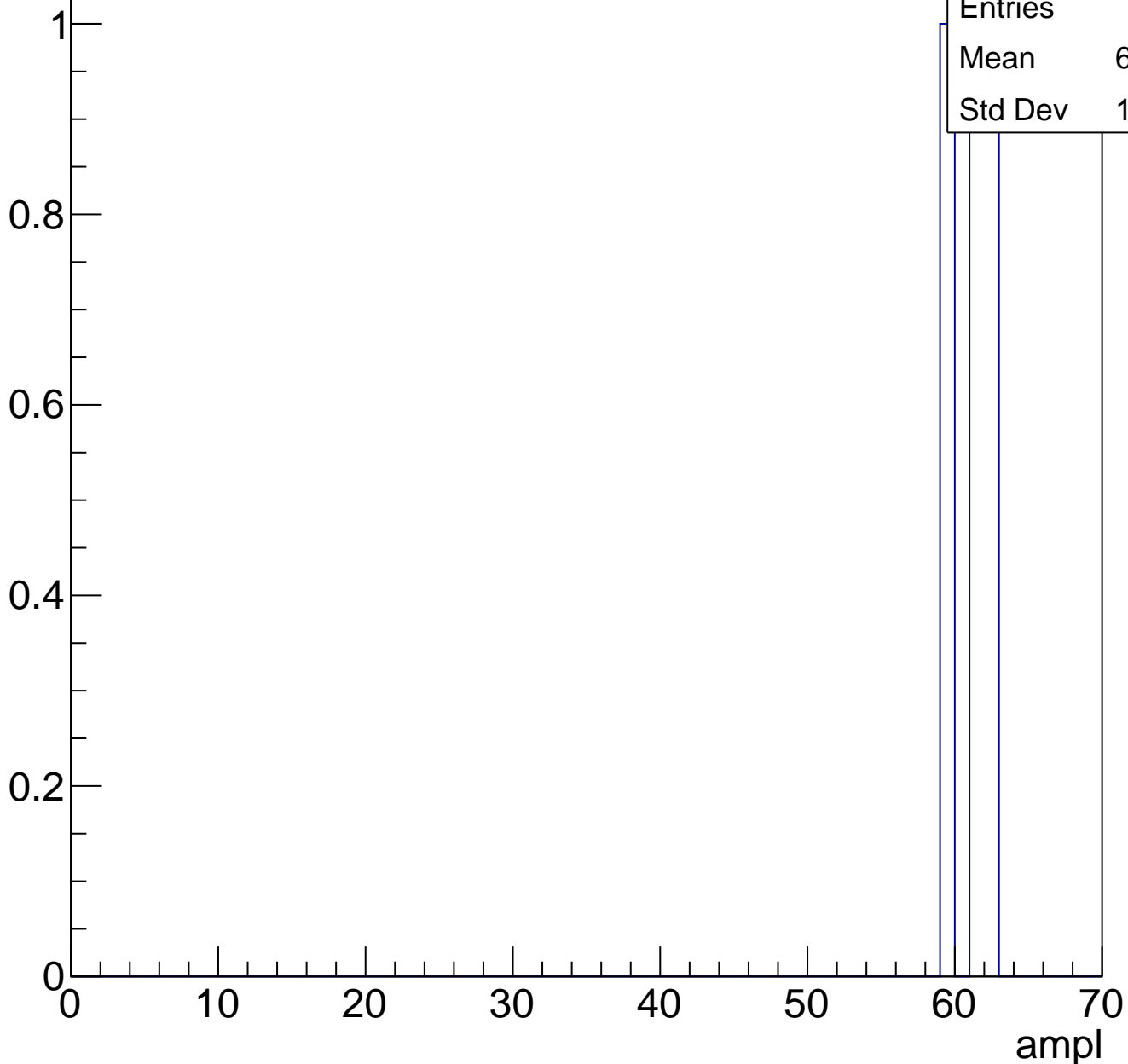
| | |
|---------|-------|
| Entries | 36 |
| Mean | 58.53 |
| Std Dev | 10.19 |



B1L100S, U9-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch47, adc0

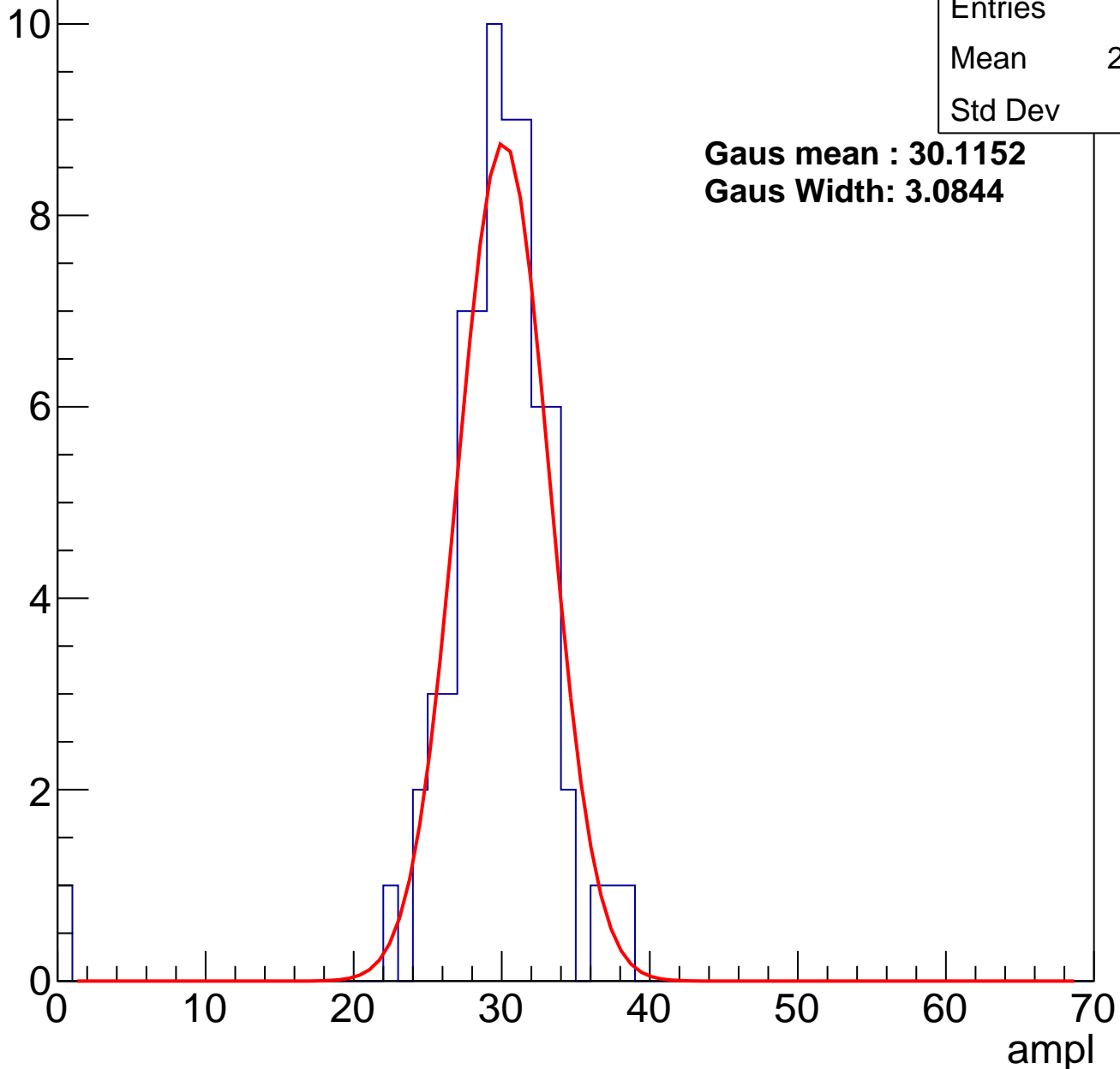
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 29.22 |
| Std Dev | 4.64 |

Gaus mean : 30.1152

Gaus Width: 3.0844

Entry



B1L100S, U9-ch47, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.99 |
| Std Dev | 3.104 |

Gaus mean : 37.0143

Gaus Width: 3.0254

10

8

6

4

2

0

0

10

20

30

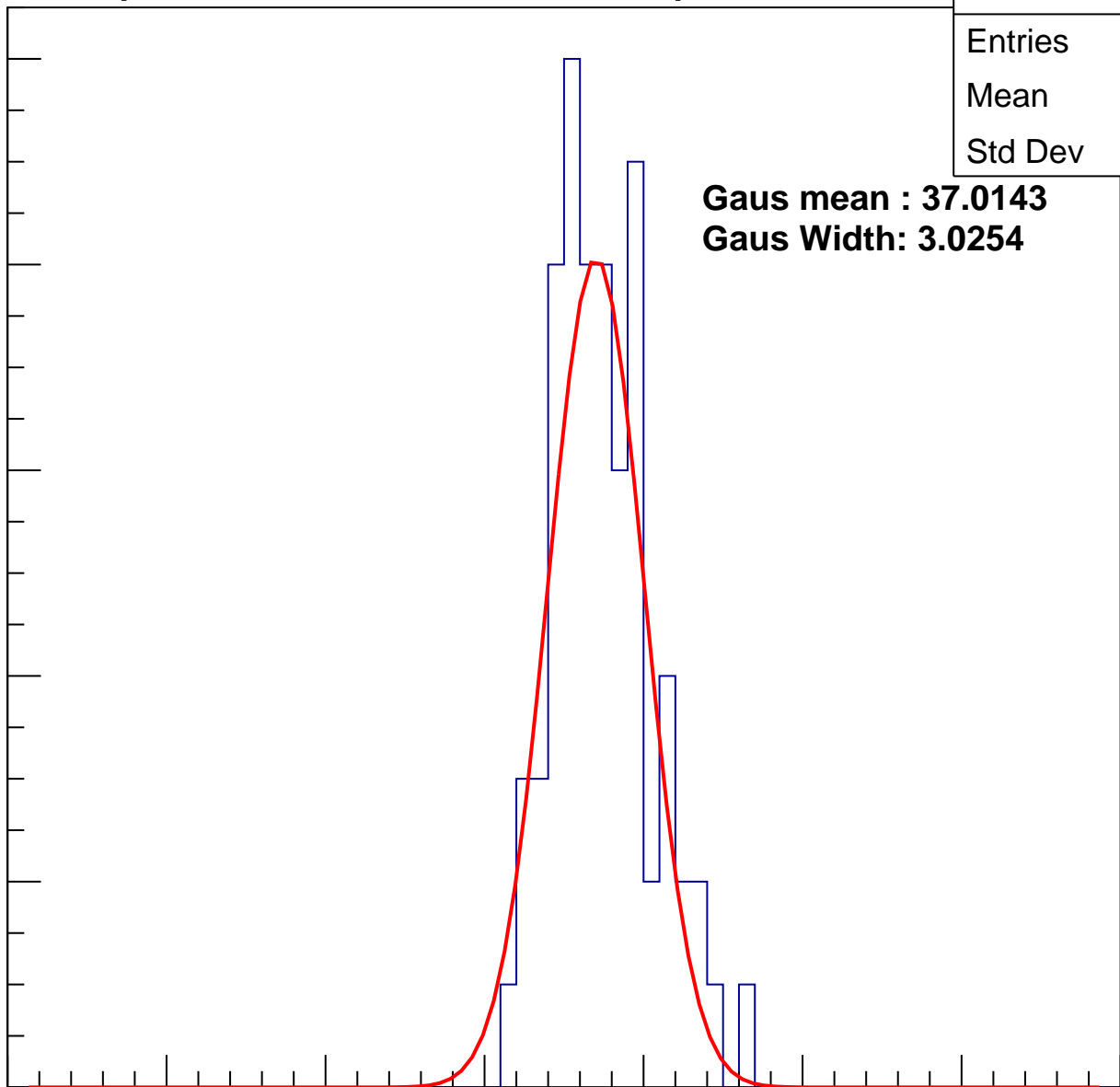
40

50

60

70

ampl



B1L100S, U9-ch47, adc2

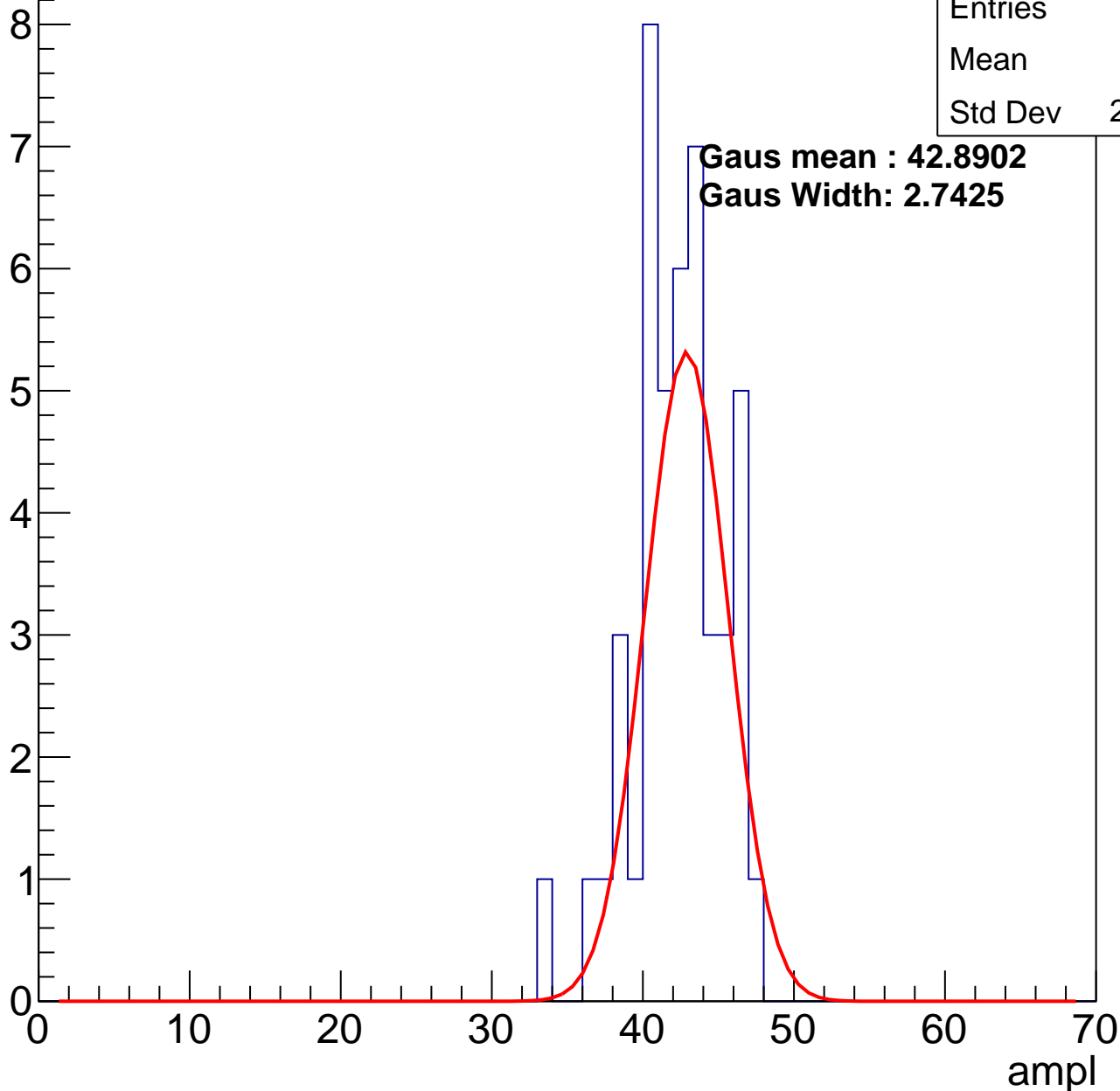
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 41.8 |
| Std Dev | 2.926 |

Gaus mean : 42.8902

Gaus Width: 2.7425

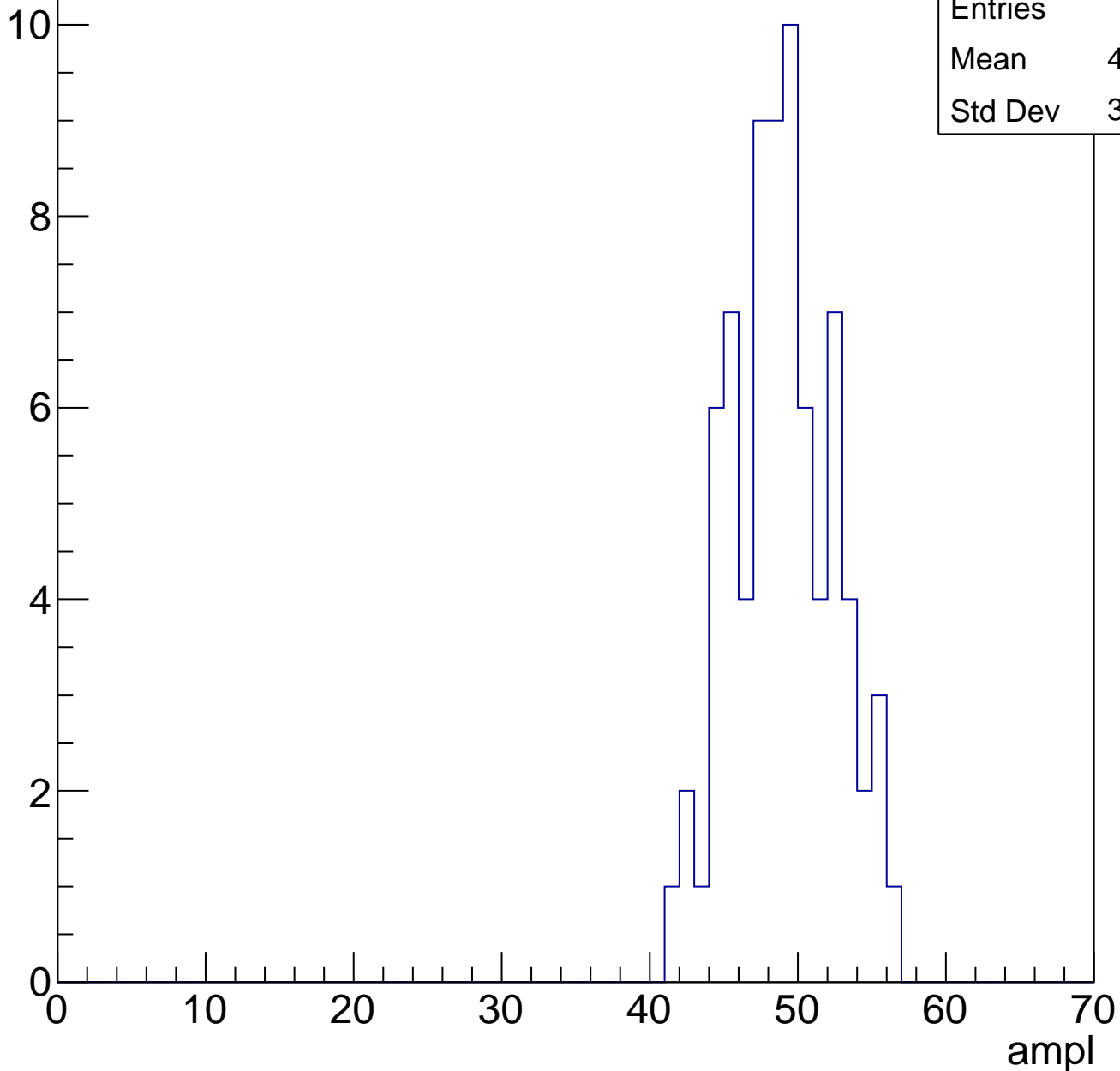


B1L100S, U9-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

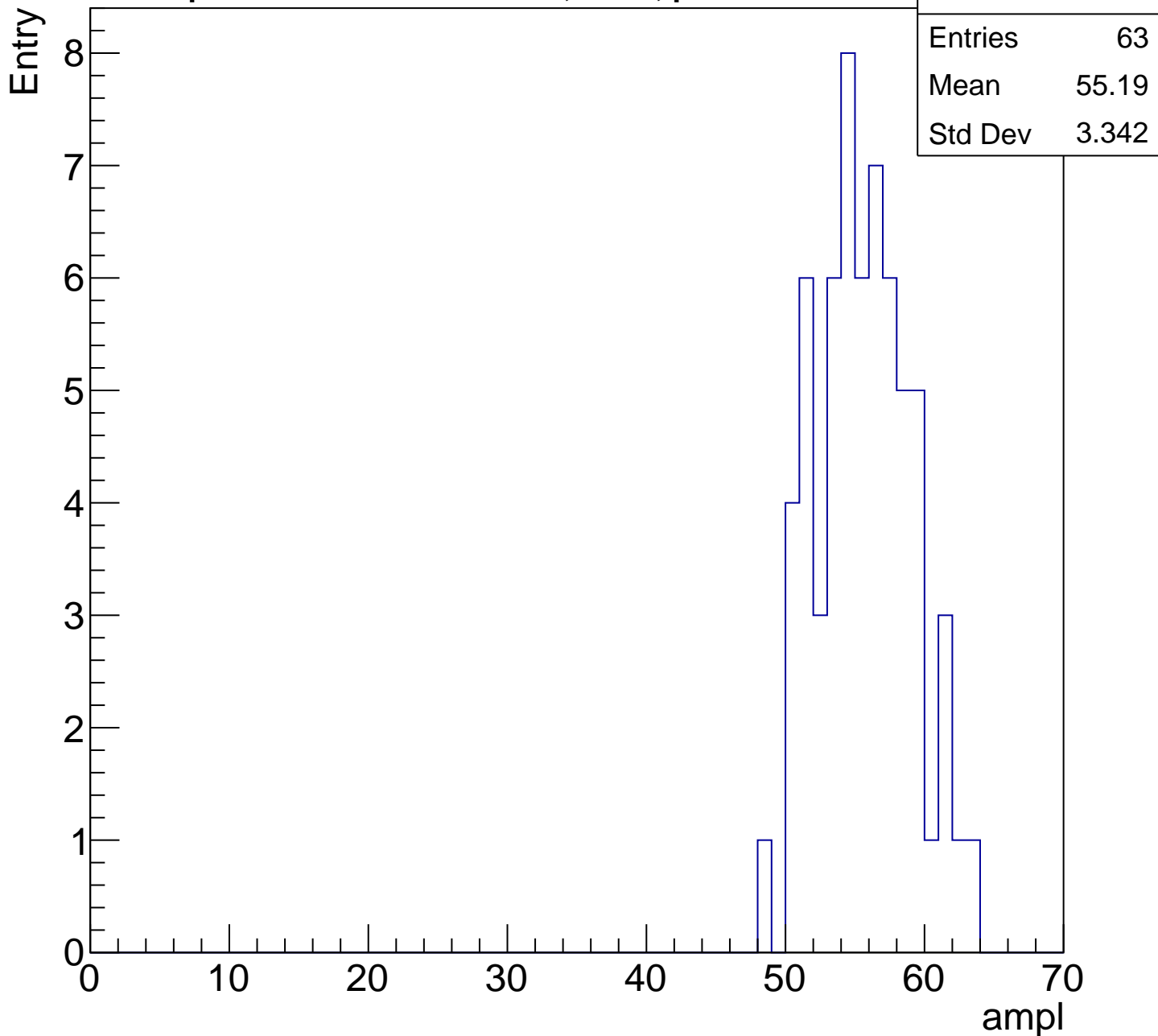
| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.49 |
| Std Dev | 3.397 |

Entry



B1L100S, U9-ch47, adc4

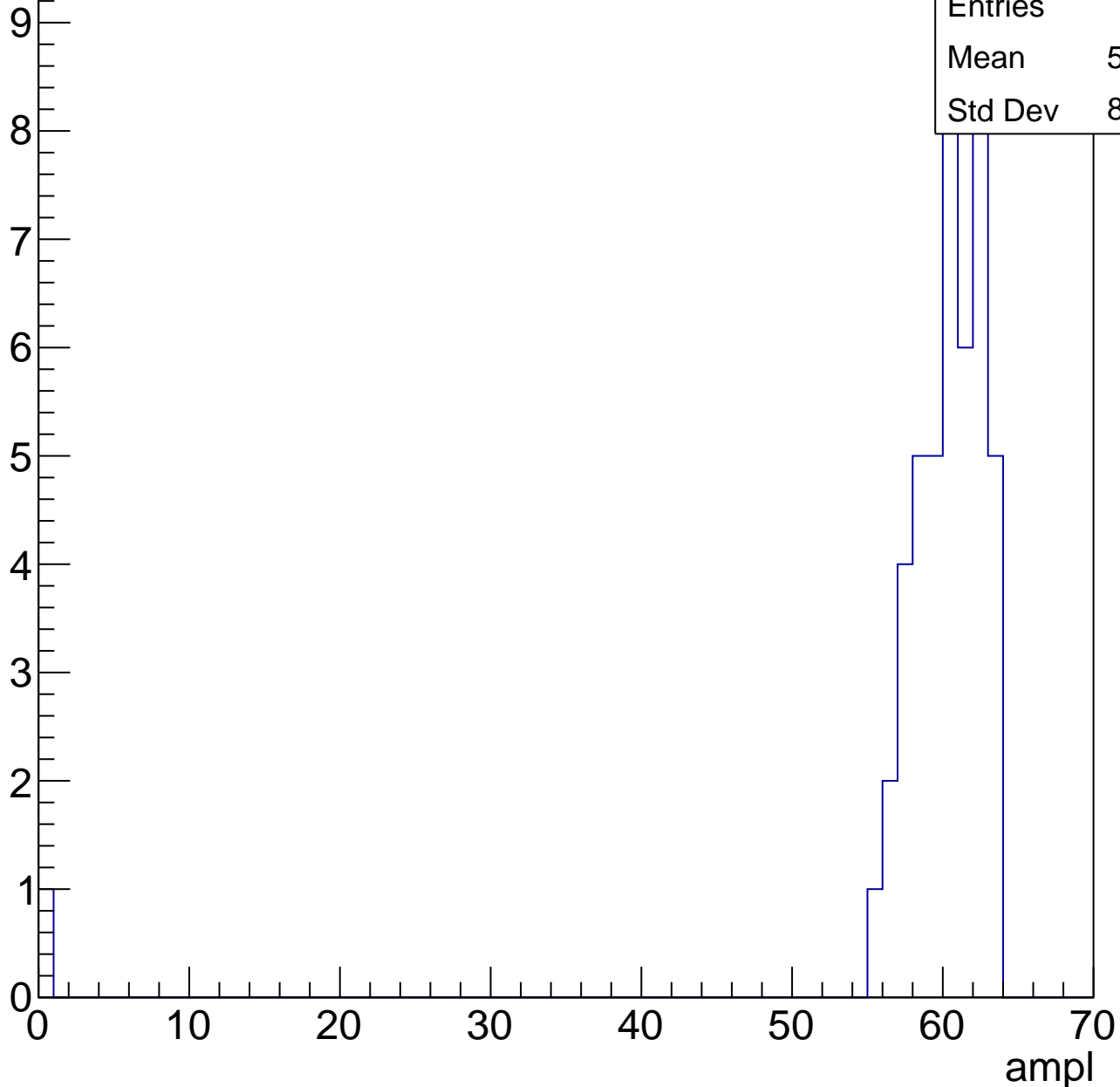
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

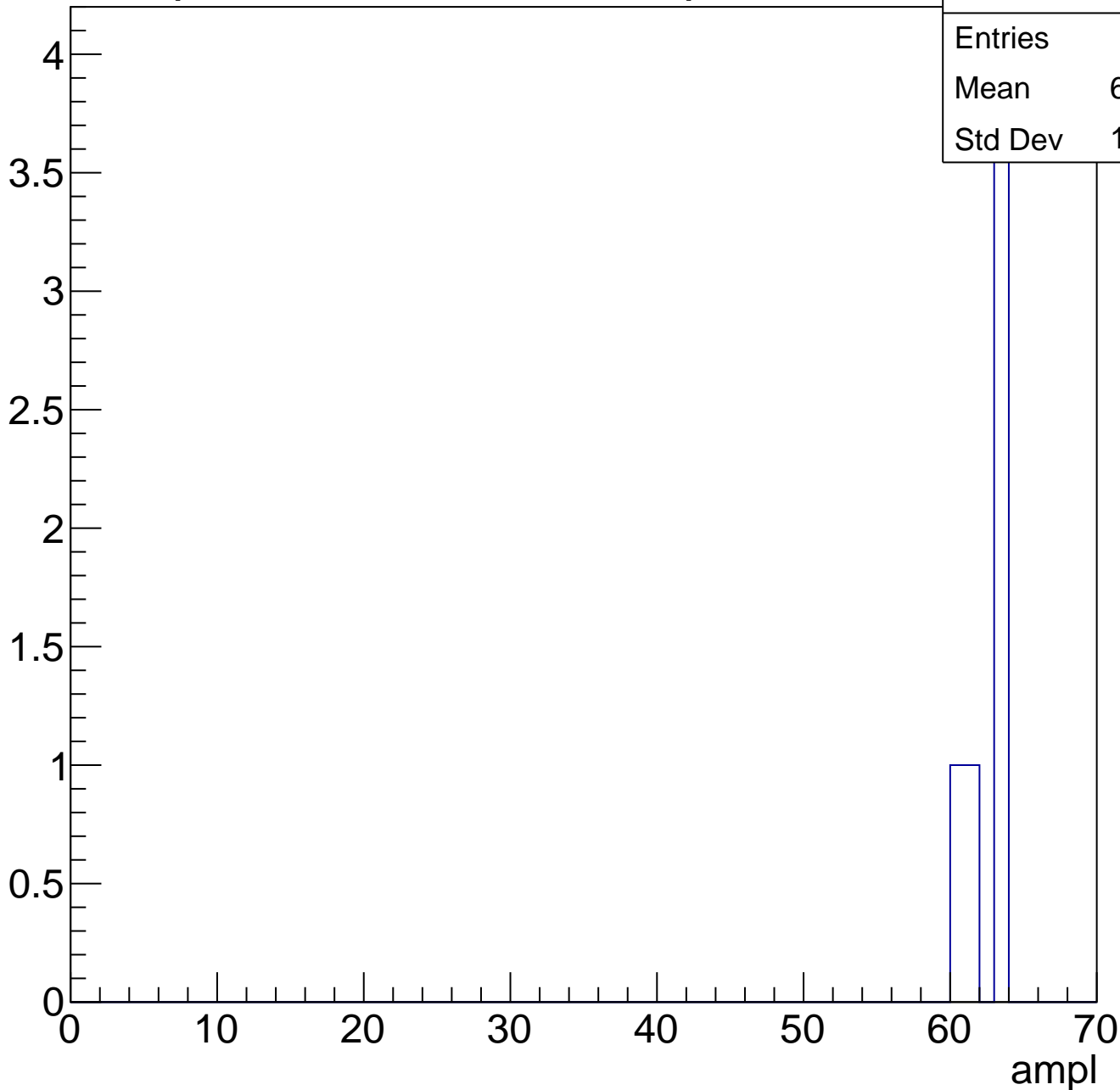
Entry



B1L100S, U9-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch48, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 29.41 |
| Std Dev | 4.862 |

Gaus mean : 30.4361

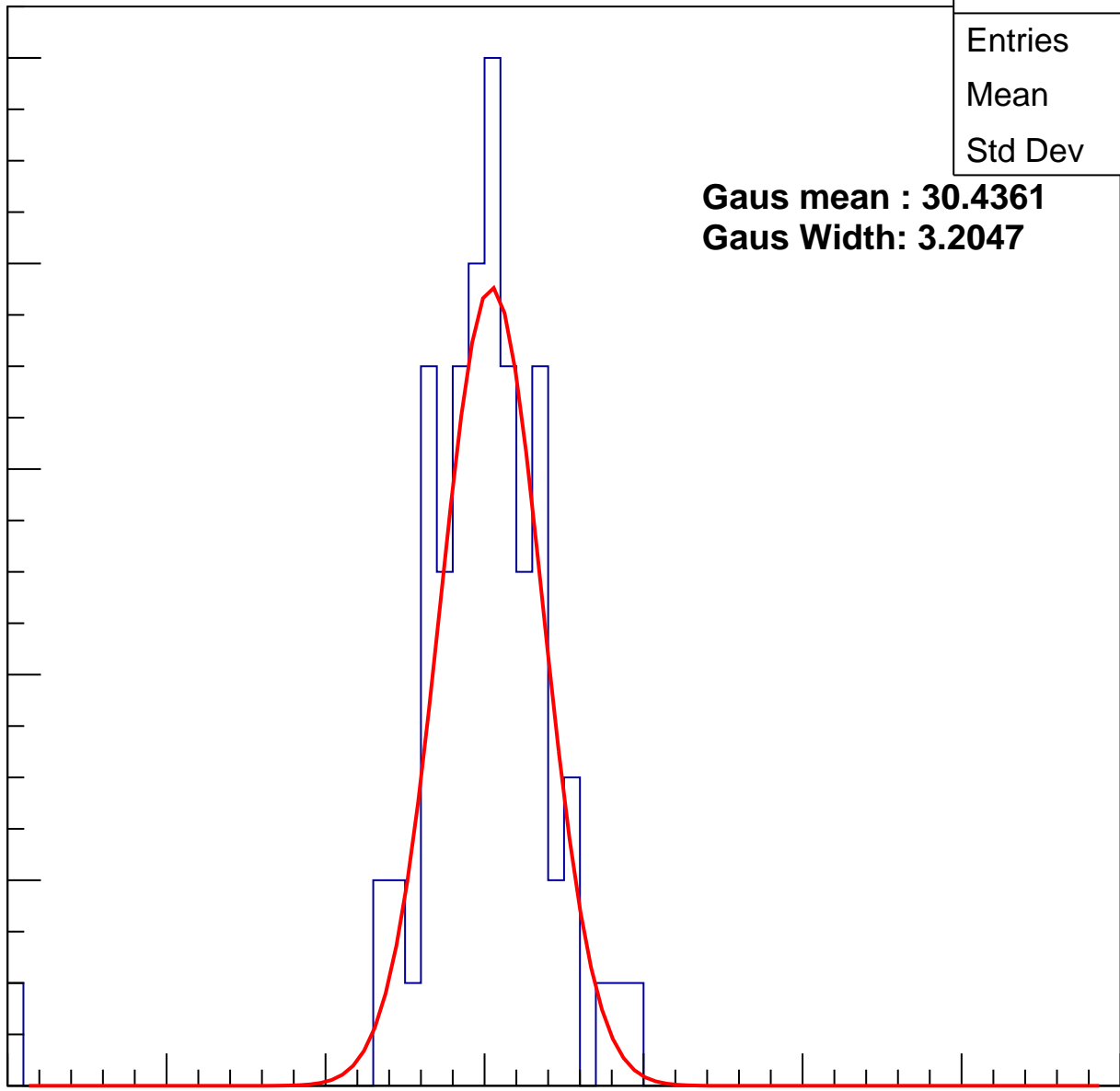
Gaus Width: 3.2047

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch48, adc1

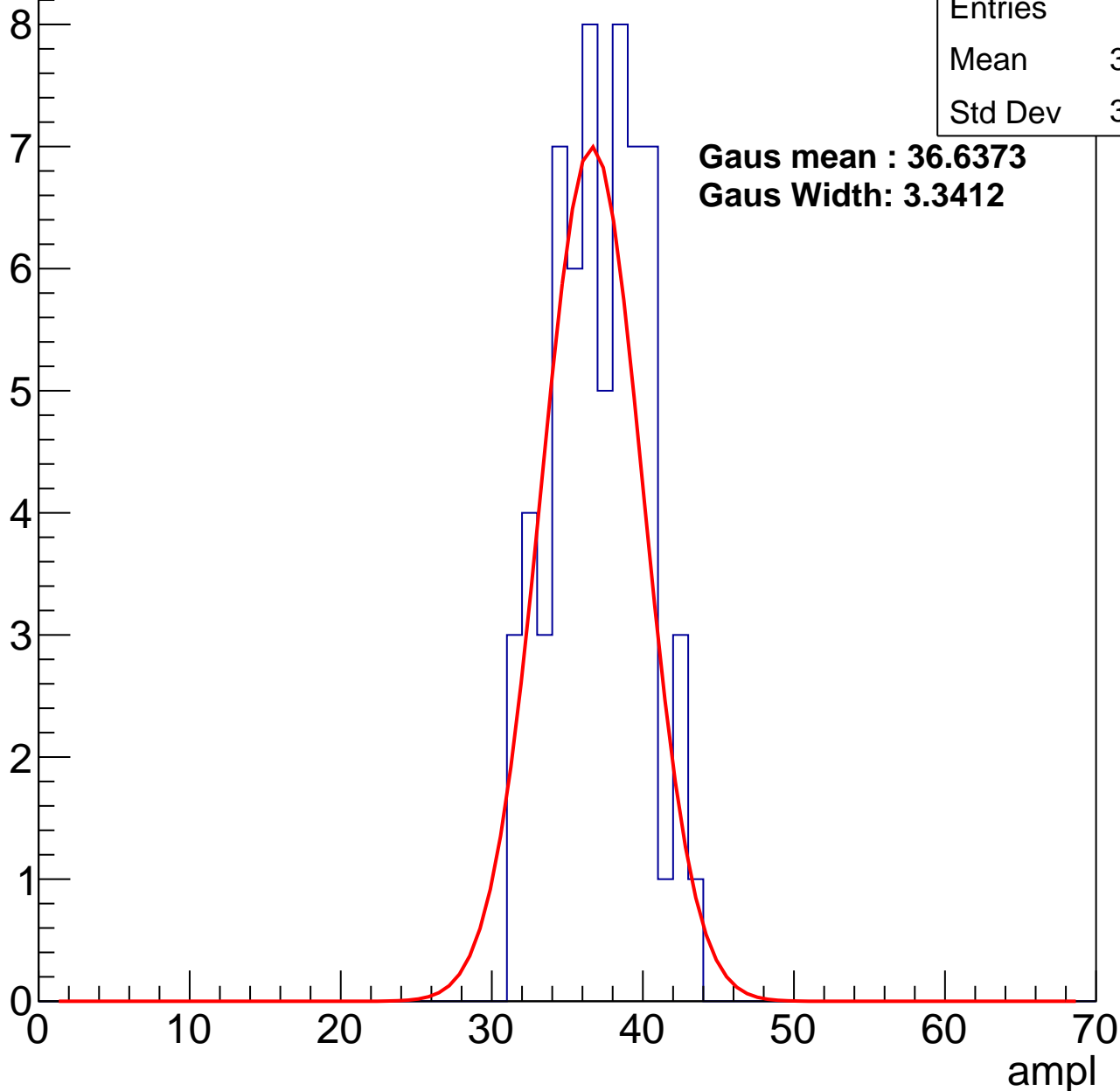
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.63 |
| Std Dev | 3.004 |

Gaus mean : 36.6373

Gaus Width: 3.3412



B1L100S, U9-ch48, adc2

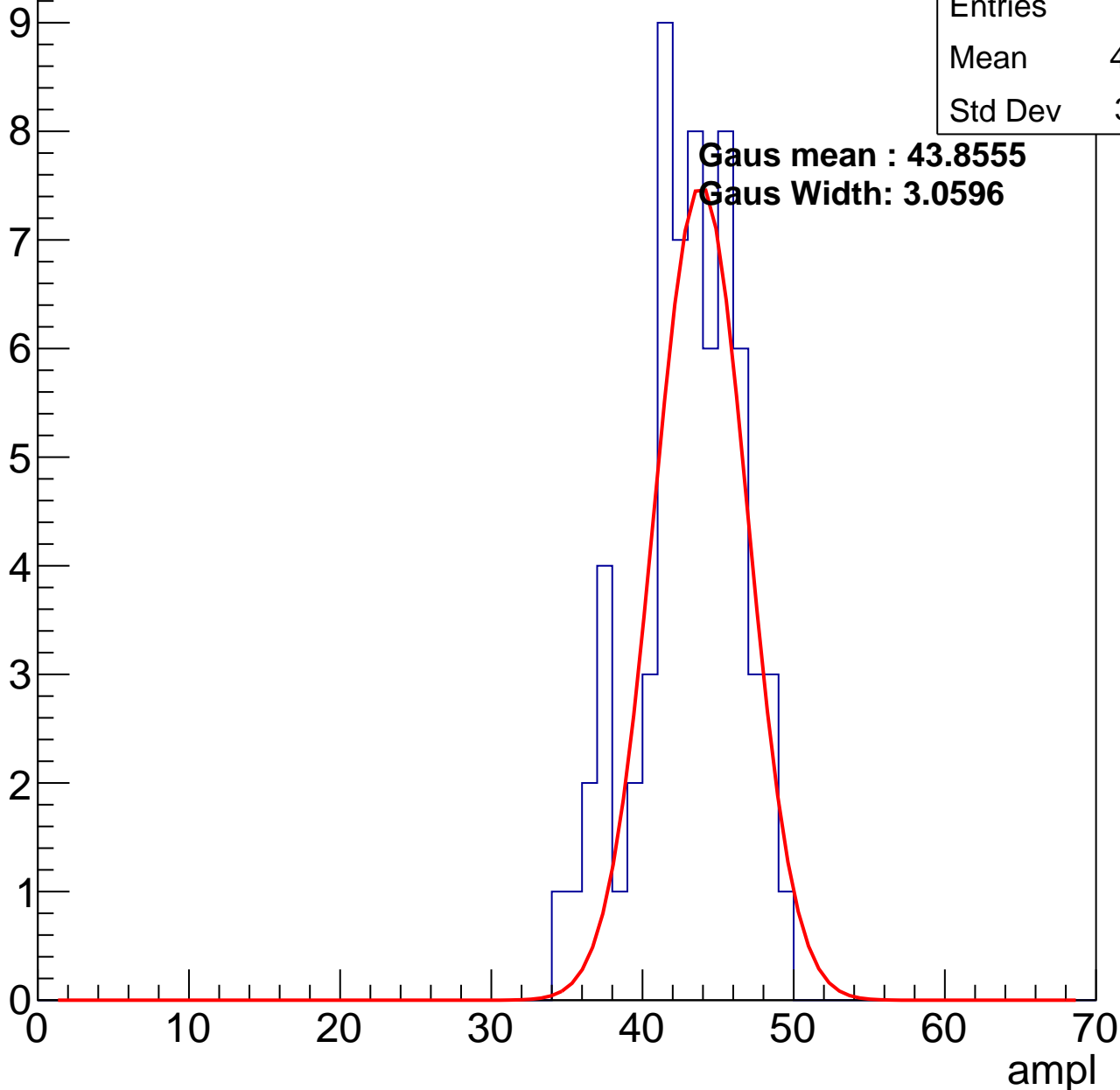
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 42.55 |
| Std Dev | 3.401 |

Gaus mean : 43.8555

Gaus Width: 3.0596



B1L100S, U9-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

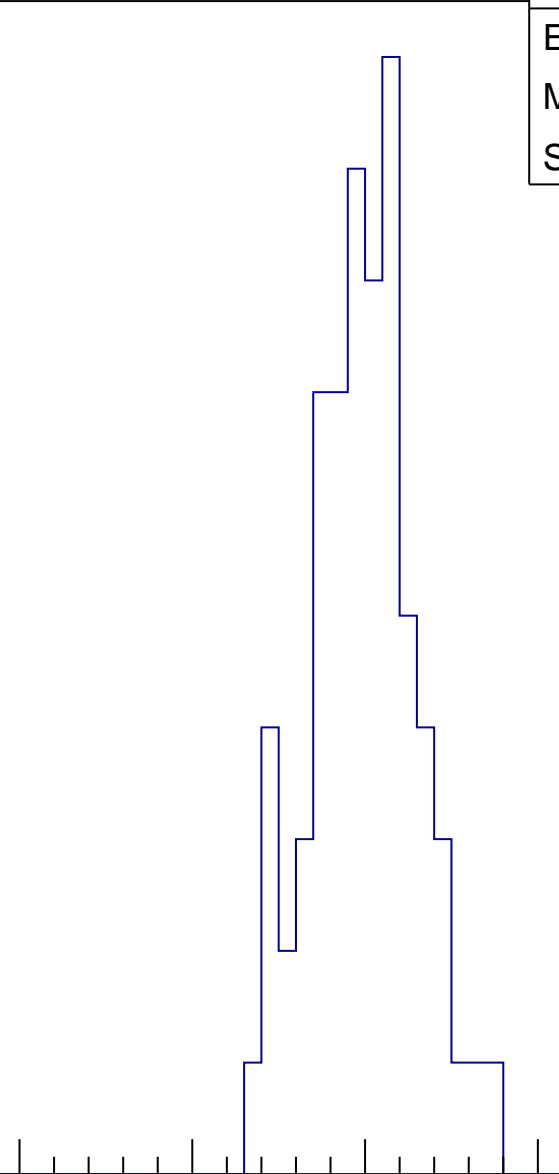
| | |
|---------|-------|
| Entries | 66 |
| Mean | 49.47 |
| Std Dev | 3.006 |

Entry

10
8
6
4
2
0

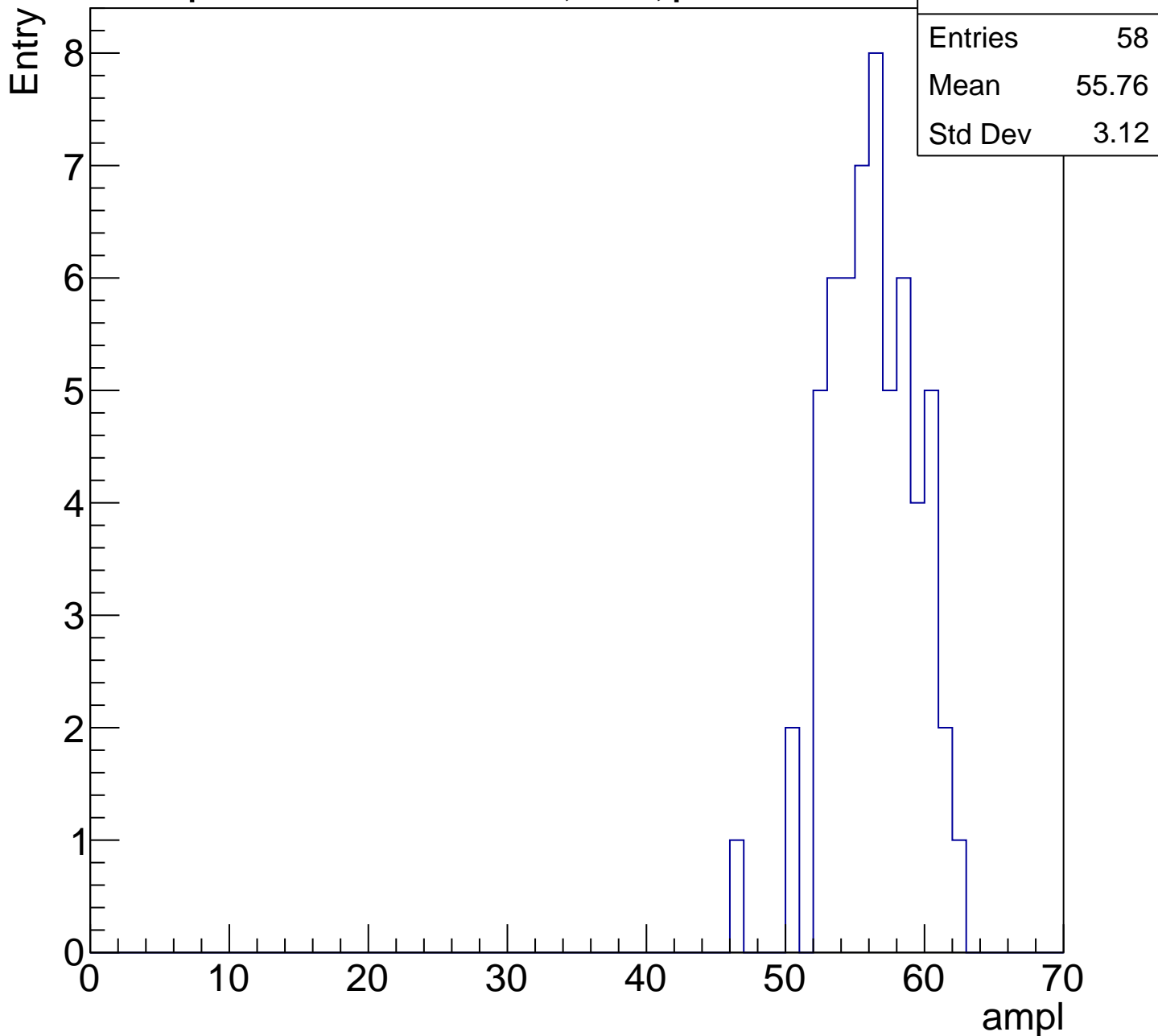
0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

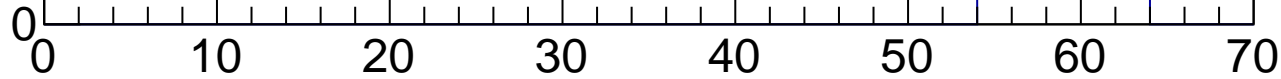
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.98 |
| Std Dev | 2.512 |

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

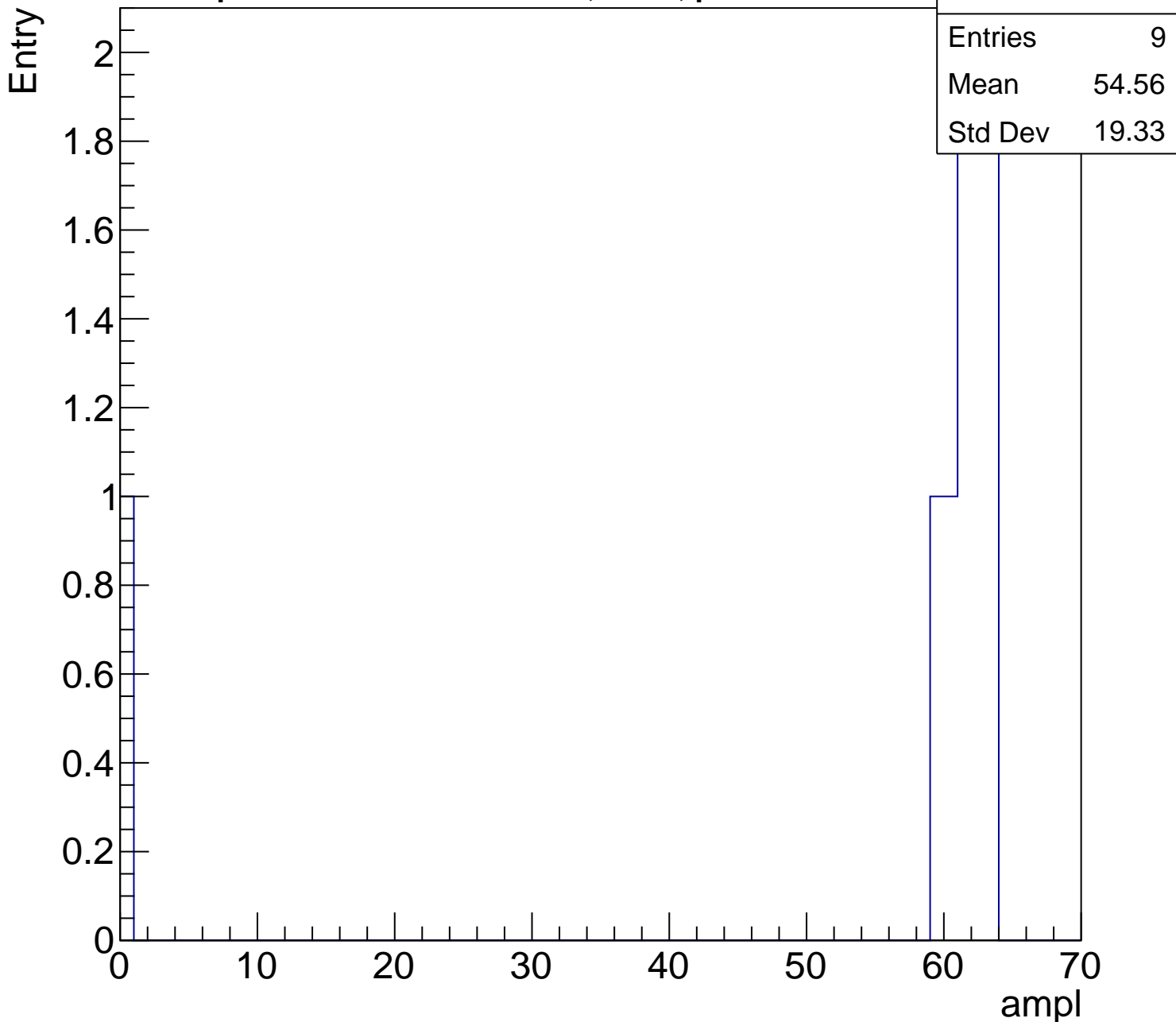
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 9 |
| Mean | 54.56 |
| Std Dev | 19.33 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch49, adc0

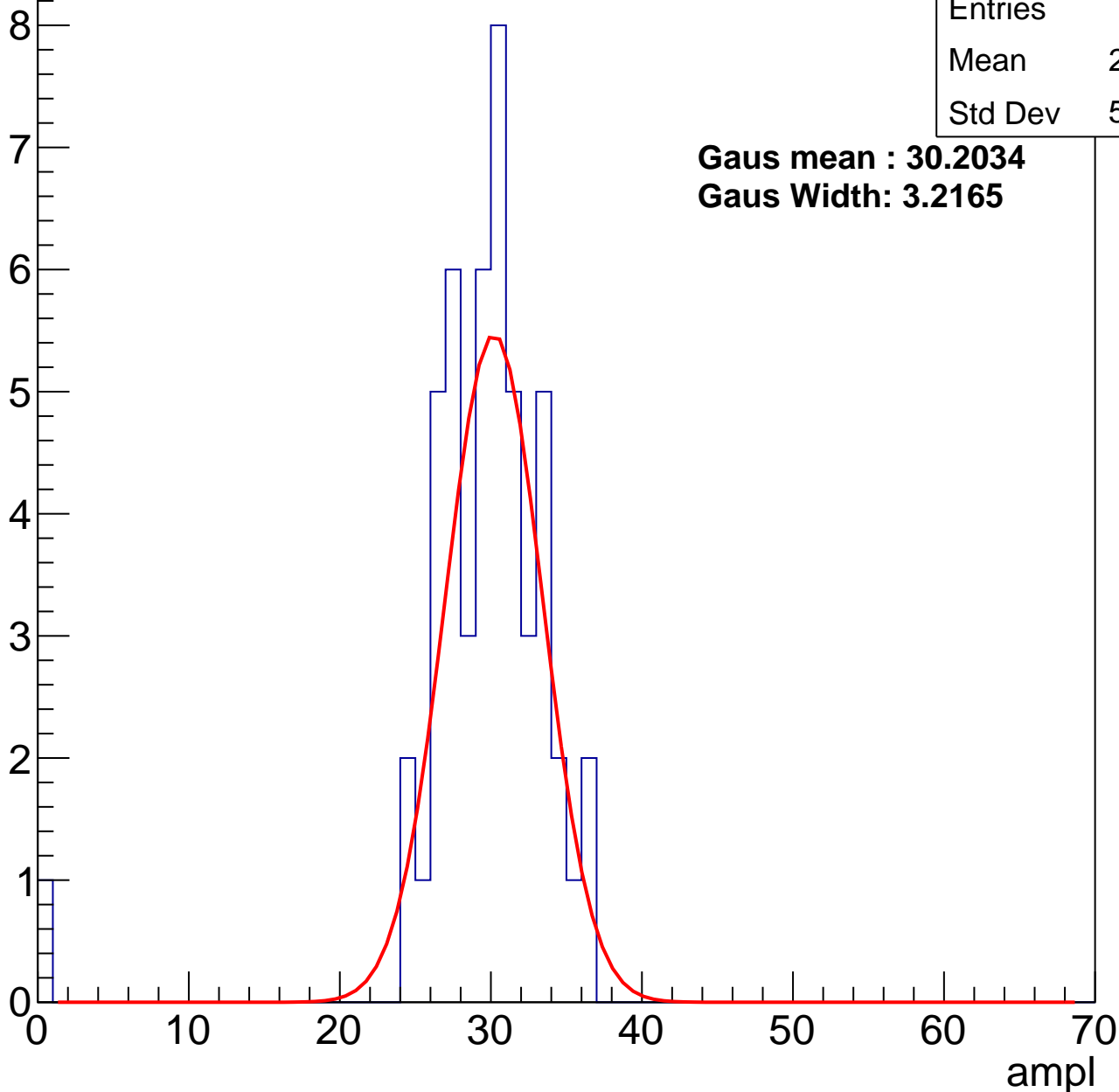
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 29.08 |
| Std Dev | 5.094 |

Gaus mean : 30.2034

Gaus Width: 3.2165



B1L100S, U9-ch49, adc1

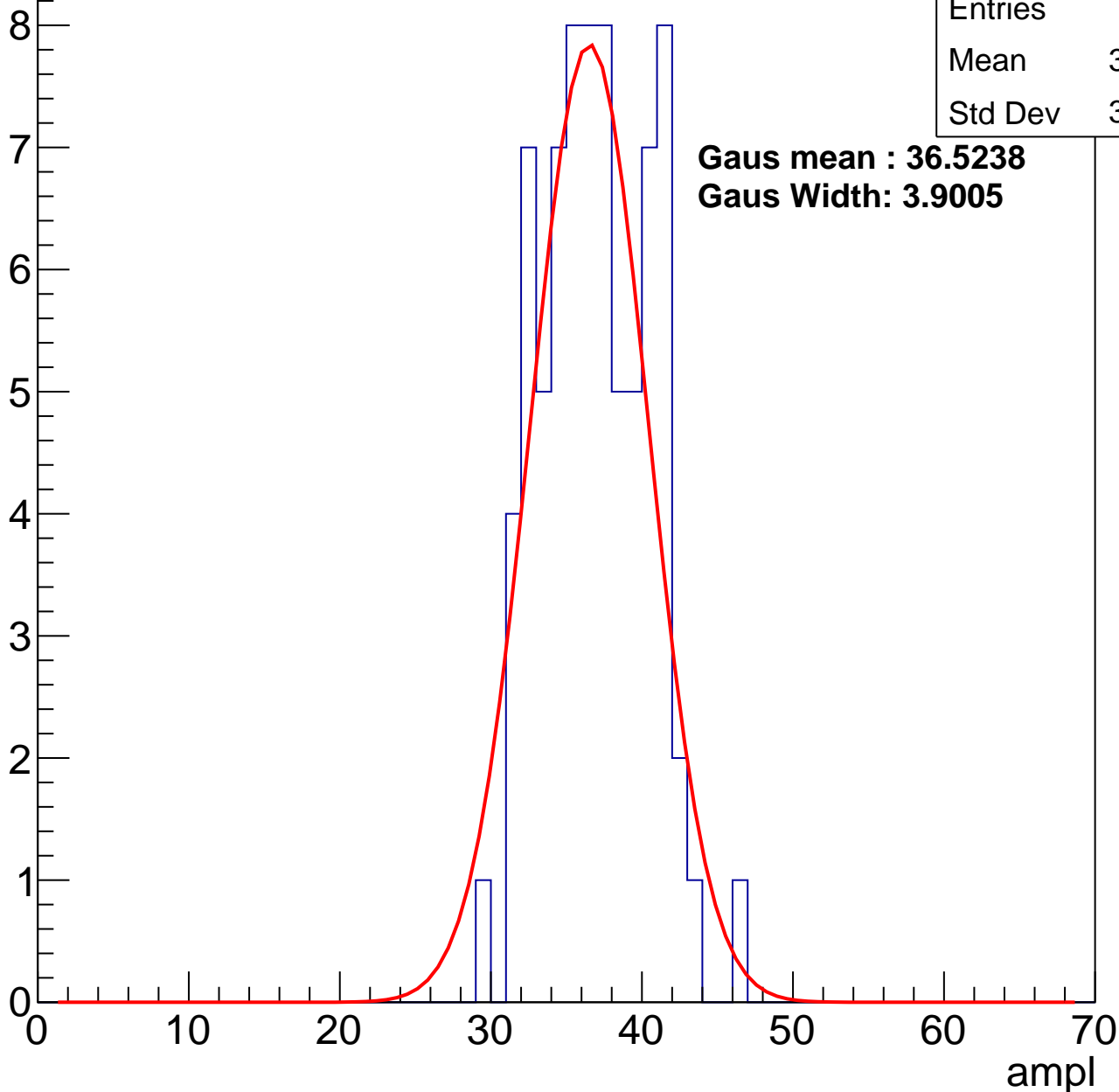
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 36.49 |
| Std Dev | 3.474 |

Gaus mean : 36.5238

Gaus Width: 3.9005



B1L100S, U9-ch49, adc2

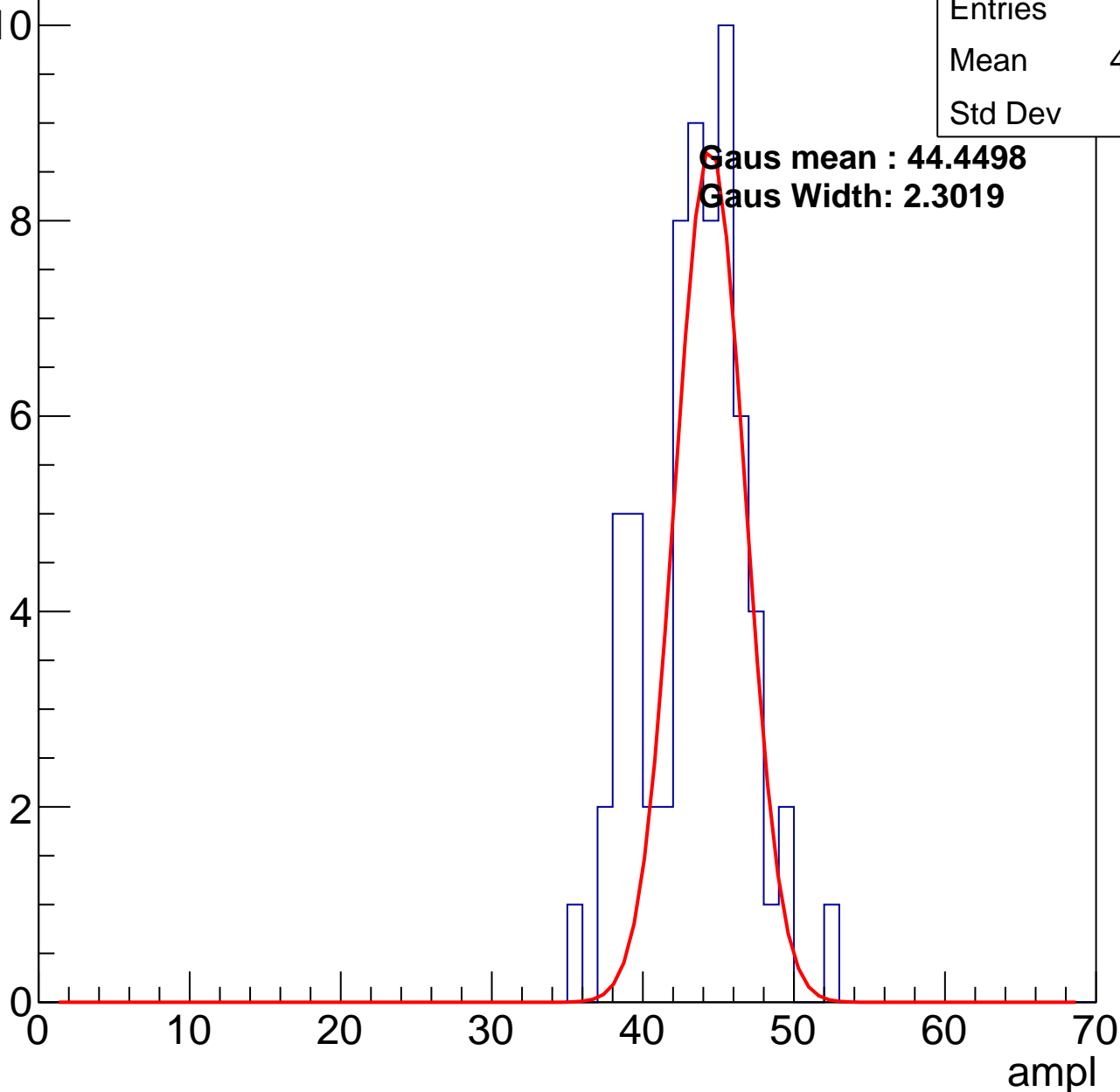
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 43.08 |
| Std Dev | 3.3 |

Gaus mean : 44.4498

Gaus Width: 2.3019

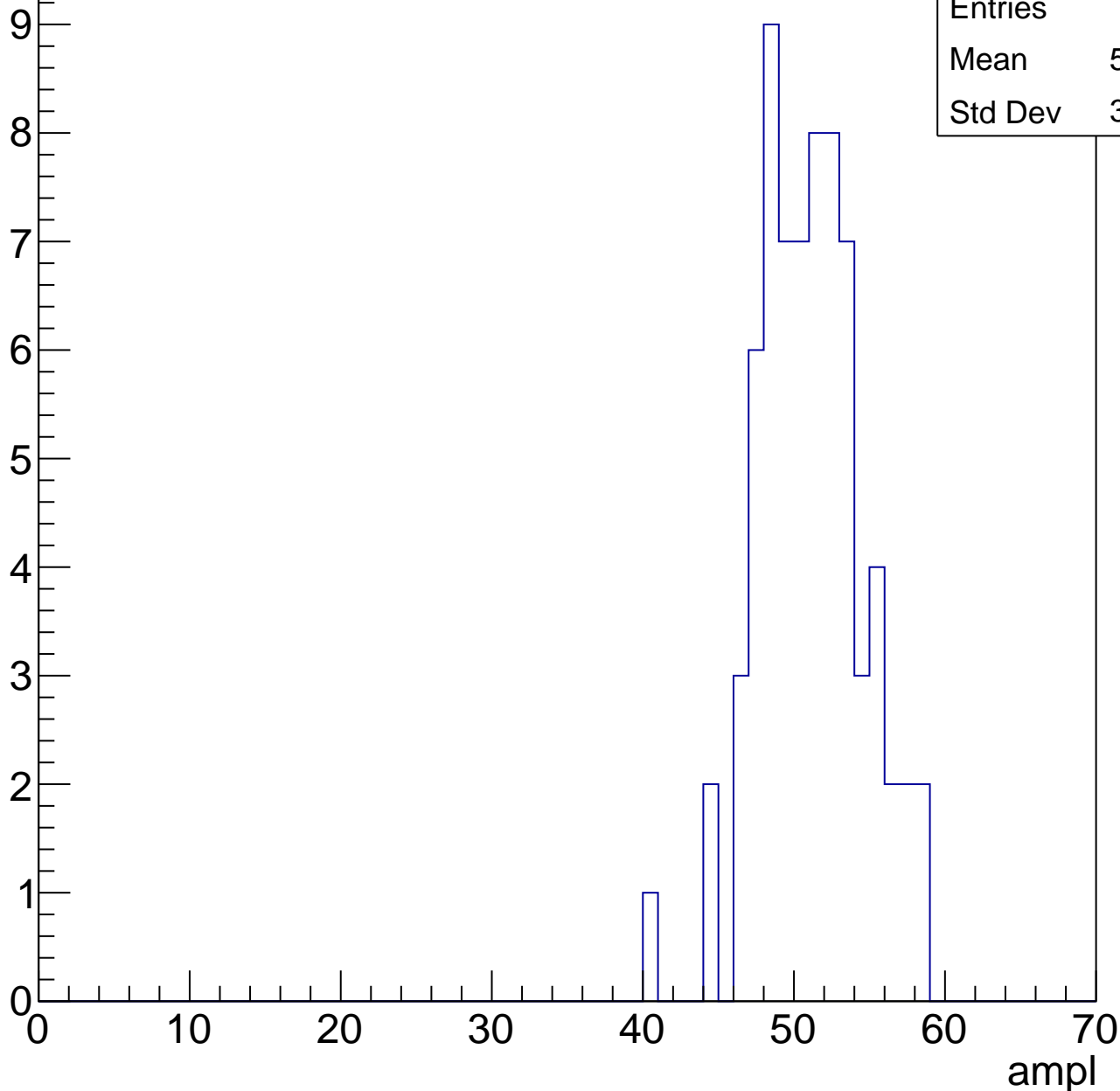


B1L100S, U9-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 50.59 |
| Std Dev | 3.442 |

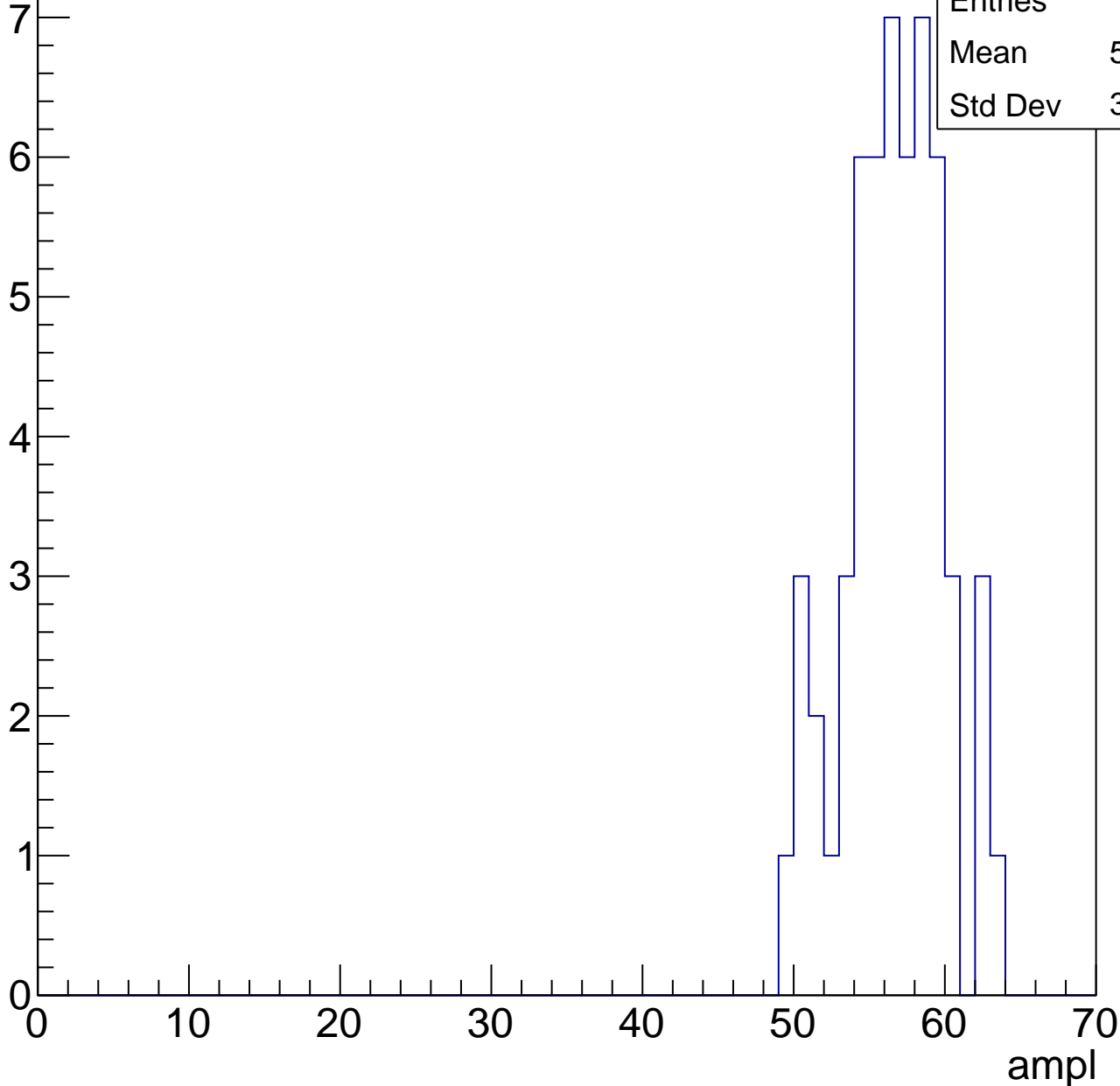


B1L100S, U9-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 56.16 |
| Std Dev | 3.224 |

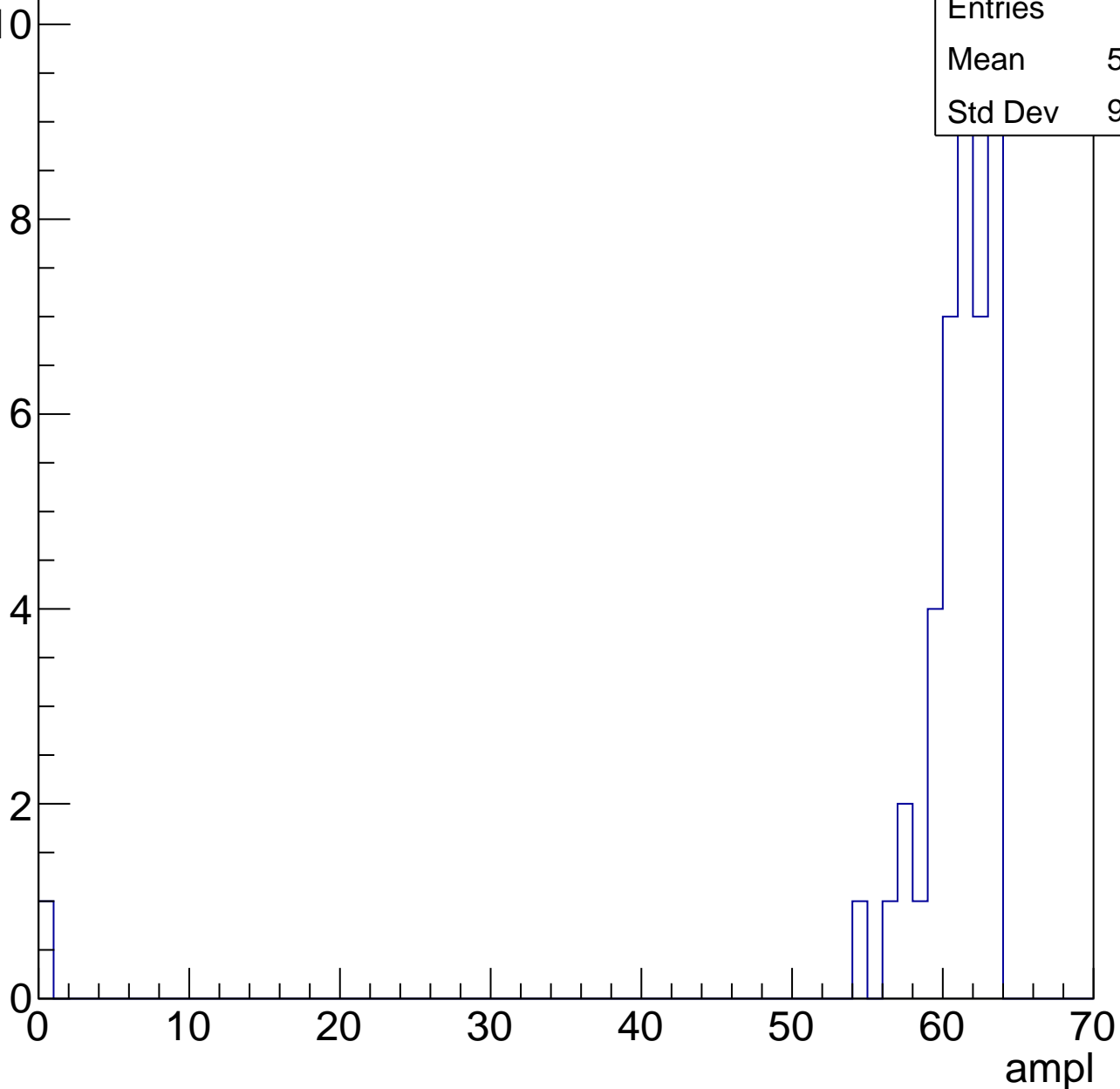


B1L100S, U9-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.28 |
| Std Dev | 9.369 |



B1L100S, U9-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch50, adc0

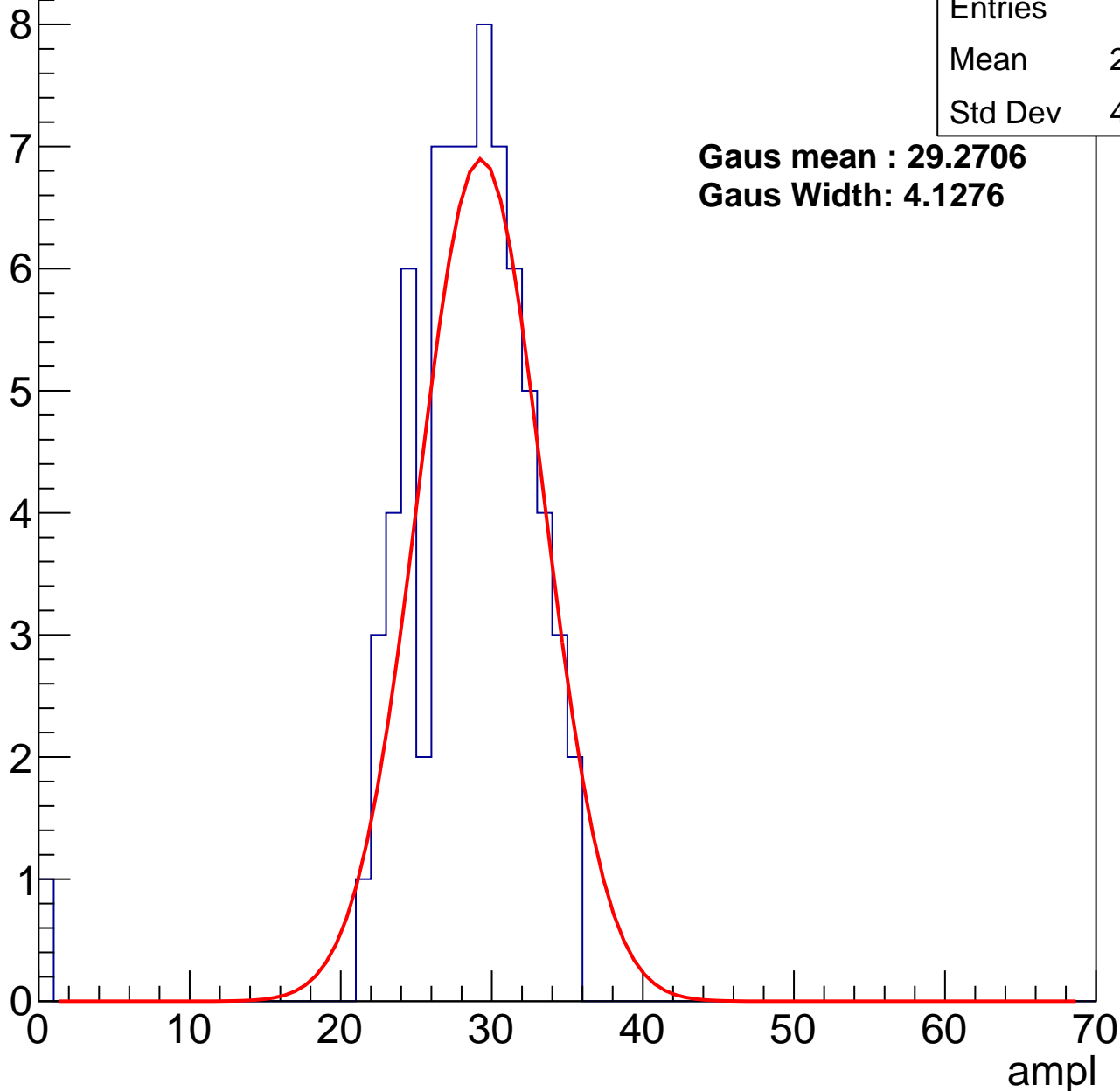
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 27.84 |
| Std Dev | 4.774 |

Gaus mean : 29.2706

Gaus Width: 4.1276



B1L100S, U9-ch50, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.21 |
| Std Dev | 3.338 |

Gaus mean : 36.5083

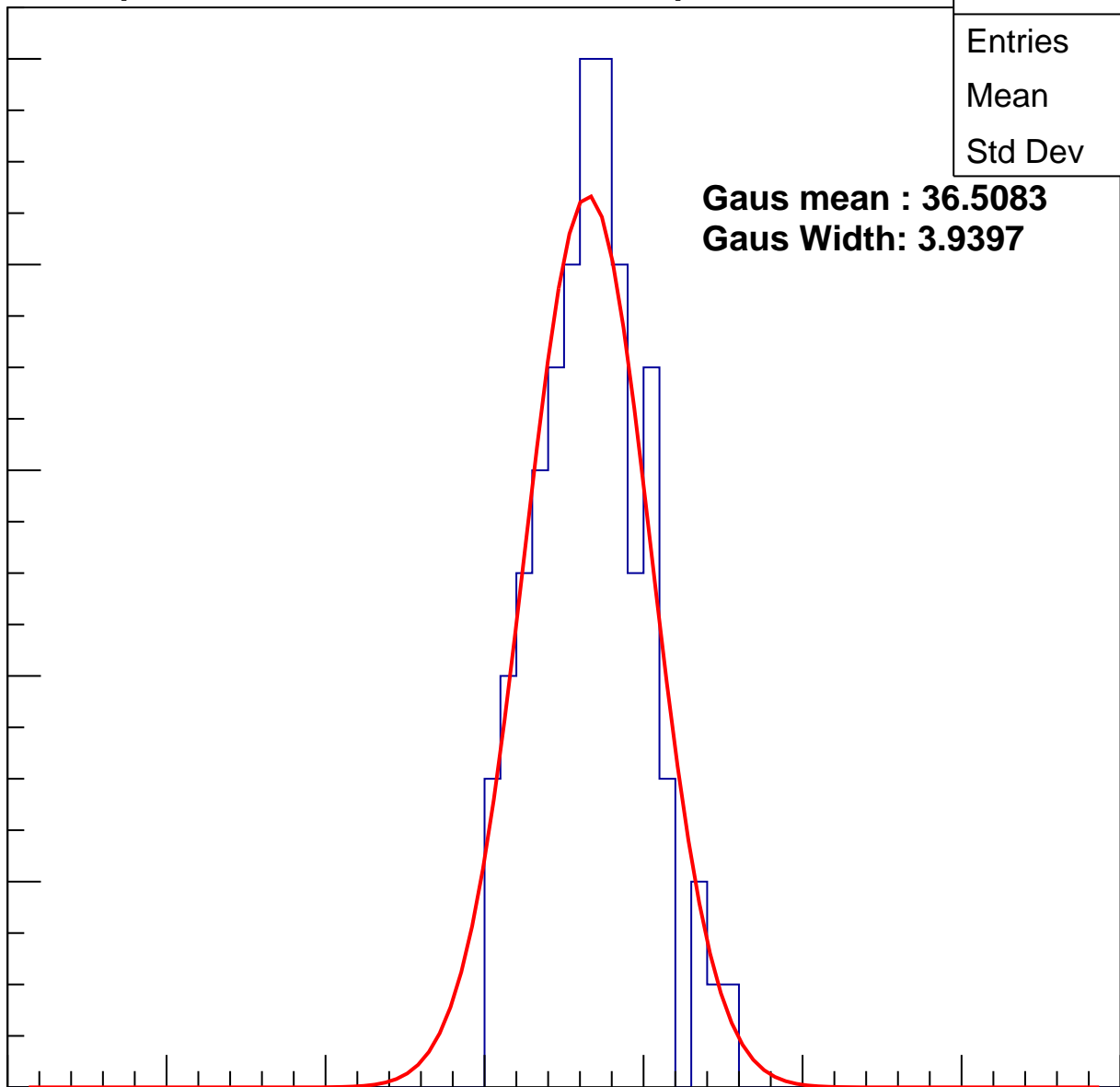
Gaus Width: 3.9397

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch50, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 43.91 |
| Std Dev | 3.121 |

Gaus mean : 44.1214

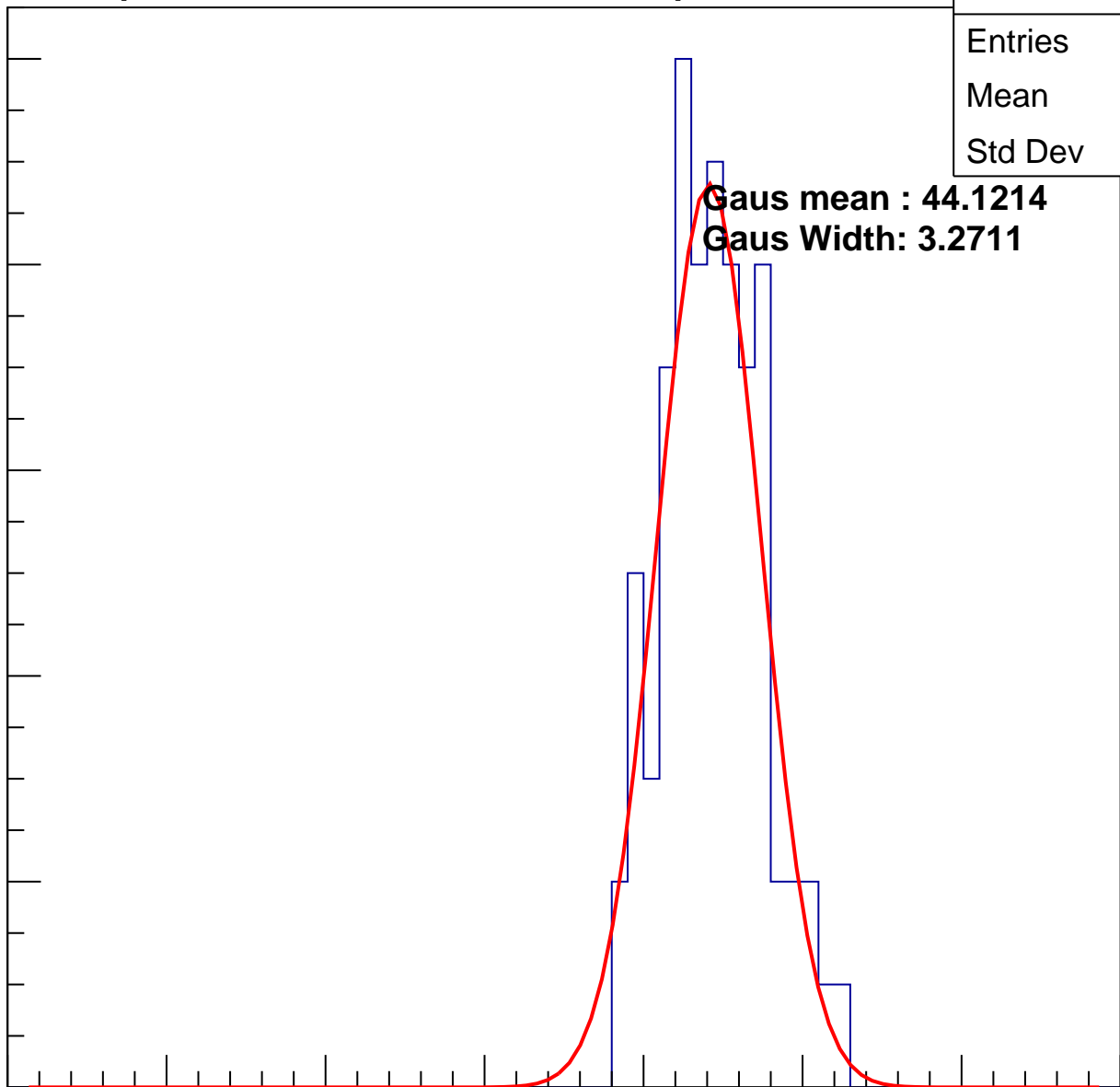
Gaus Width: 3.2711

Entry

10
8
6
4
2
0

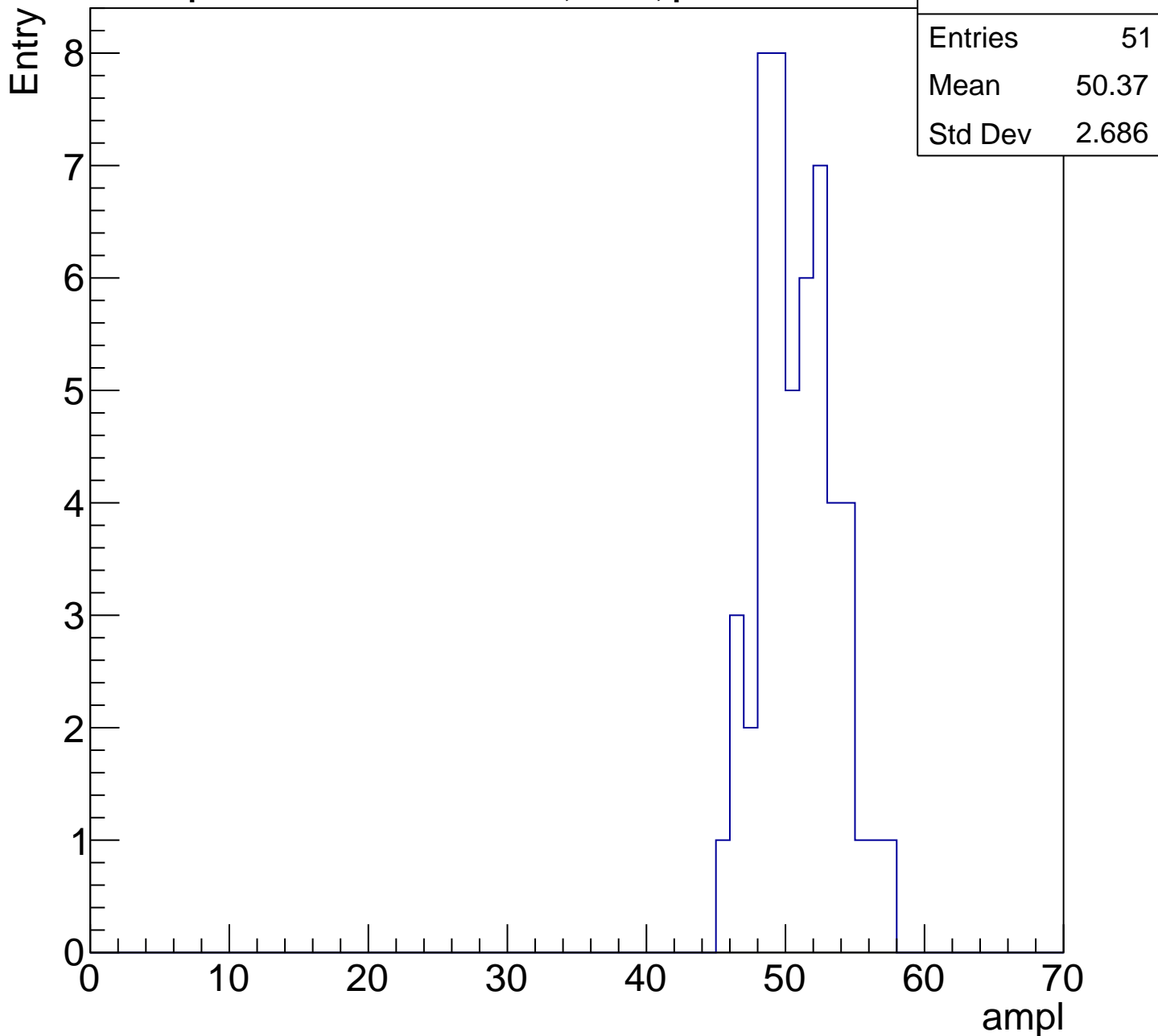
ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch50, adc3

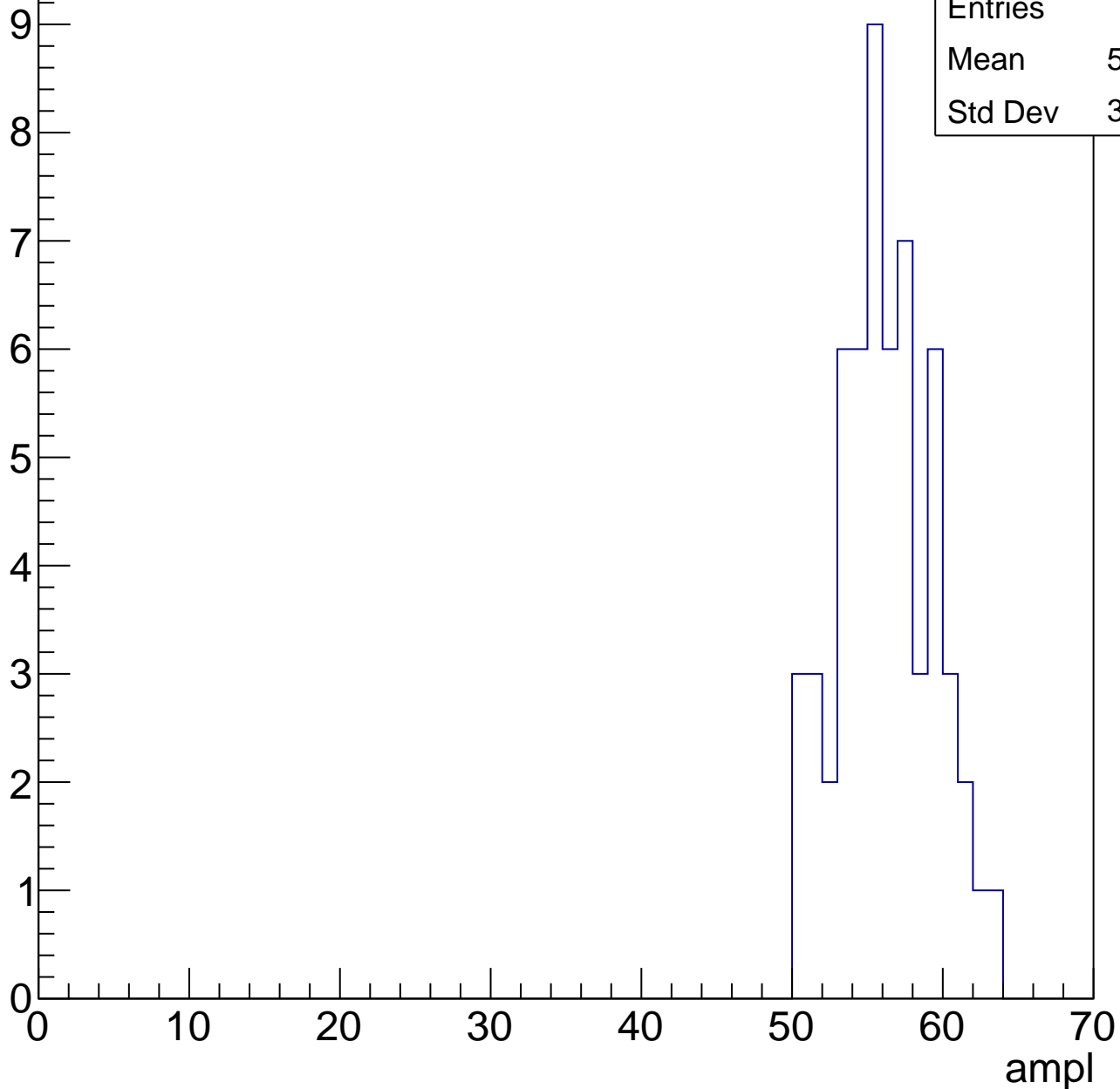
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

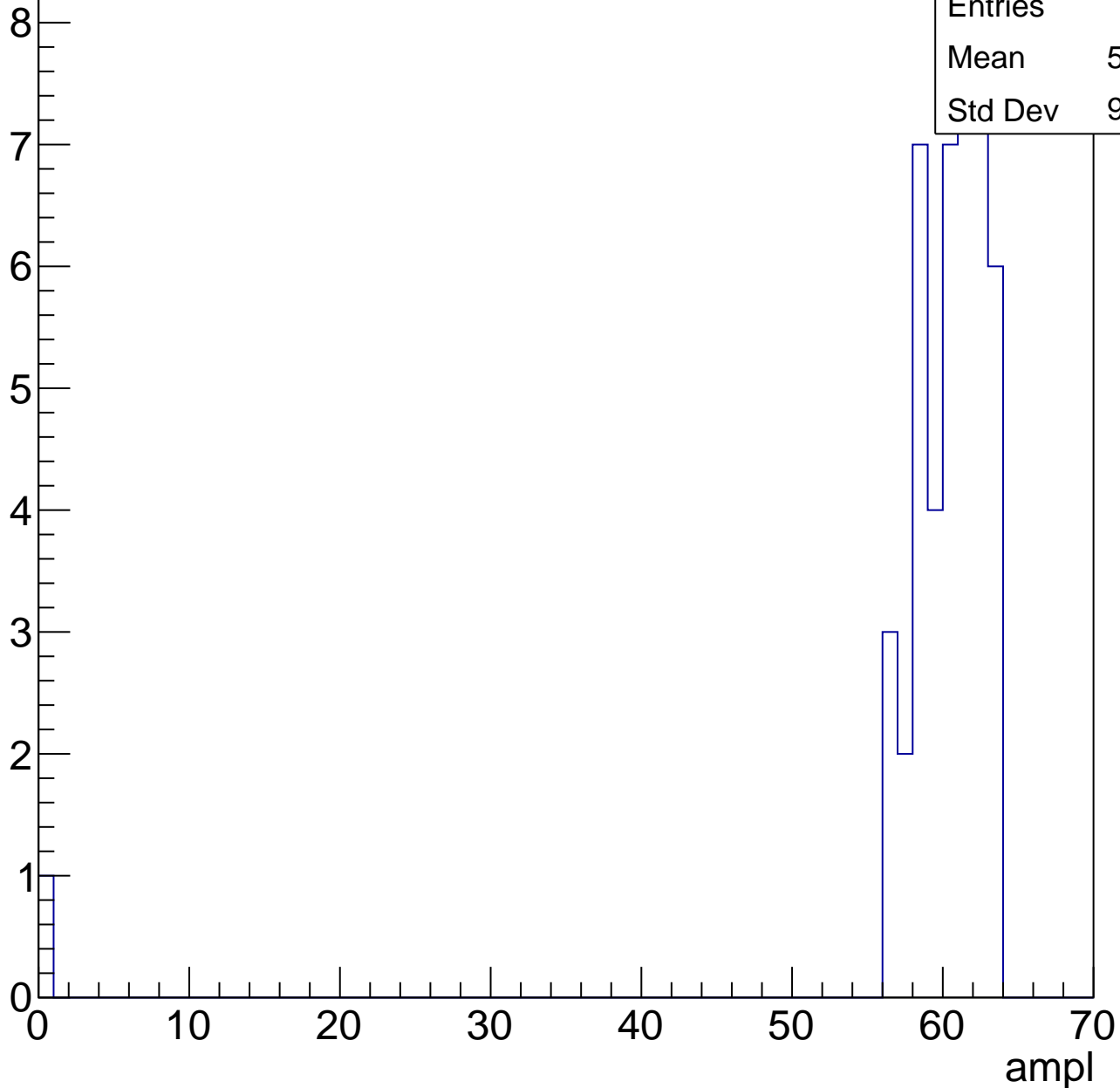
Entry



B1L100S, U9-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch51, adc0

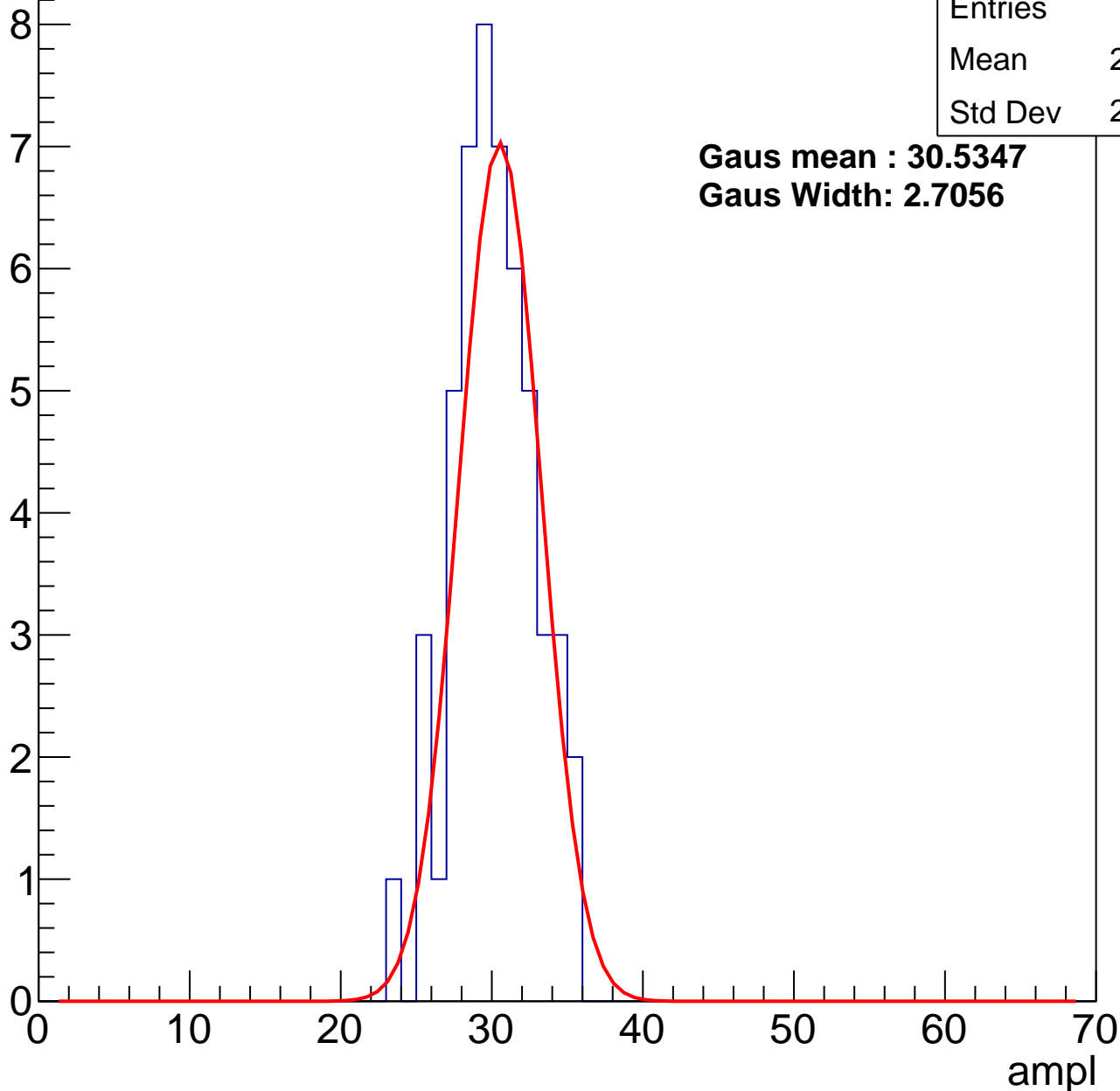
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 29.69 |
| Std Dev | 2.682 |

Gaus mean : 30.5347

Gaus Width: 2.7056



B1L100S, U9-ch51, adc1

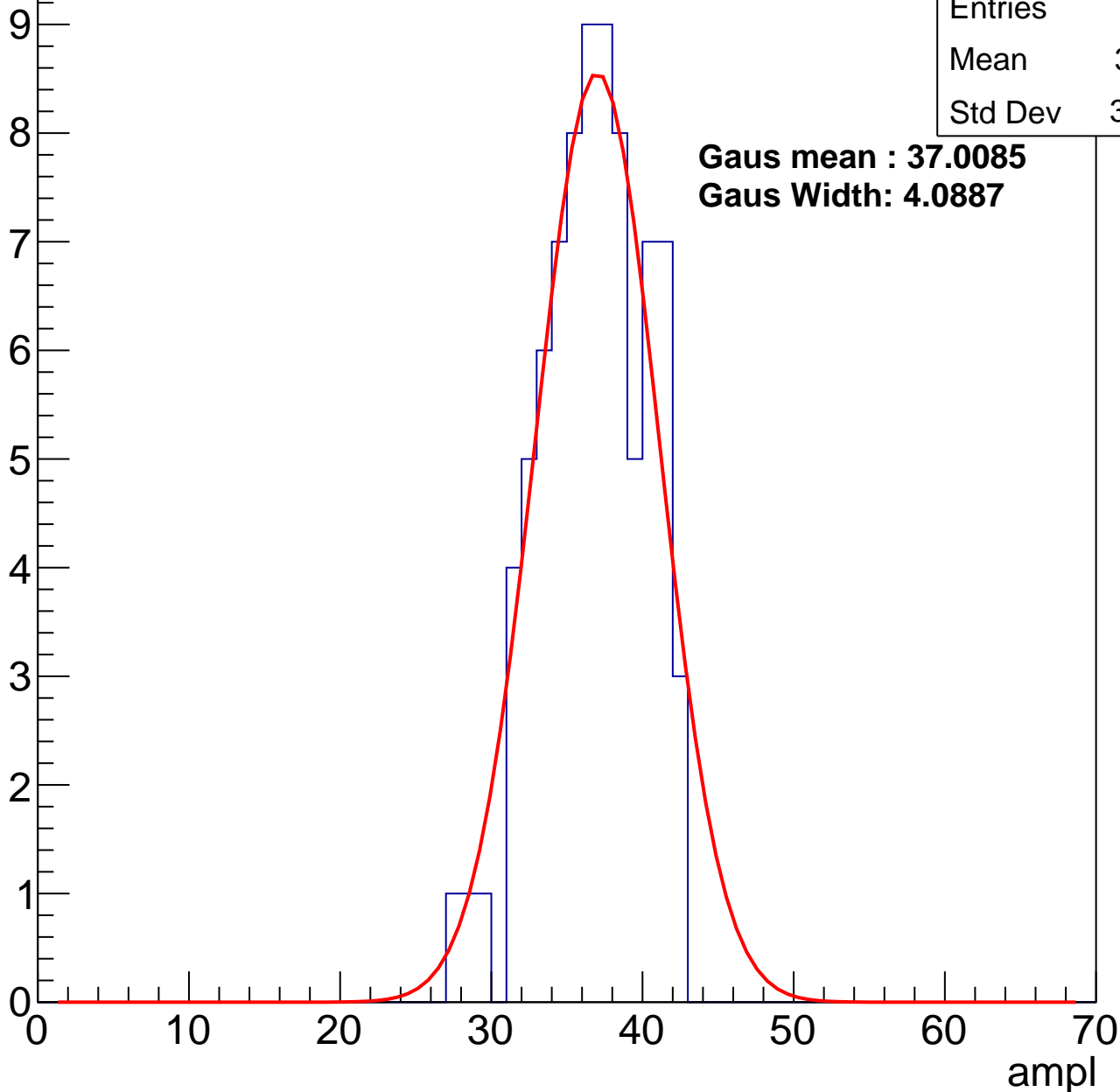
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 36.21 |
| Std Dev | 3.409 |

Gaus mean : 37.0085

Gaus Width: 4.0887



B1L100S, U9-ch51, adc2

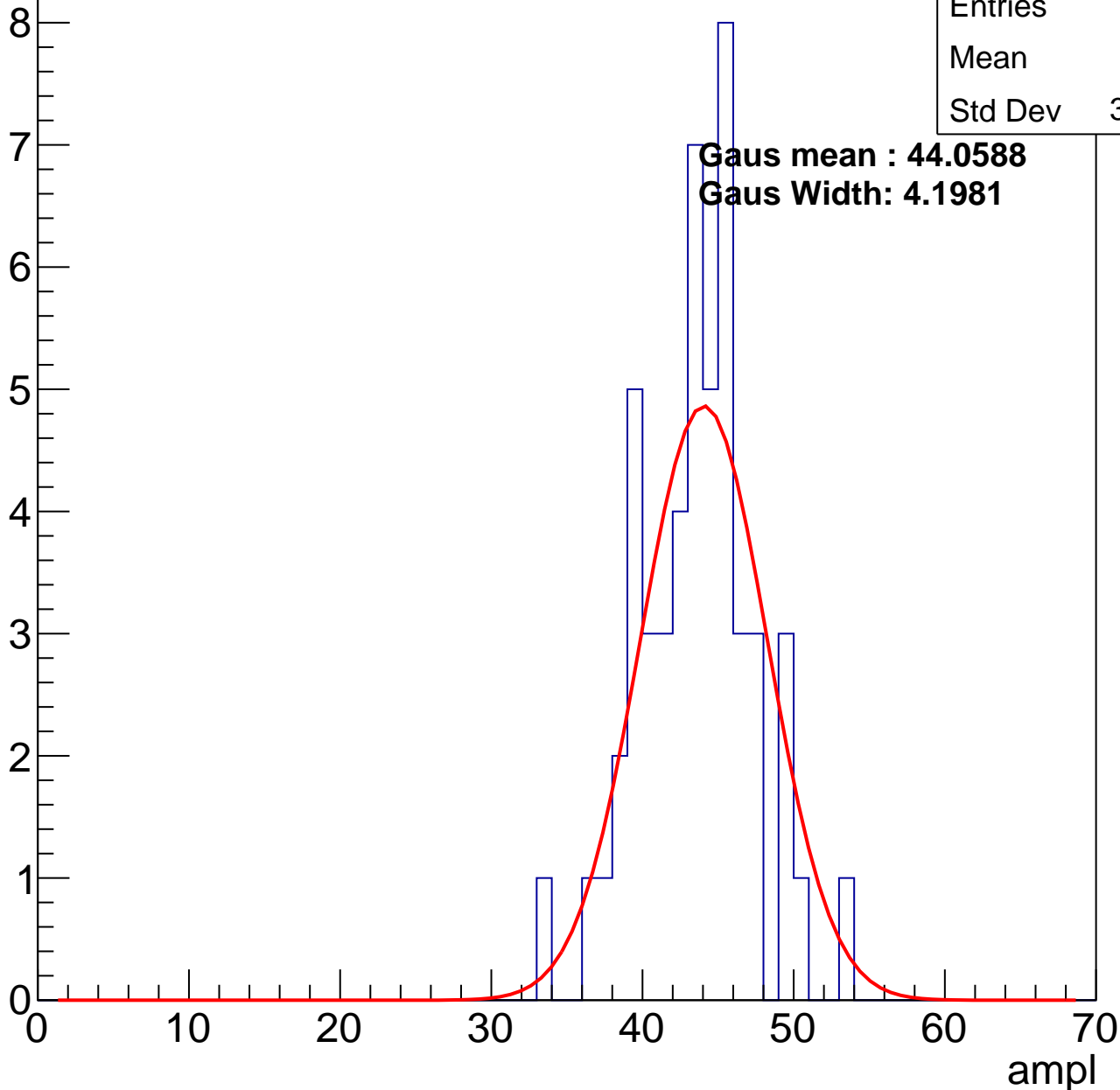
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 43.1 |
| Std Dev | 3.774 |

Gaus mean : 44.0588

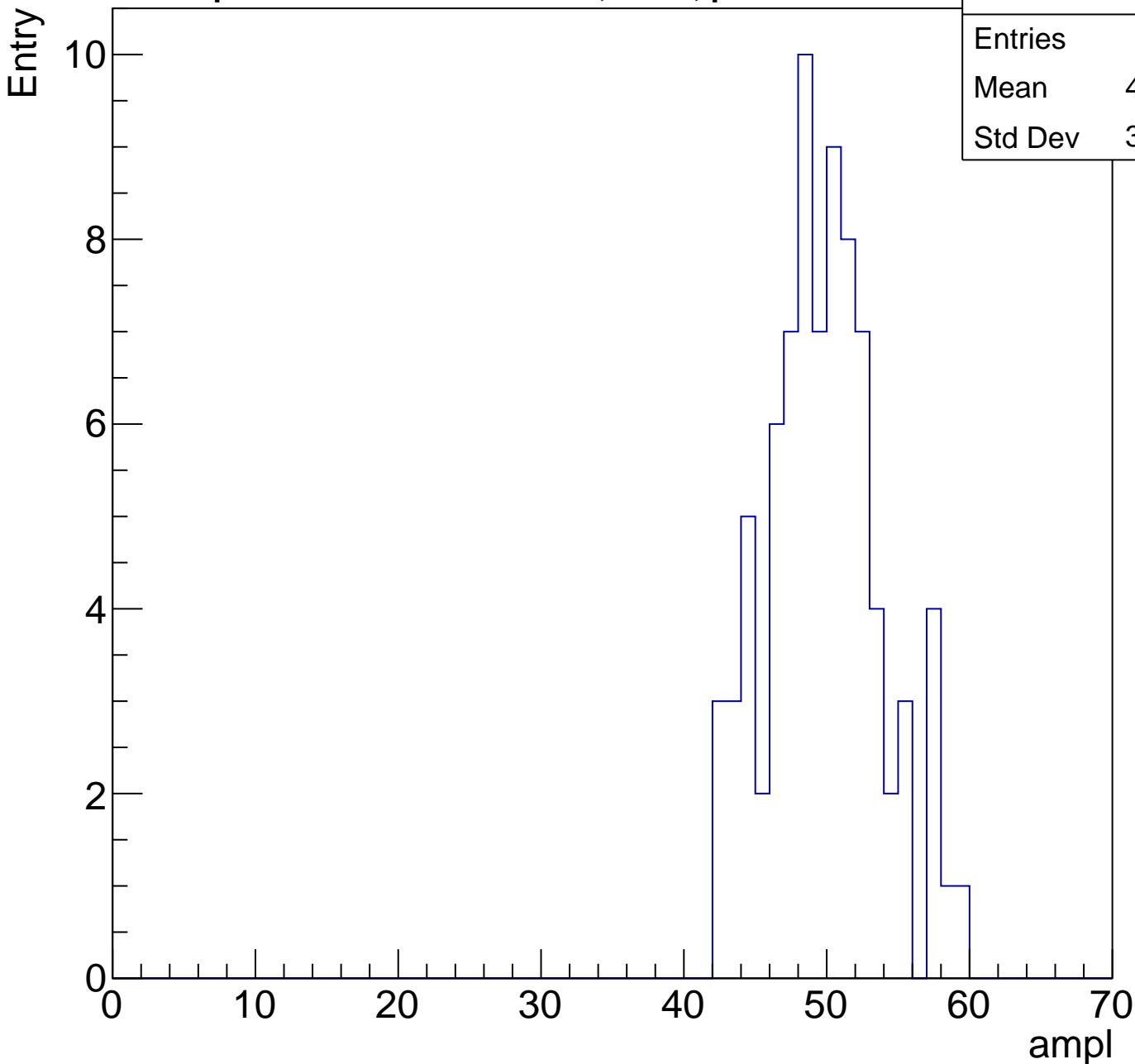
Gaus Width: 4.1981



B1L100S, U9-ch51, adc3

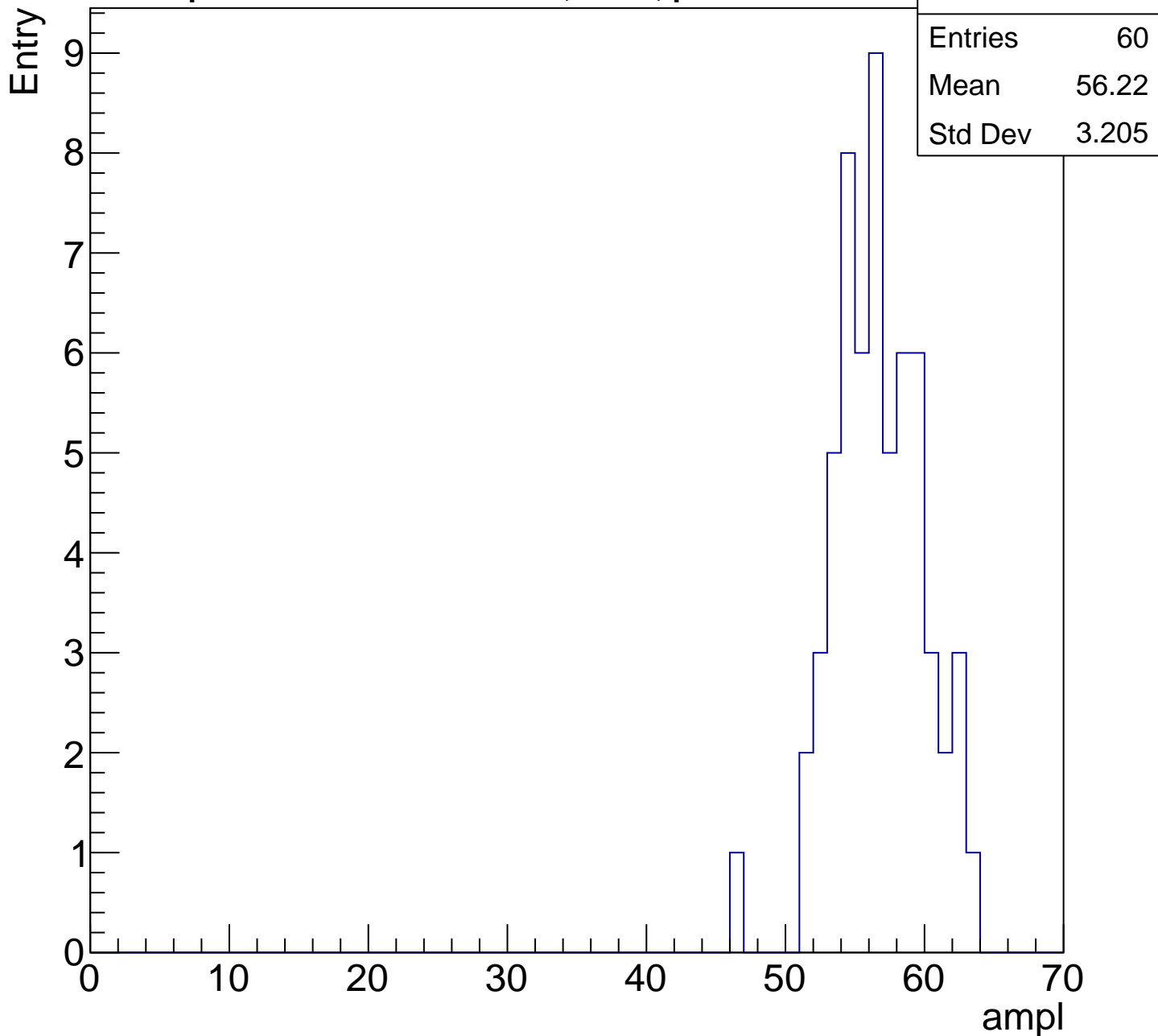
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 49.33 |
| Std Dev | 3.917 |



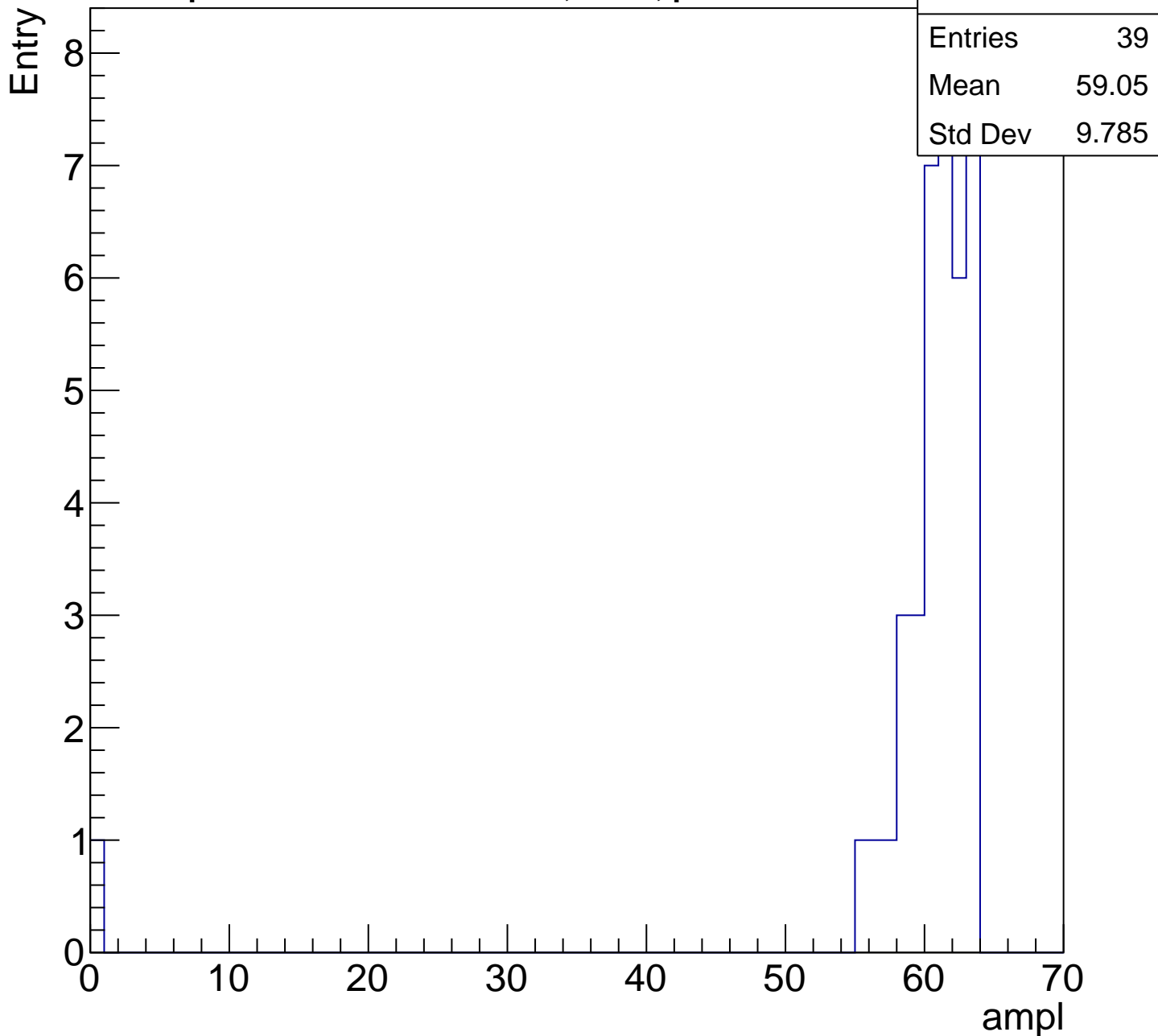
B1L100S, U9-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch52, adc0

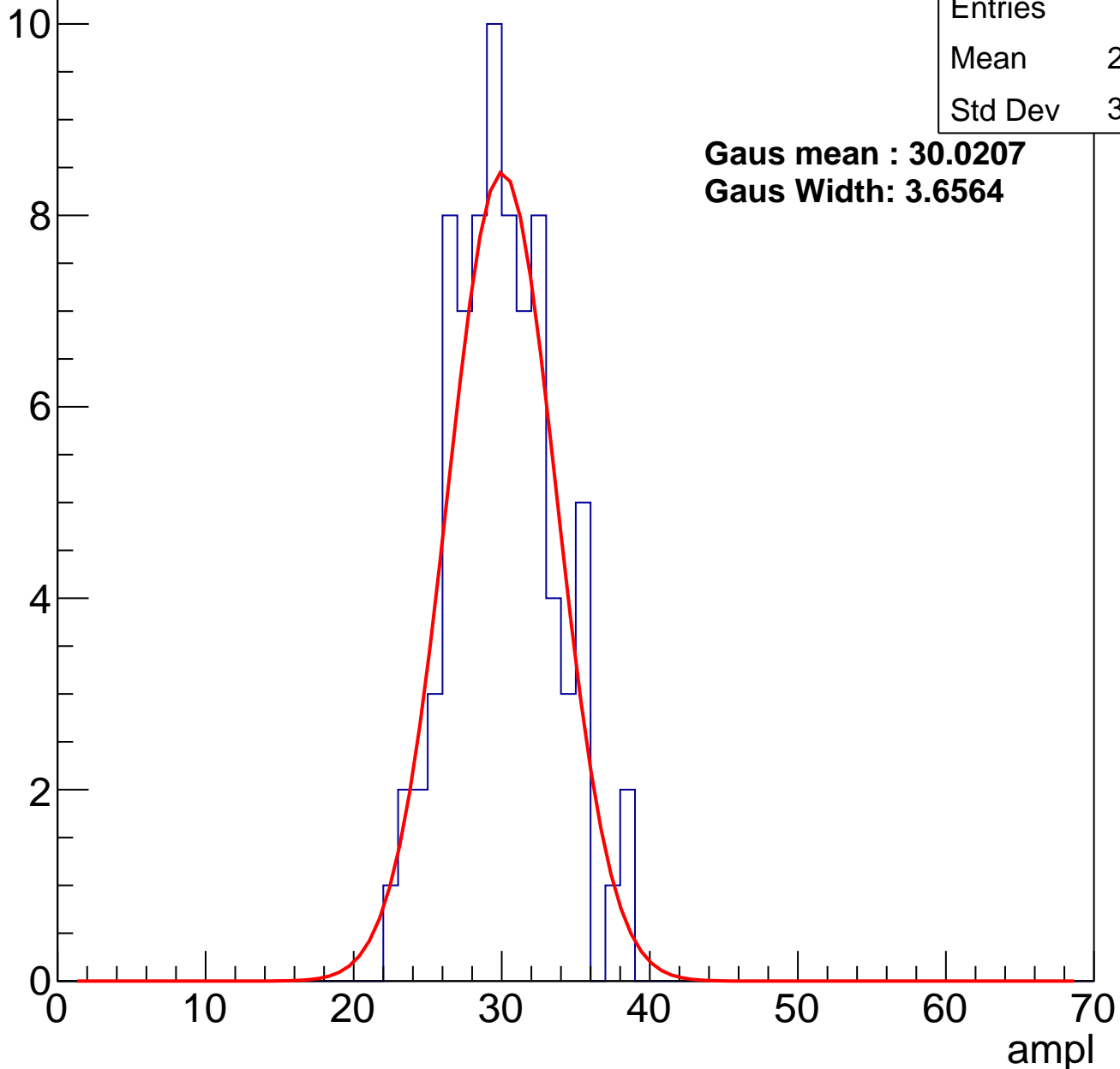
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.58 |
| Std Dev | 3.474 |

Gaus mean : 30.0207

Gaus Width: 3.6564

Entry



B1L100S, U9-ch52, adc1

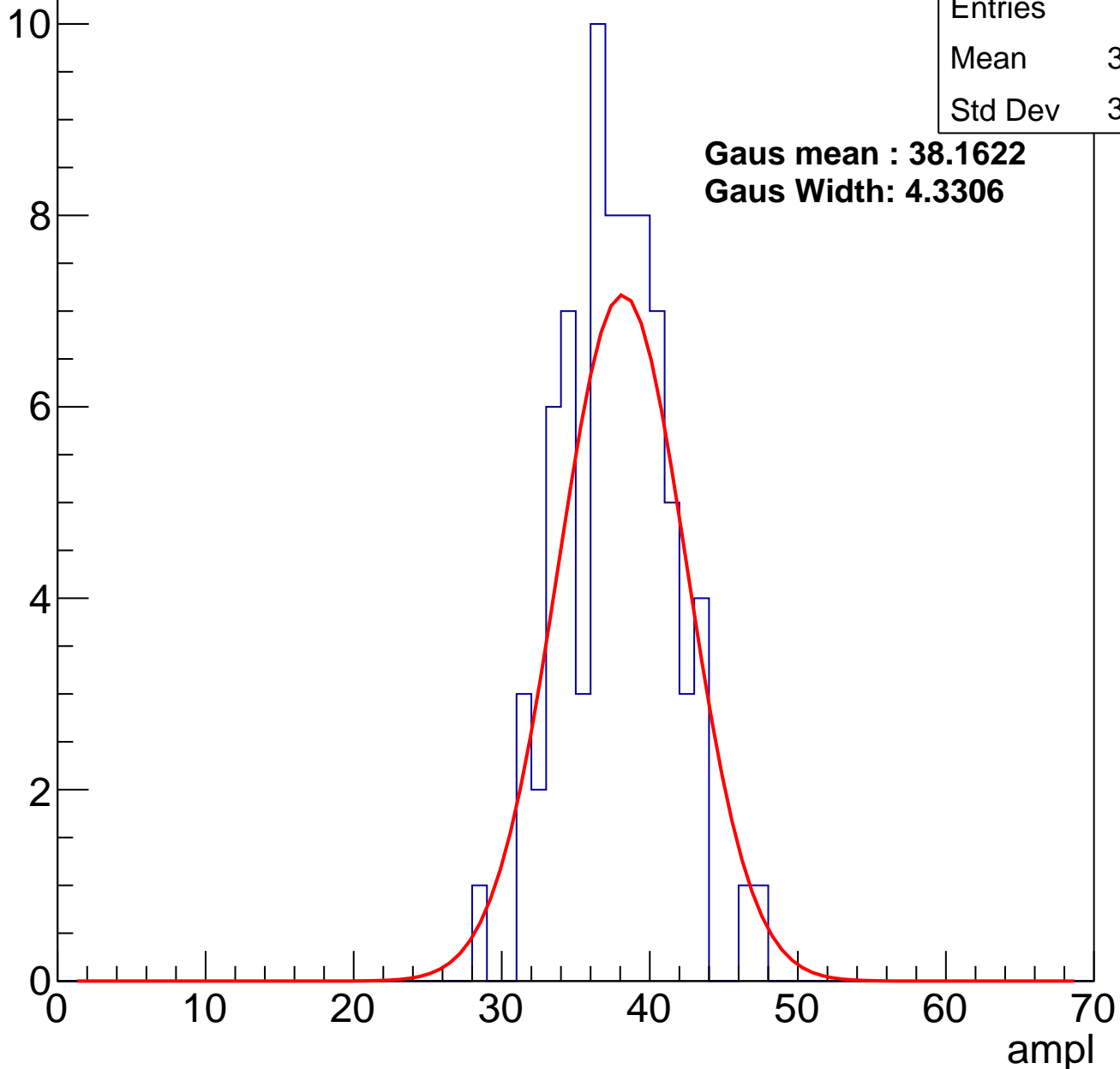
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.32 |
| Std Dev | 3.595 |

Gaus mean : 38.1622

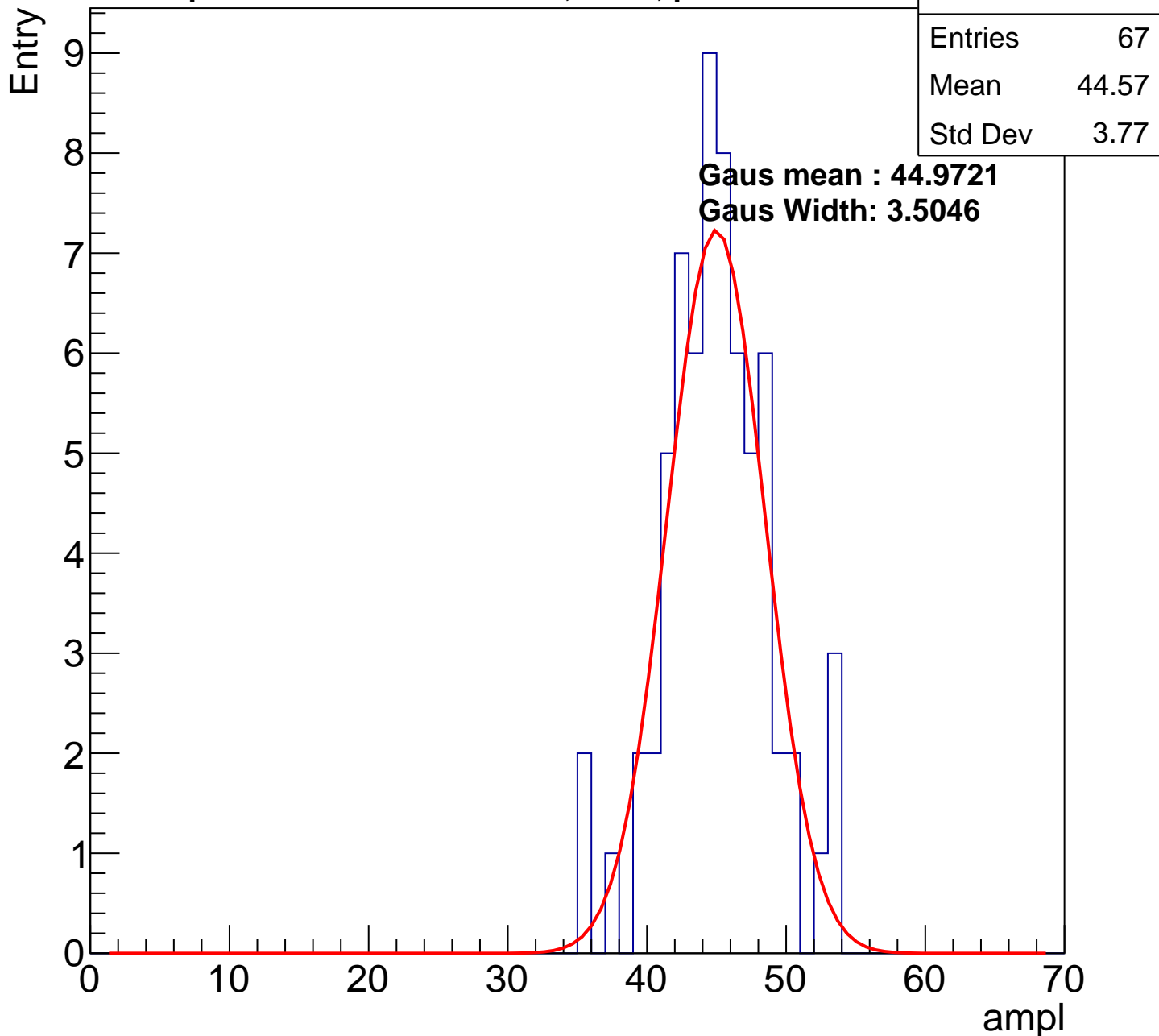
Gaus Width: 4.3306

Entry



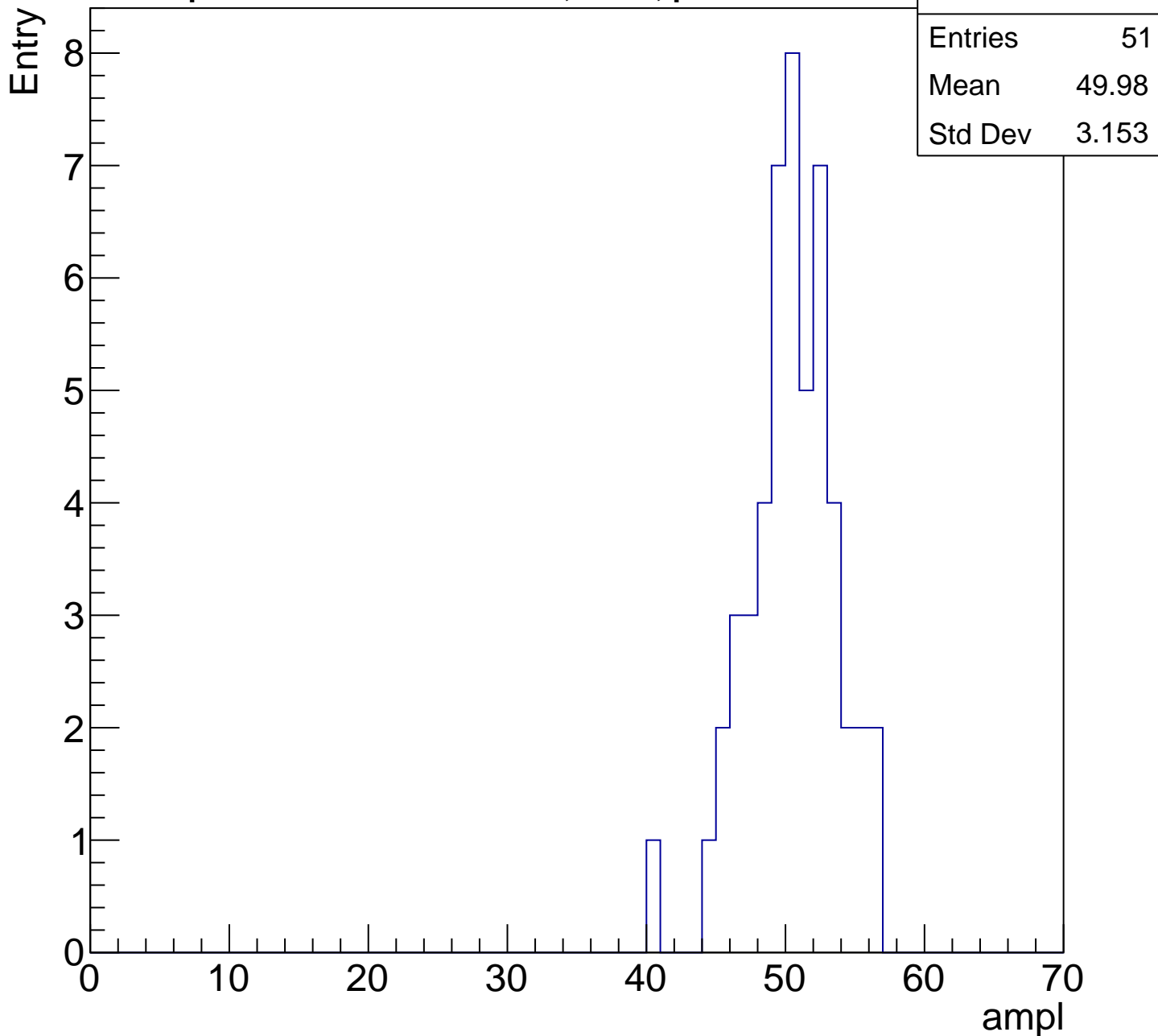
B1L100S, U9-ch52, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

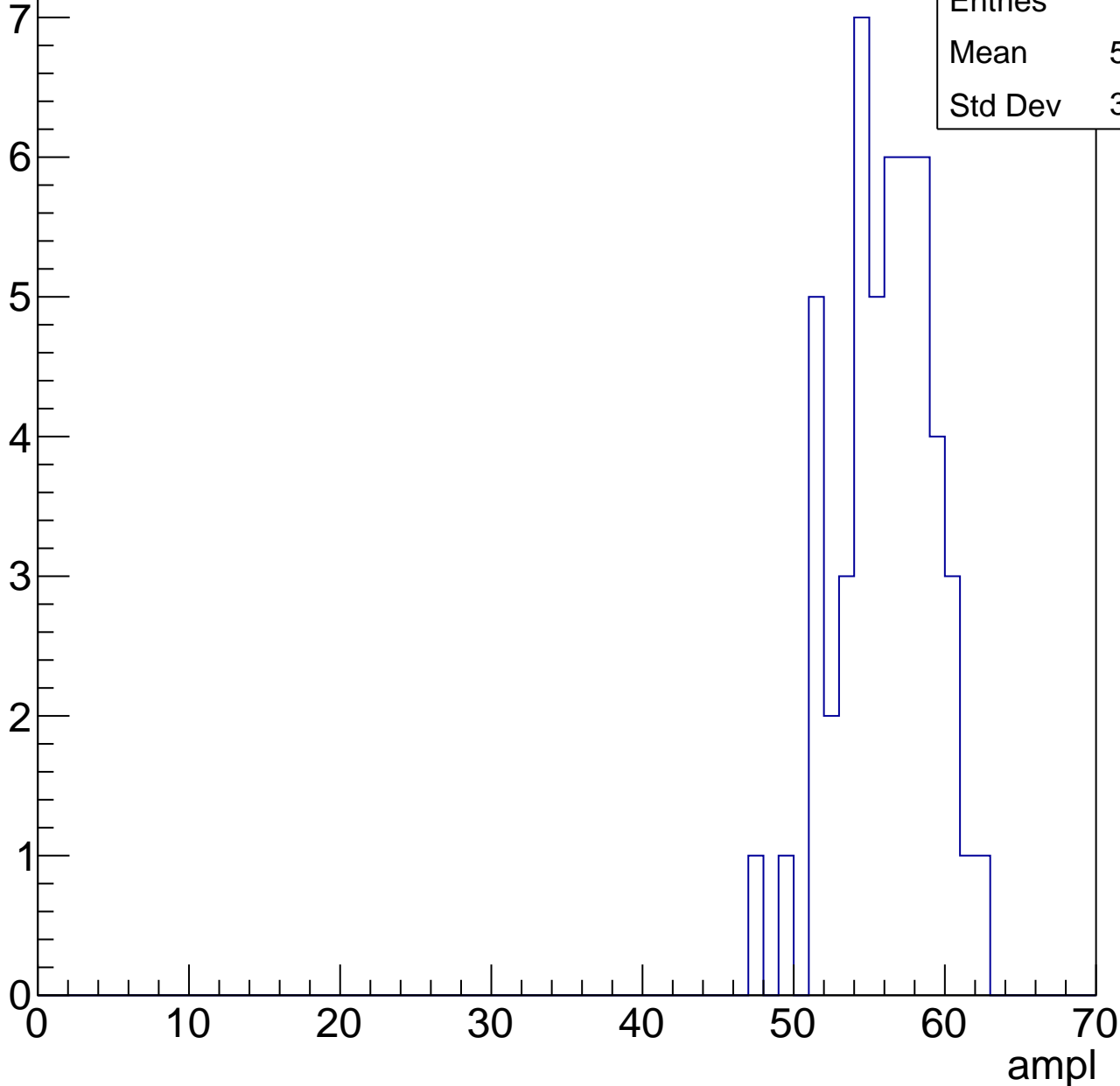


B1L100S, U9-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 55.53 |
| Std Dev | 3.164 |

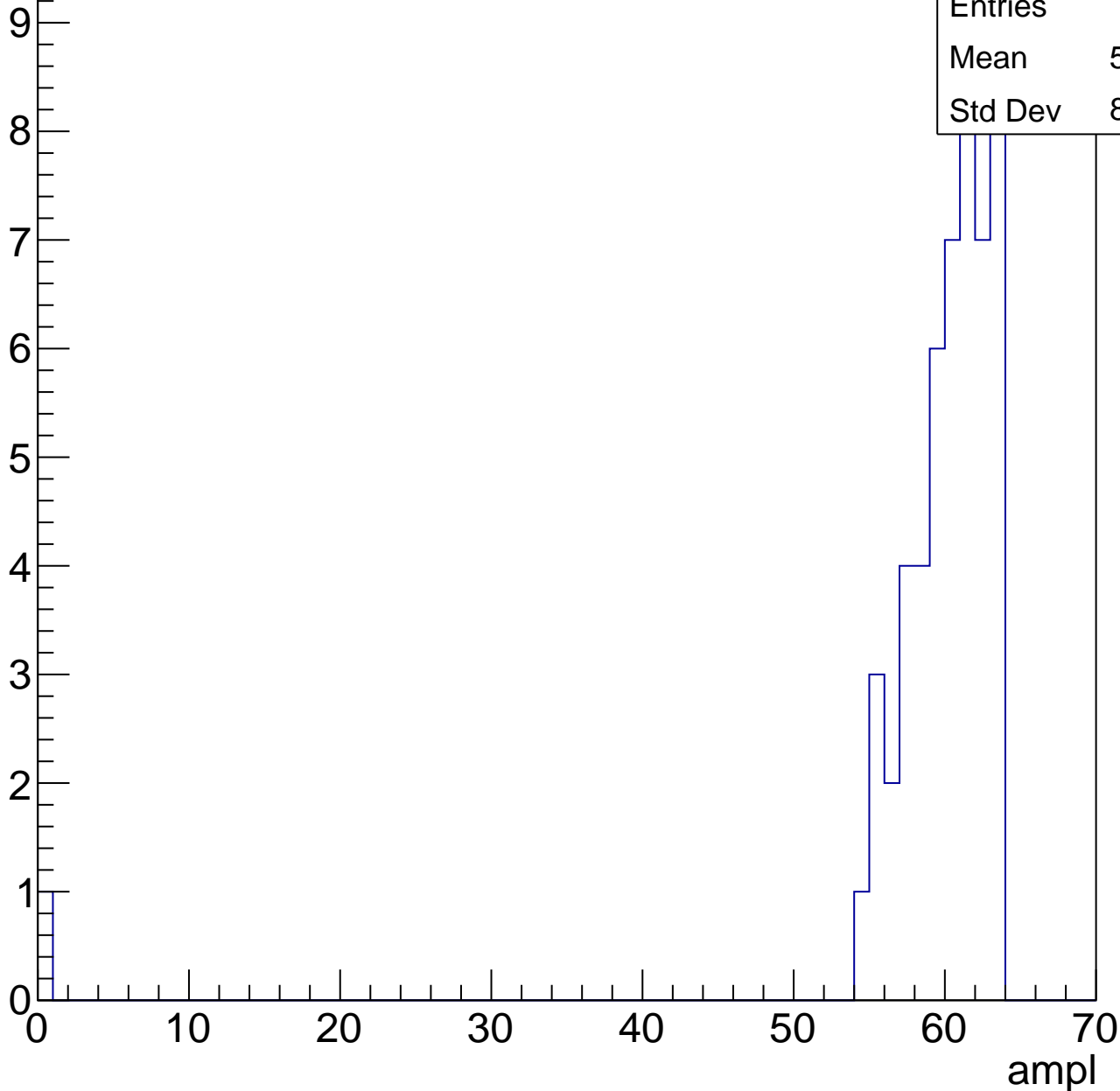


B1L100S, U9-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.69 |
| Std Dev | 8.572 |



B1L100S, U9-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch53, adc0

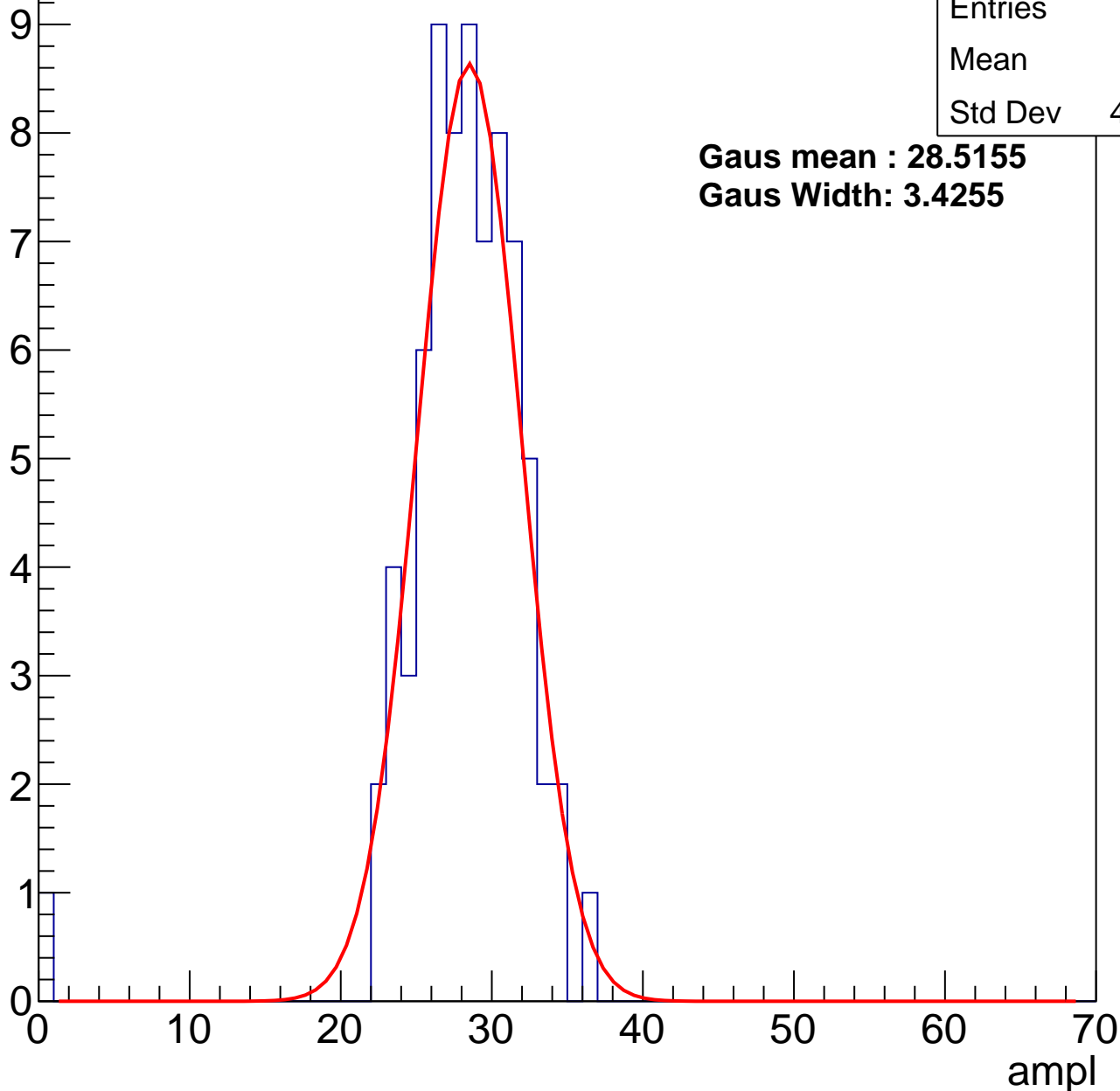
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 27.7 |
| Std Dev | 4.447 |

Gaus mean : 28.5155

Gaus Width: 3.4255



B1L100S, U9-ch53, adc1

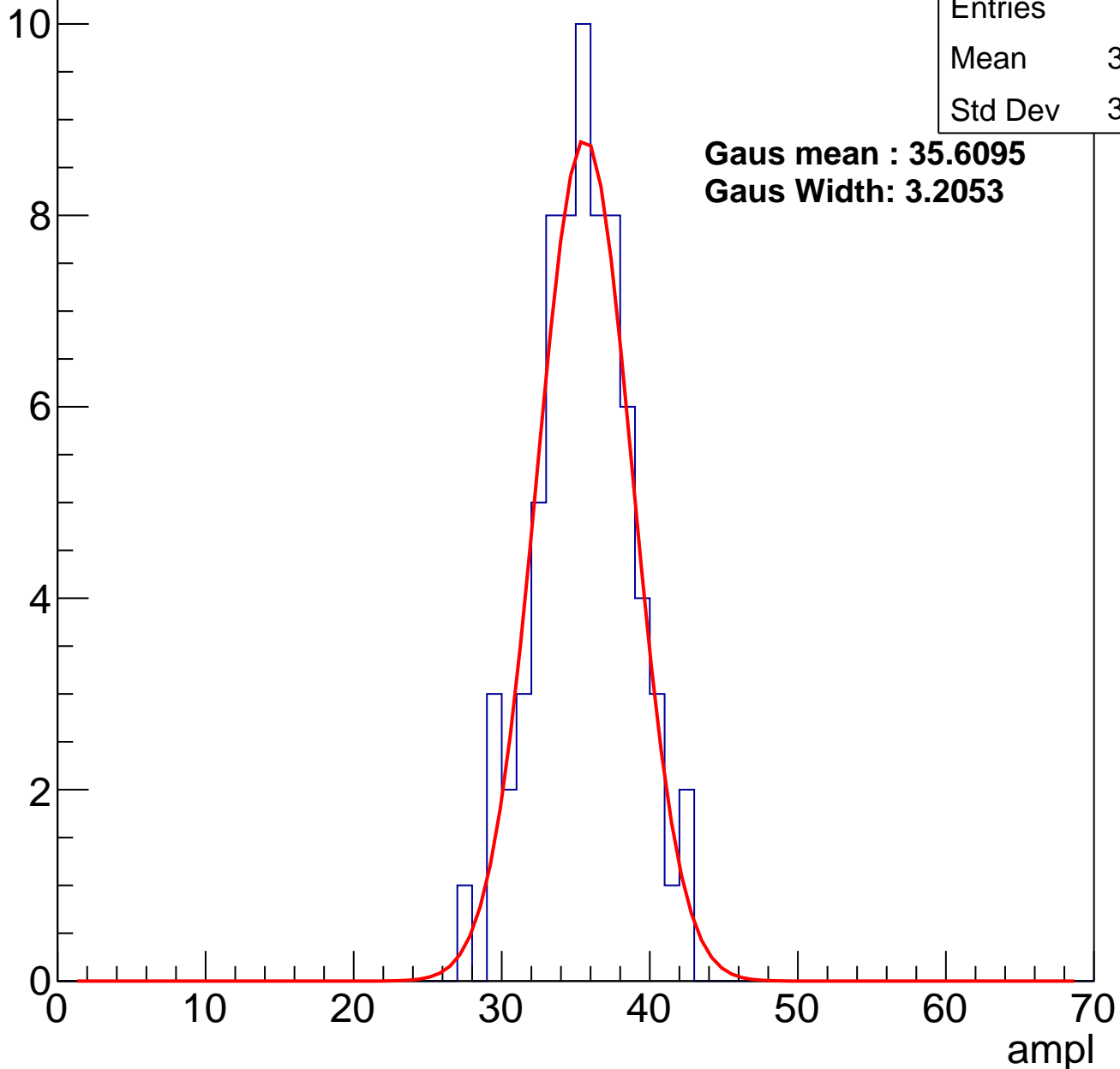
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.08 |
| Std Dev | 3.166 |

Gaus mean : 35.6095

Gaus Width: 3.2053

Entry



B1L100S, U9-ch53, adc2

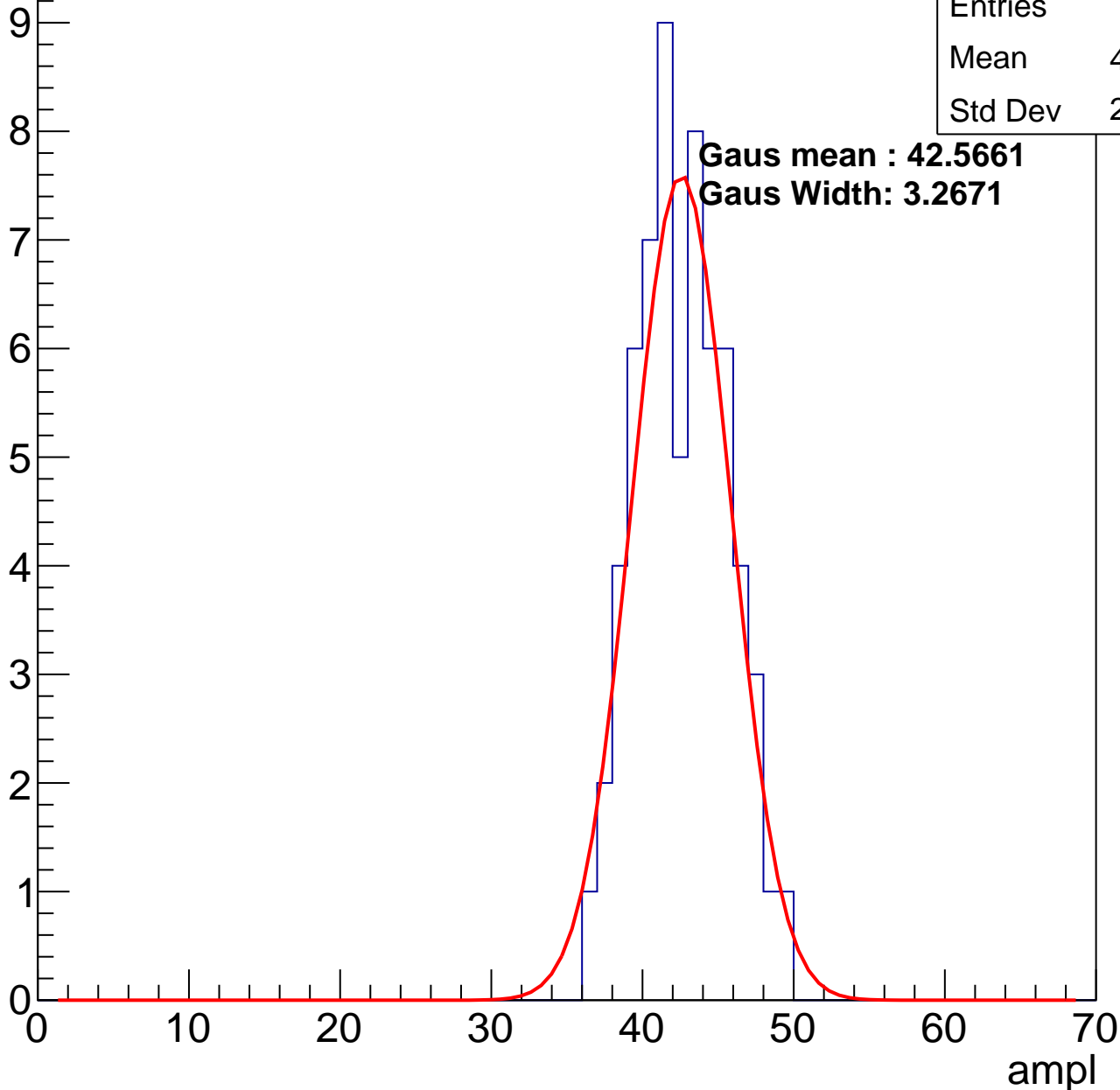
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 42.14 |
| Std Dev | 2.954 |

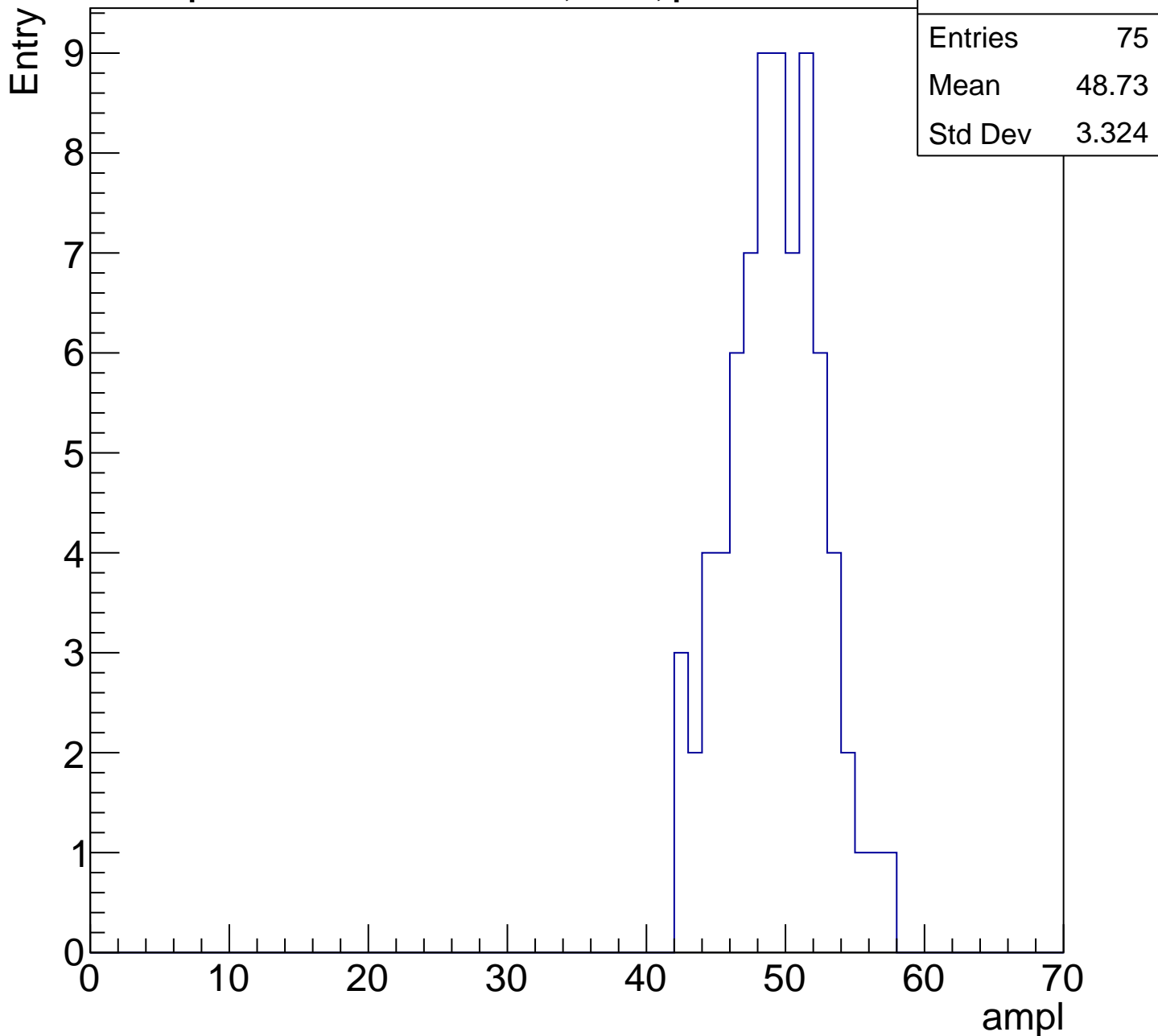
Gaus mean : 42.5661

Gaus Width: 3.2671



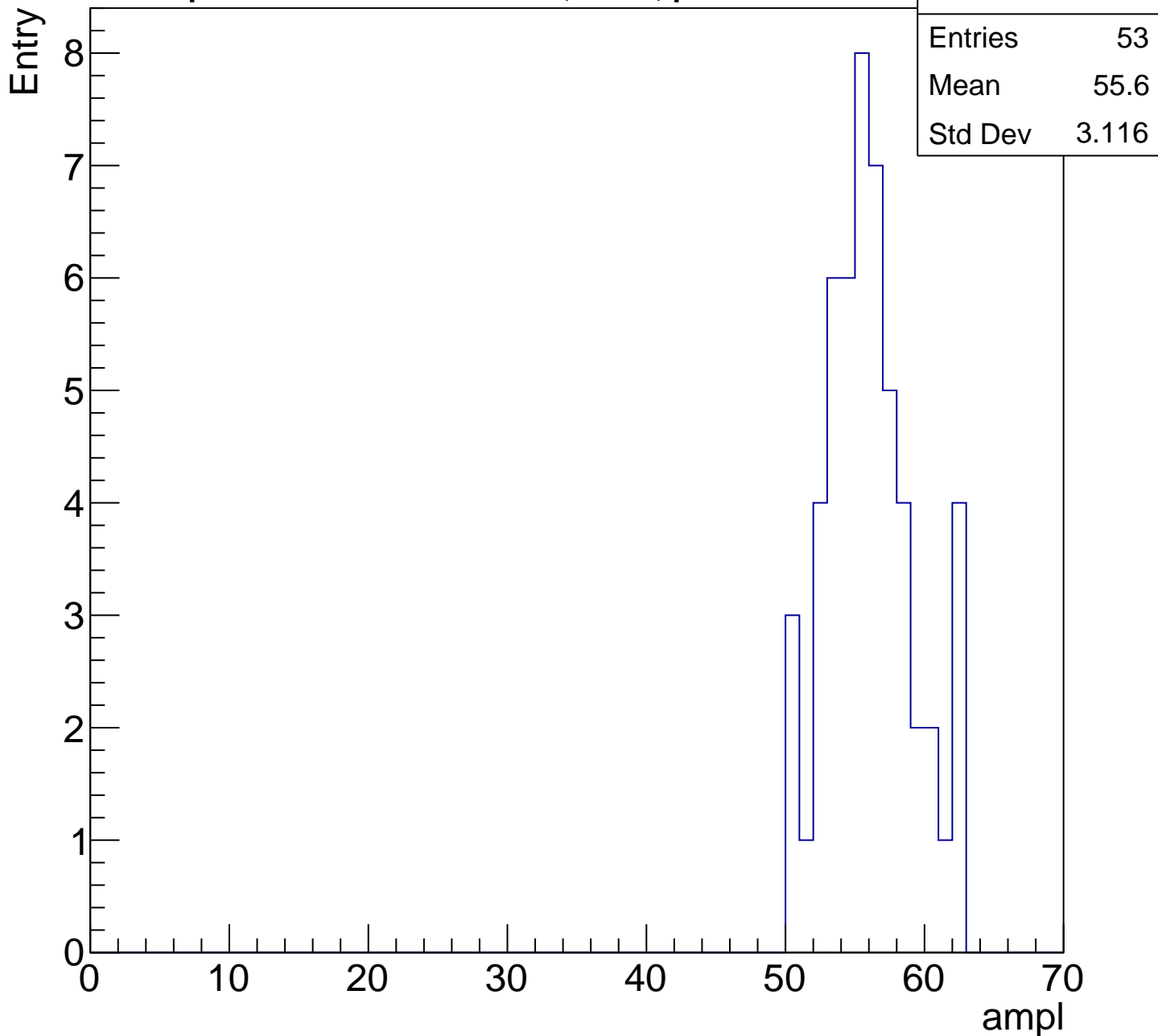
B1L100S, U9-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch53, adc5

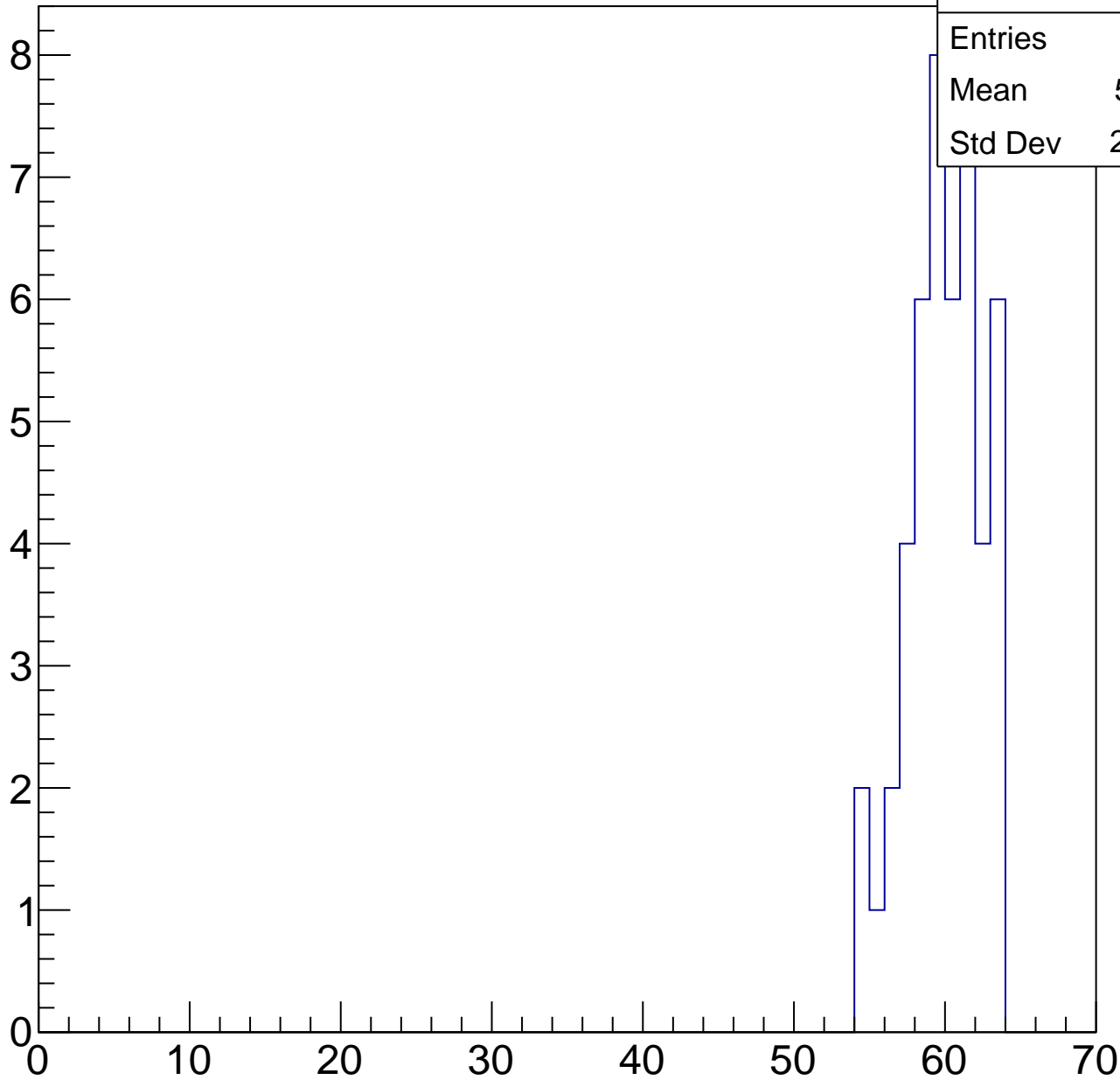
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 47 |
| Mean | 59.51 |
| Std Dev | 2.369 |

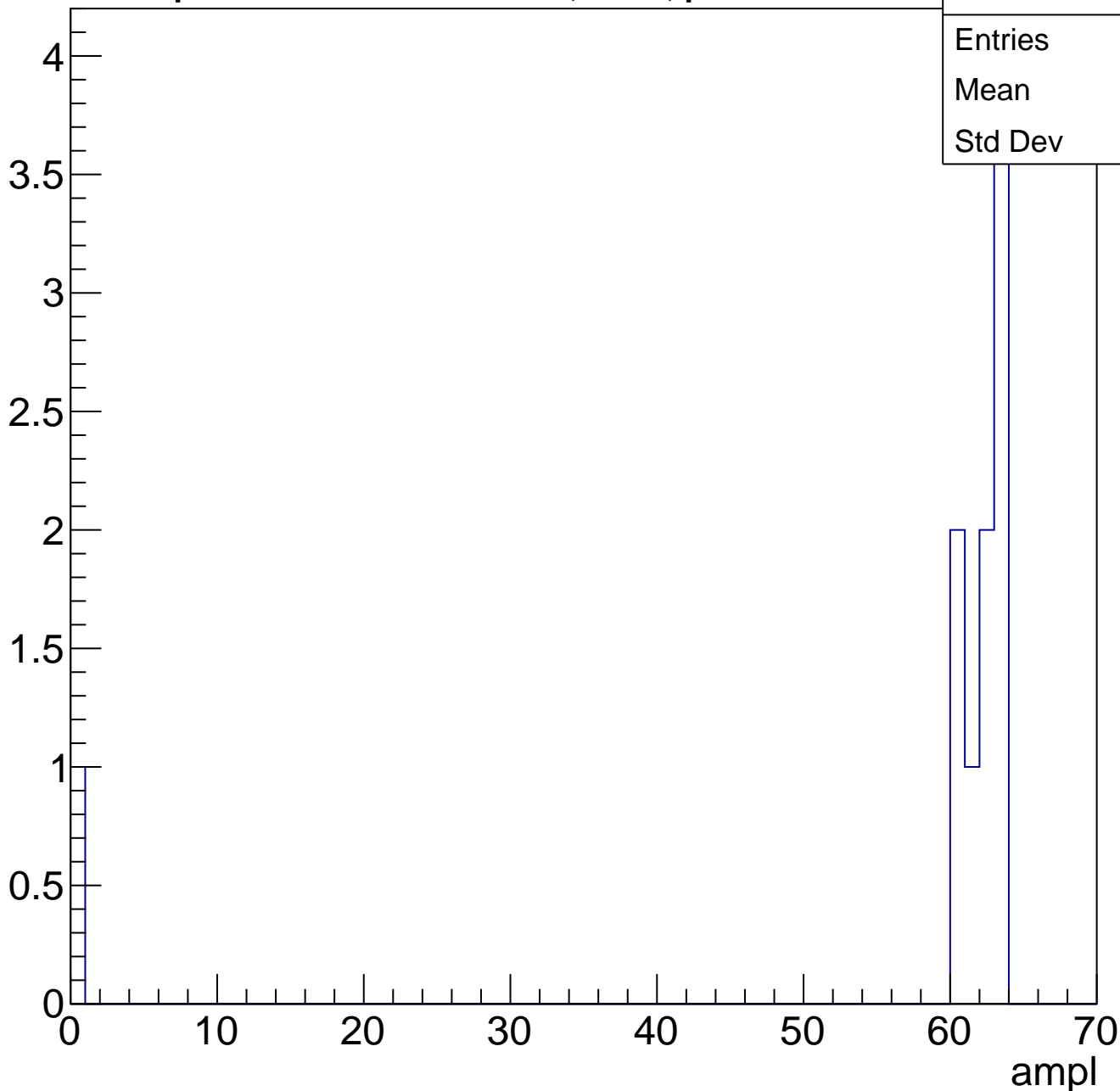
ampl



B1L100S, U9-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 10 |
| Mean | 55.7 |
| Std Dev | 18.6 |

B1L100S, U9-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



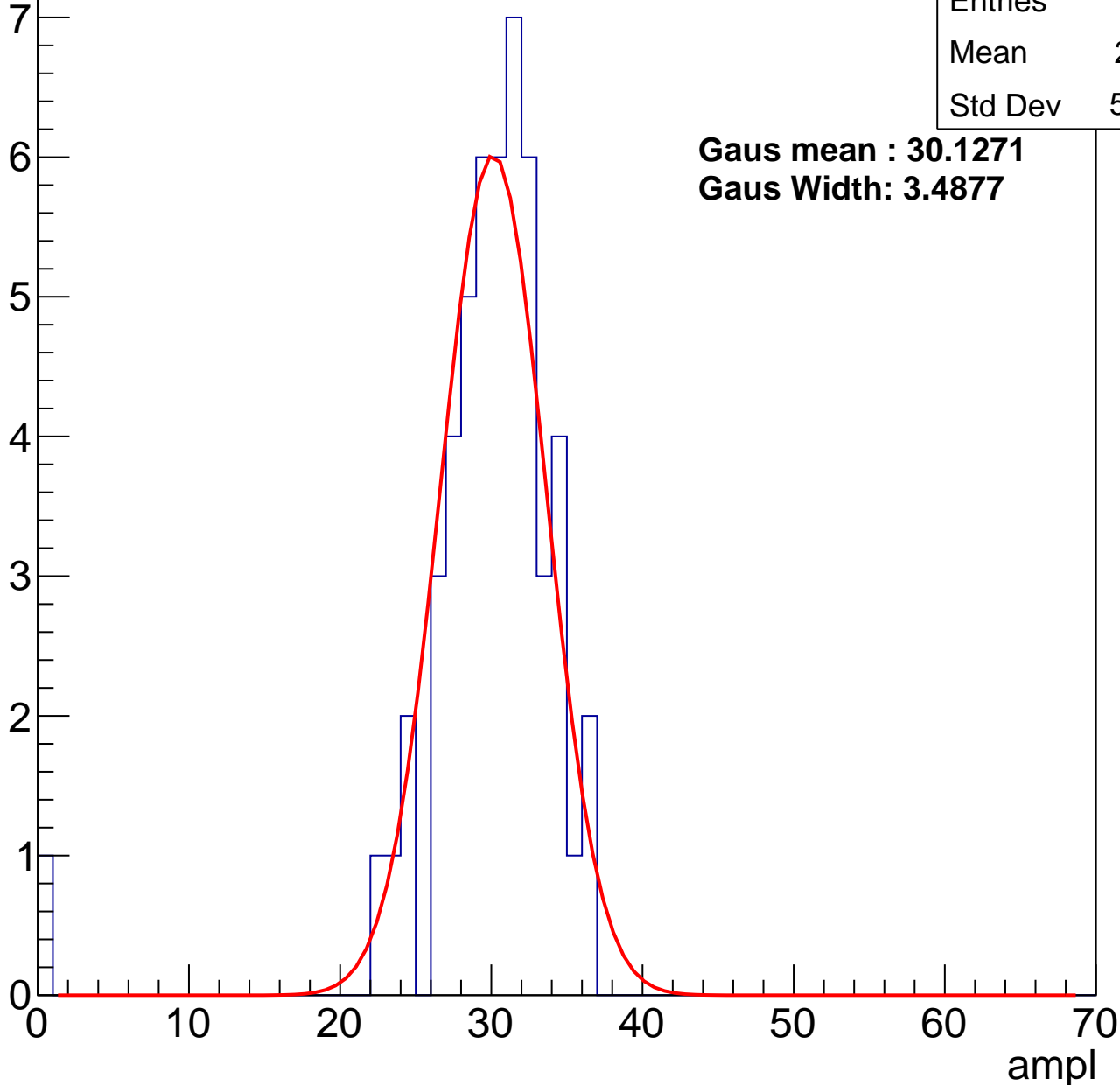
B1L100S, U9-ch54, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 29.31 |
| Std Dev | 5.168 |

Gaus mean : 30.1271
Gaus Width: 3.4877



B1L100S, U9-ch54, adc1

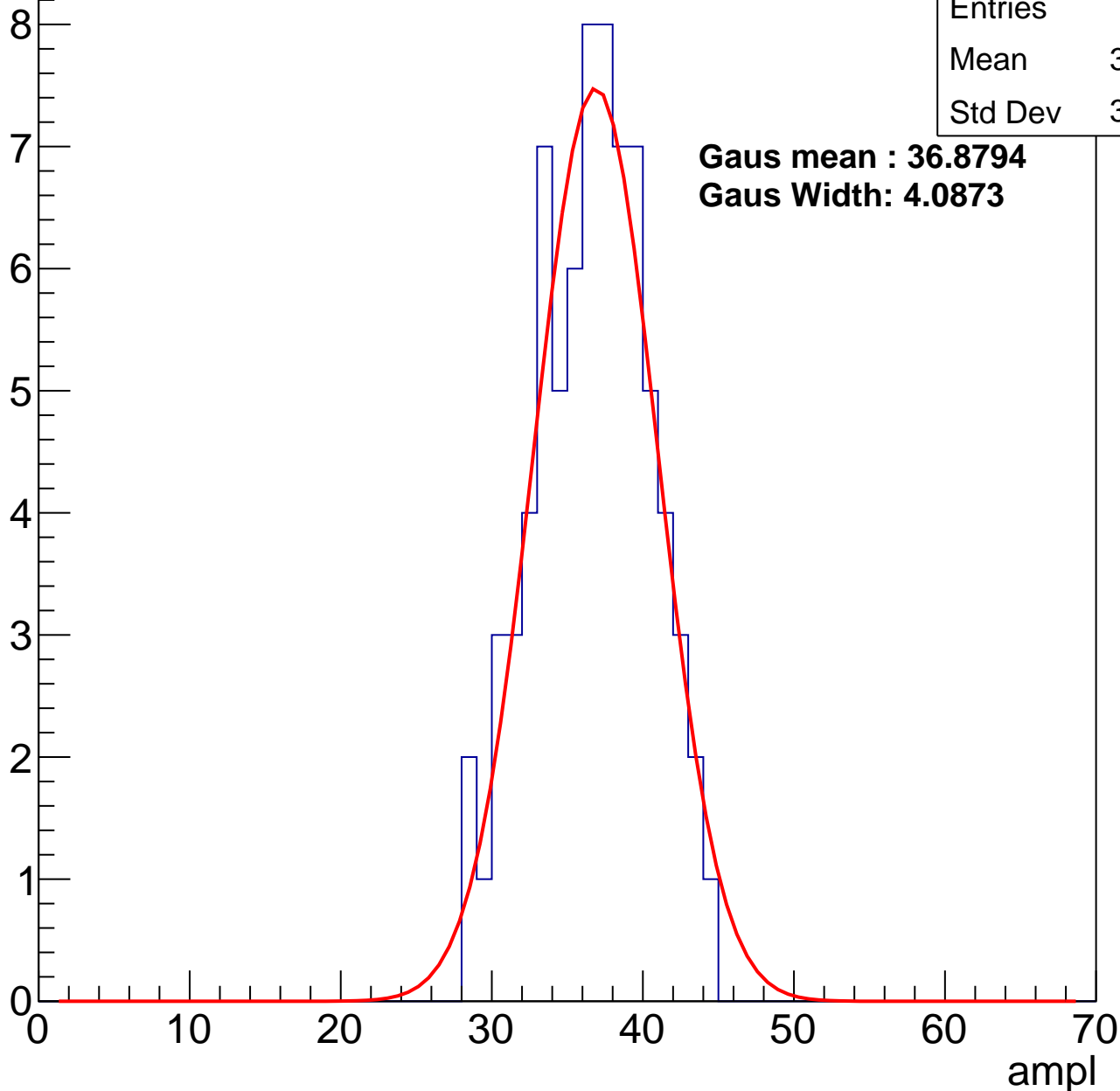
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.18 |
| Std Dev | 3.734 |

Gaus mean : 36.8794

Gaus Width: 4.0873



B1L100S, U9-ch54, adc2

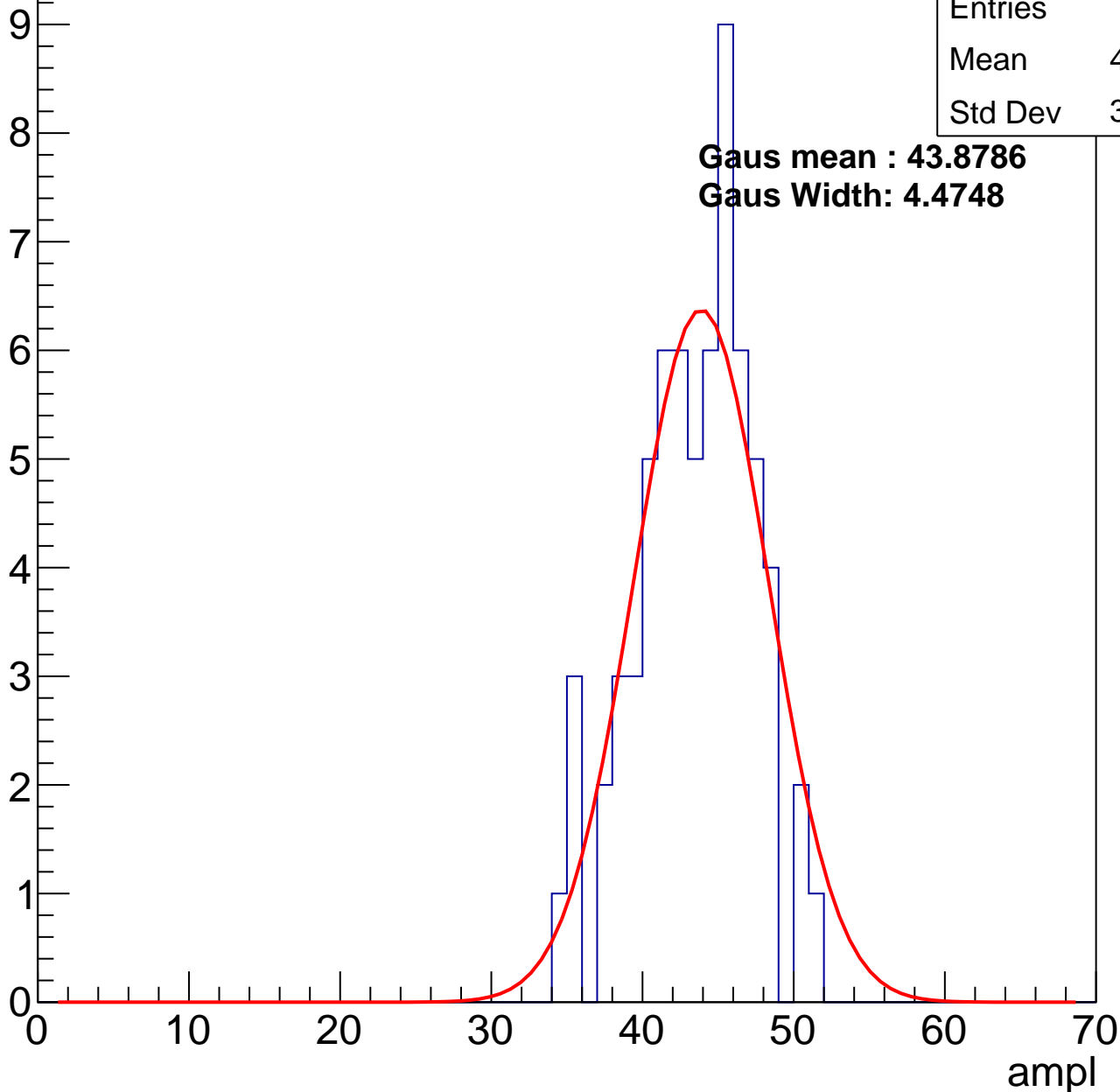
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 42.99 |
| Std Dev | 3.826 |

Gaus mean : 43.8786

Gaus Width: 4.4748

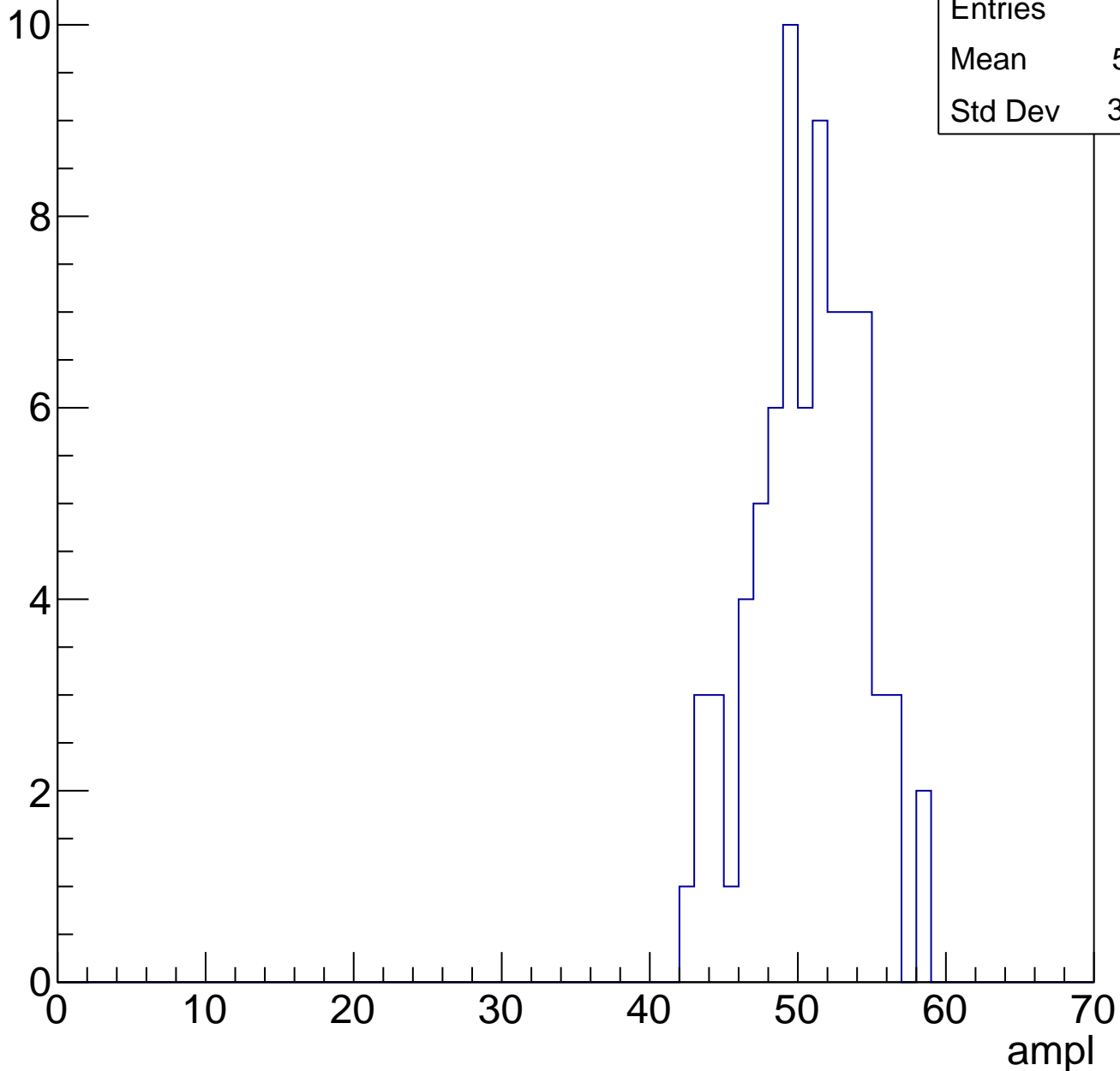


B1L100S, U9-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.21 |
| Std Dev | 3.616 |

Entry

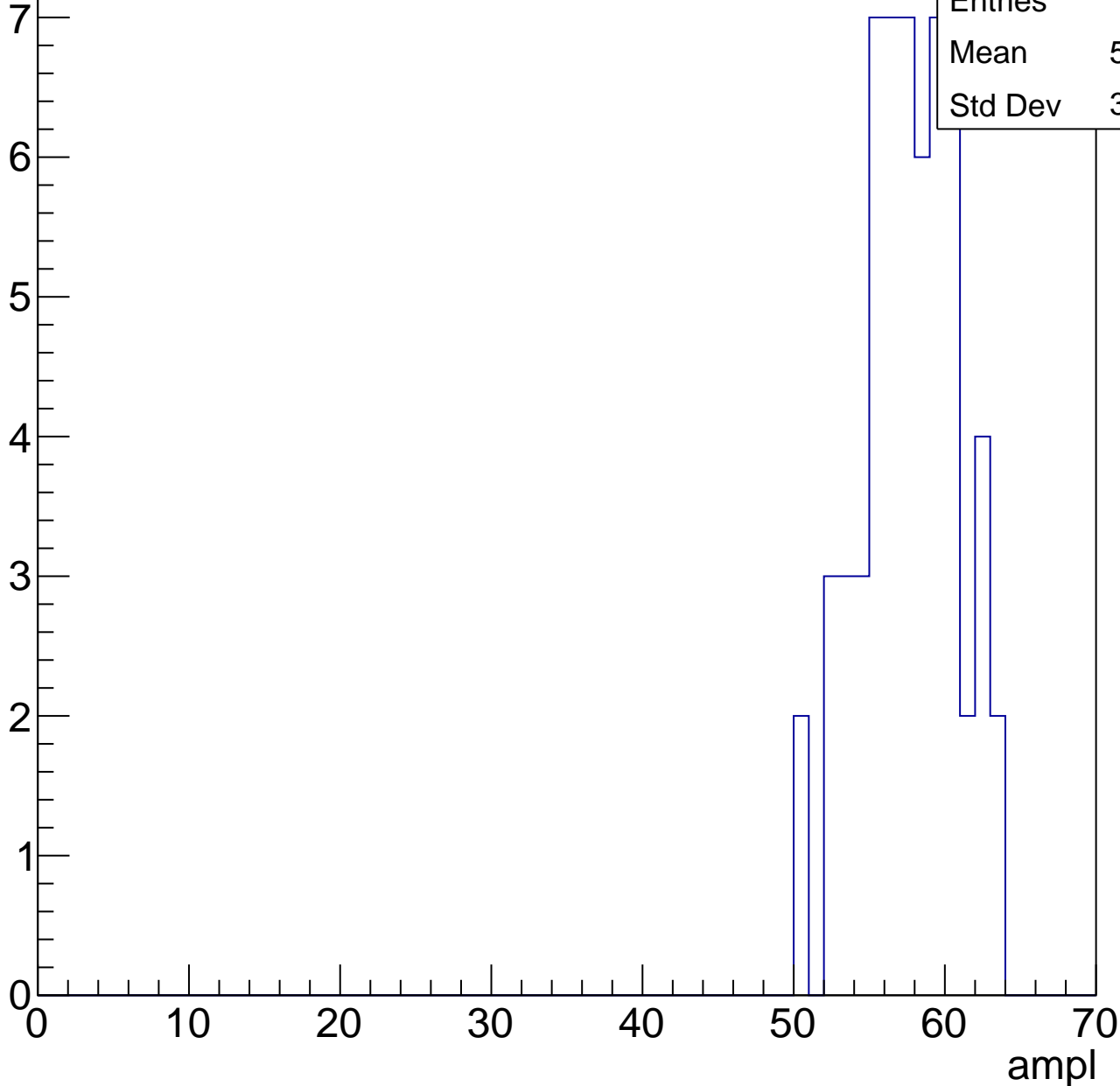


B1L100S, U9-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 57.17 |
| Std Dev | 3.115 |



B1L100S, U9-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

6

5

4

3

2

1

0

Entries

27

Mean

58.37

Std Dev

11.61

ampl

0

10

20

30

40

50

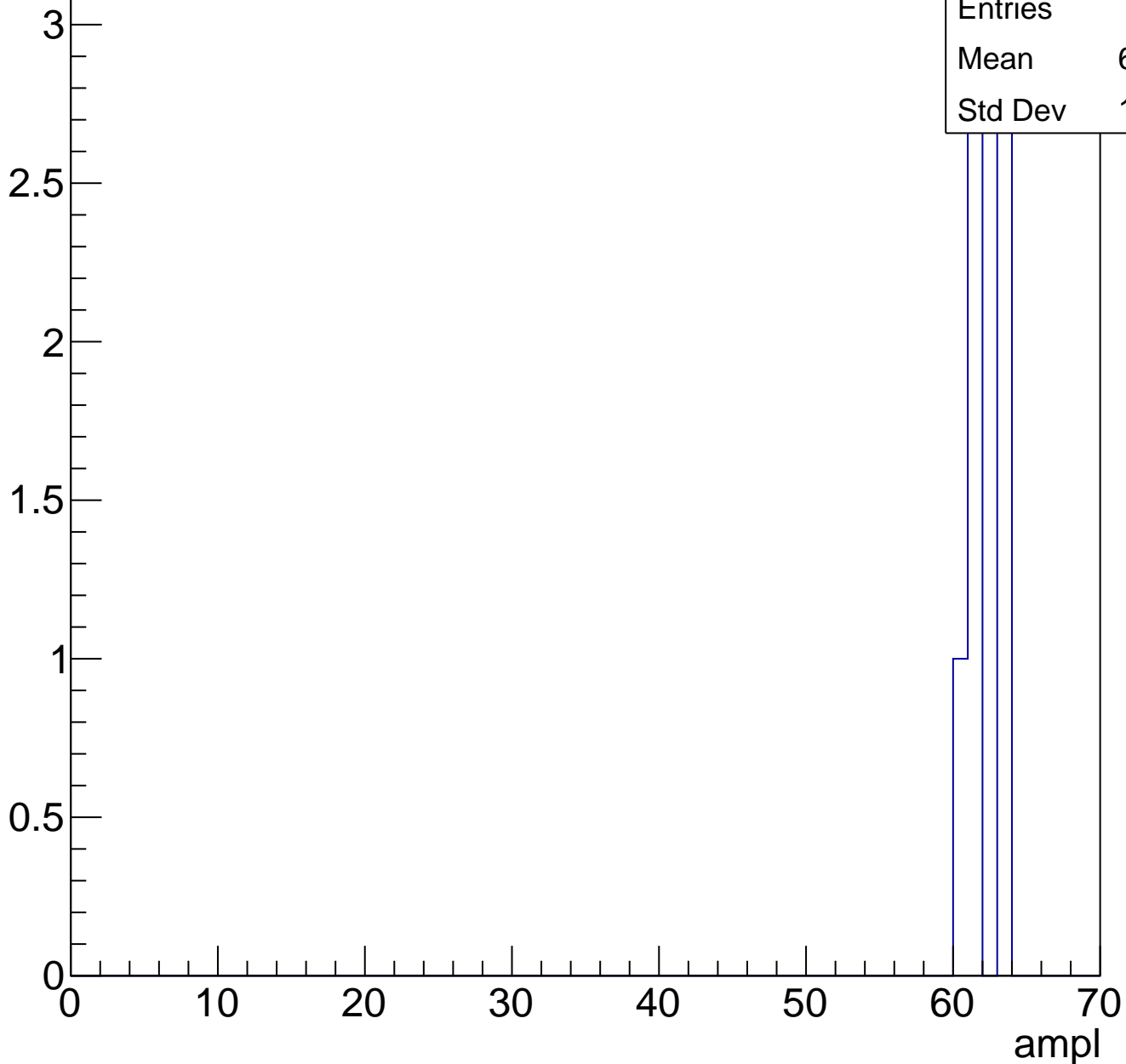
60

70

B1L100S, U9-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch55, adc0

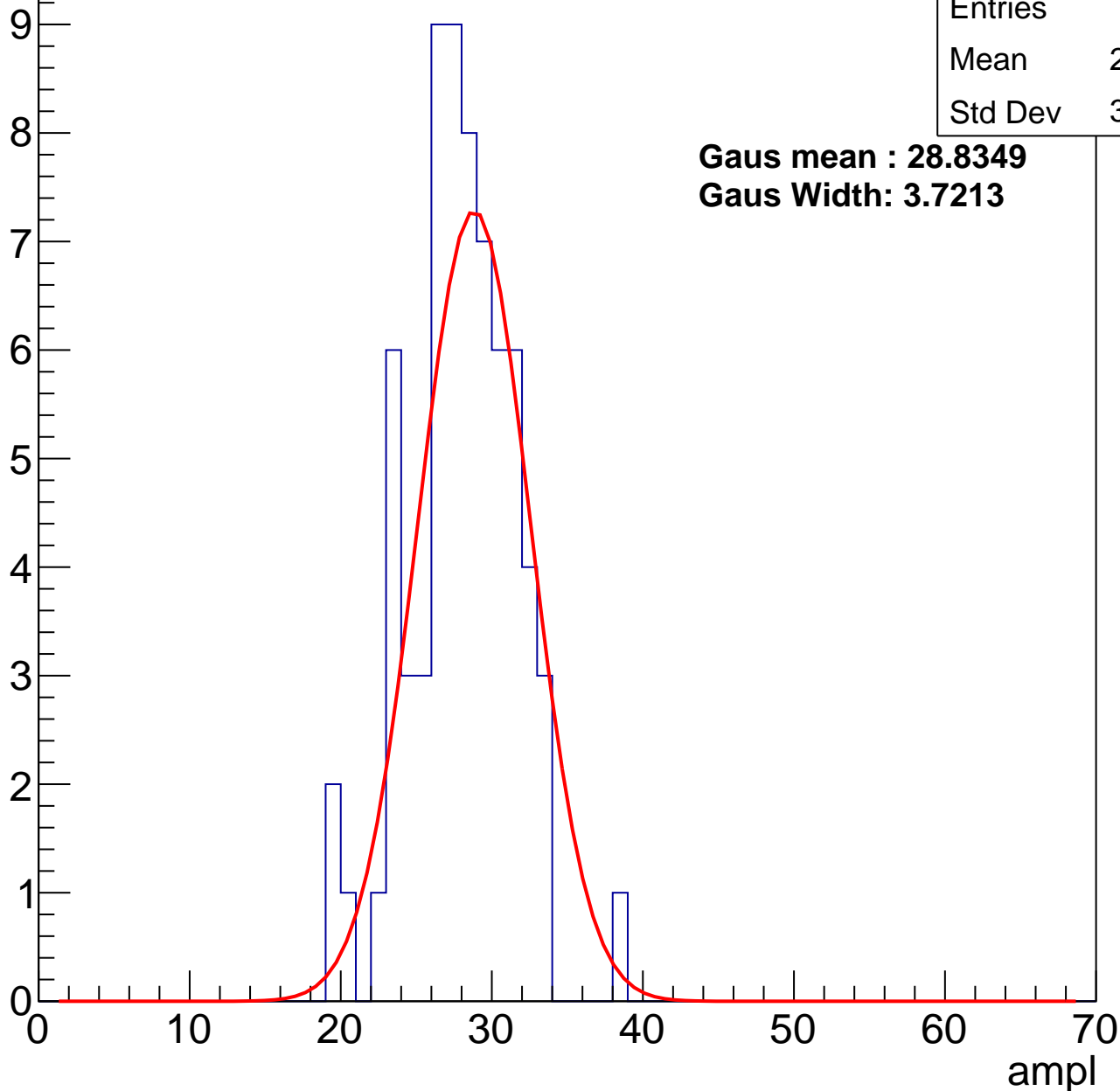
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 27.54 |
| Std Dev | 3.492 |

Gaus mean : 28.8349

Gaus Width: 3.7213



B1L100S, U9-ch55, adc1

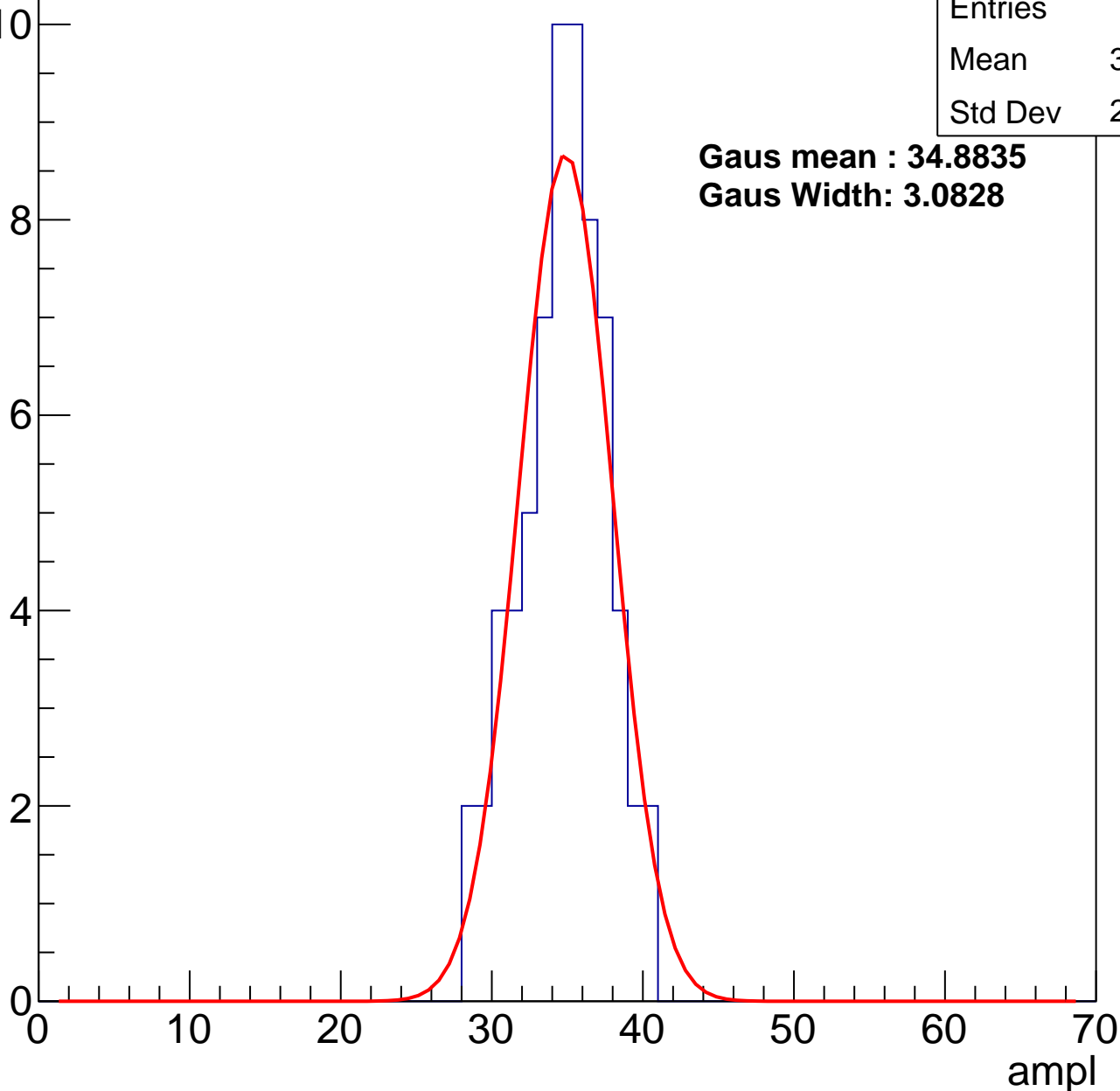
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 34.27 |
| Std Dev | 2.826 |

Gaus mean : 34.8835

Gaus Width: 3.0828



B1L100S, U9-ch55, adc2

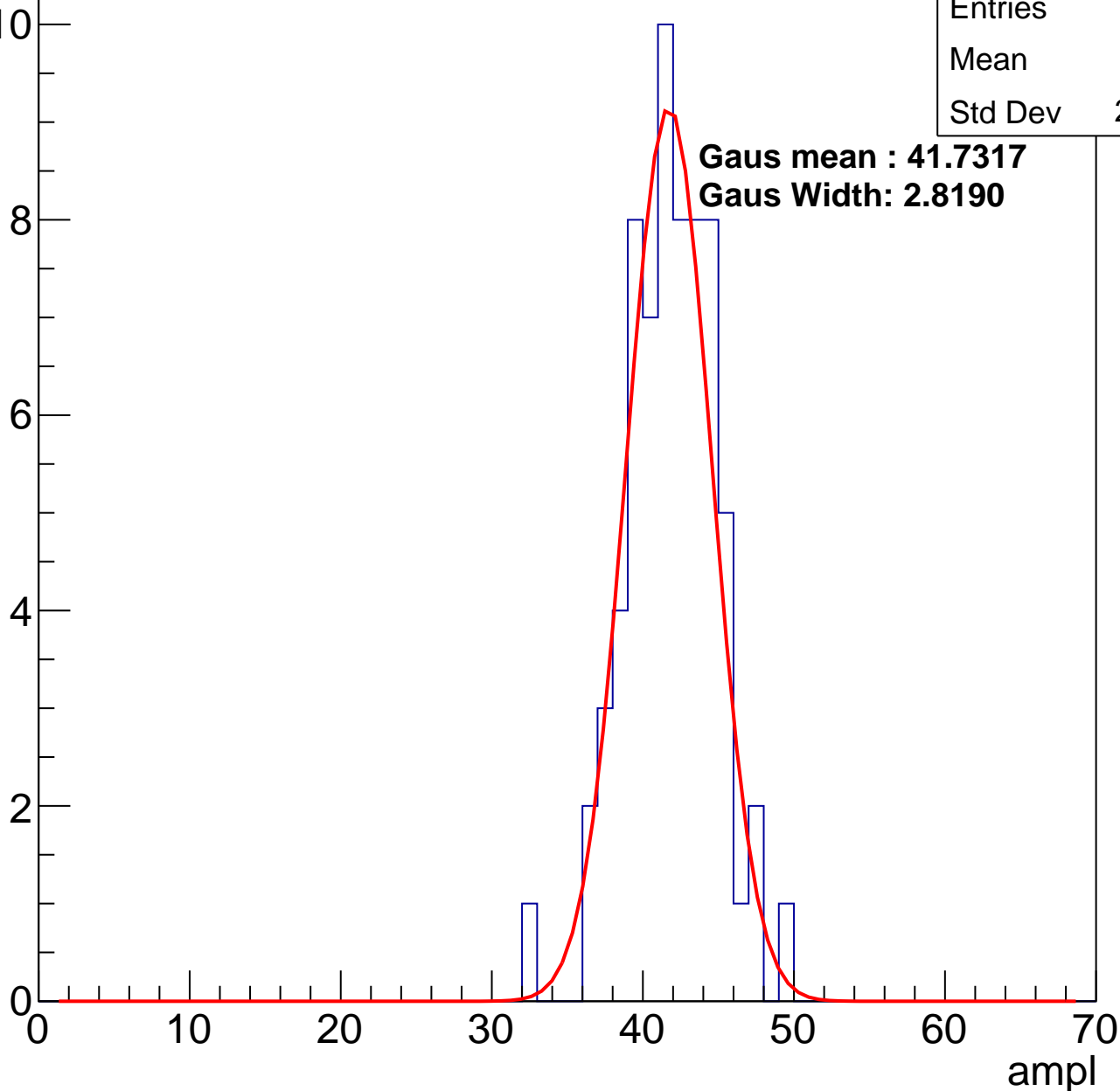
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 41.4 |
| Std Dev | 2.961 |

Gaus mean : 41.7317

Gaus Width: 2.8190

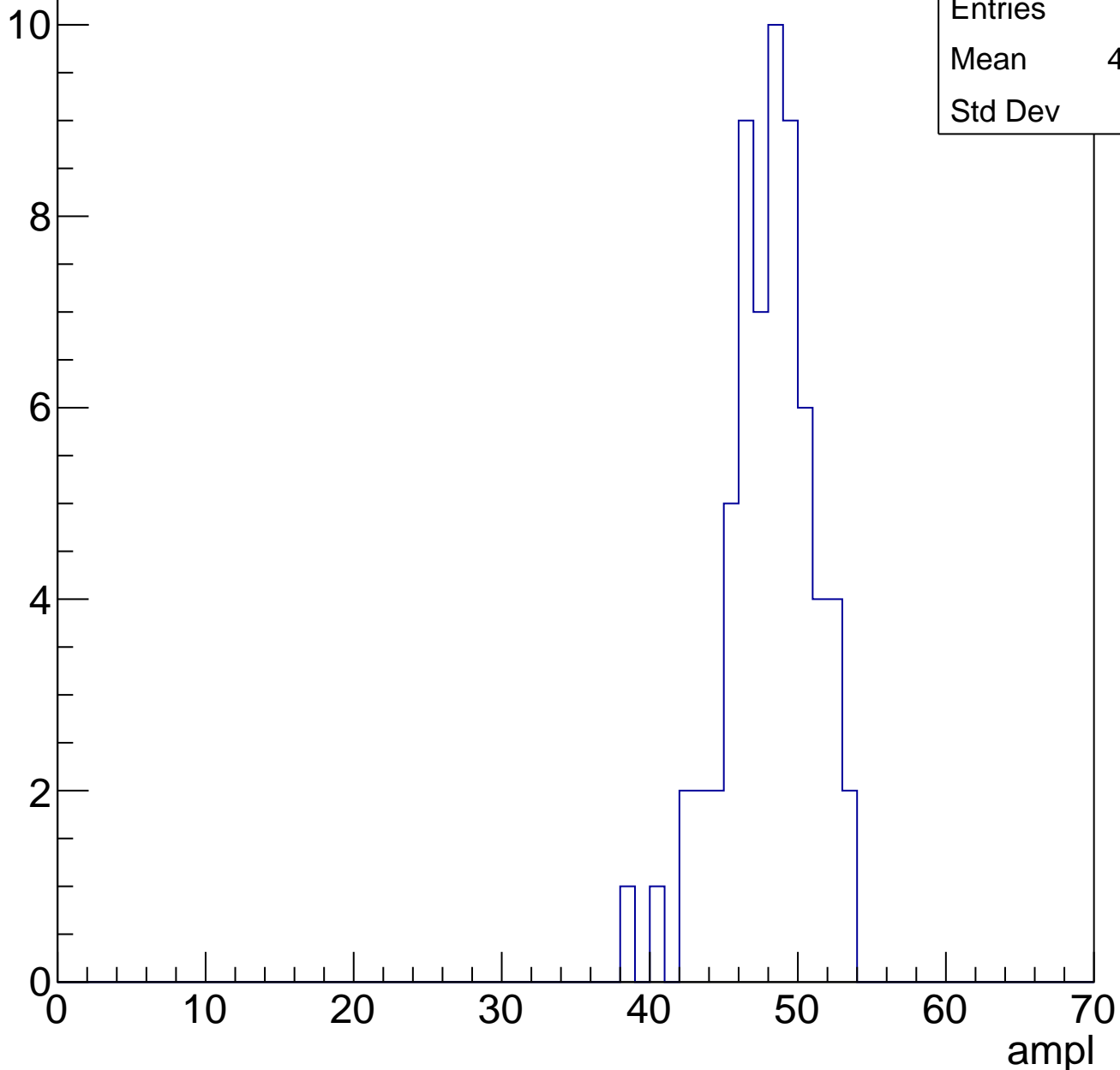


B1L100S, U9-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 64 |
| Mean | 47.55 |
| Std Dev | 3 |

Entry

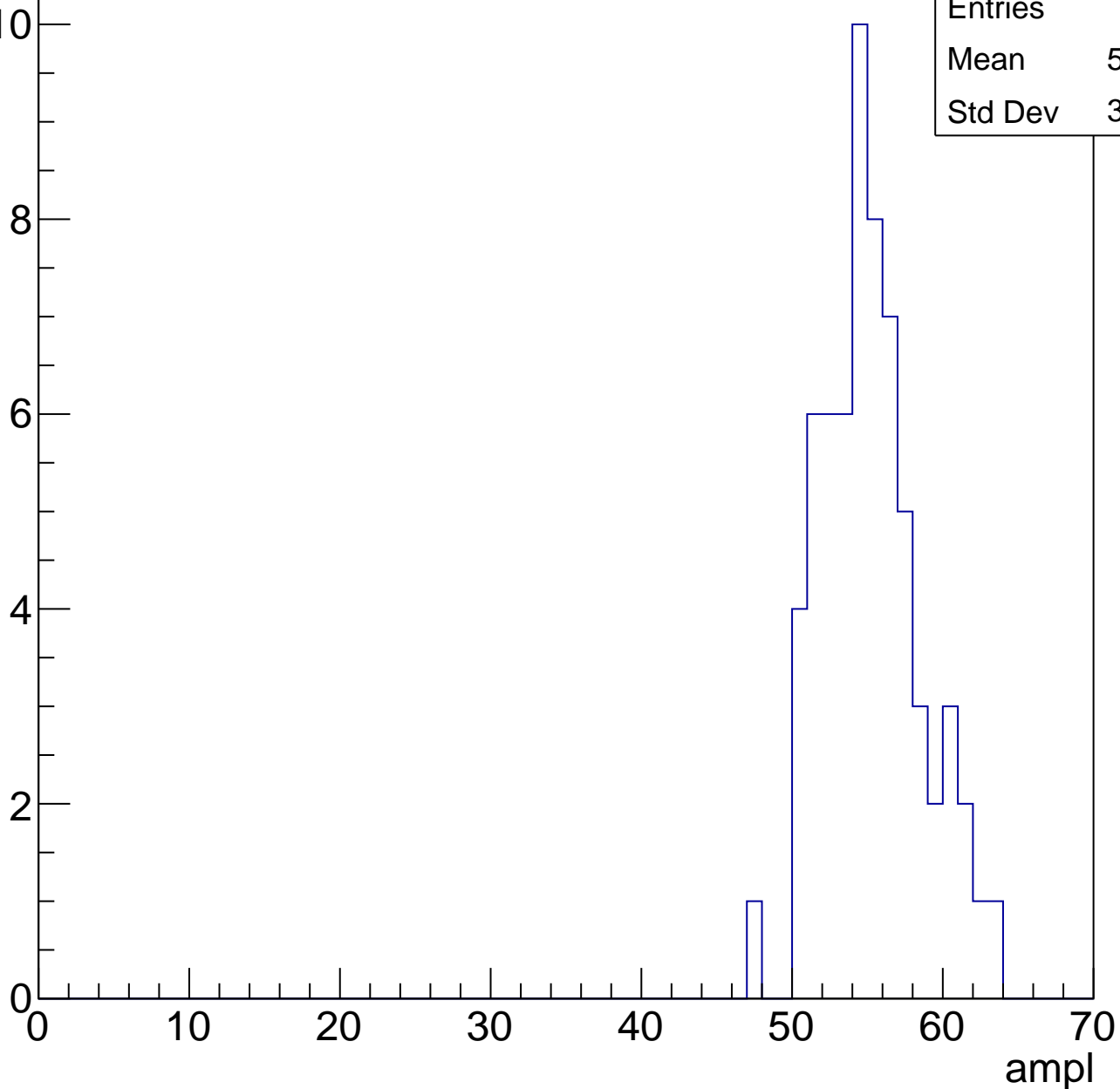


B1L100S, U9-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

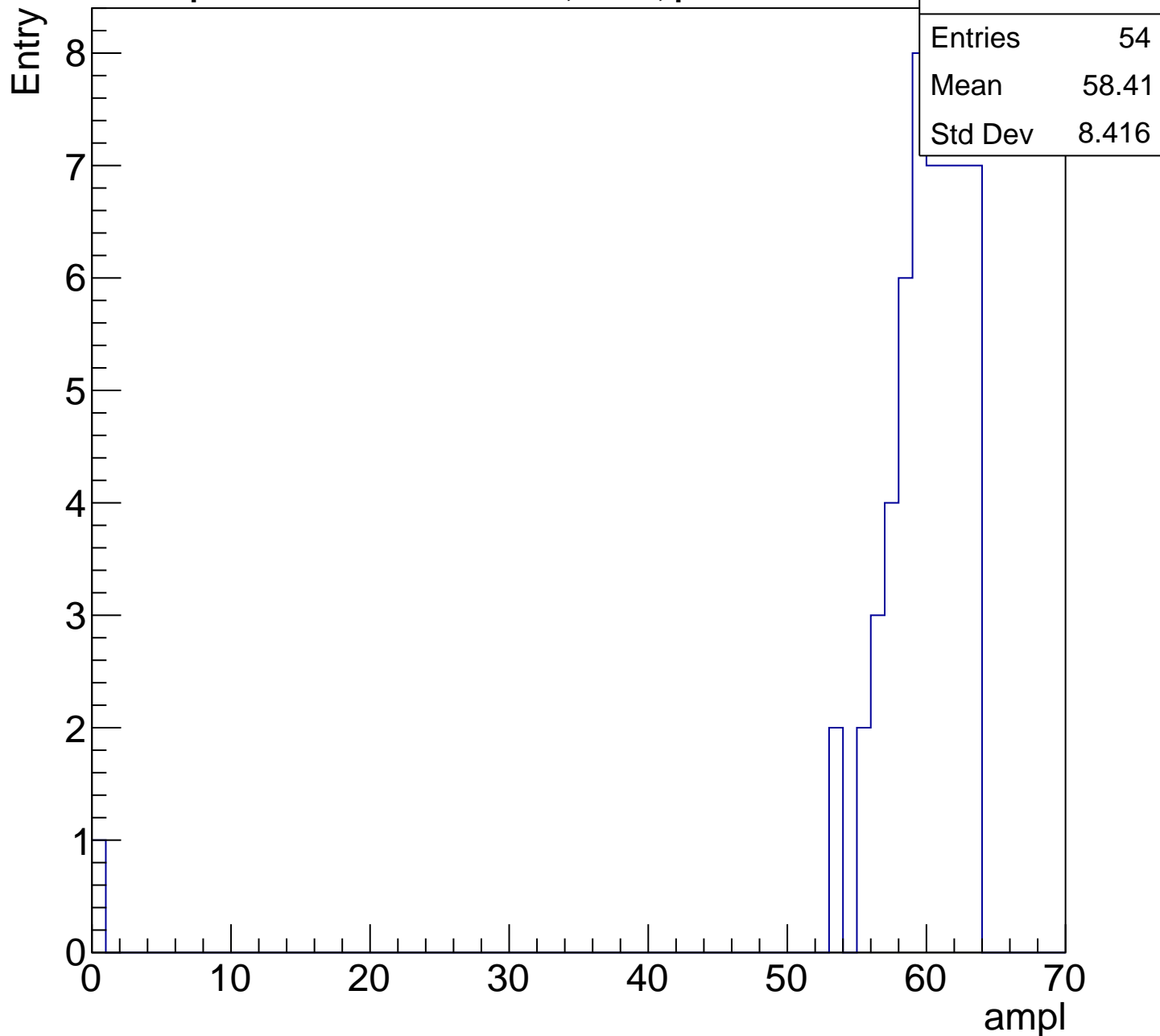
Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.75 |
| Std Dev | 3.258 |



B1L100S, U9-ch55, adc5

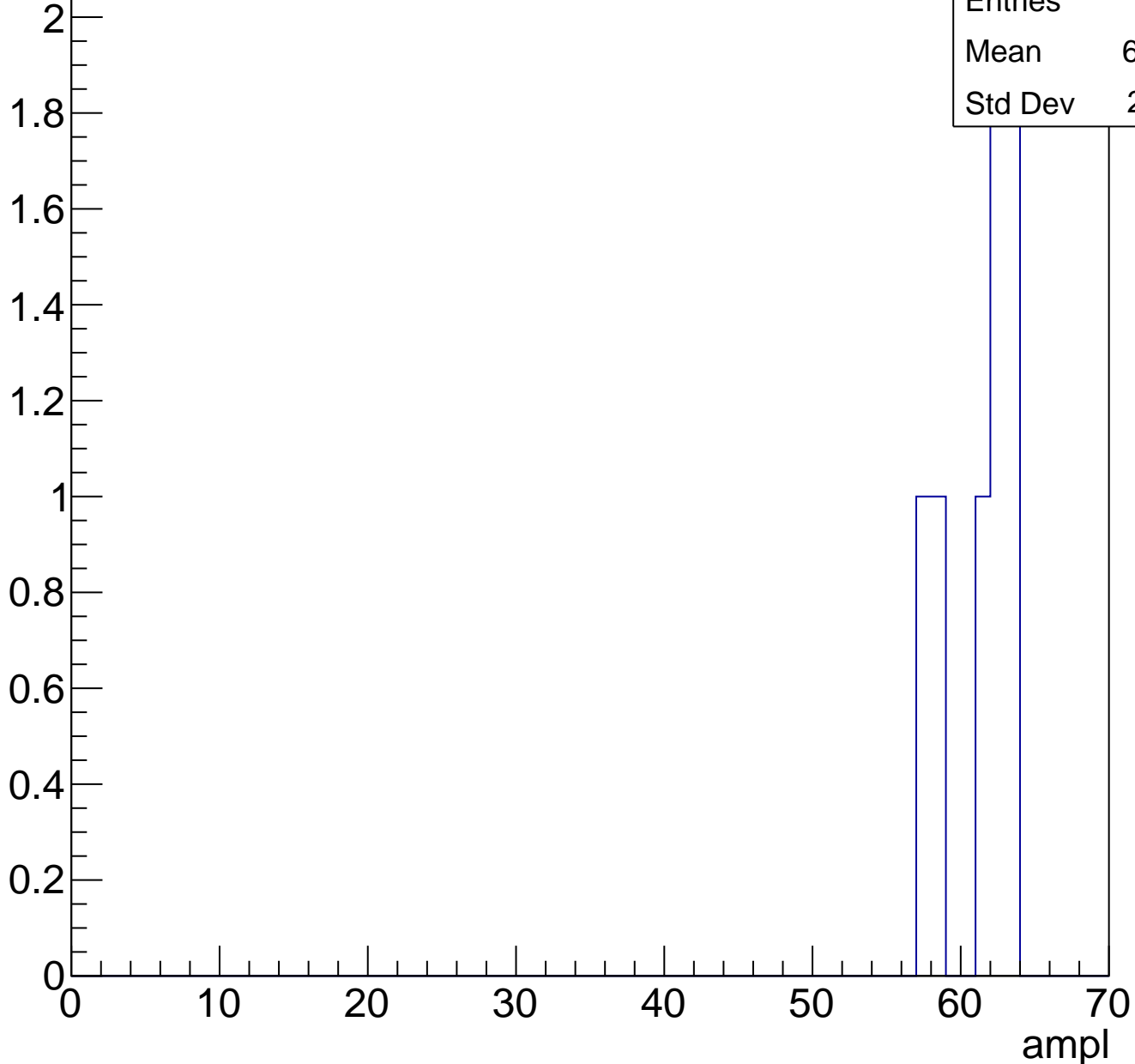
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch56, adc0

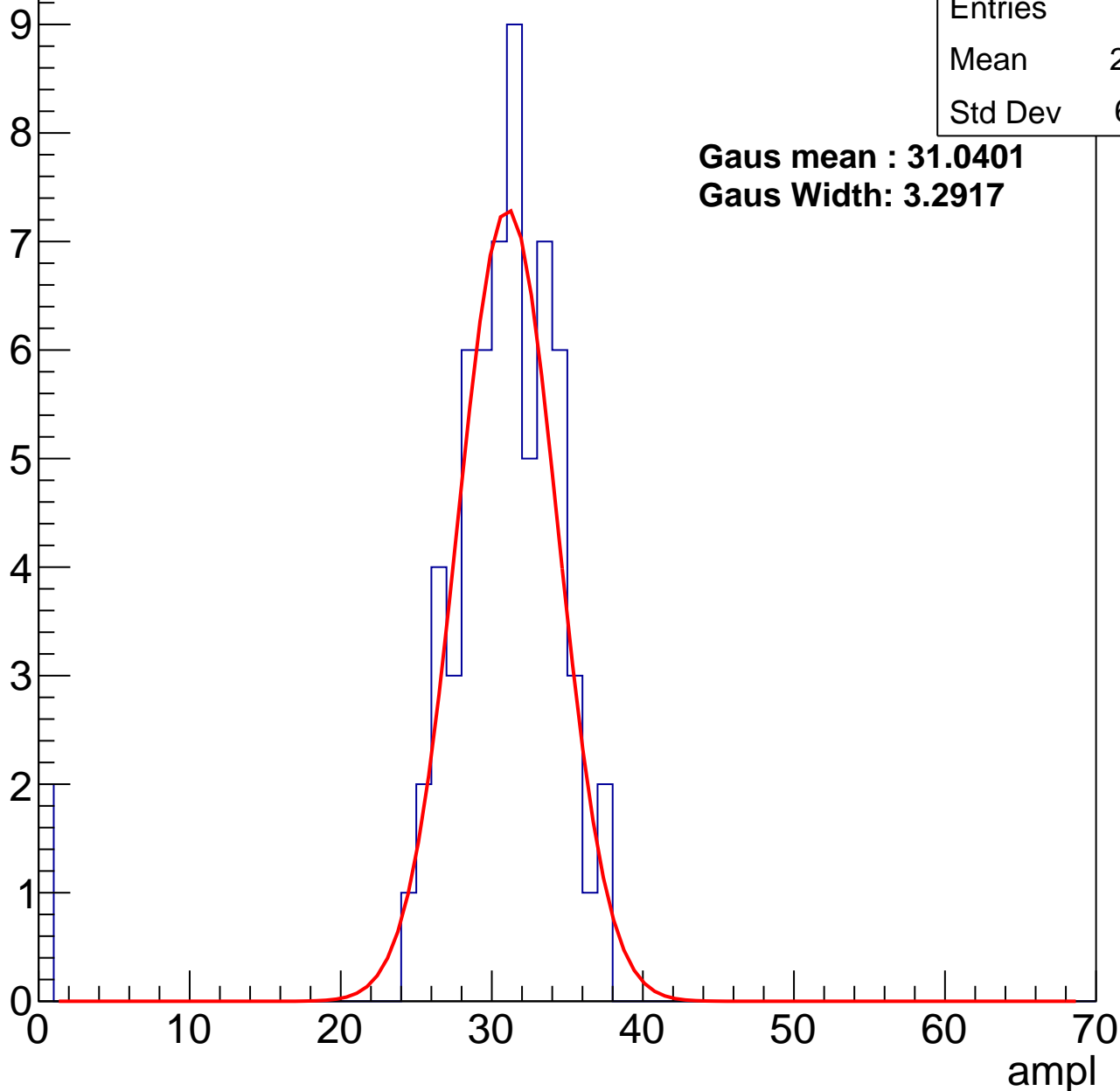
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.69 |
| Std Dev | 6.121 |

Gaus mean : 31.0401

Gaus Width: 3.2917



B1L100S, U9-ch56, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.41 |
| Std Dev | 3.075 |

Gaus mean : 37.9208

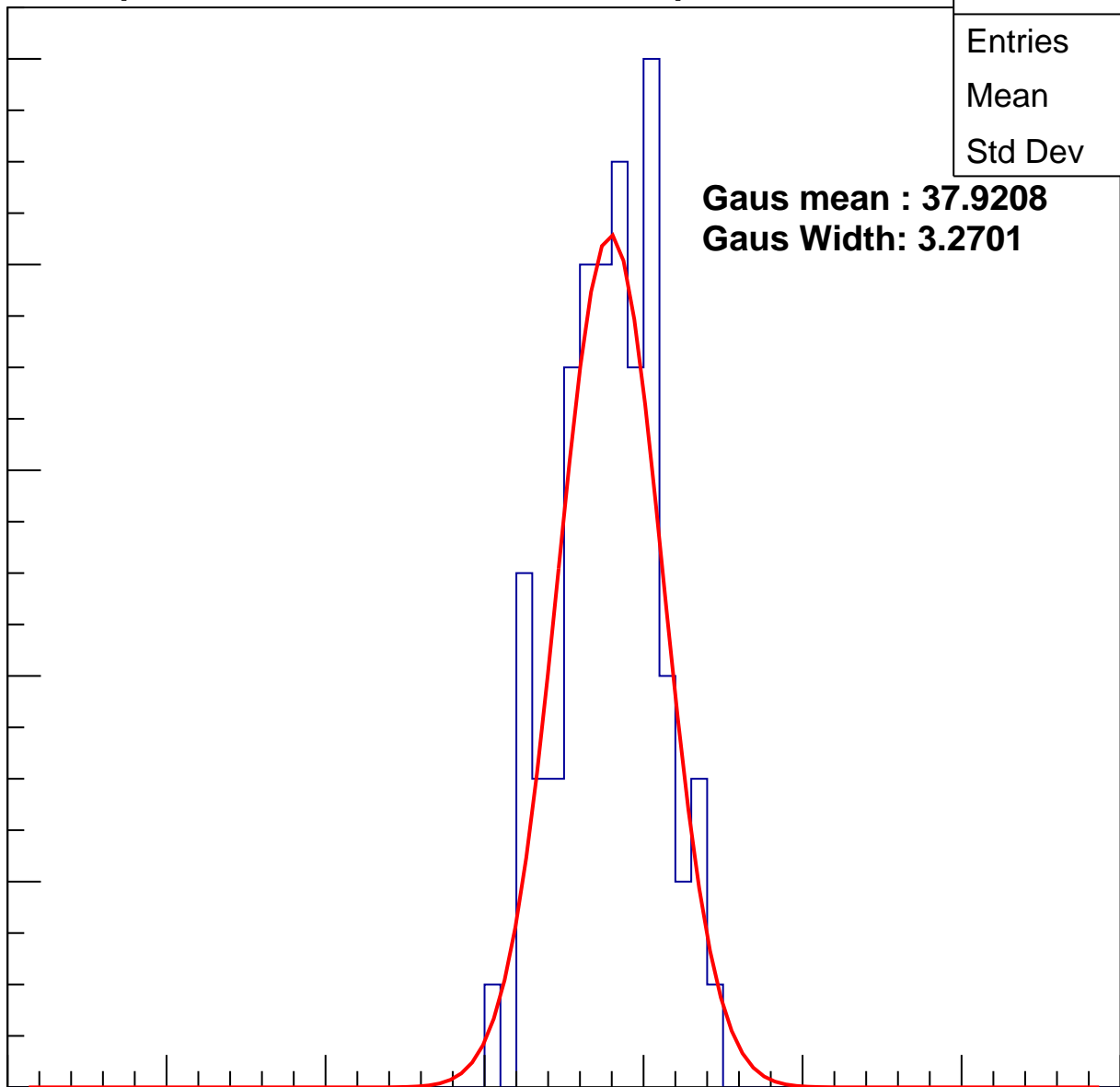
Gaus Width: 3.2701

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

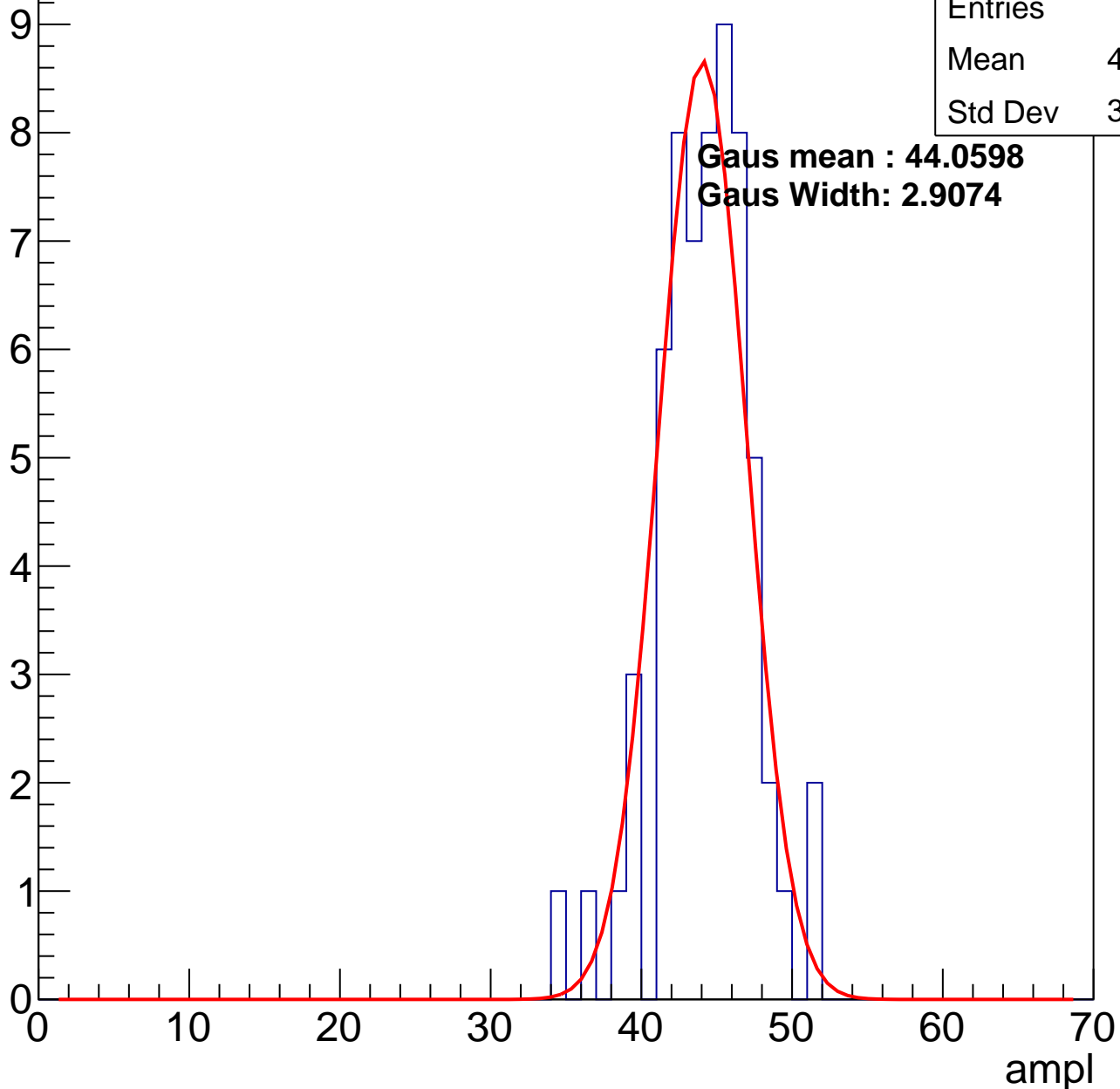
ampl



B1L100S, U9-ch56, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

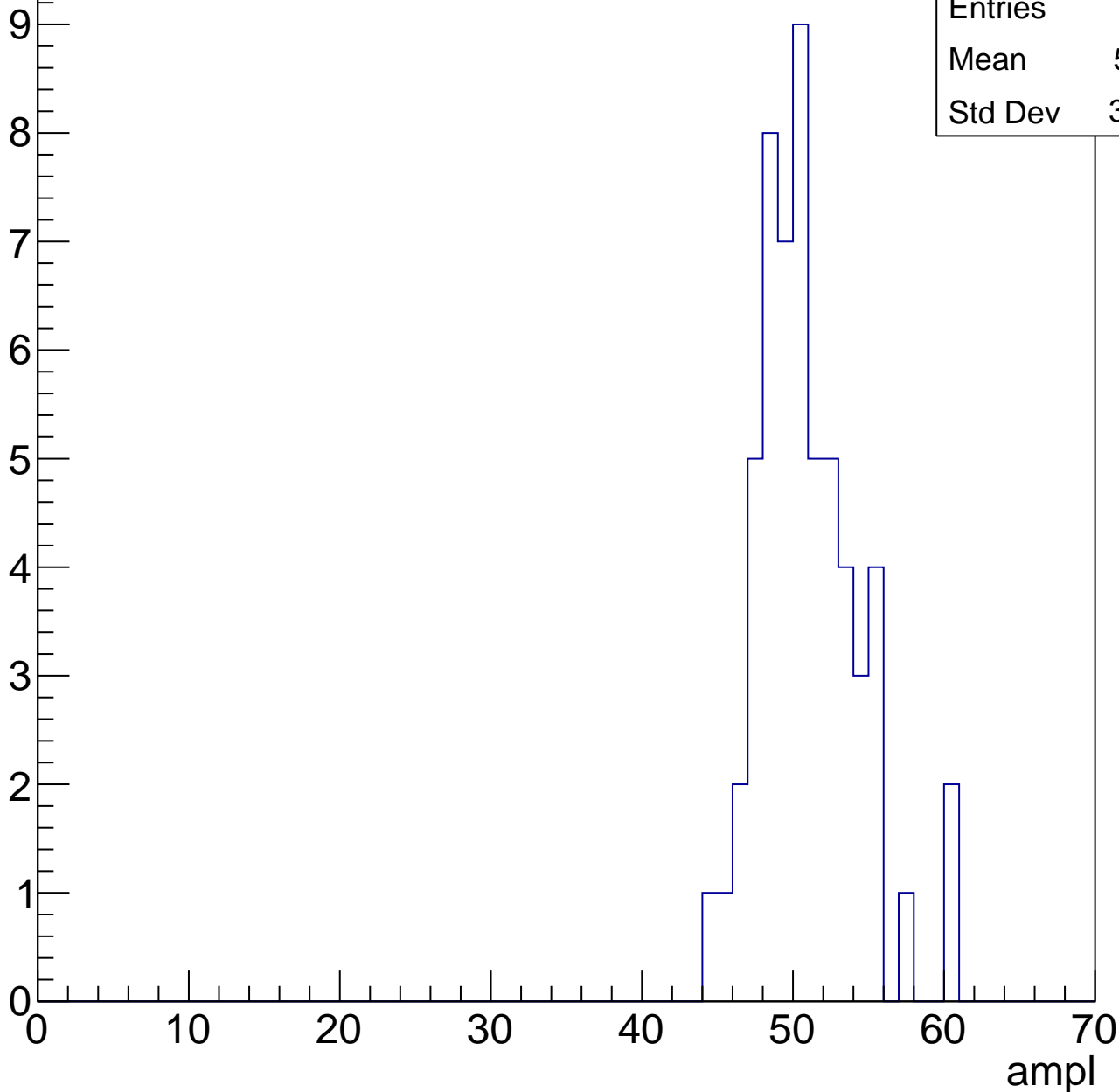


B1L100S, U9-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

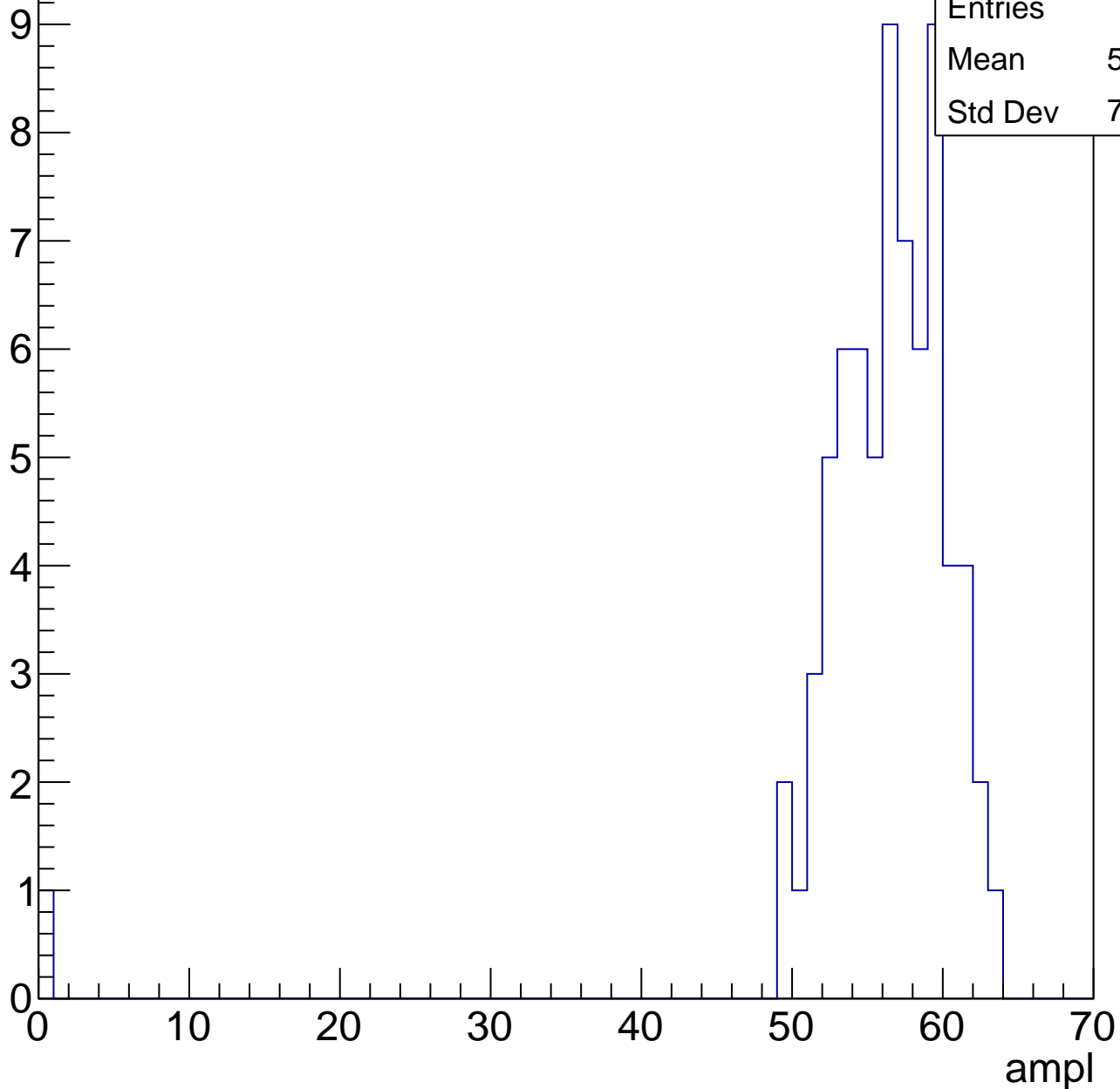
| | |
|---------|-------|
| Entries | 57 |
| Mean | 50.51 |
| Std Dev | 3.299 |



B1L100S, U9-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



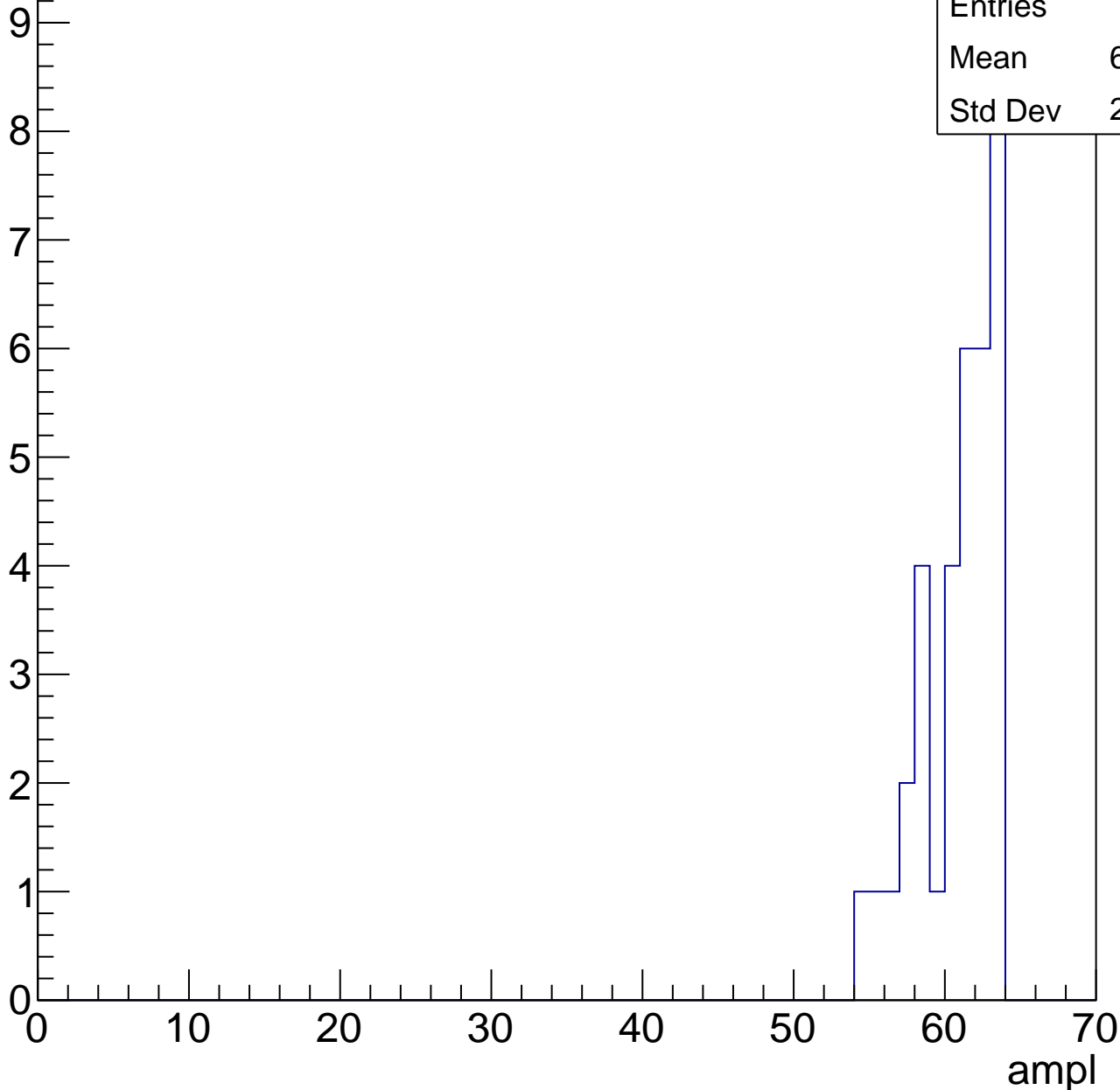
| | |
|---------|-------|
| Entries | 71 |
| Mean | 55.37 |
| Std Dev | 7.389 |

B1L100S, U9-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.43 |
| Std Dev | 2.488 |



B1L100S, U9-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 62 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl

B1L100S, U9-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 10.5 |
| Std Dev | 10.5 |

B1L100S, U9-ch57, adc0

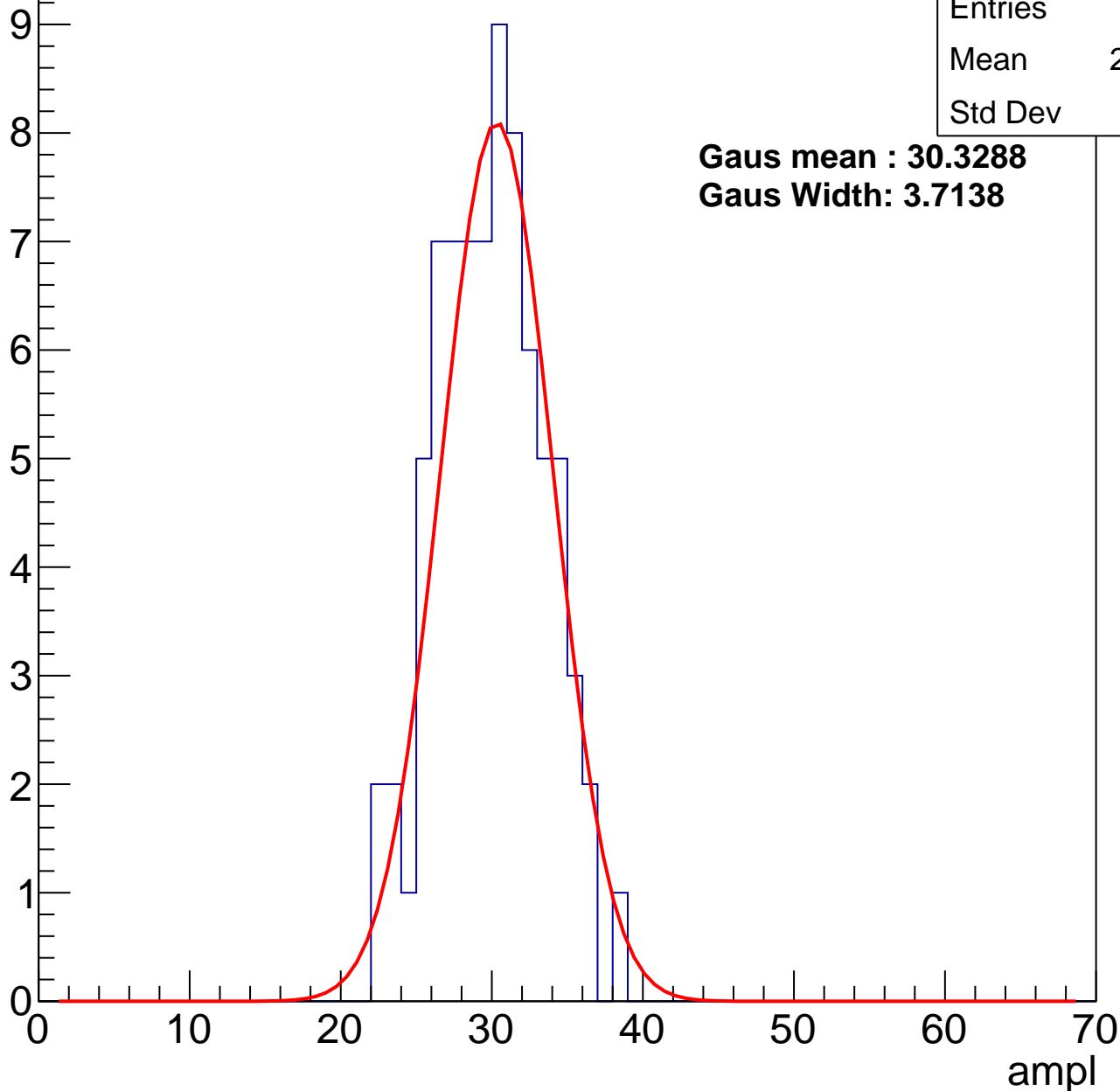
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 29.47 |
| Std Dev | 3.5 |

Gaus mean : 30.3288

Gaus Width: 3.7138



B1L100S, U9-ch57, adc1

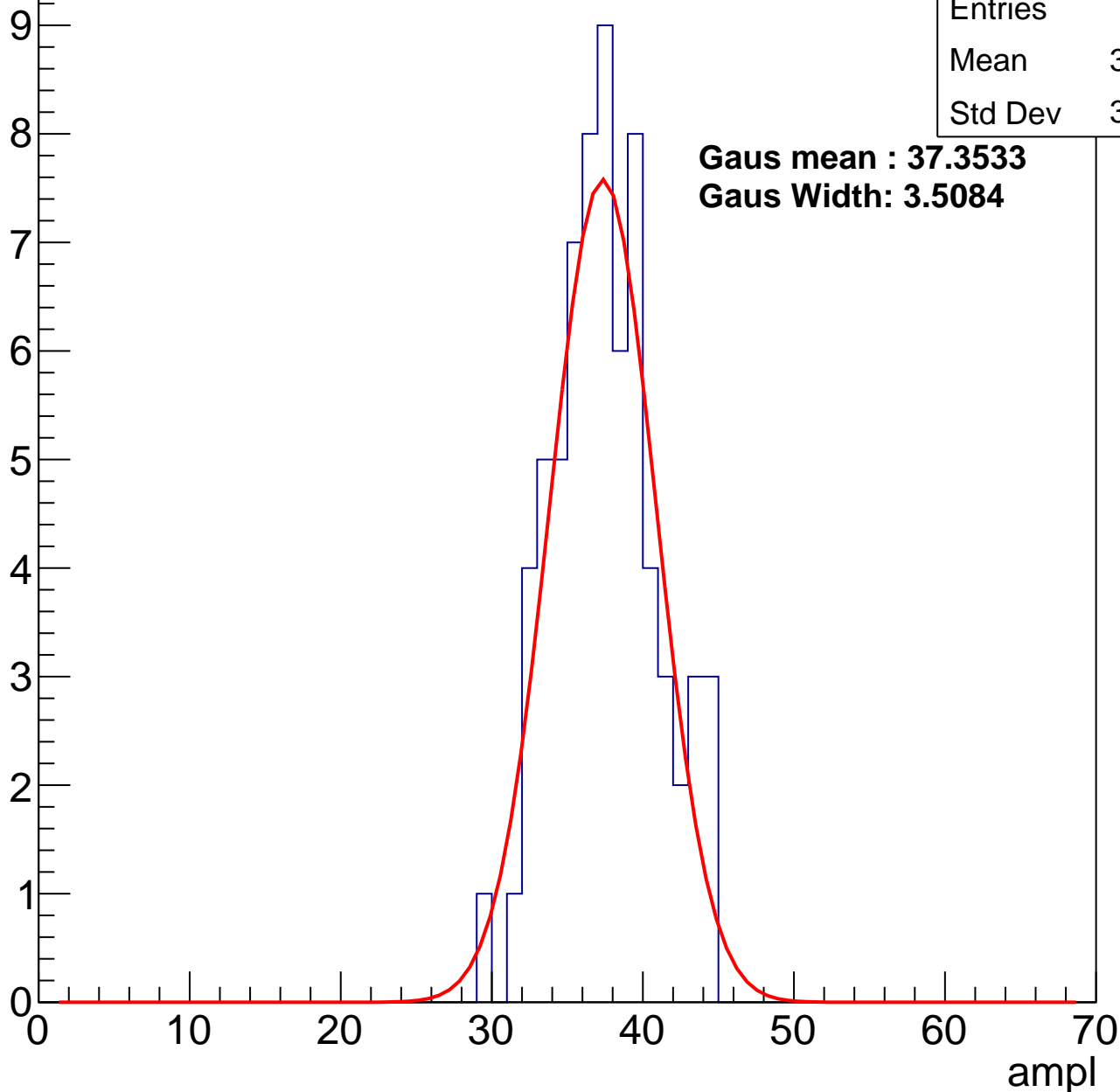
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 37.06 |
| Std Dev | 3.379 |

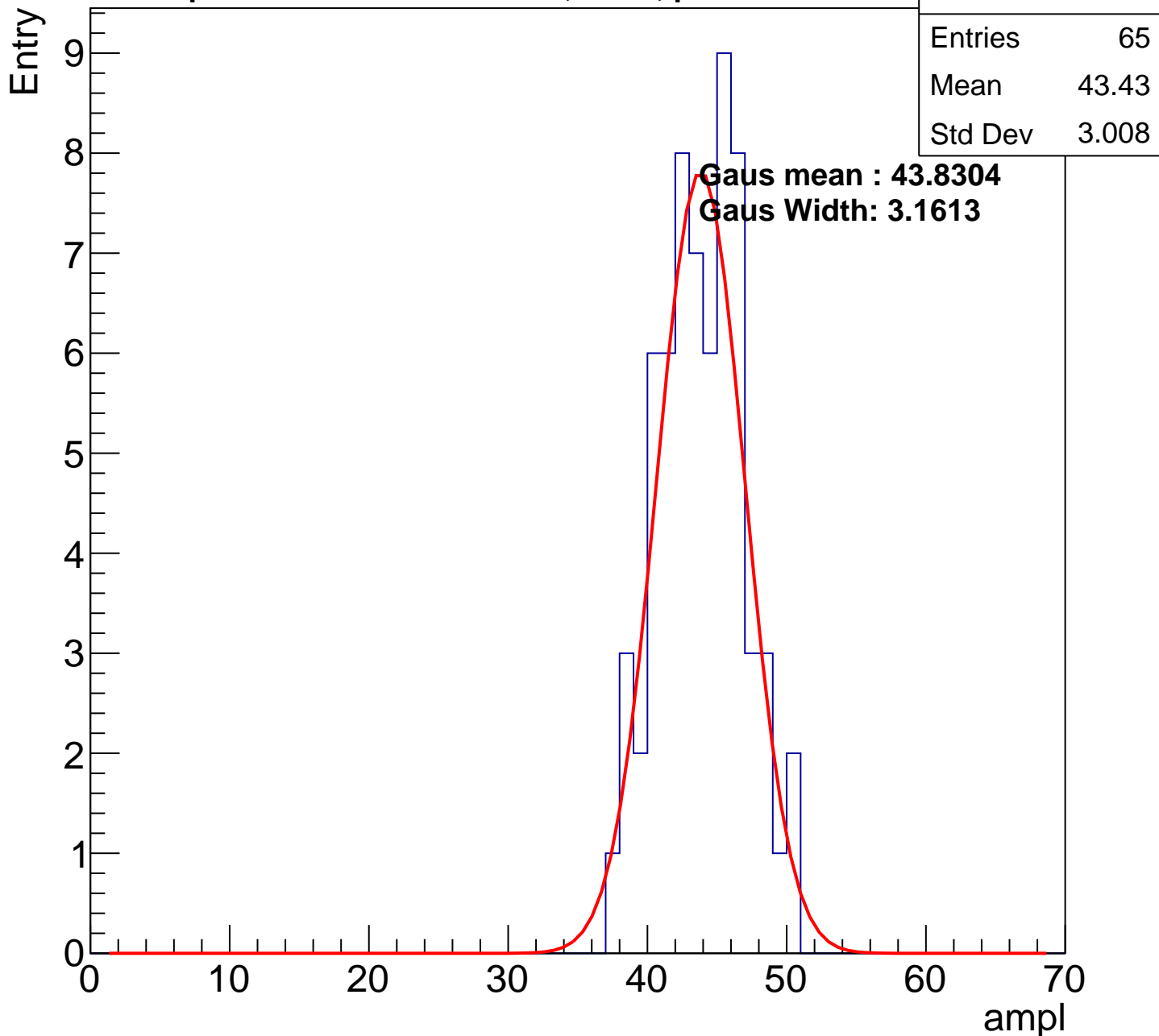
Gaus mean : 37.3533

Gaus Width: 3.5084



B1L100S, U9-ch57, adc2

calib_packv5_042523_0143.root, FC#4, port A2

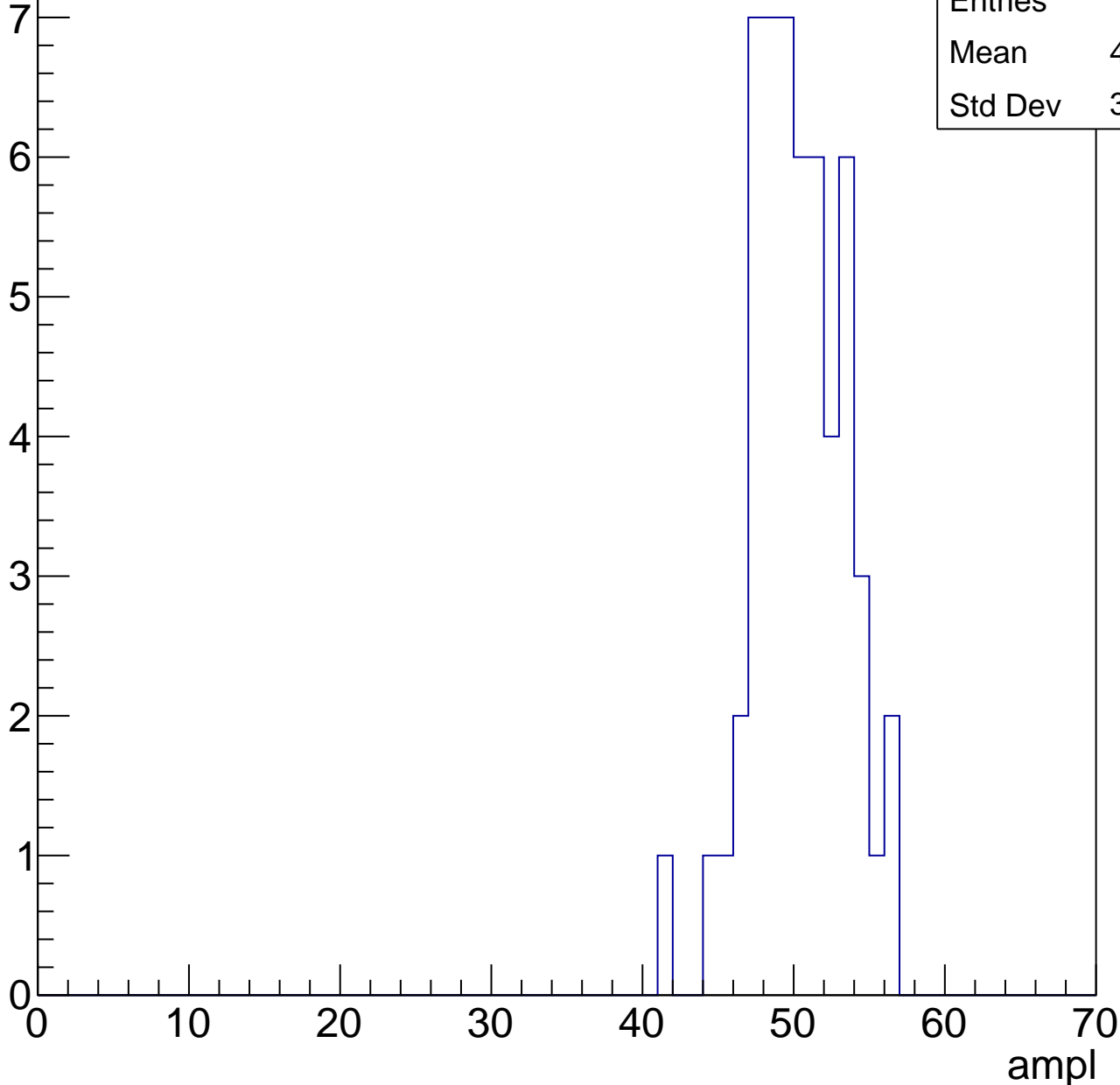


B1L100S, U9-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

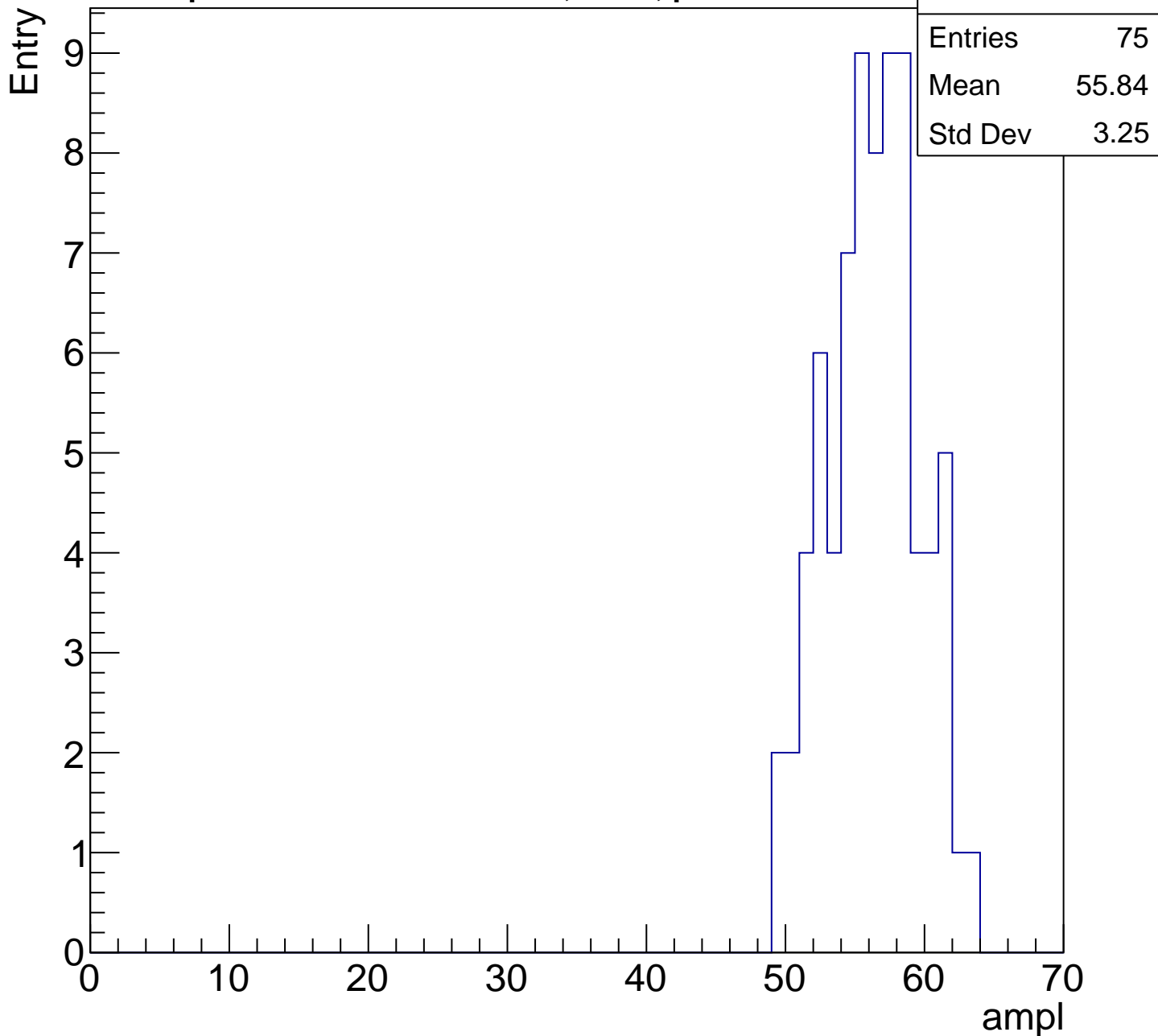
Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 49.83 |
| Std Dev | 3.017 |



B1L100S, U9-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

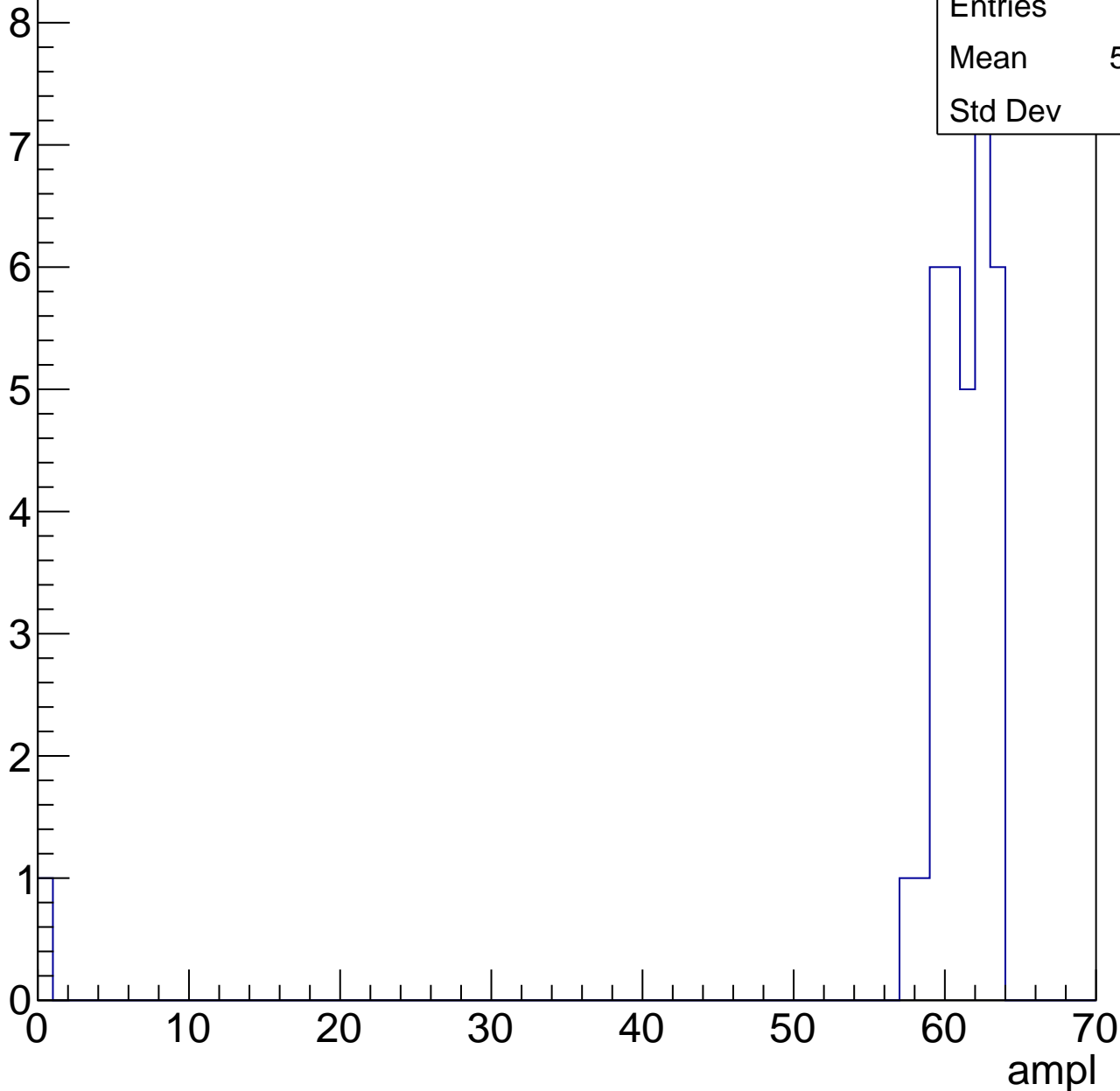


B1L100S, U9-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 59.06 |
| Std Dev | 10.4 |



B1L100S, U9-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch58, adc0

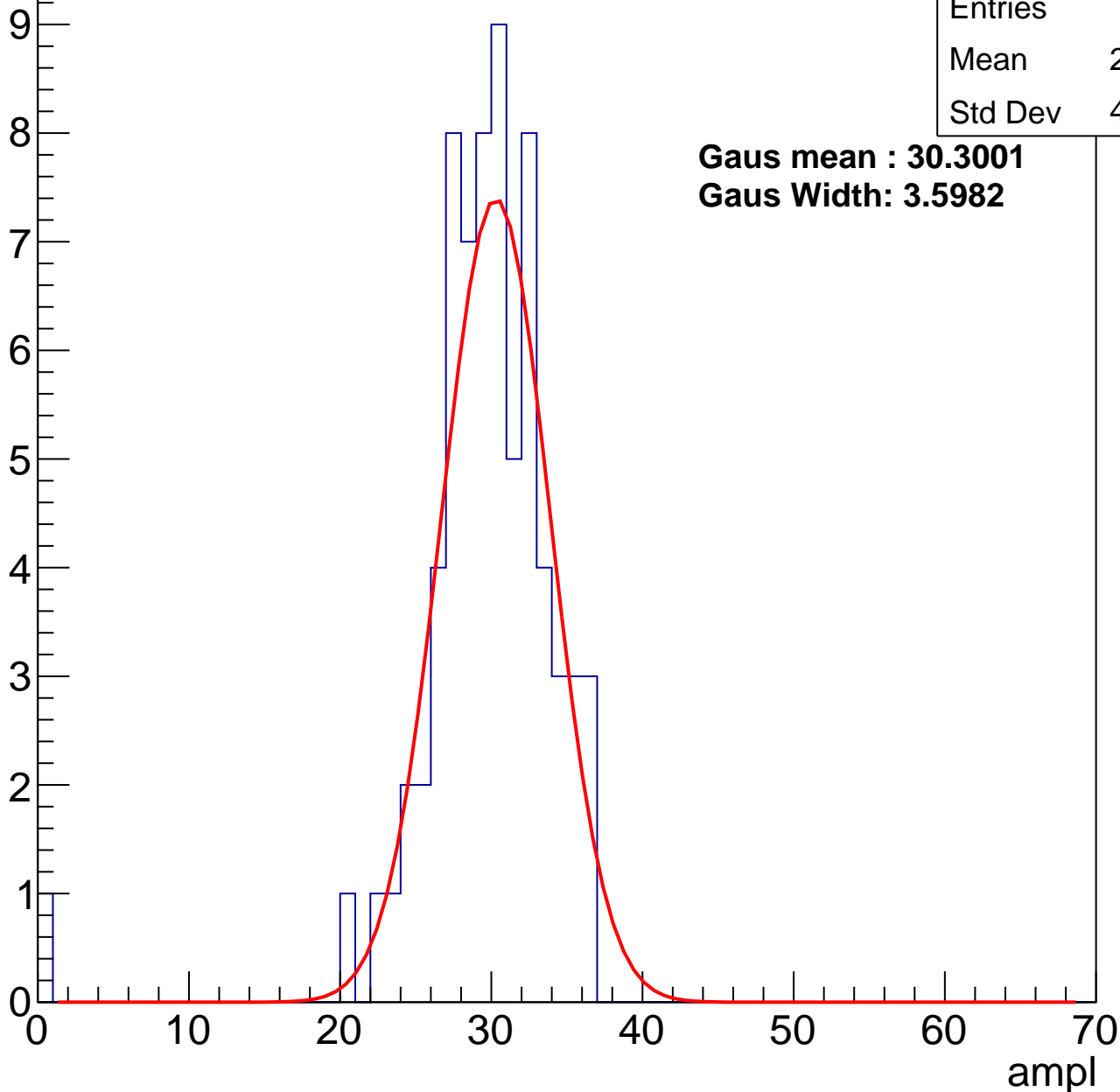
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 29.13 |
| Std Dev | 4.867 |

Gaus mean : 30.3001

Gaus Width: 3.5982



B1L100S, U9-ch58, adc1

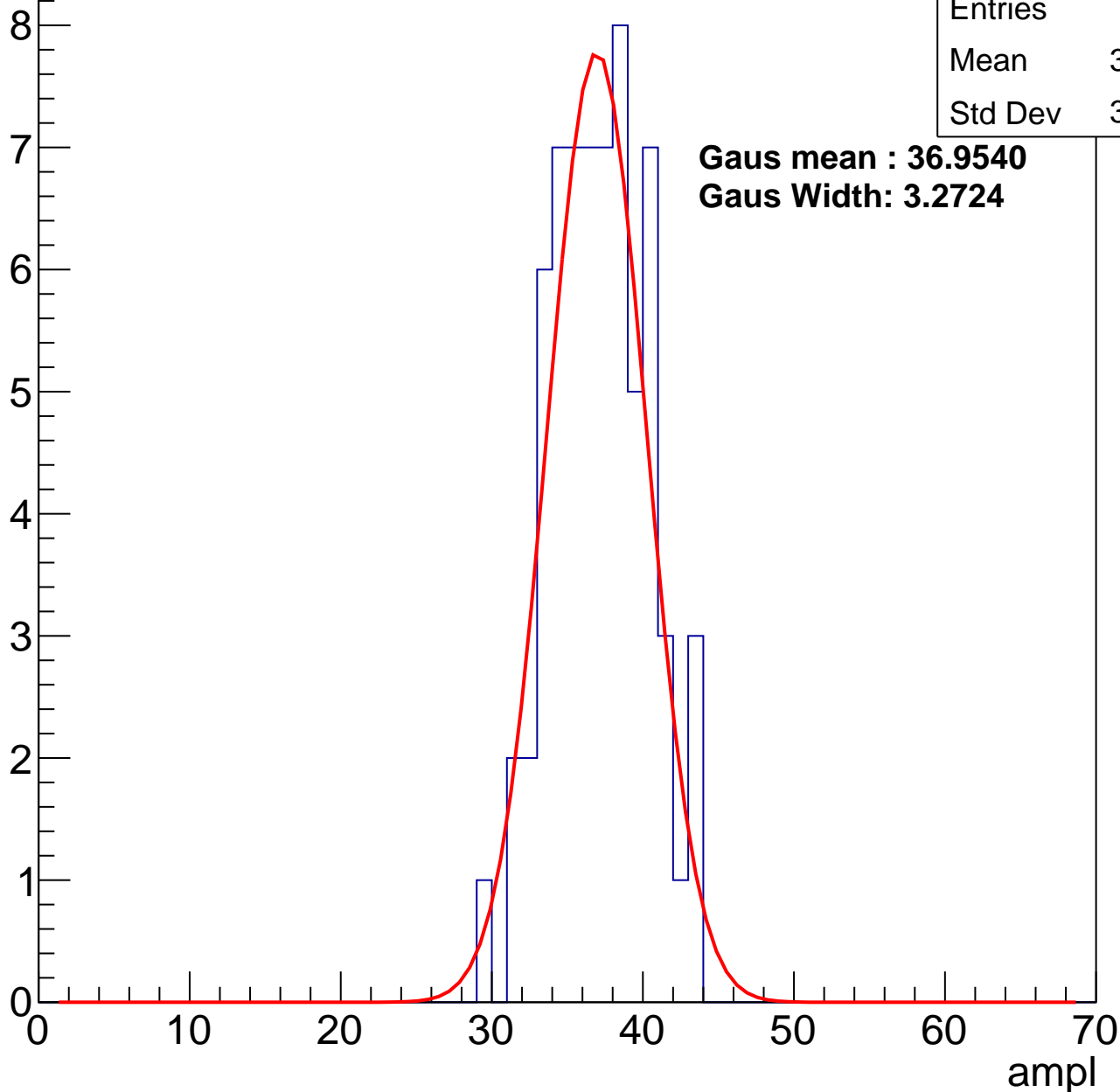
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.67 |
| Std Dev | 3.125 |

Gaus mean : 36.9540

Gaus Width: 3.2724



B1L100S, U9-ch58, adc2

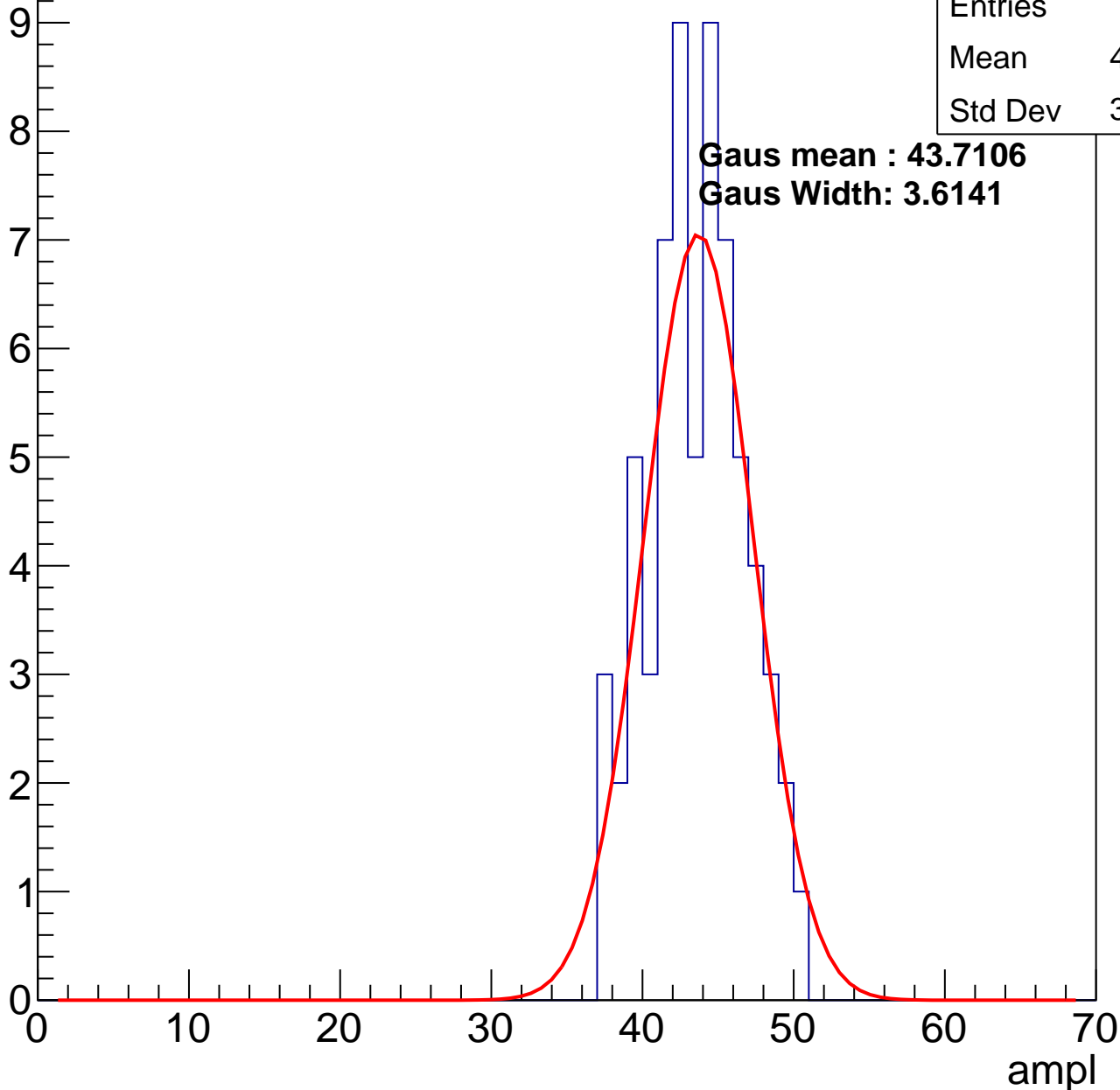
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 43.12 |
| Std Dev | 3.145 |

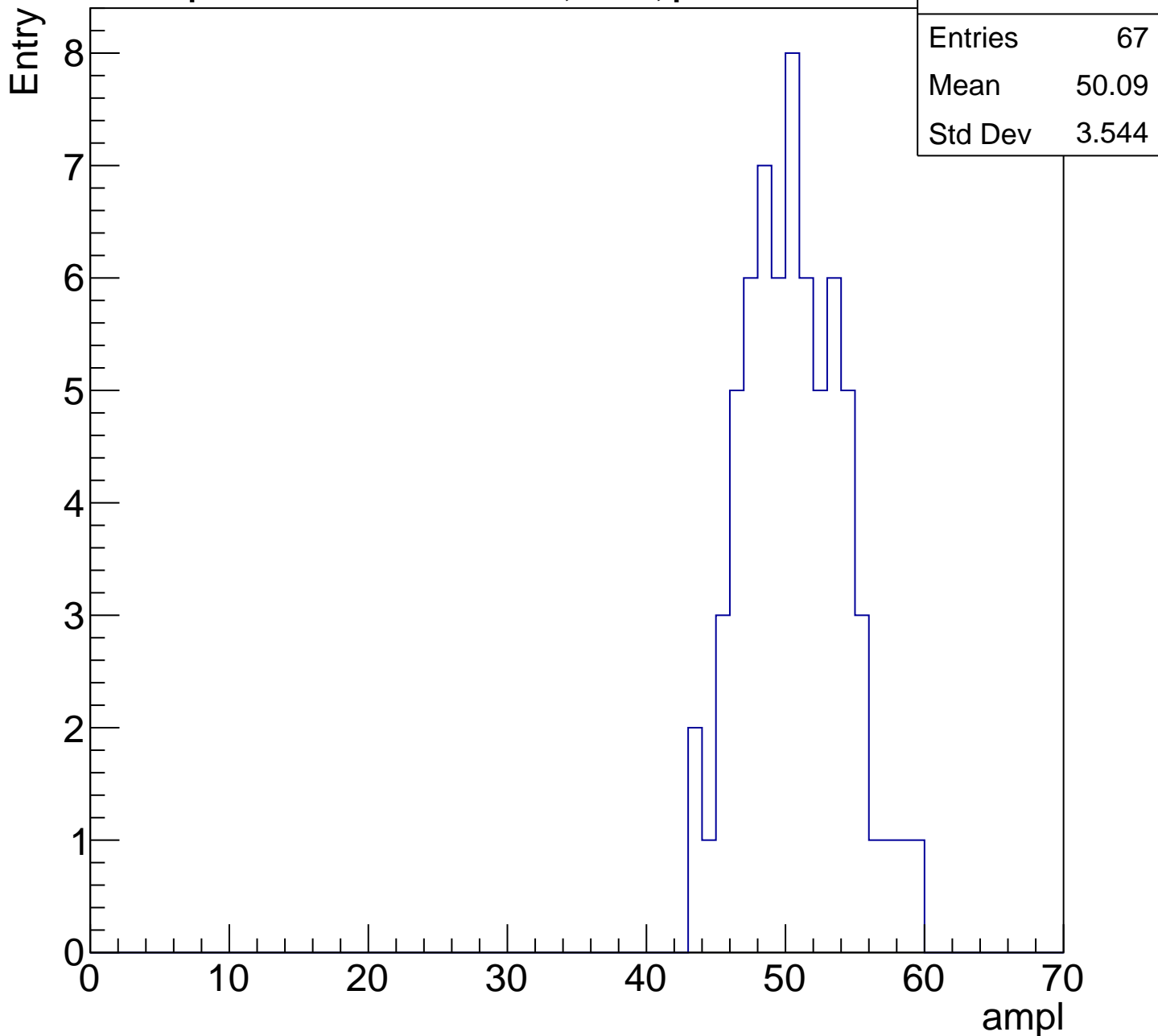
Gaus mean : 43.7106

Gaus Width: 3.6141



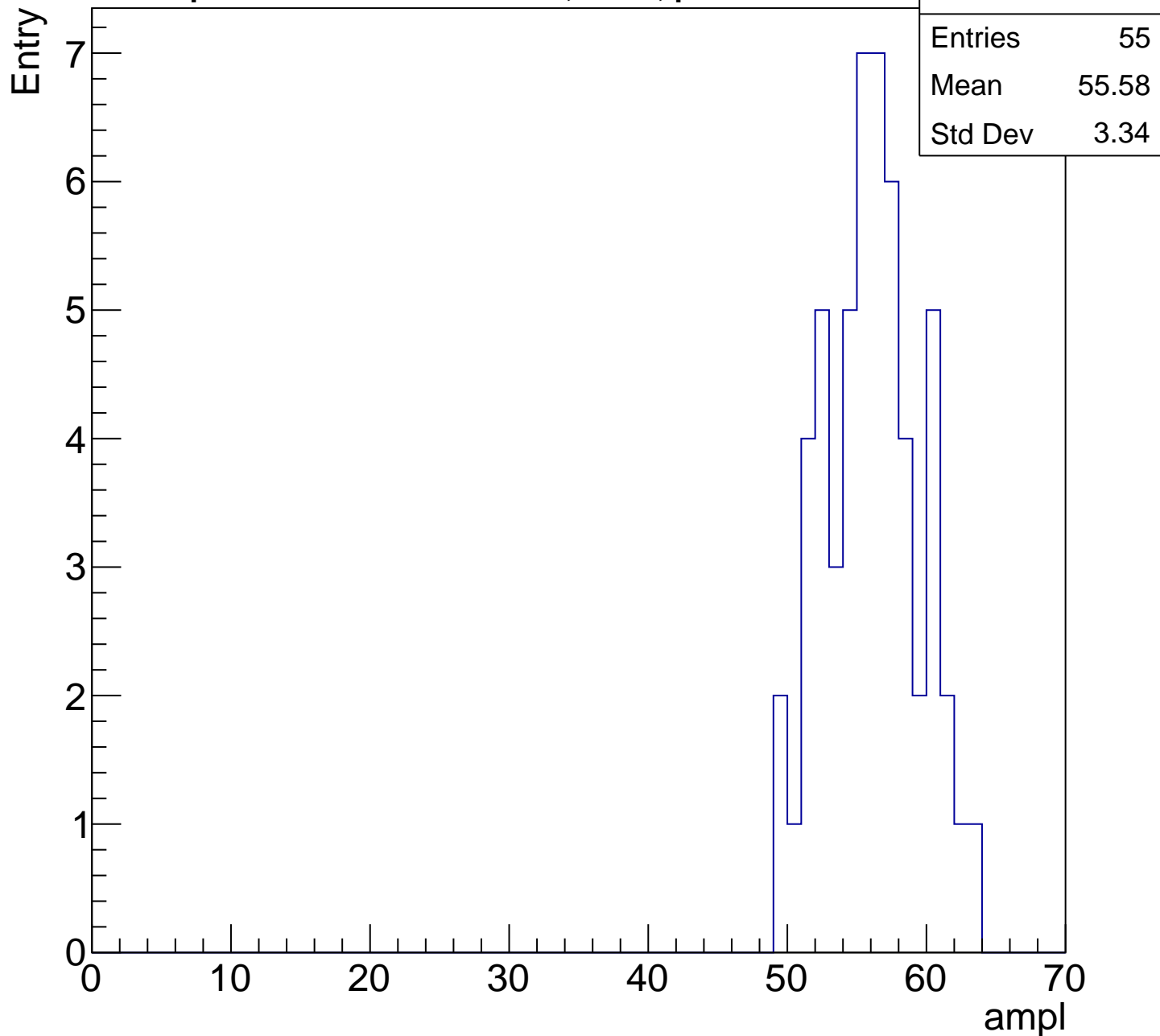
B1L100S, U9-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

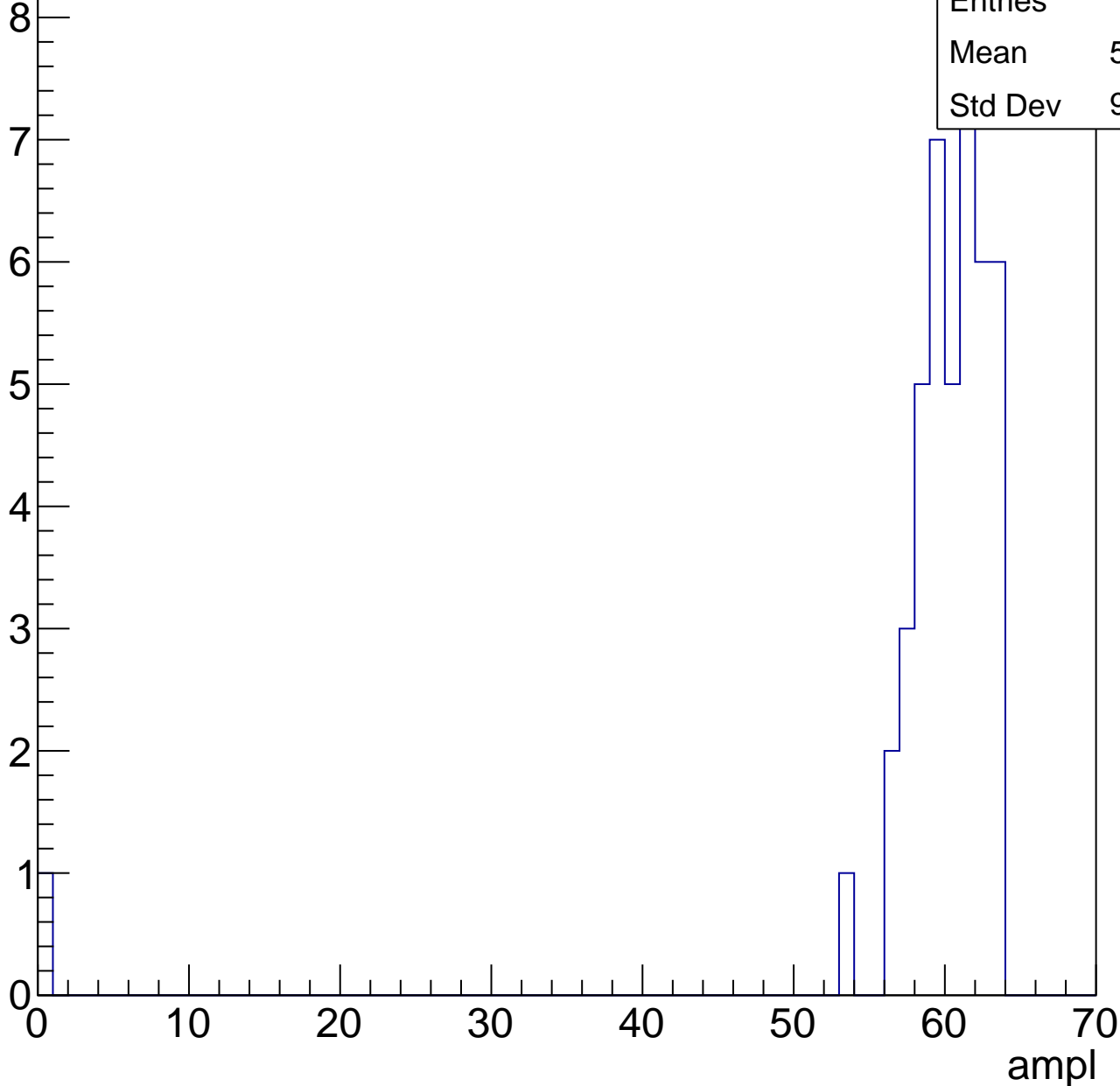


B1L100S, U9-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

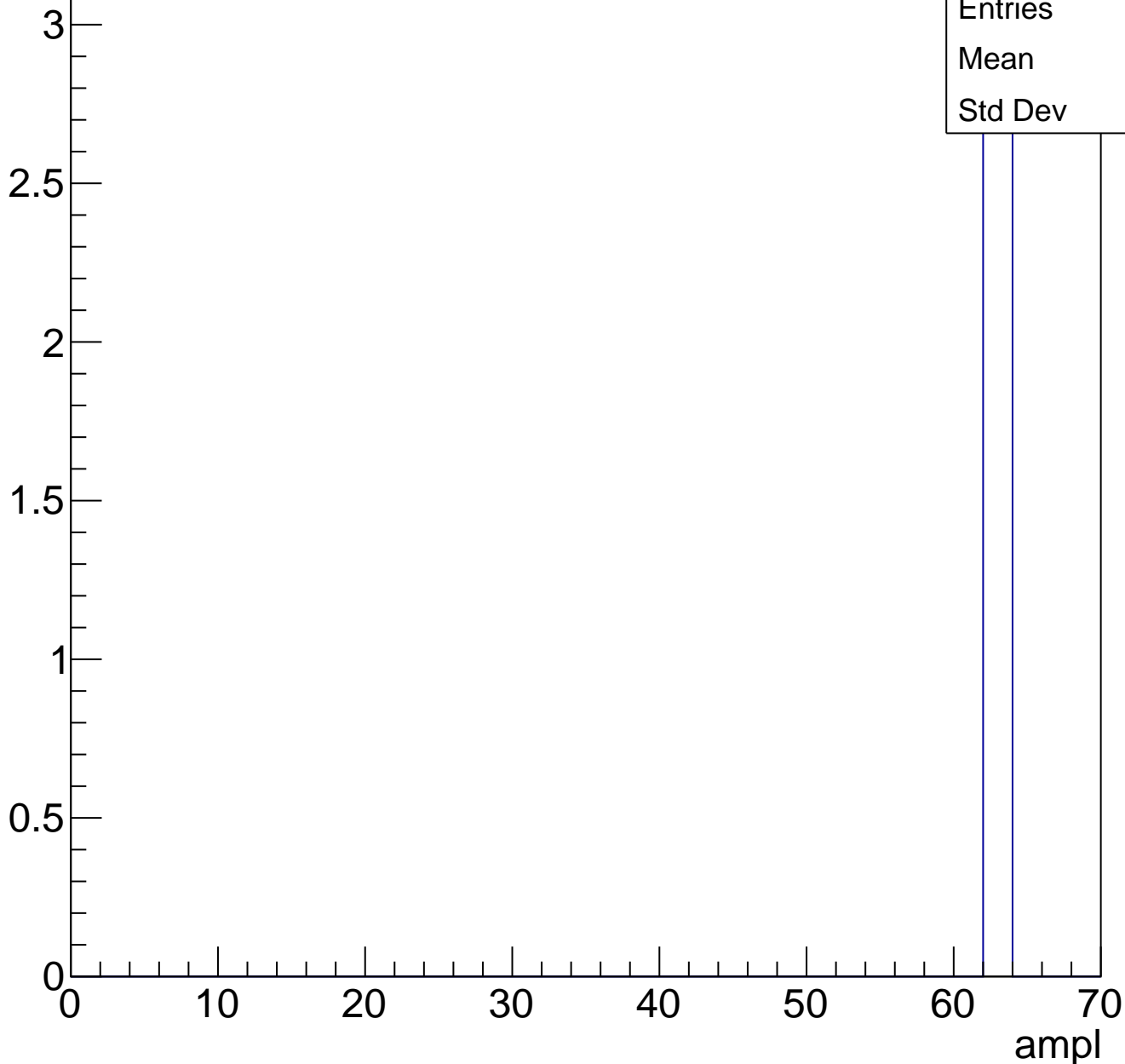
| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.57 |
| Std Dev | 9.208 |



B1L100S, U9-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch59, adc0

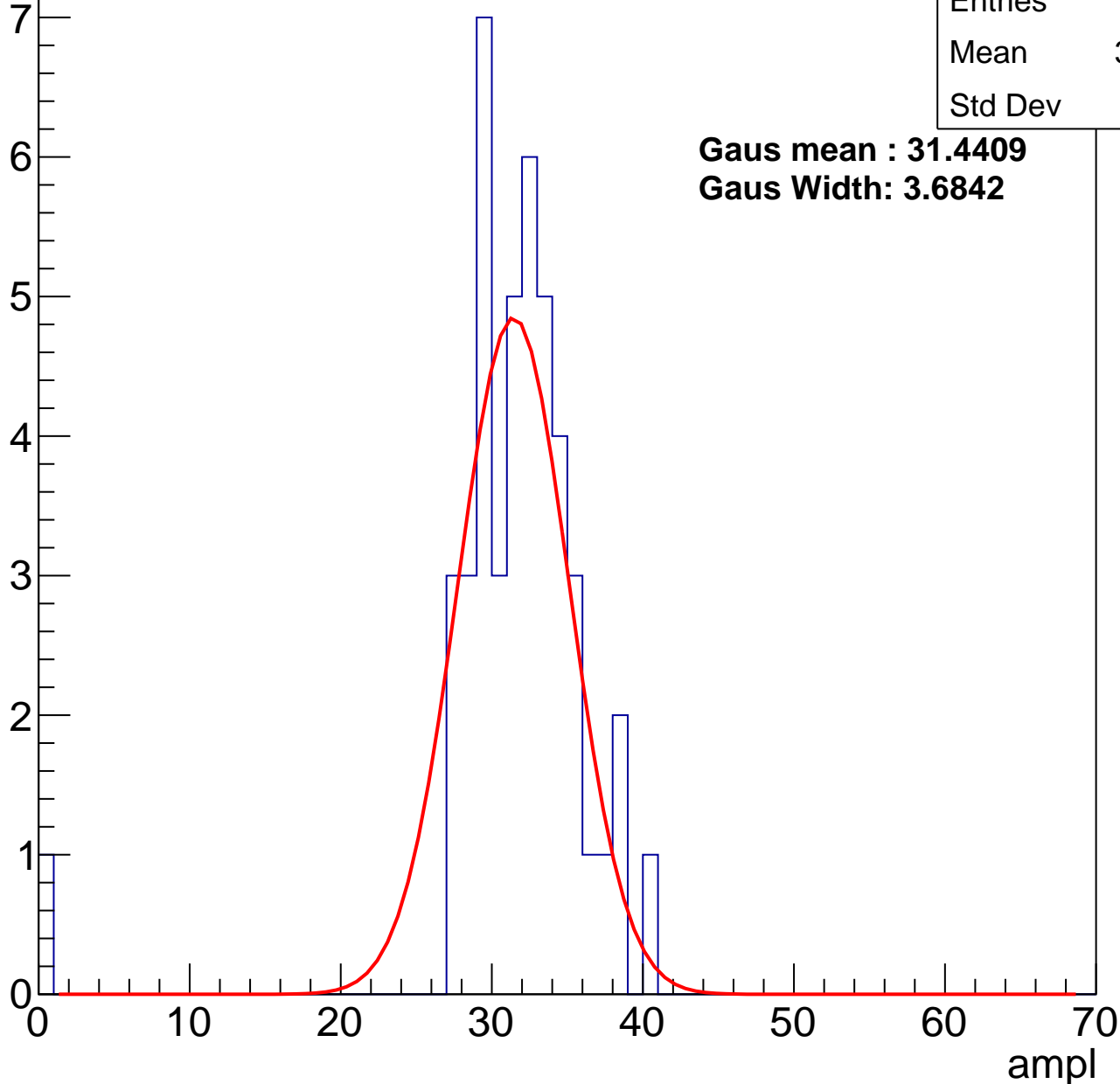
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 31.11 |
| Std Dev | 5.61 |

Gaus mean : 31.4409

Gaus Width: 3.6842



B1L100S, U9-ch59, adc1

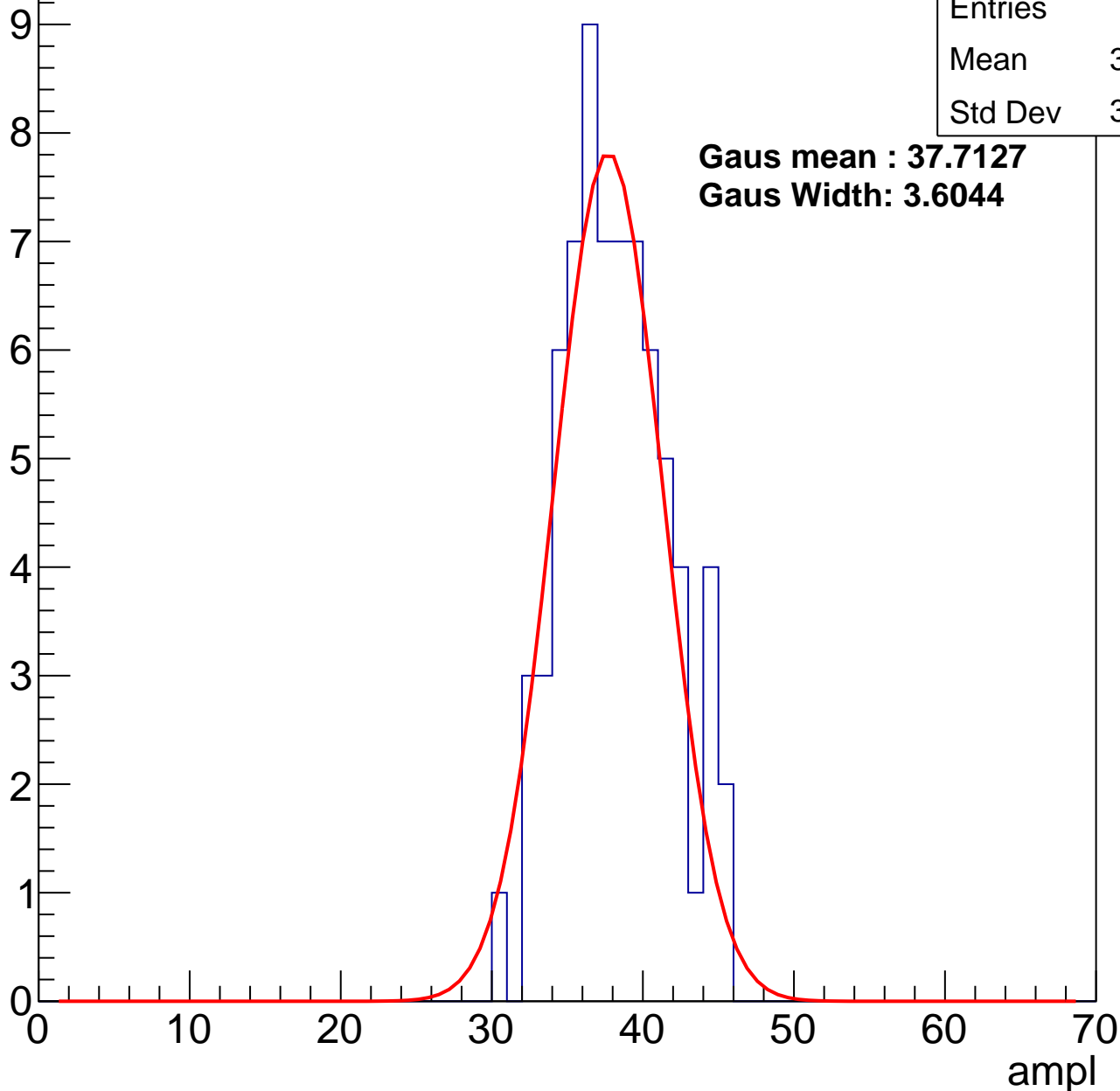
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.75 |
| Std Dev | 3.439 |

Gaus mean : 37.7127

Gaus Width: 3.6044



B1L100S, U9-ch59, adc2

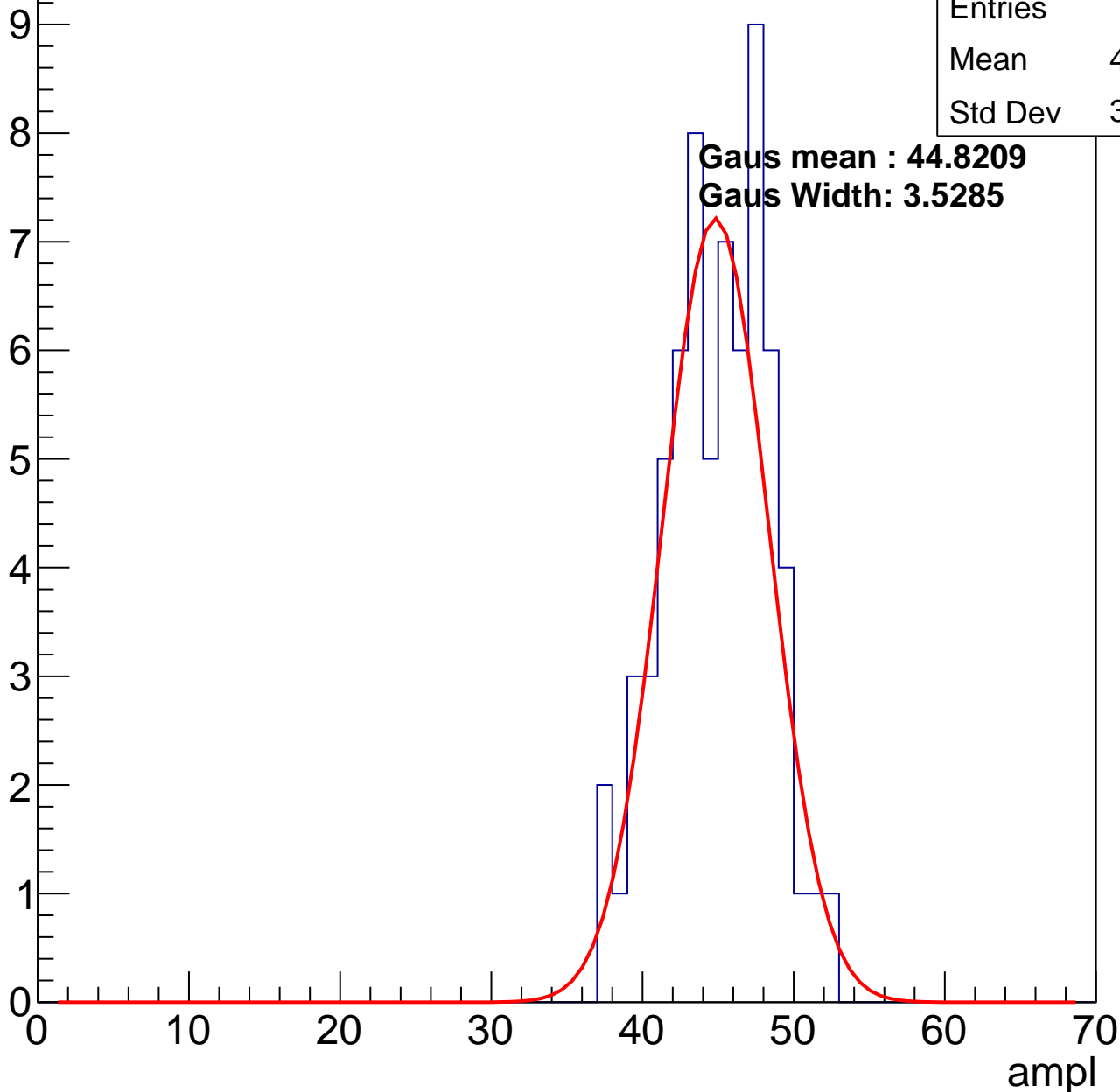
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 44.43 |
| Std Dev | 3.384 |

Gaus mean : 44.8209

Gaus Width: 3.5285

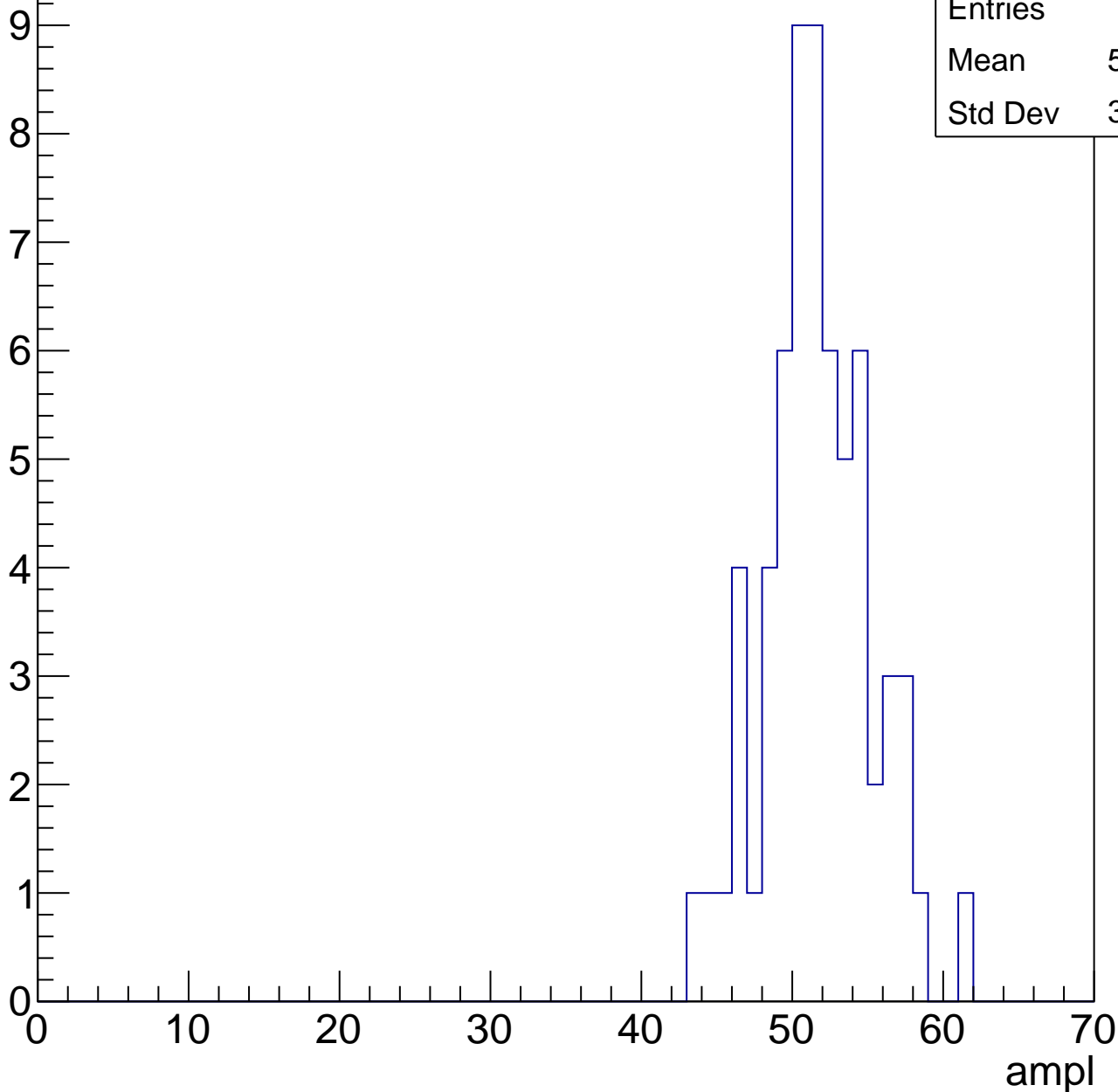


B1L100S, U9-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 51.22 |
| Std Dev | 3.507 |



B1L100S, U9-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 60 |
| Mean | 57.18 |
| Std Dev | 2.907 |

0

1

2

3

4

5

6

7

8

9

10

11

12

13

14

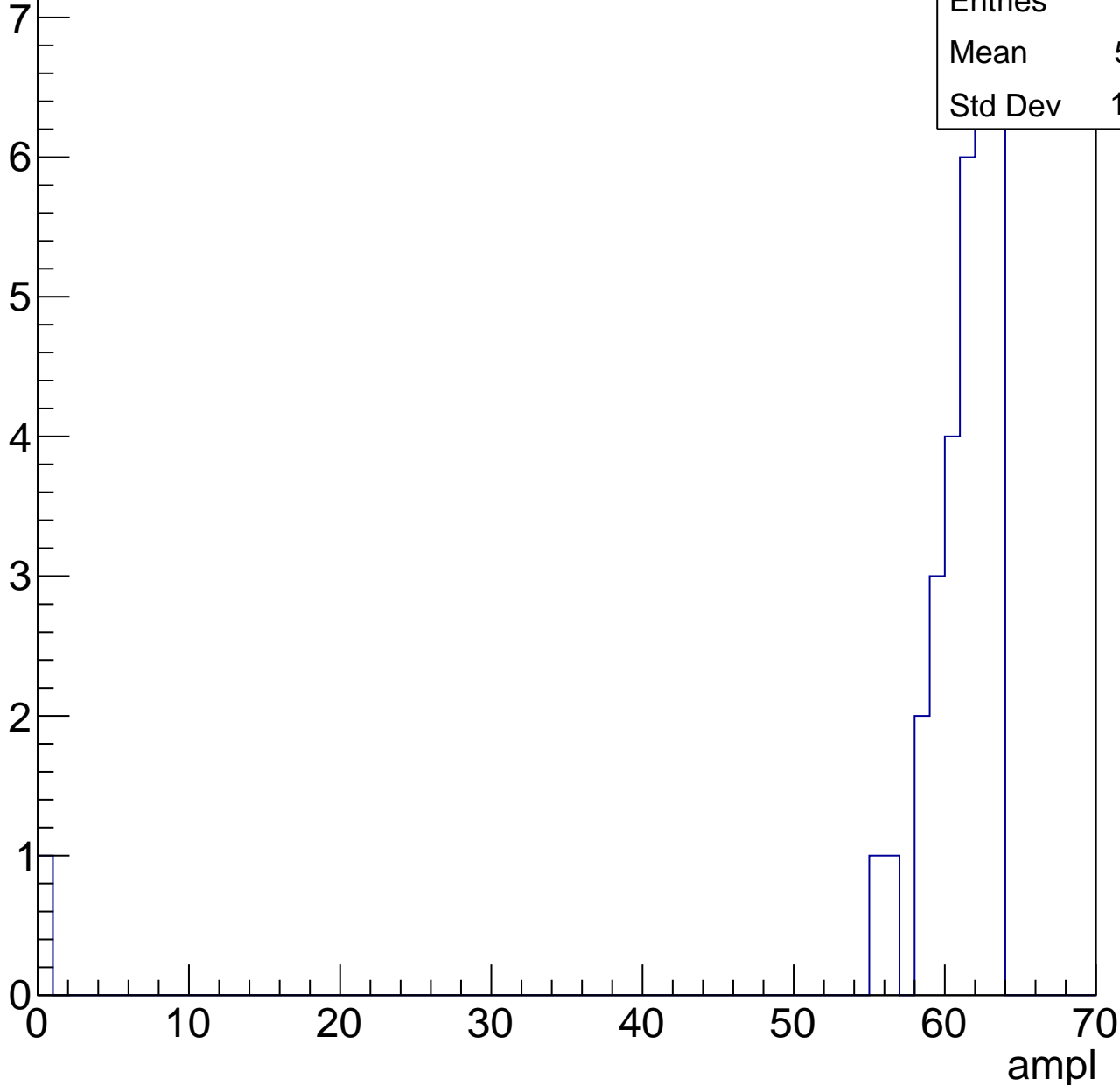
15

B1L100S, U9-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.91 |
| Std Dev | 10.77 |



B1L100S, U9-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 12.5 |
| Std Dev | 12.5 |

B1L100S, U9-ch60, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 28.56 |
| Std Dev | 3.492 |

Gaus mean : 28.8130

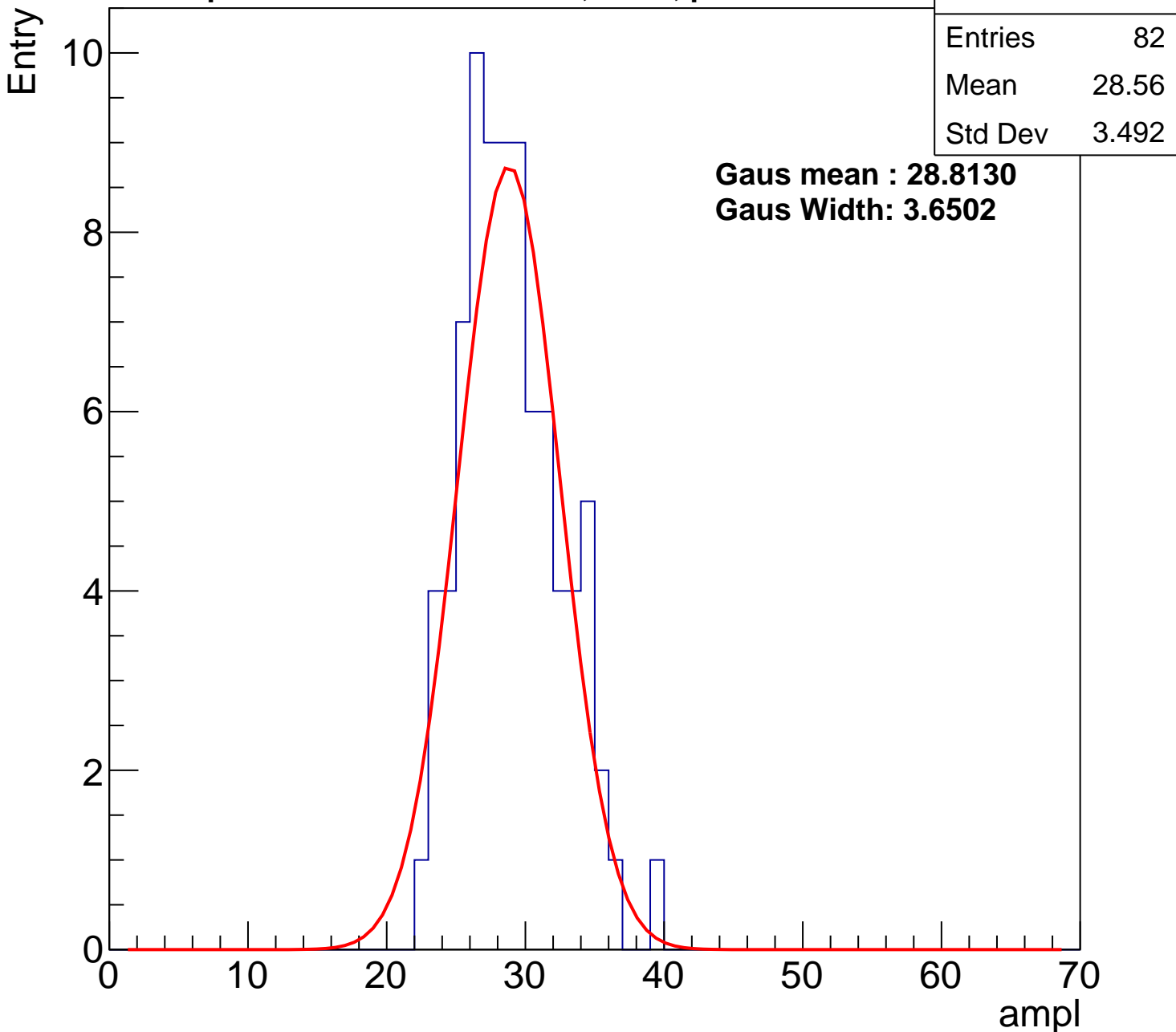
Gaus Width: 3.6502

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch60, adc1

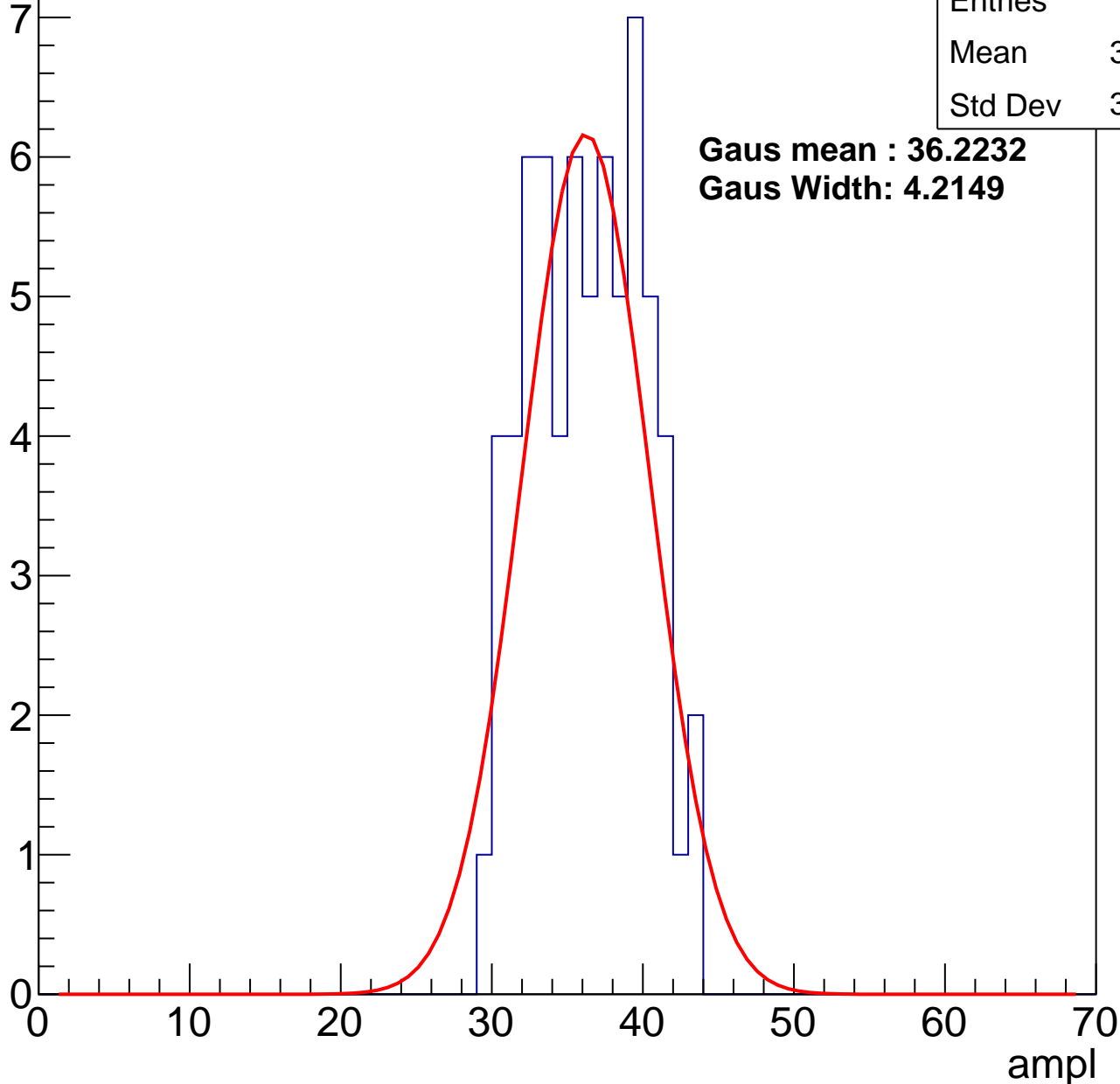
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 35.85 |
| Std Dev | 3.623 |

Gaus mean : 36.2232

Gaus Width: 4.2149



B1L100S, U9-ch60, adc2

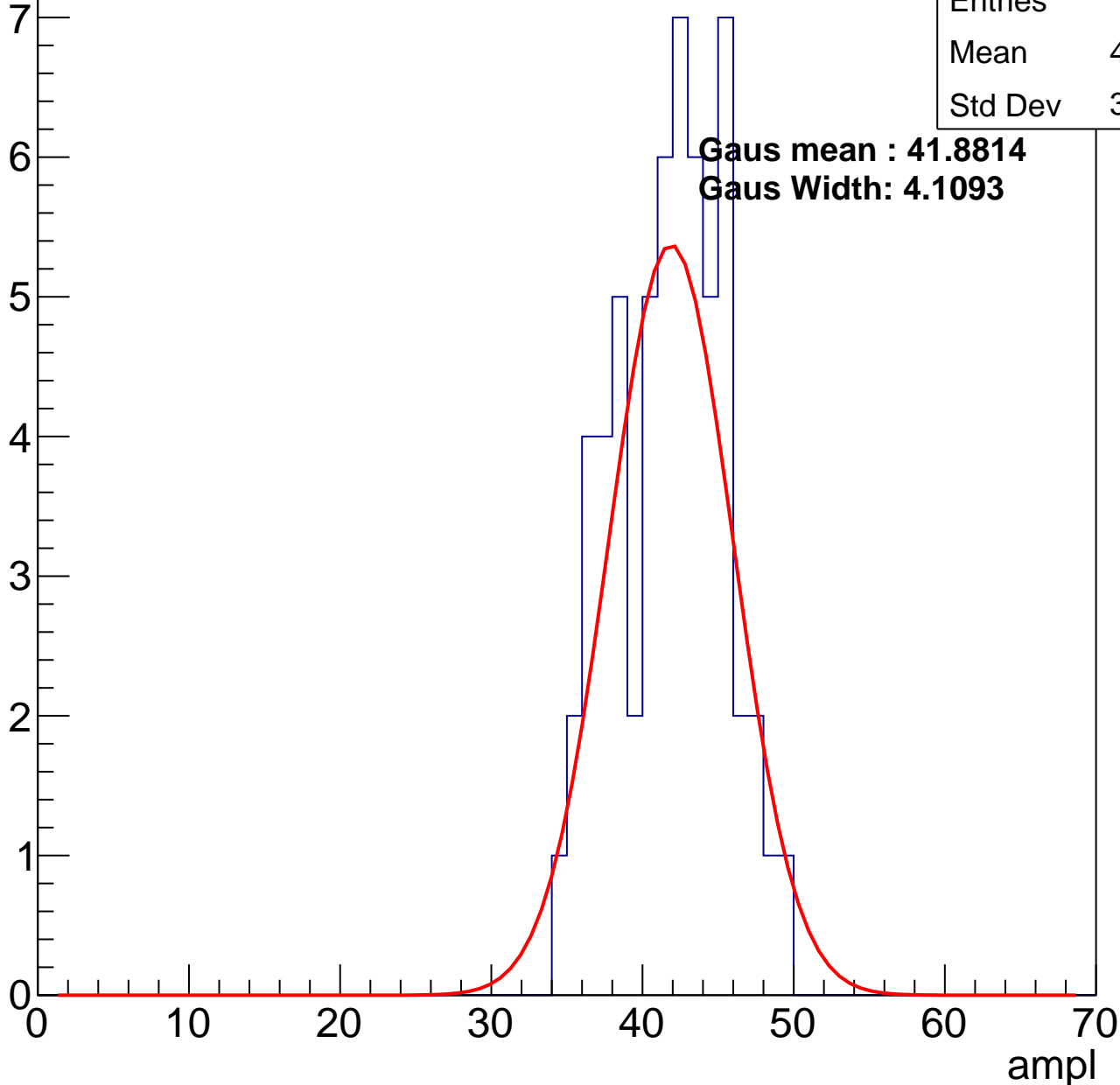
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 41.33 |
| Std Dev | 3.557 |

Gaus mean : 41.8814

Gaus Width: 4.1093

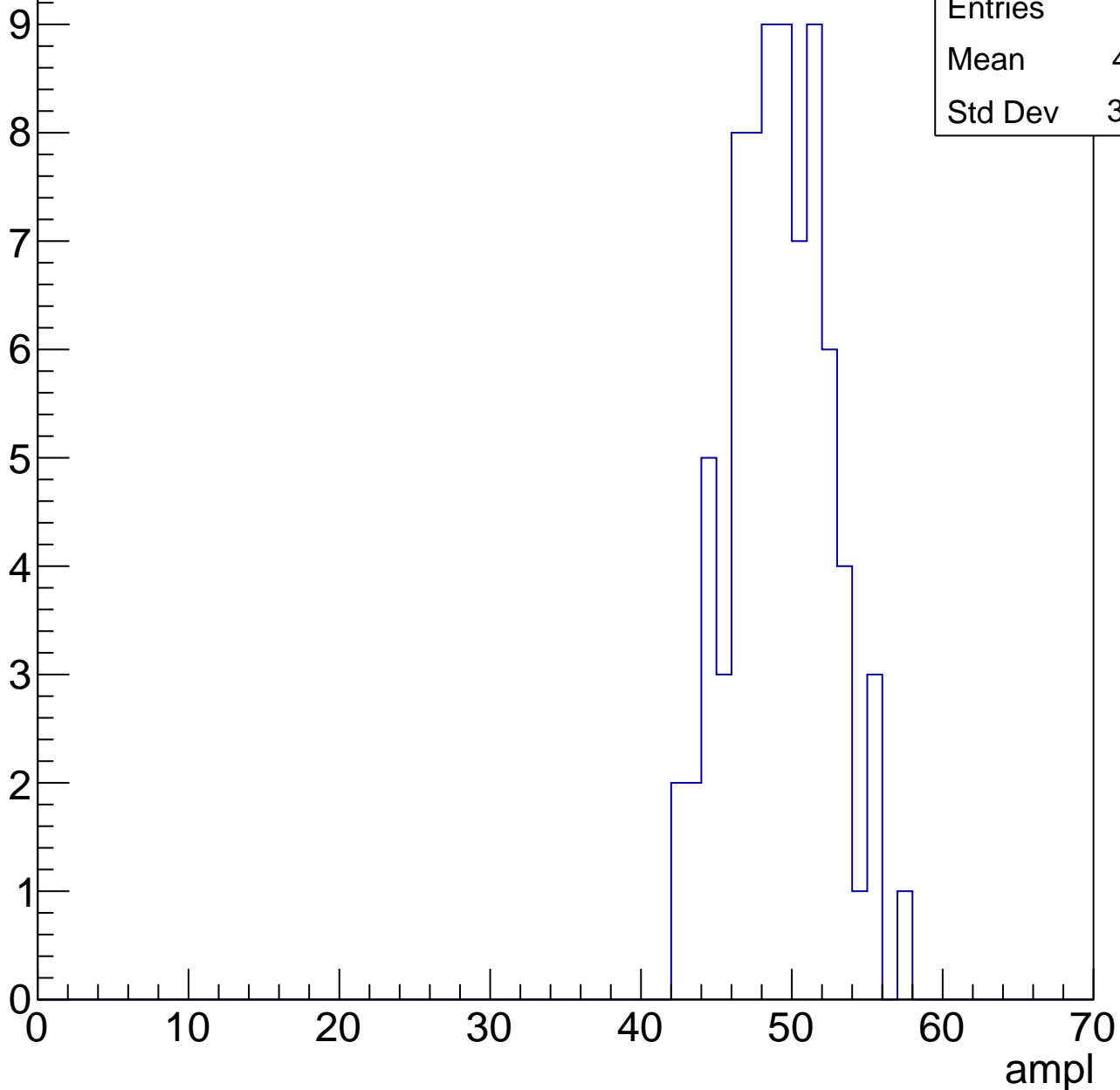


B1L100S, U9-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

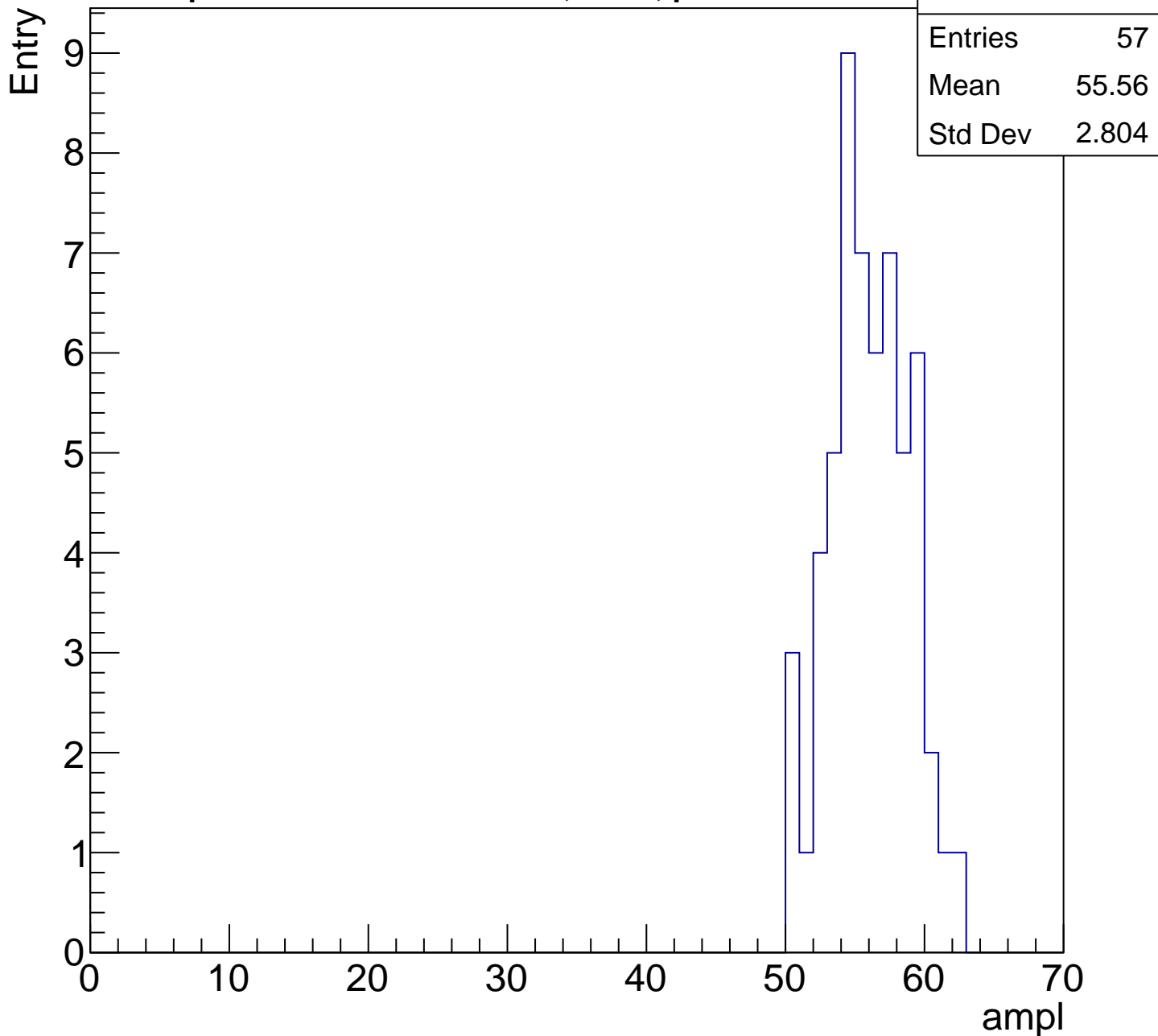
Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 48.71 |
| Std Dev | 3.239 |



B1L100S, U9-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

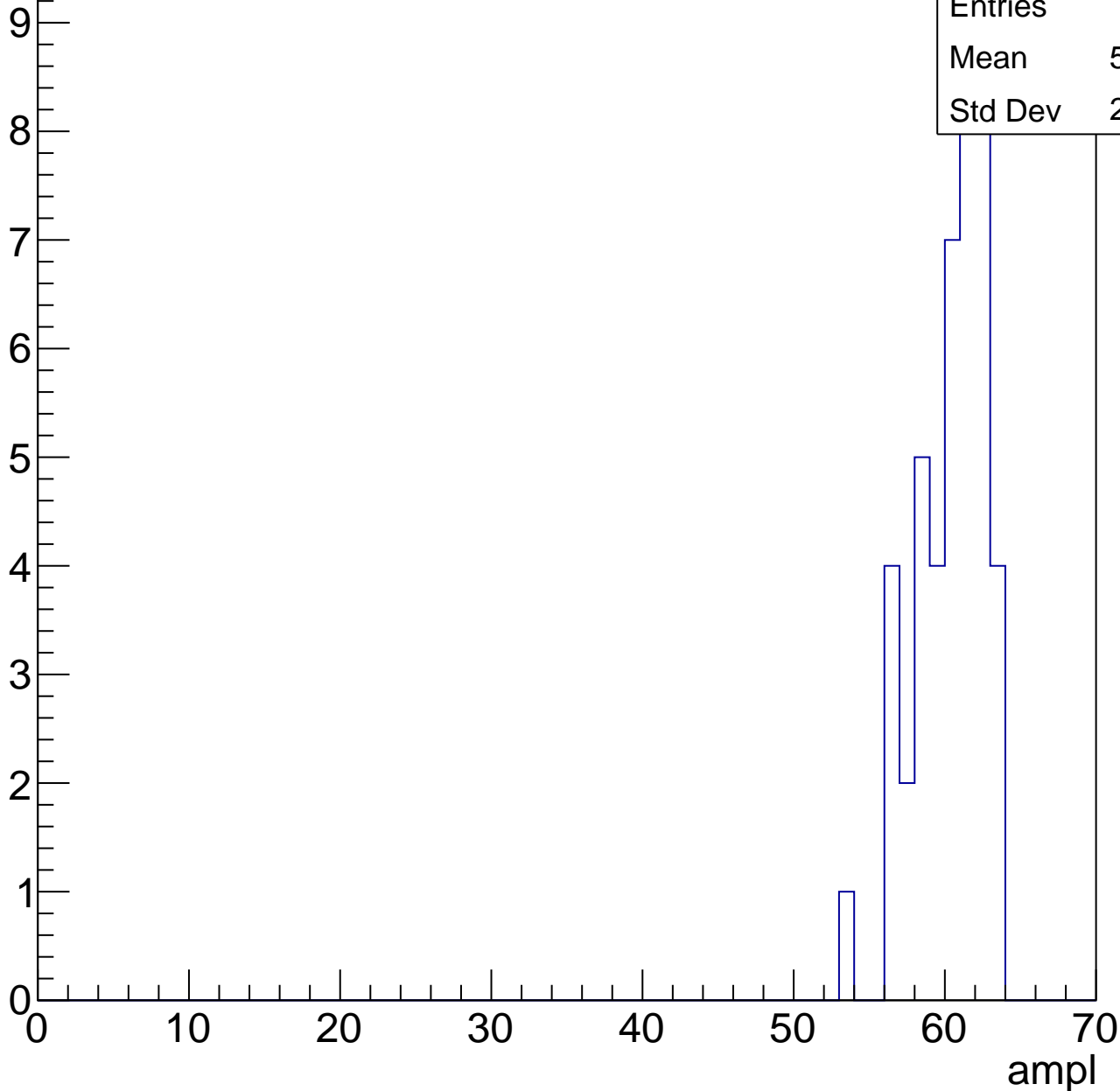


B1L100S, U9-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.89 |
| Std Dev | 2.308 |

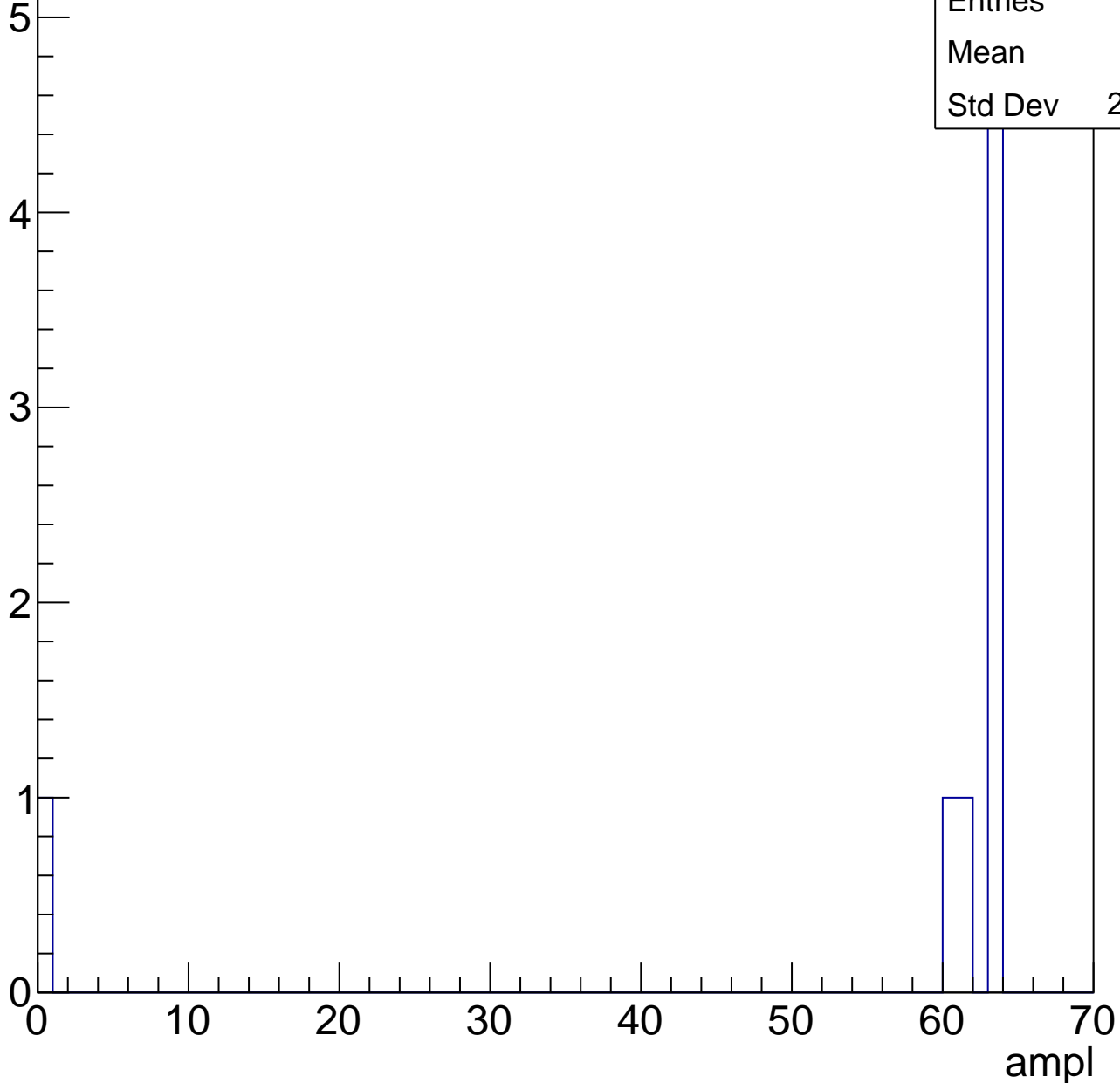


B1L100S, U9-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54.5 |
| Std Dev | 20.63 |



B1L100S, U9-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch61, adc0

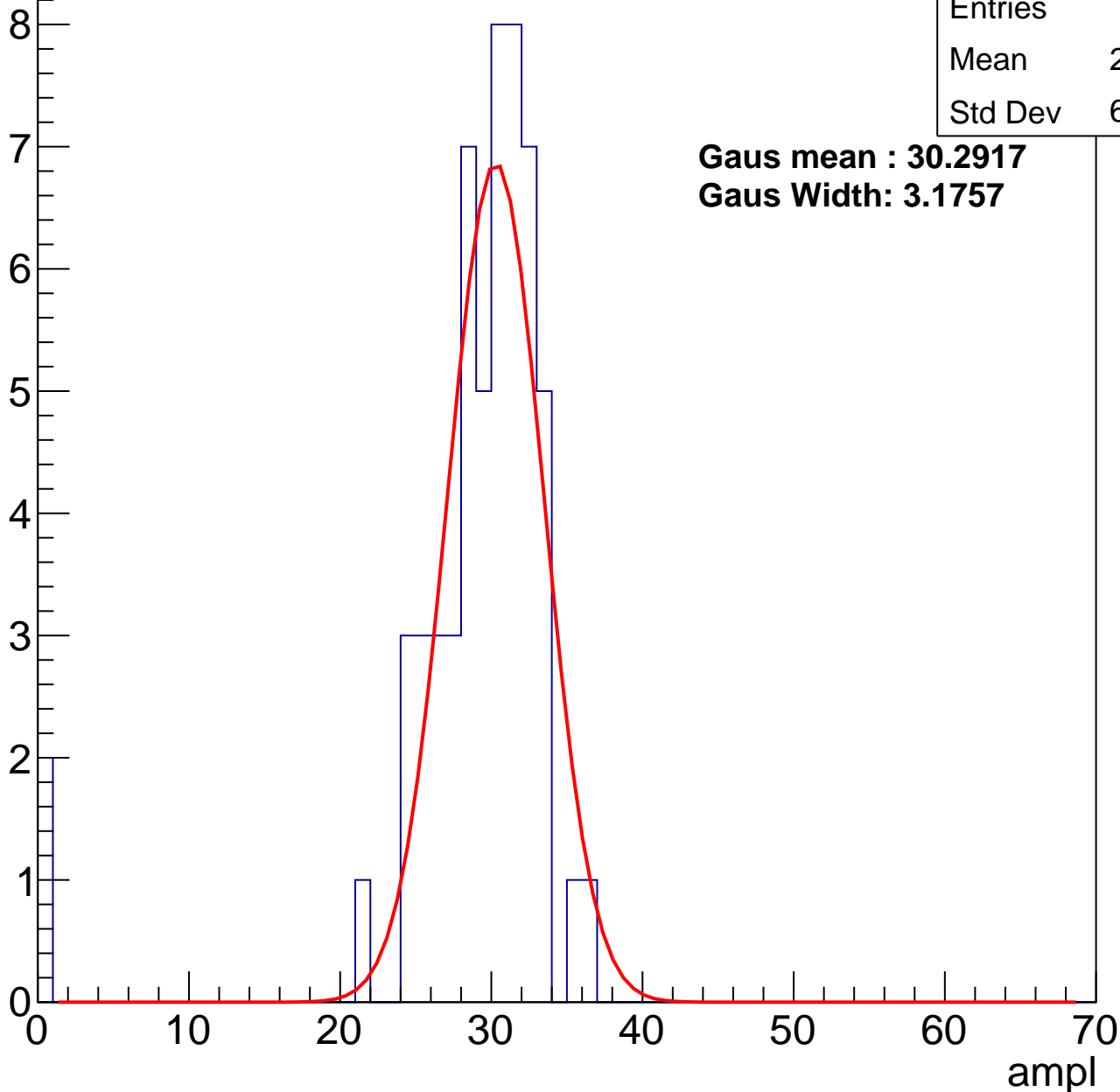
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 28.35 |
| Std Dev | 6.154 |

Gaus mean : 30.2917

Gaus Width: 3.1757



B1L100S, U9-ch61, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.81 |
| Std Dev | 3.104 |

Gaus mean : 36.7029

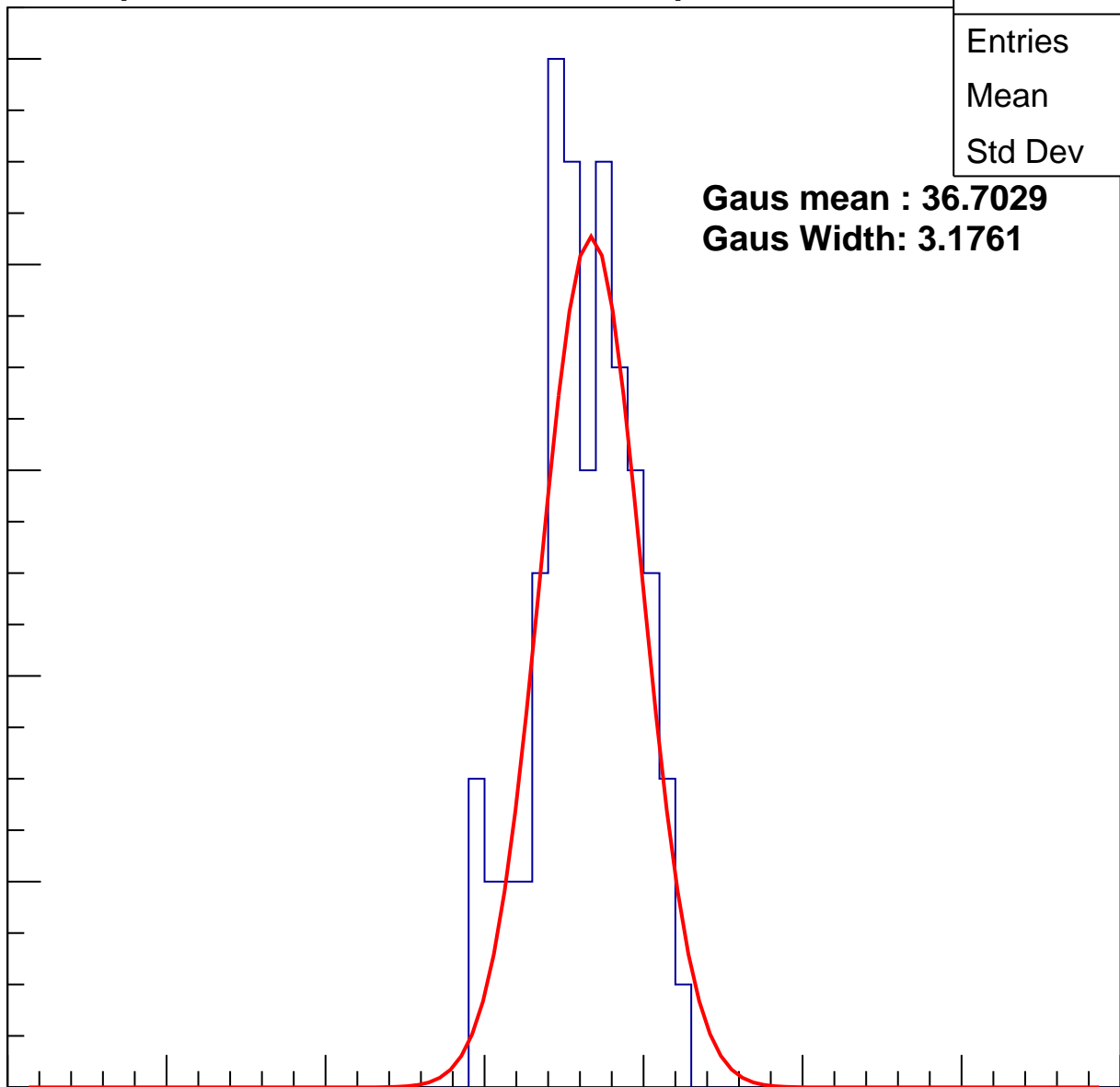
Gaus Width: 3.1761

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch61, adc2

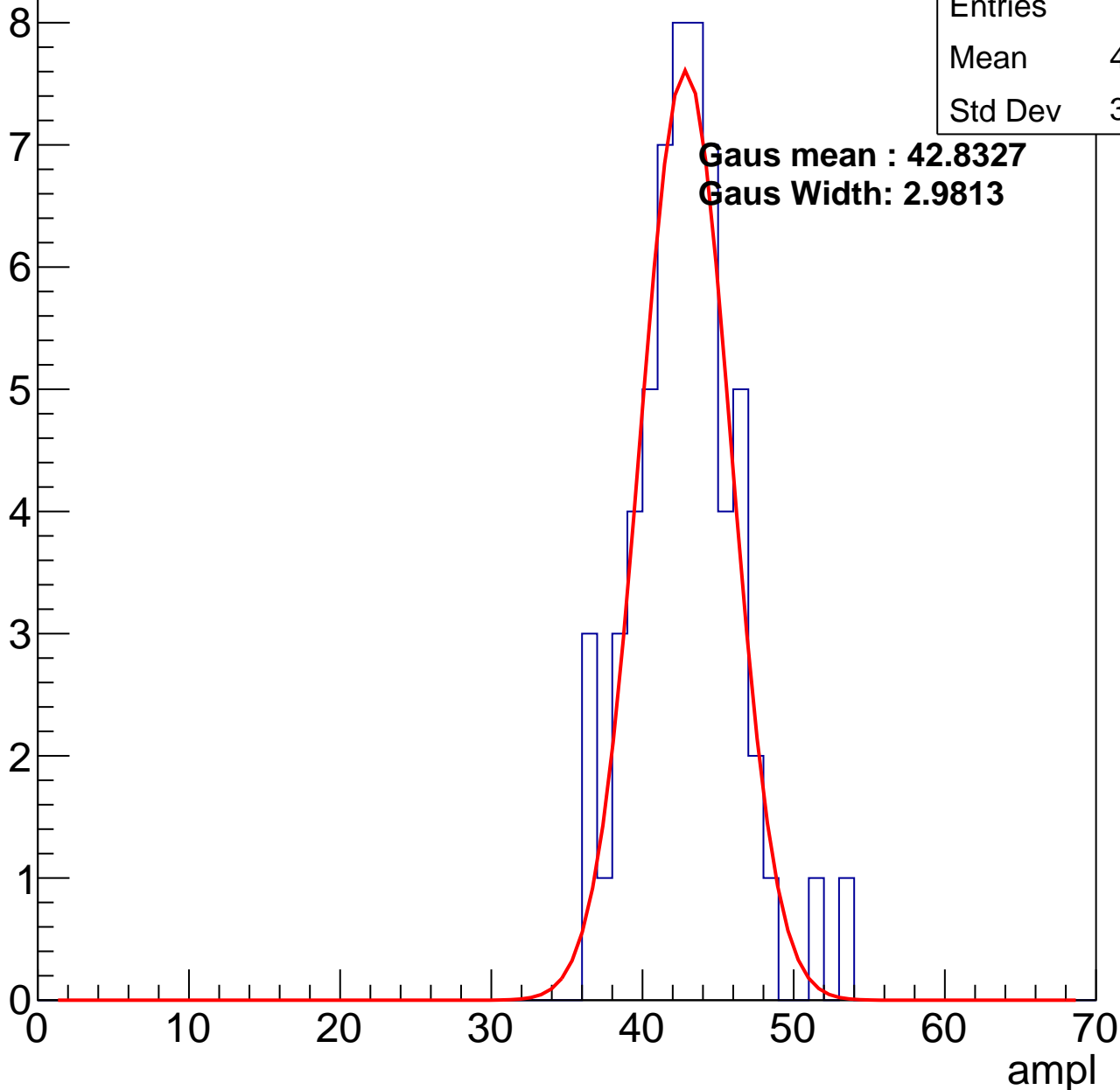
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.43 |
| Std Dev | 3.348 |

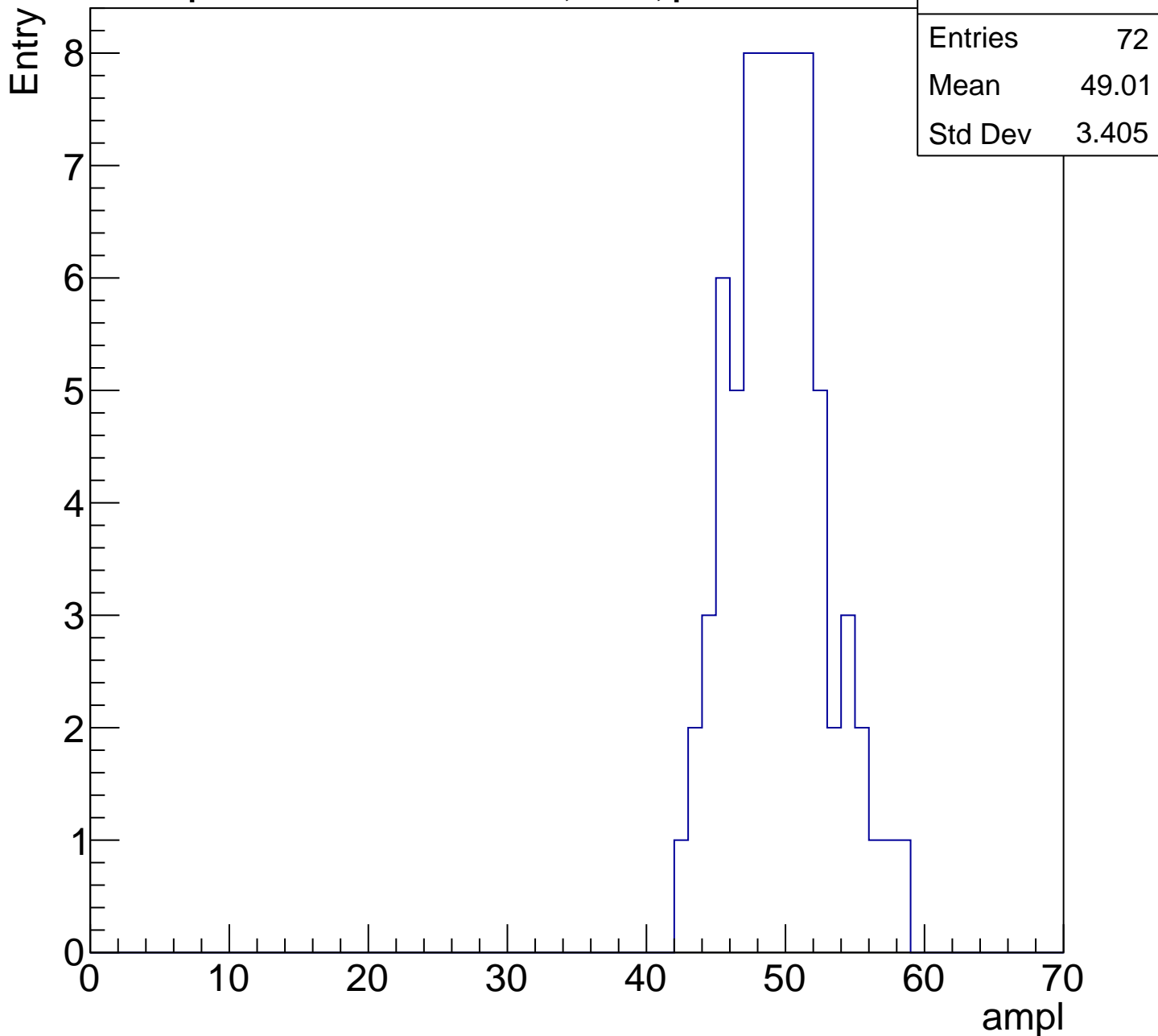
Gaus mean : 42.8327

Gaus Width: 2.9813



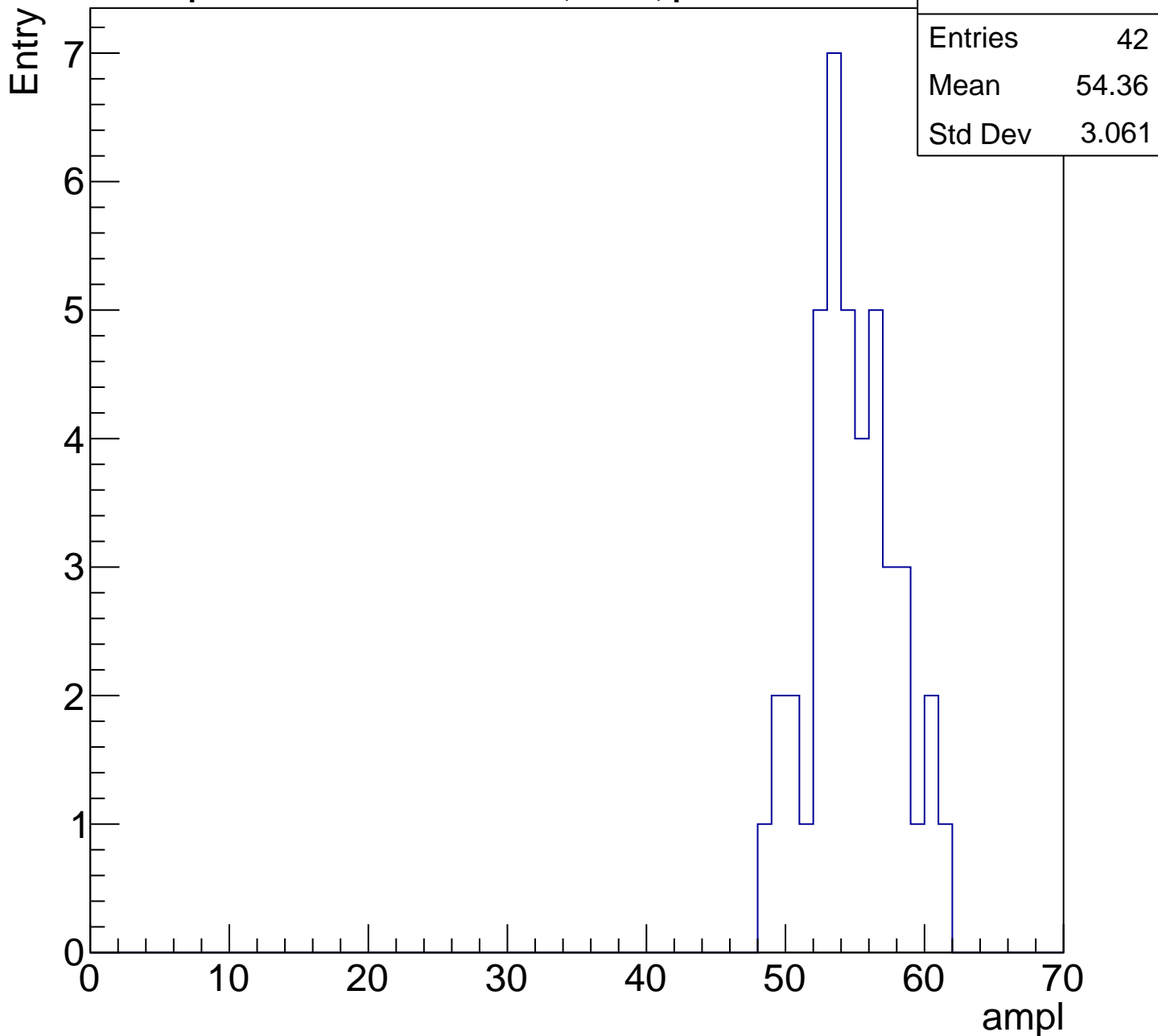
B1L100S, U9-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch61, adc5

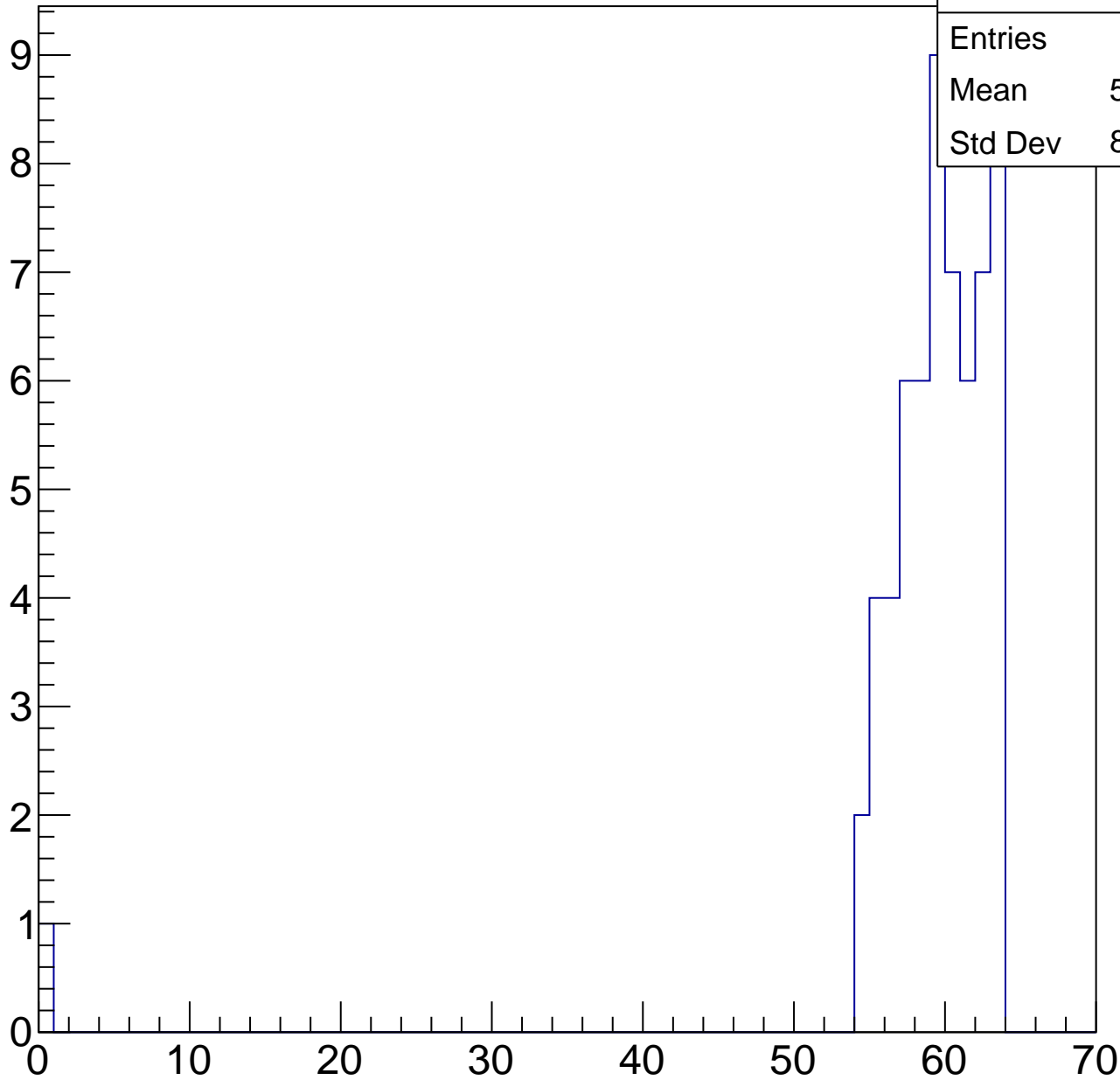
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 60 |
| Mean | 58.28 |
| Std Dev | 8.013 |

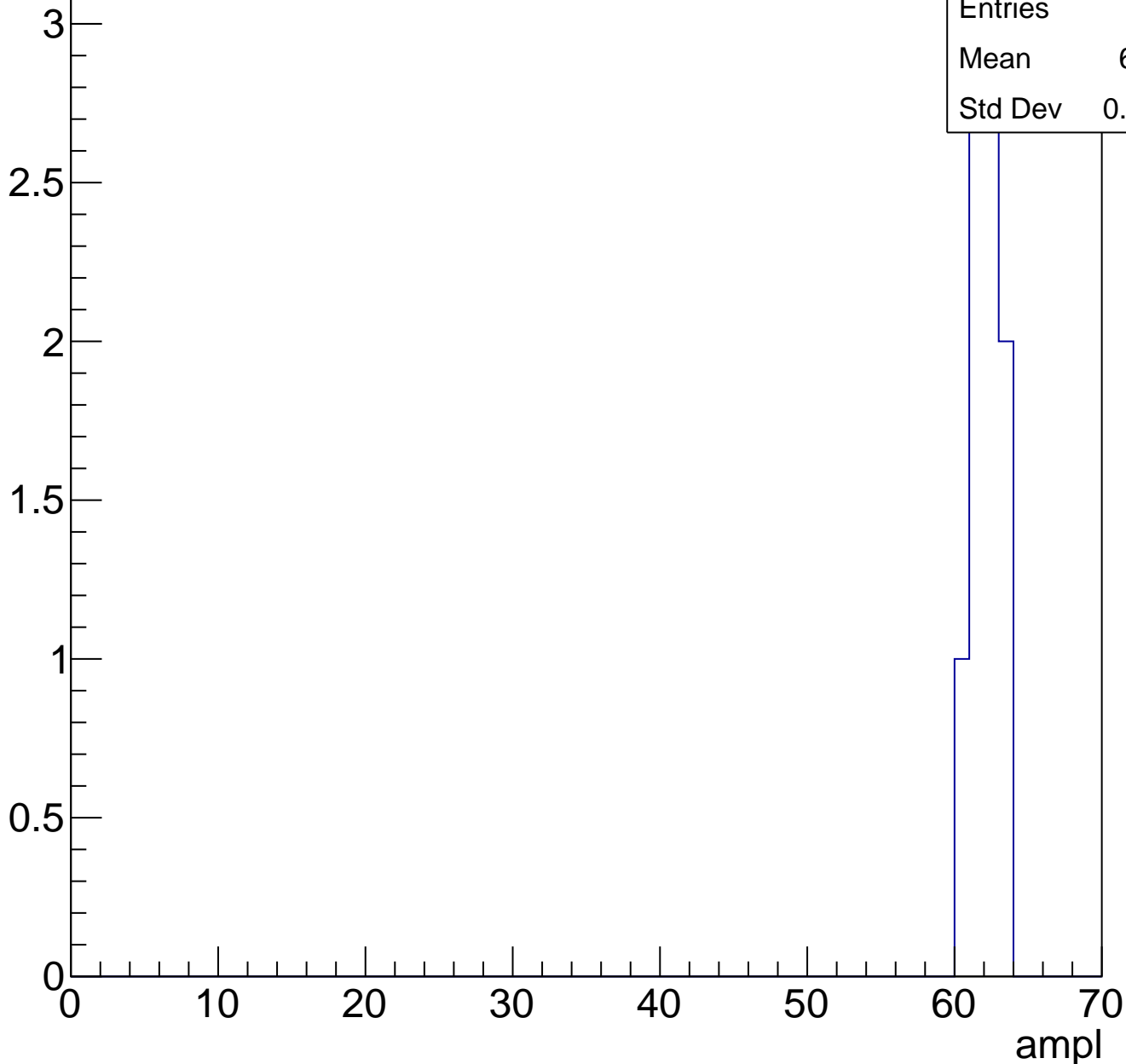
ampl



B1L100S, U9-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch62, adc0

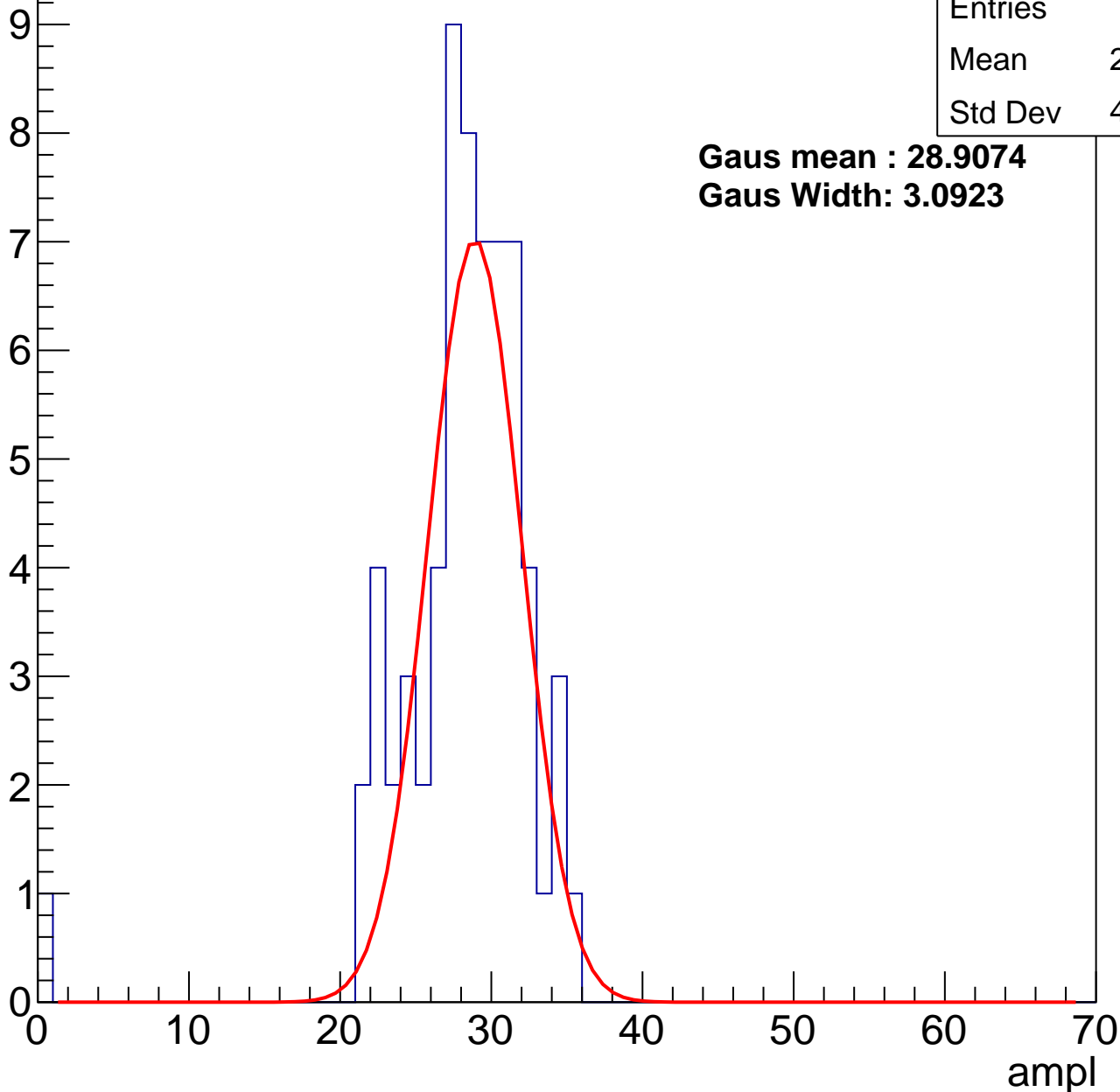
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 27.65 |
| Std Dev | 4.808 |

Gaus mean : 28.9074

Gaus Width: 3.0923



B1L100S, U9-ch62, adc1

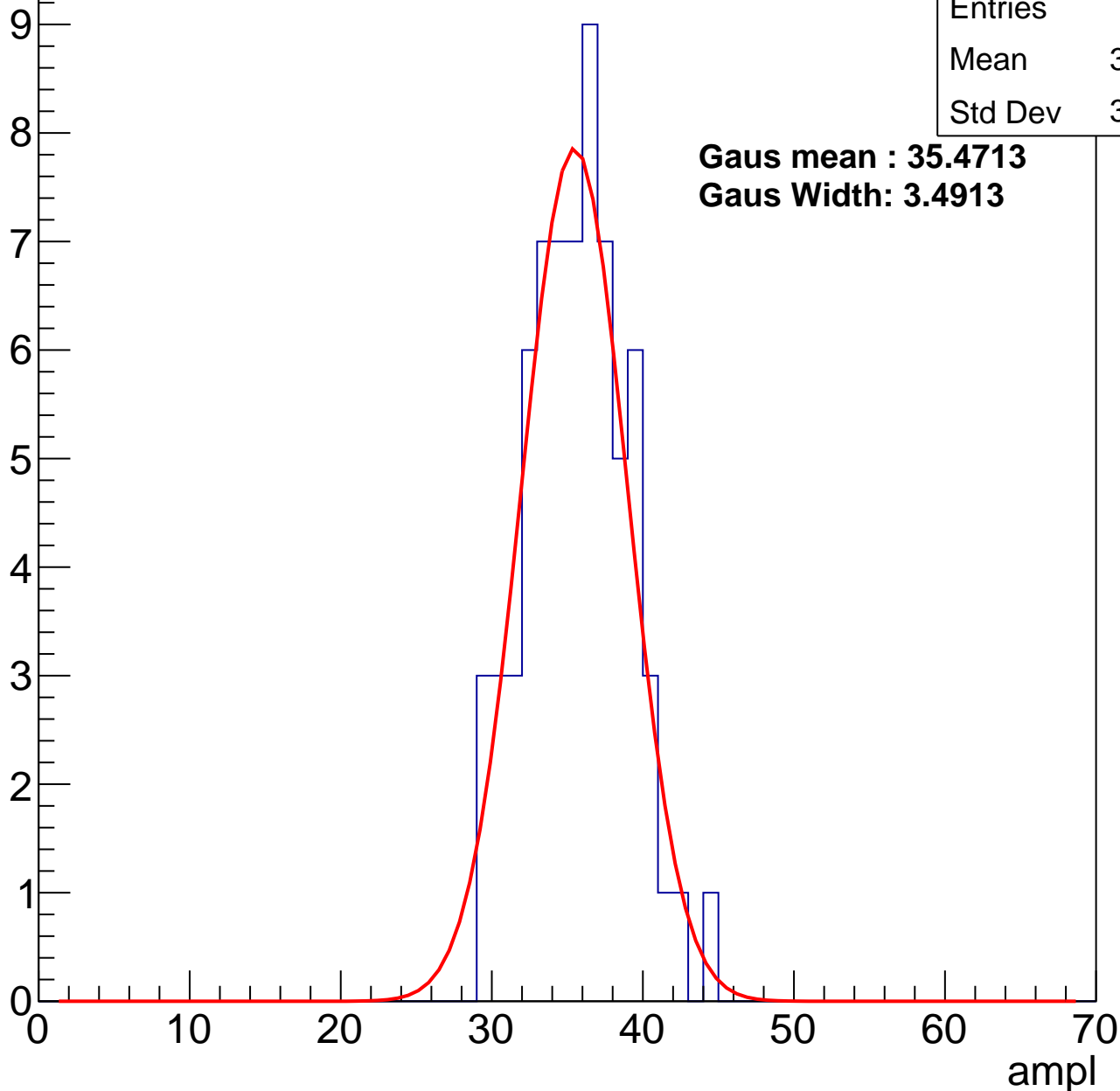
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.22 |
| Std Dev | 3.265 |

Gaus mean : 35.4713

Gaus Width: 3.4913



B1L100S, U9-ch62, adc2

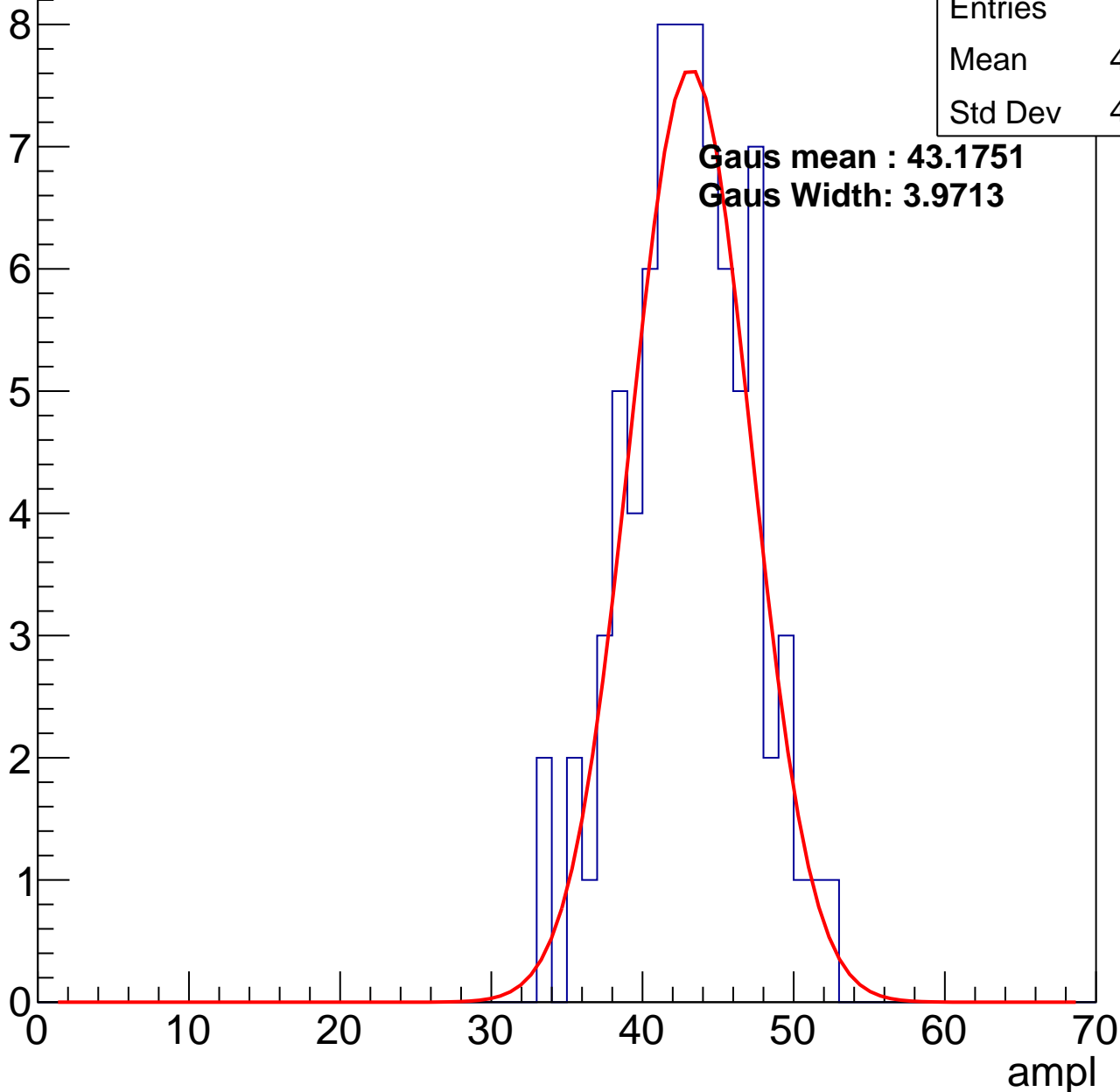
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 42.62 |
| Std Dev | 4.026 |

Gaus mean : 43.1751

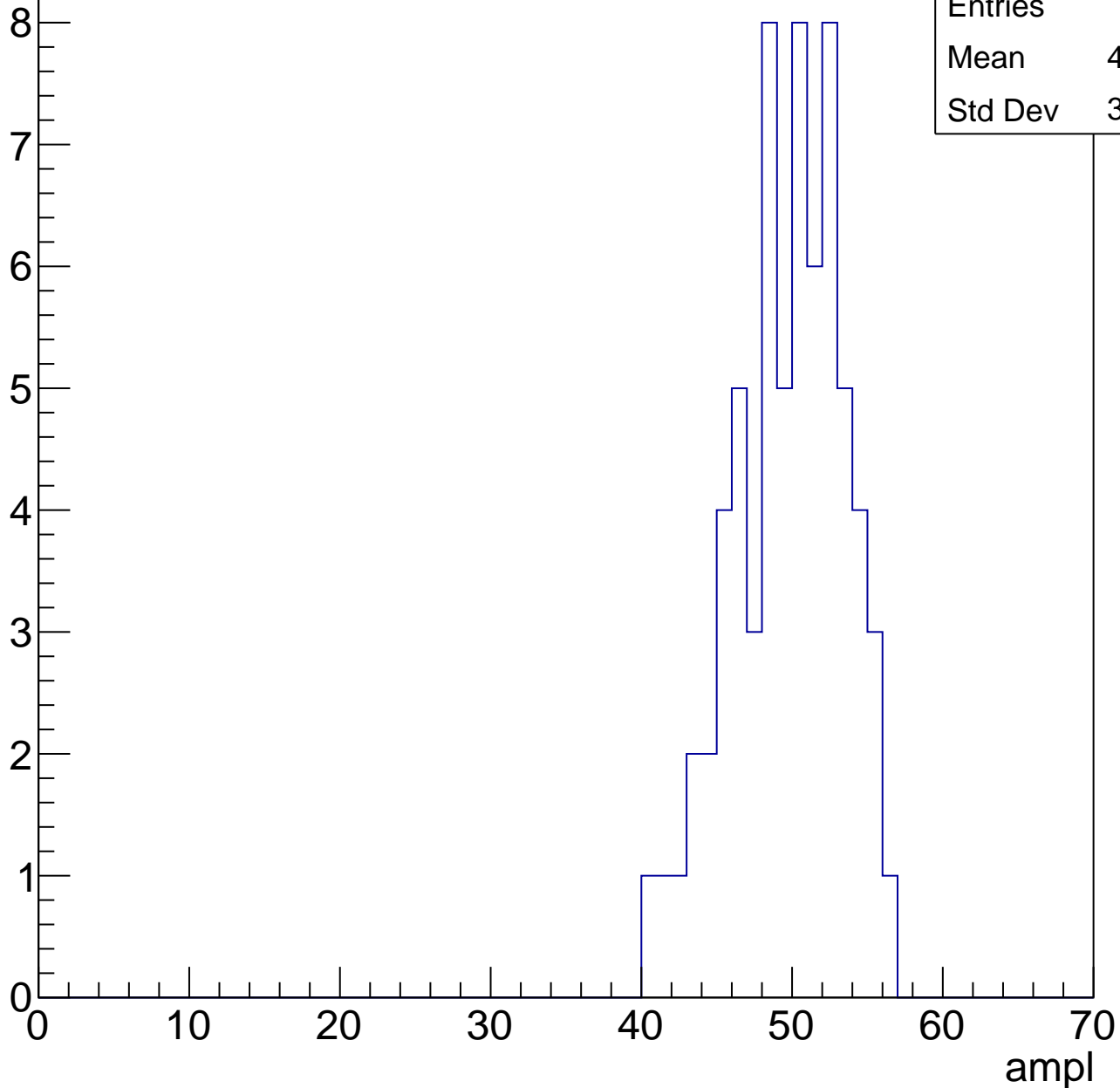
Gaus Width: 3.9713



B1L100S, U9-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

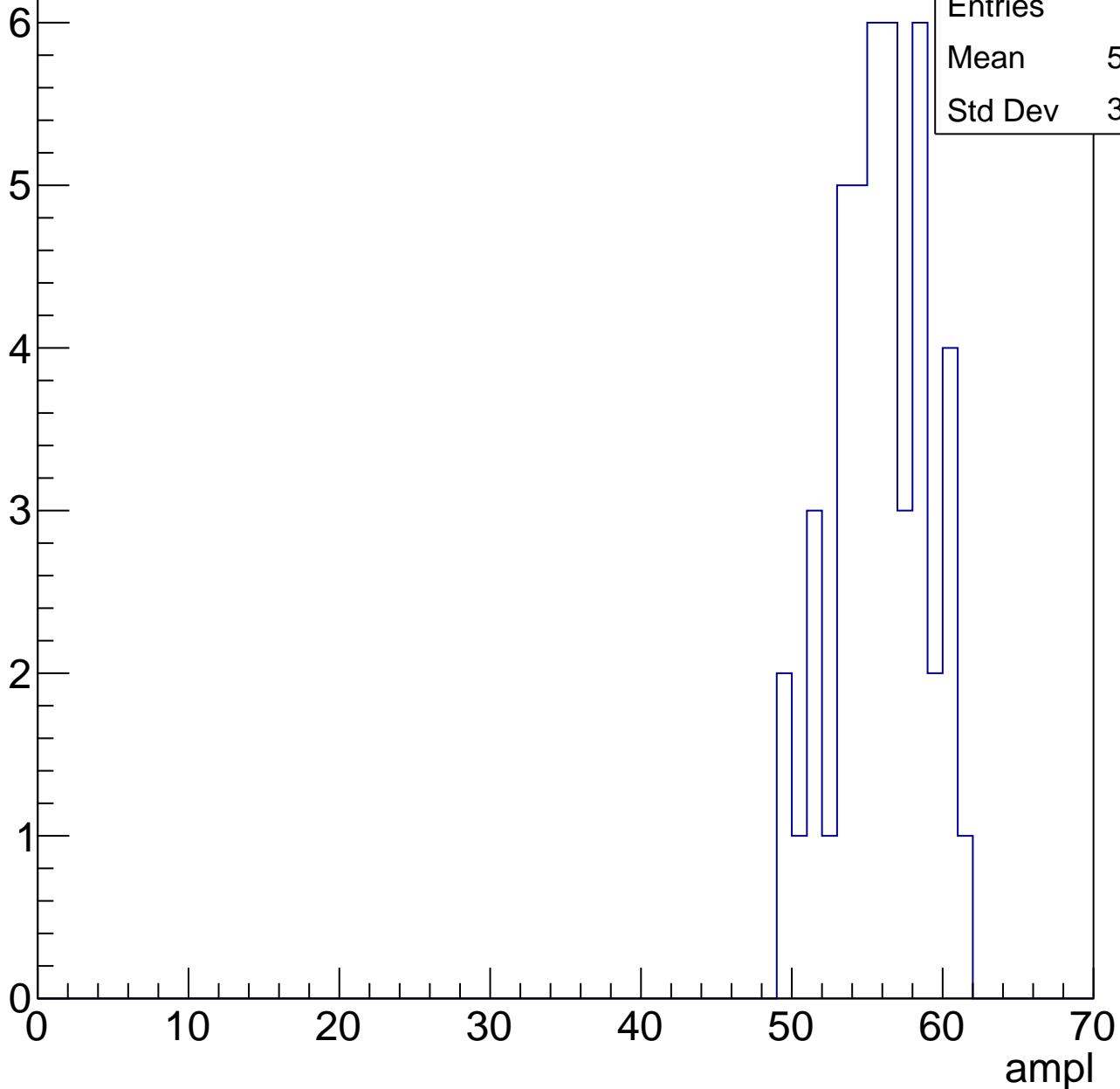


B1L100S, U9-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 55.38 |
| Std Dev | 3.028 |



B1L100S, U9-ch62, adc5

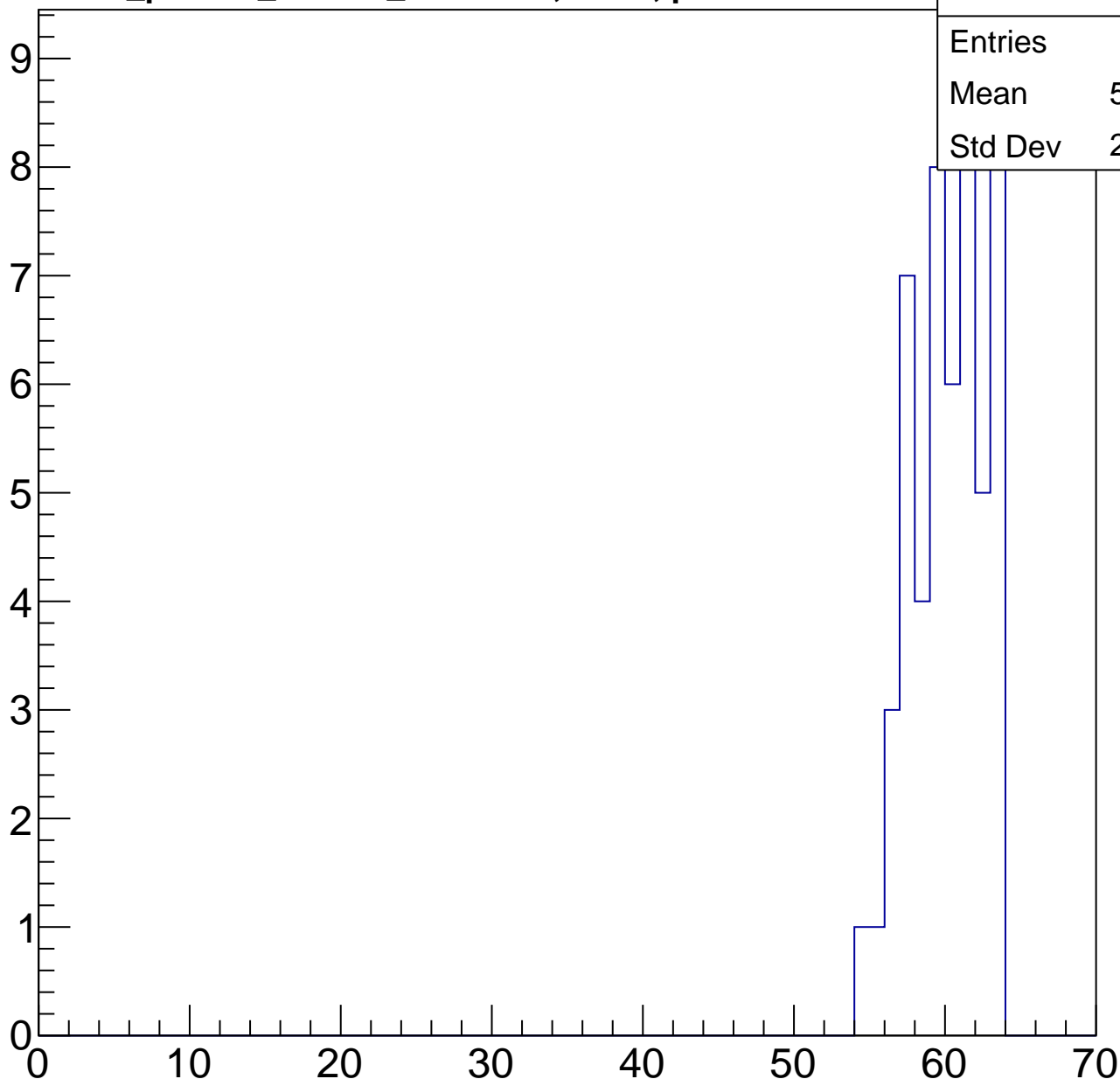
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.67 |
| Std Dev | 2.367 |

ampl

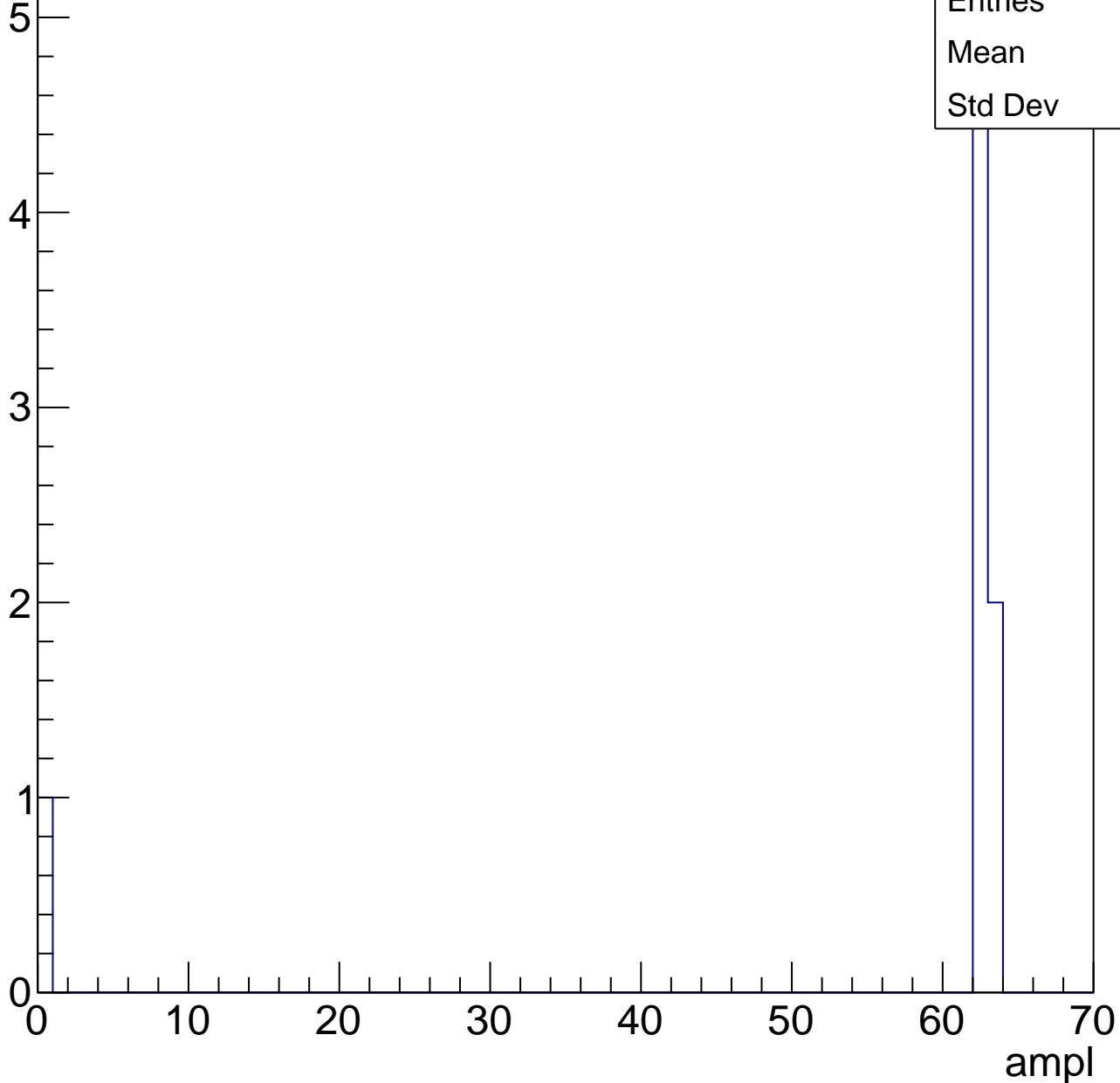


B1L100S, U9-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

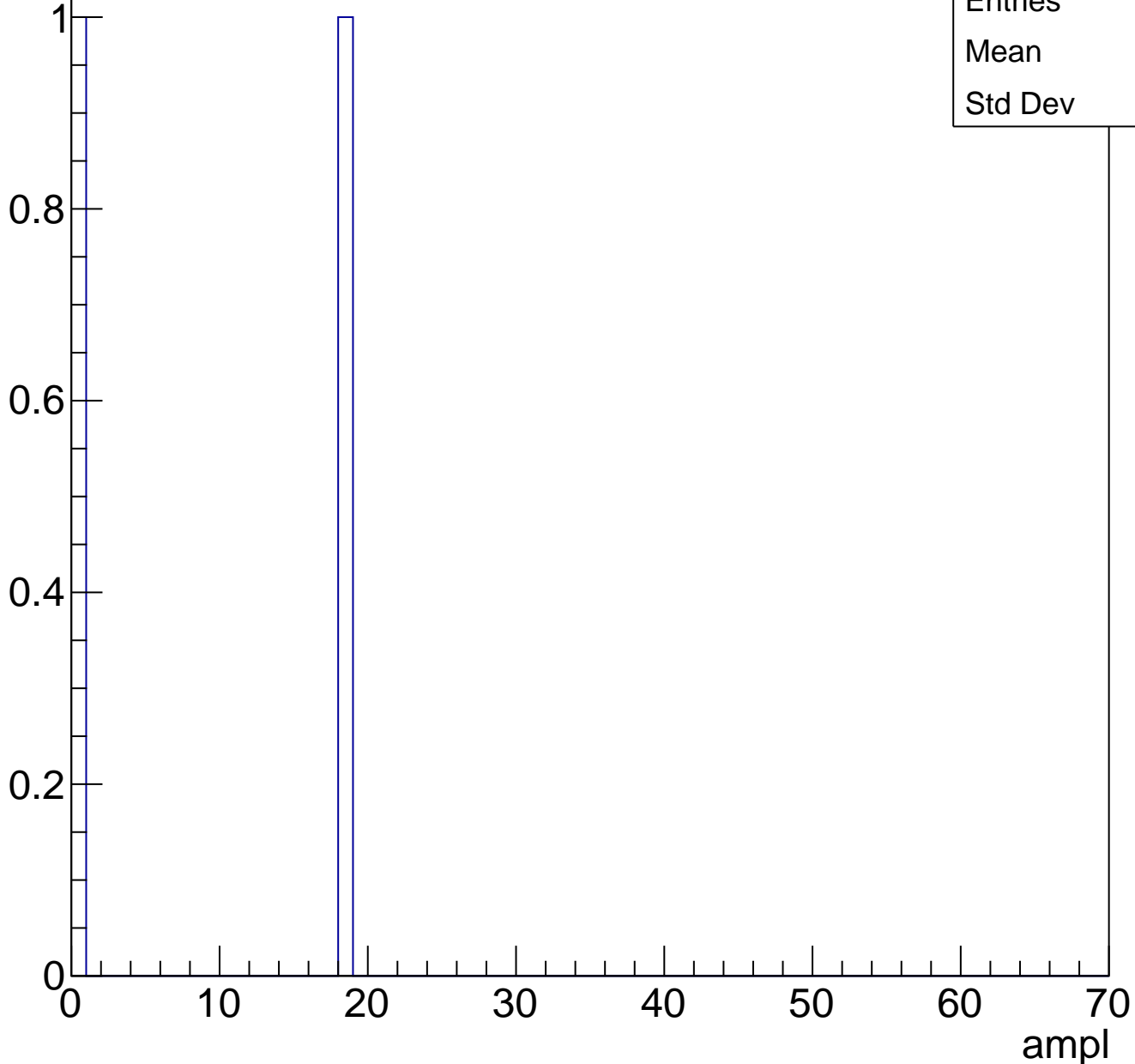
| | |
|---------|------|
| Entries | 8 |
| Mean | 54.5 |
| Std Dev | 20.6 |



B1L100S, U9-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 9 |
| Std Dev | 9 |

B1L100S, U9-ch63, adc0

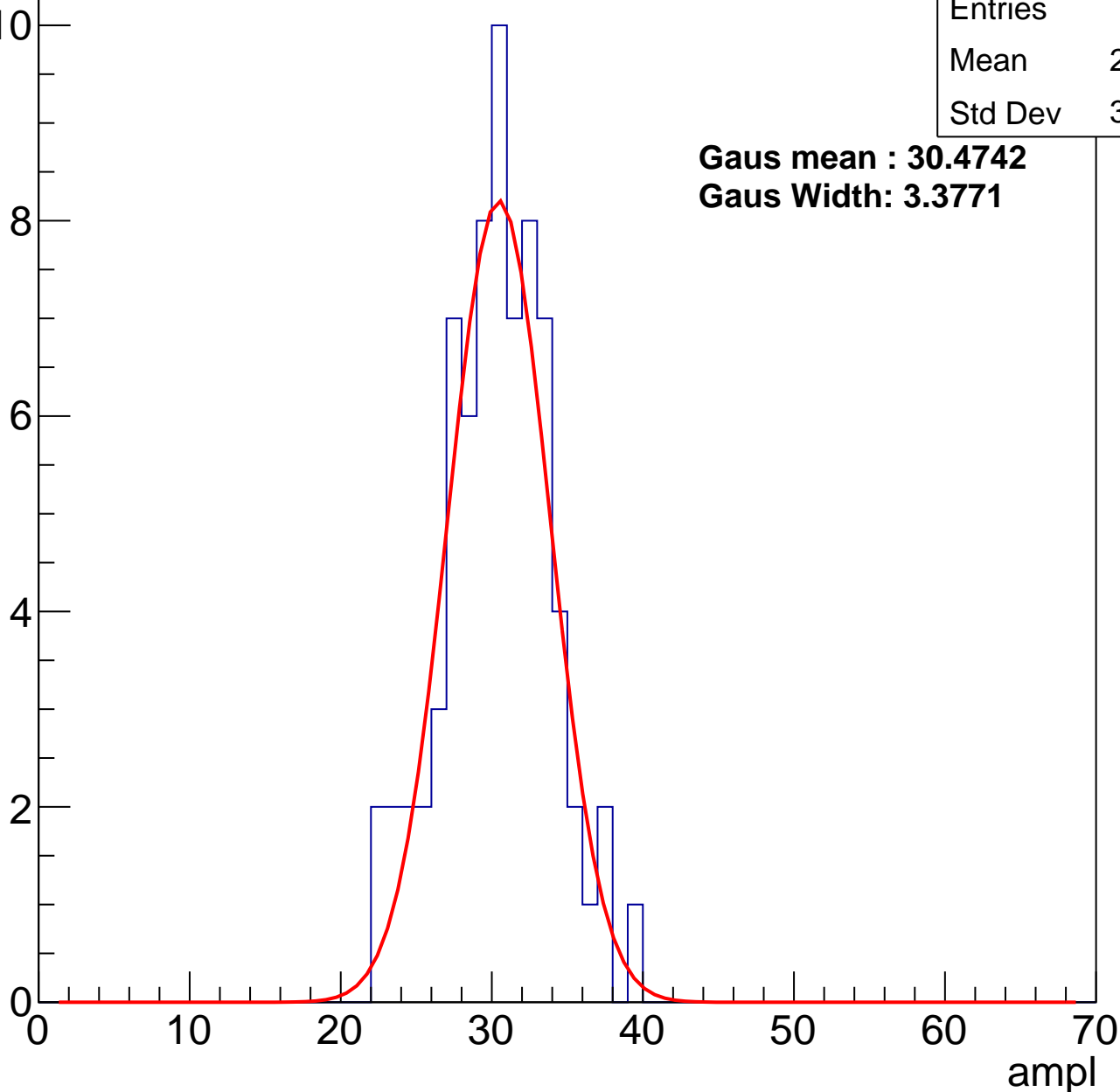
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.92 |
| Std Dev | 3.529 |

Gaus mean : 30.4742

Gaus Width: 3.3771



B1L100S, U9-ch63, adc1

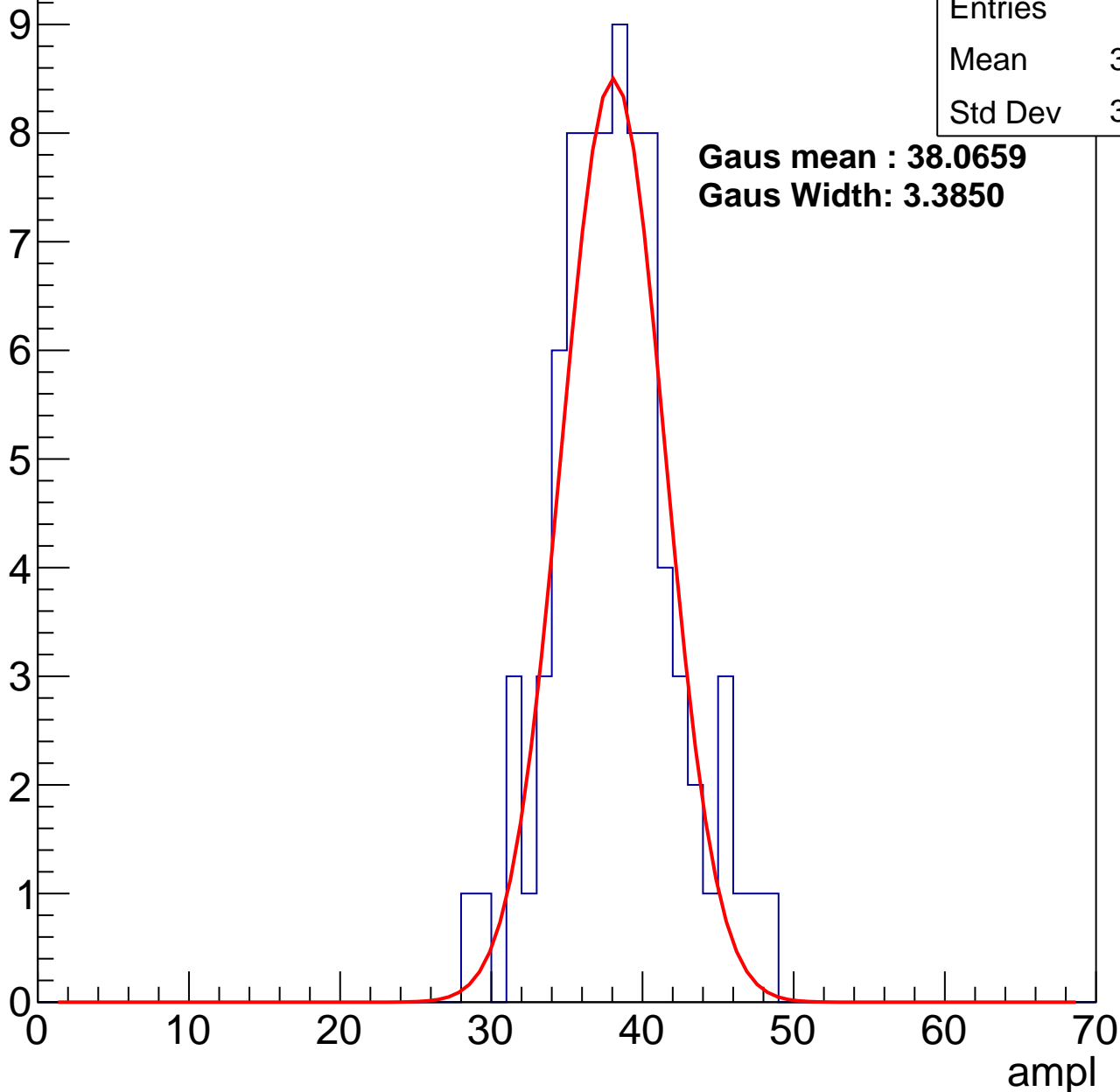
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 37.74 |
| Std Dev | 3.943 |

Gaus mean : 38.0659

Gaus Width: 3.3850



B1L100S, U9-ch63, adc2

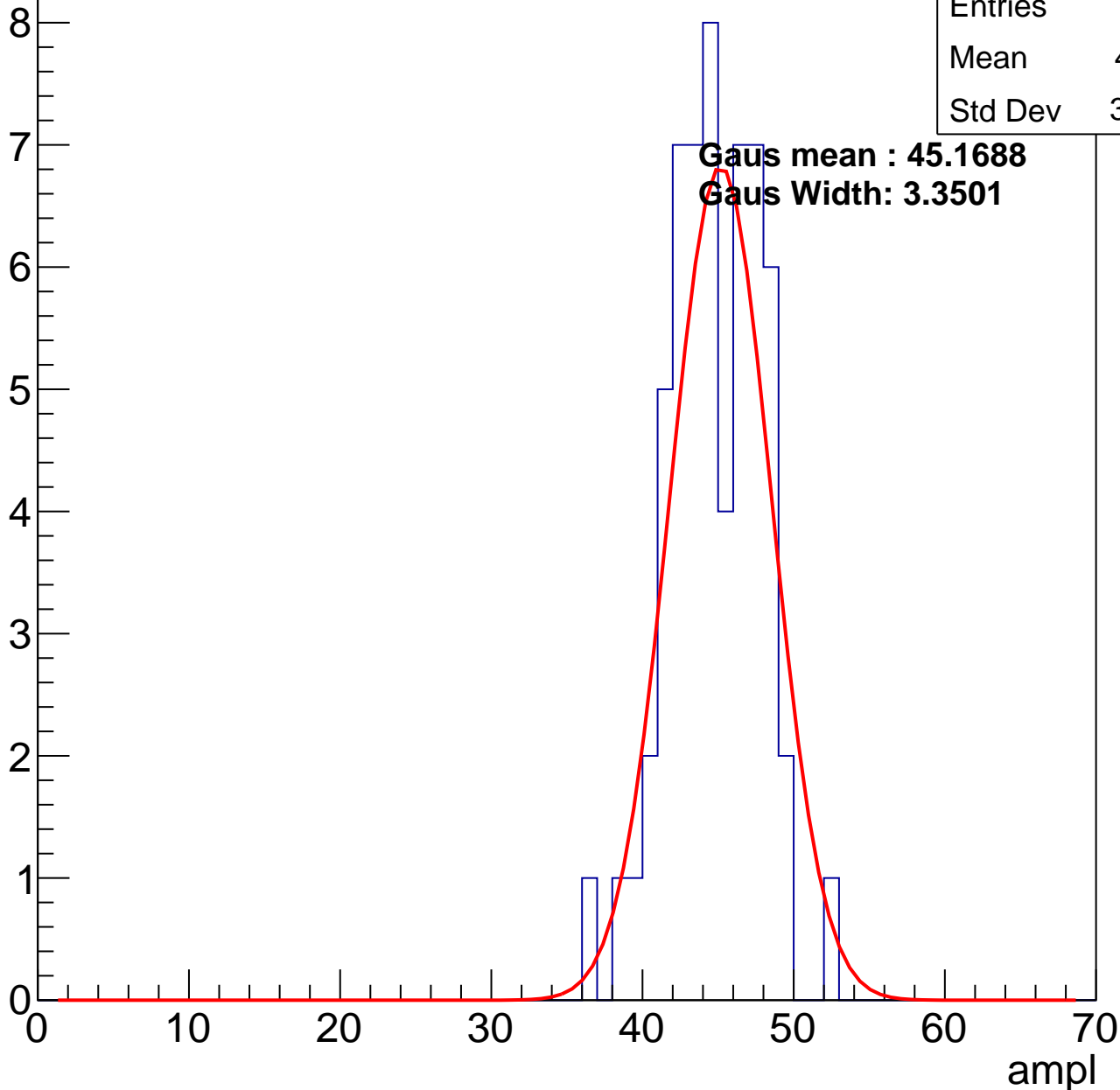
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 44.31 |
| Std Dev | 3.016 |

Gaus mean : 45.1688

Gaus Width: 3.3501

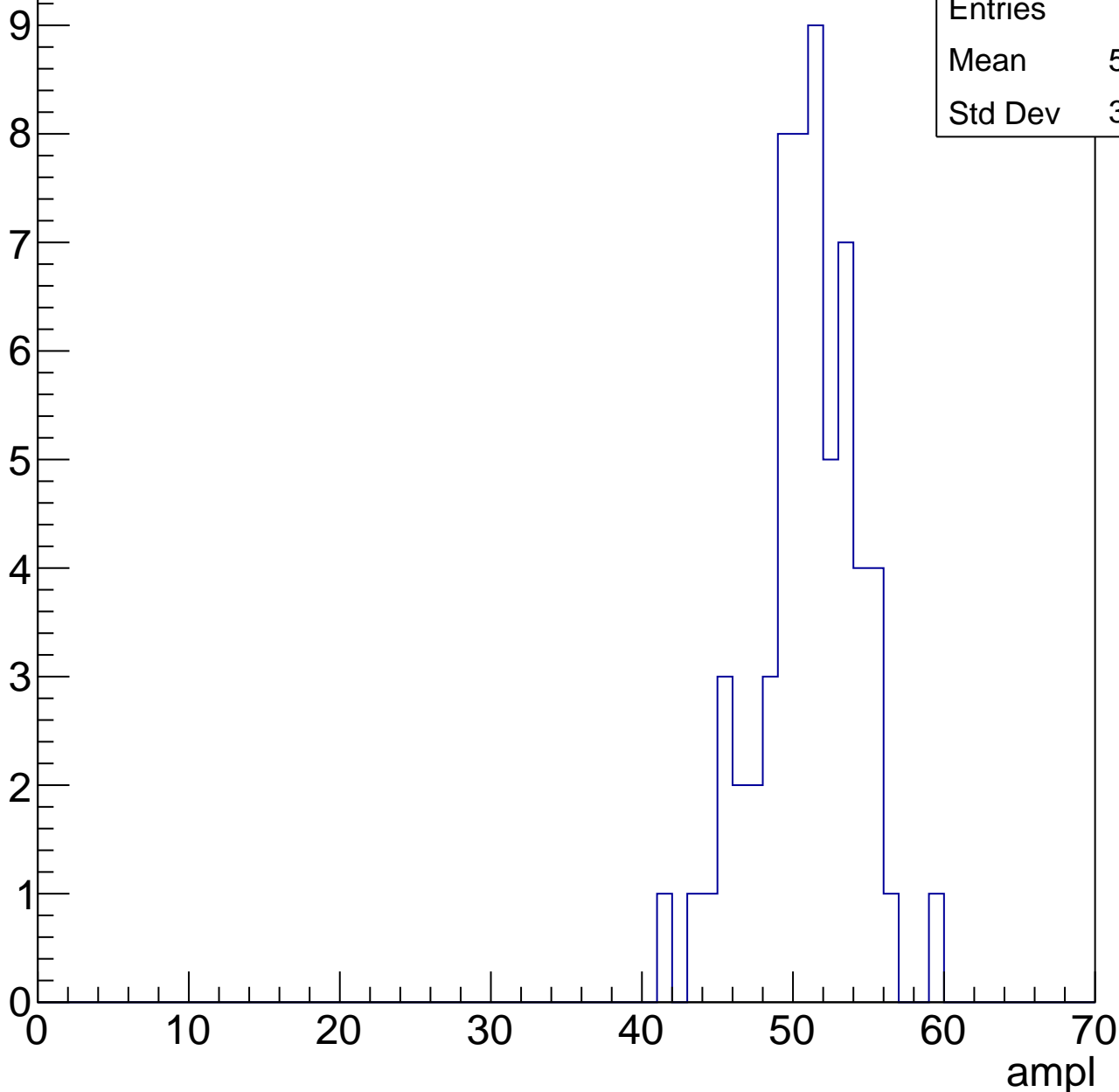


B1L100S, U9-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 50.43 |
| Std Dev | 3.348 |



B1L100S, U9-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 61 |
| Mean | 56.67 |
| Std Dev | 3.166 |

Entry

10

8

6

4

2

0

0

10

20

30

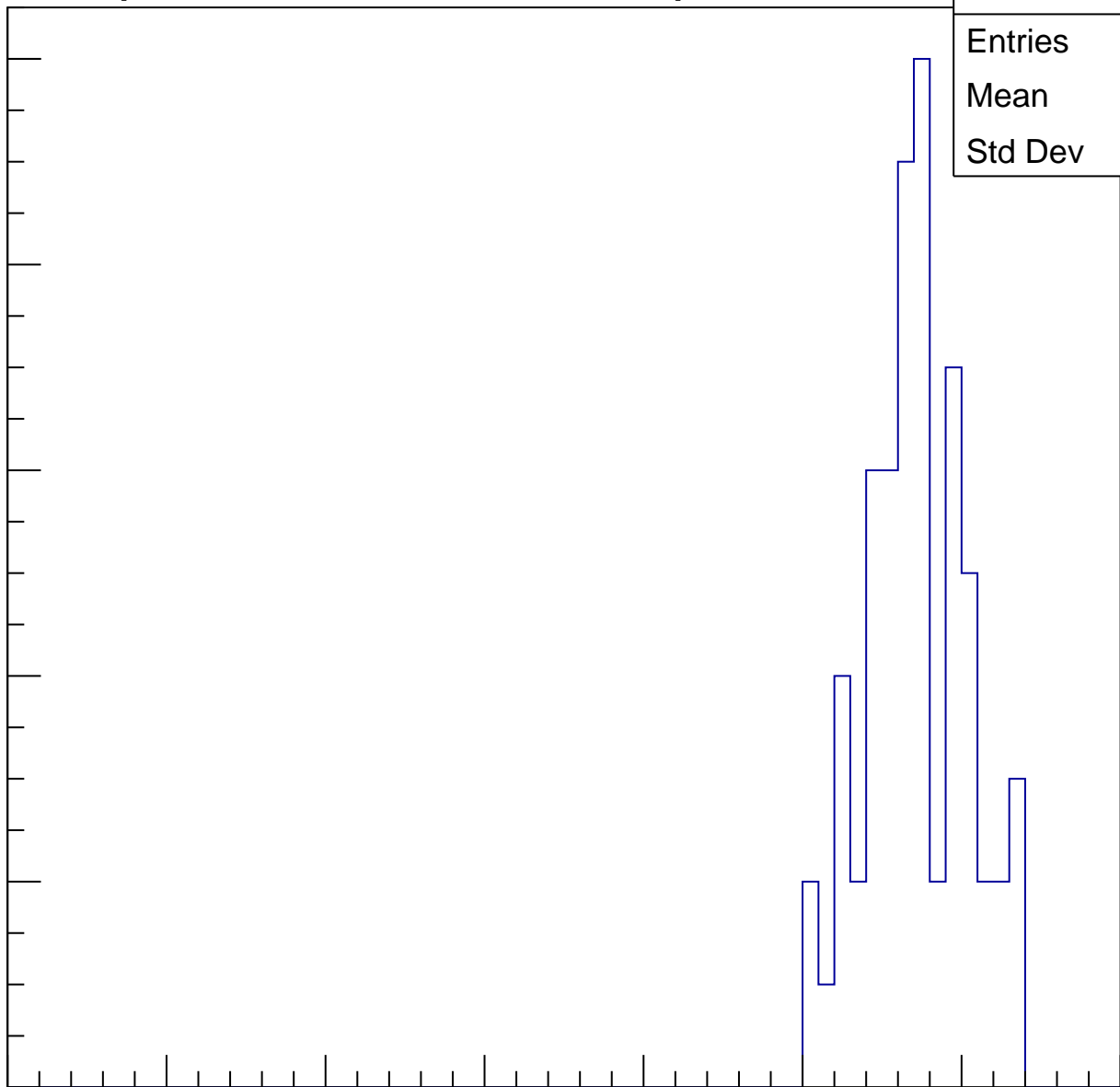
40

50

60

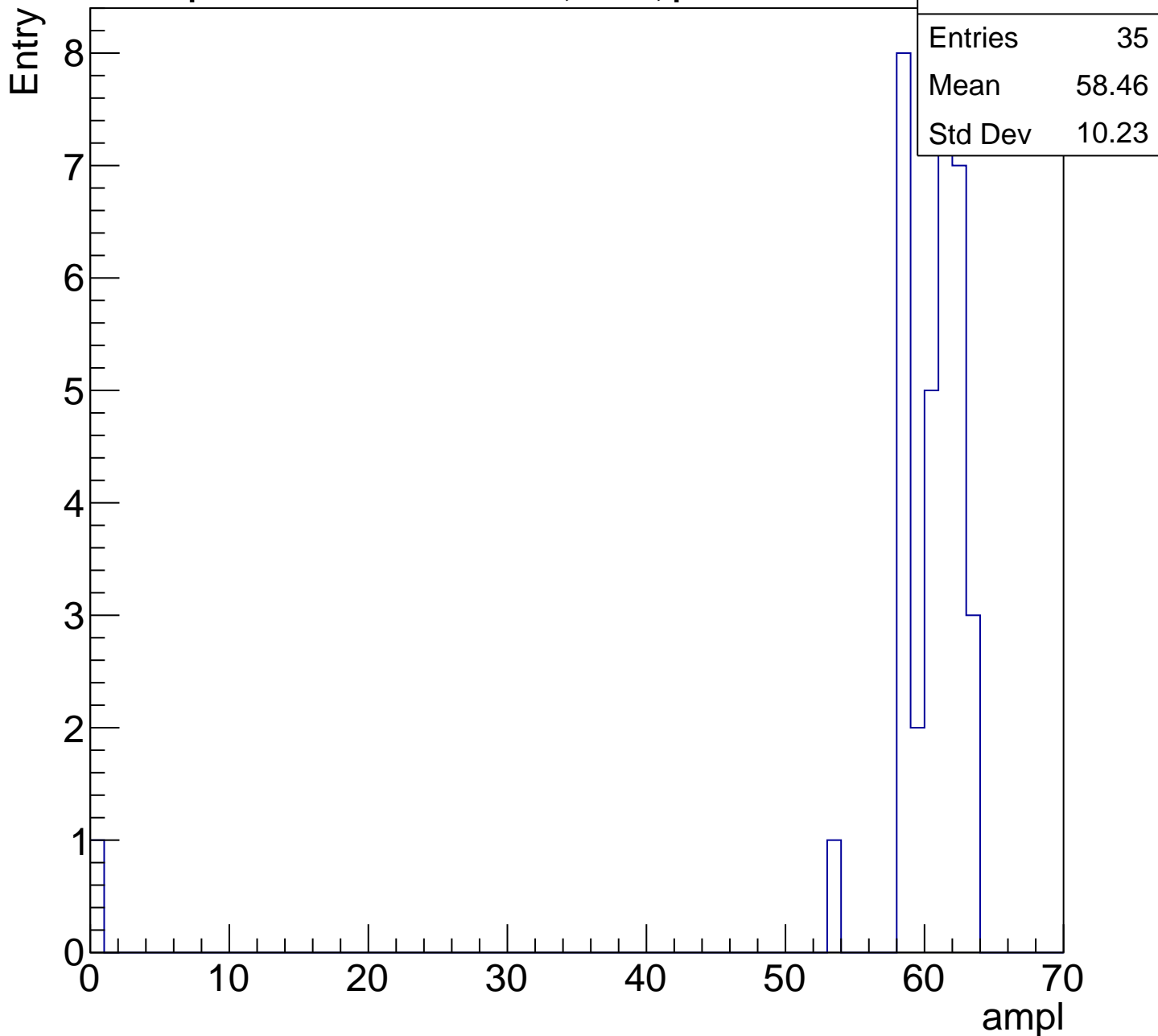
70

ampl



B1L100S, U9-ch63, adc5

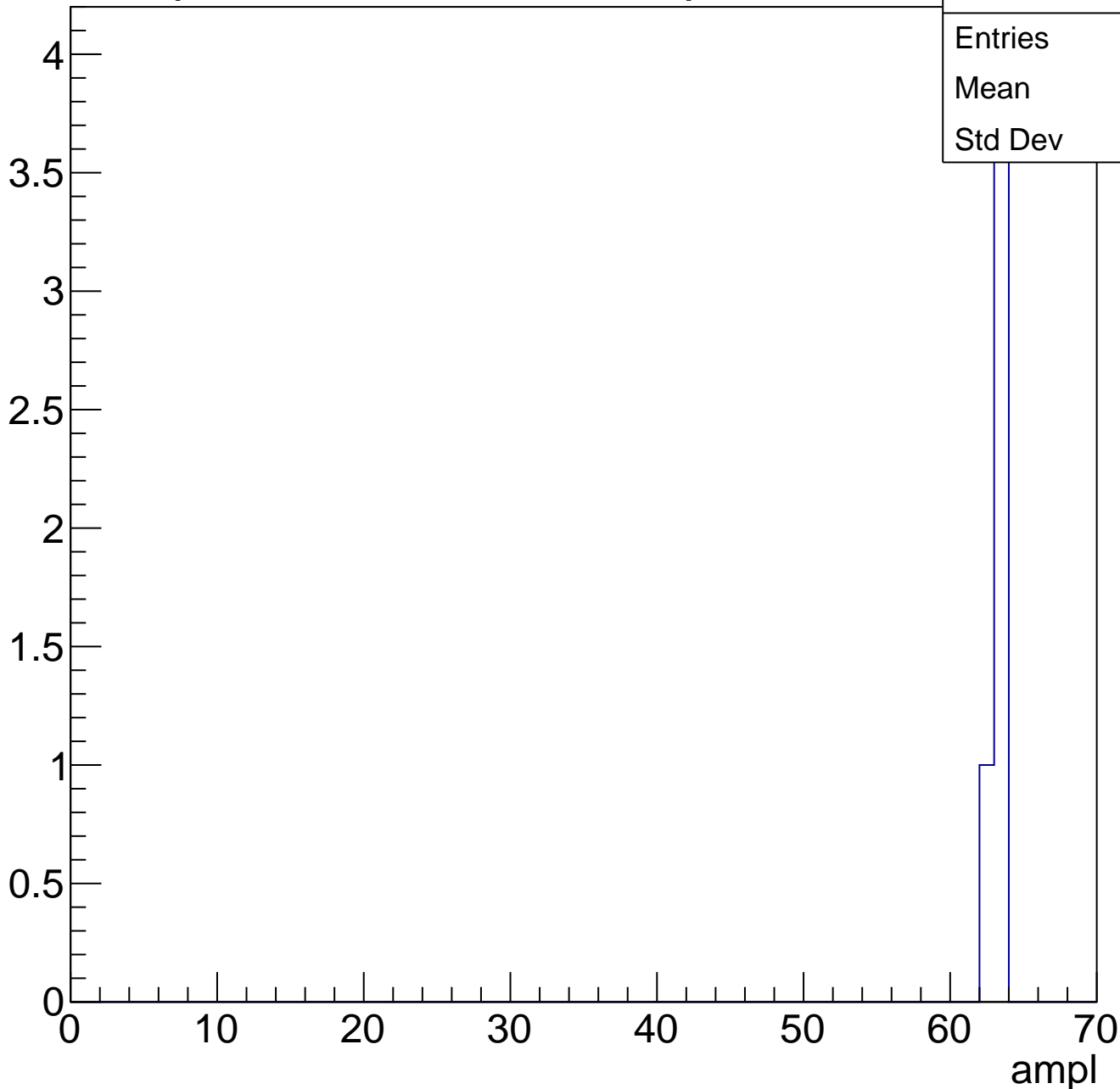
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch64, adc0

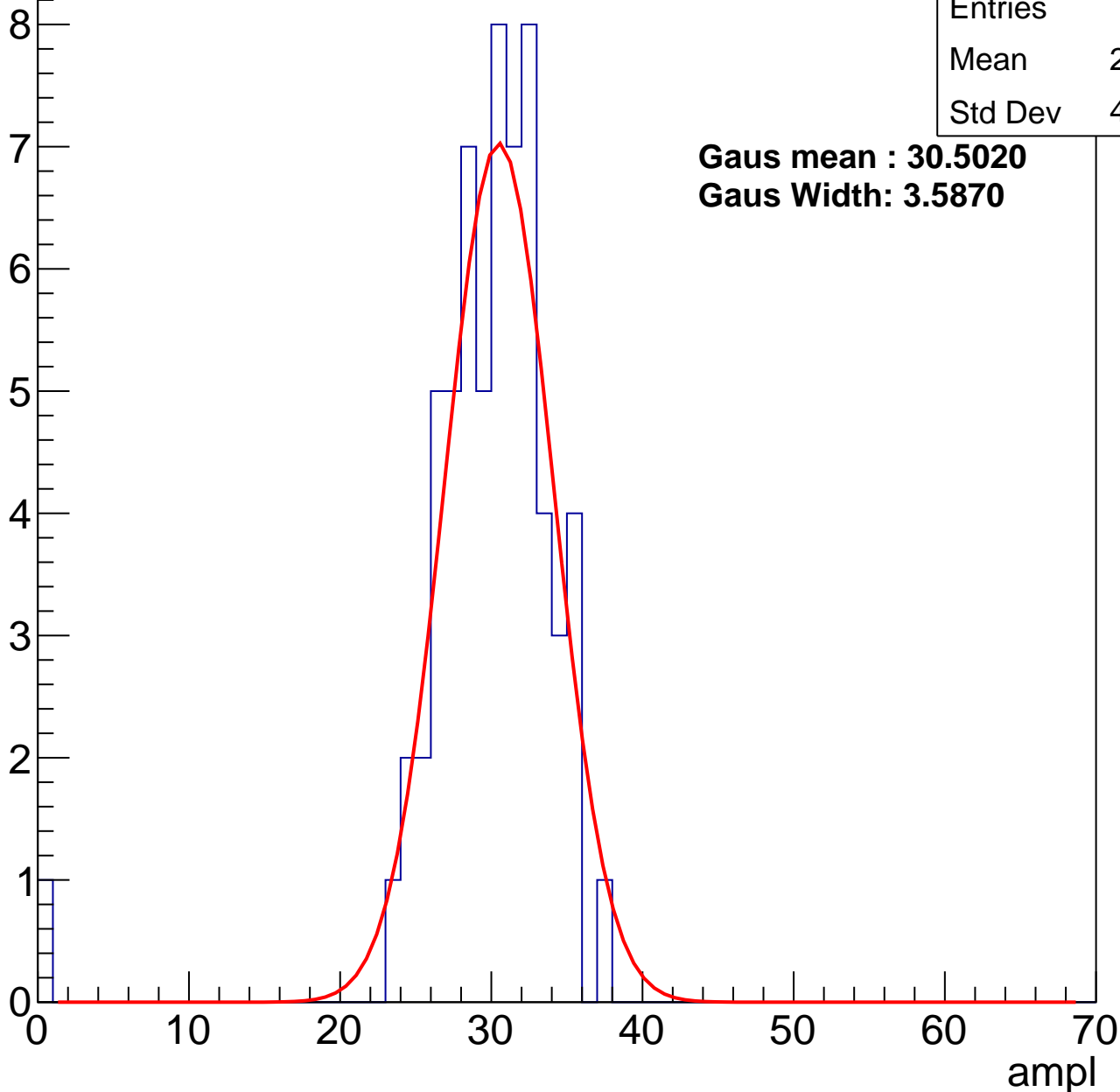
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 29.38 |
| Std Dev | 4.842 |

Gaus mean : 30.5020

Gaus Width: 3.5870



B1L100S, U9-ch64, adc1

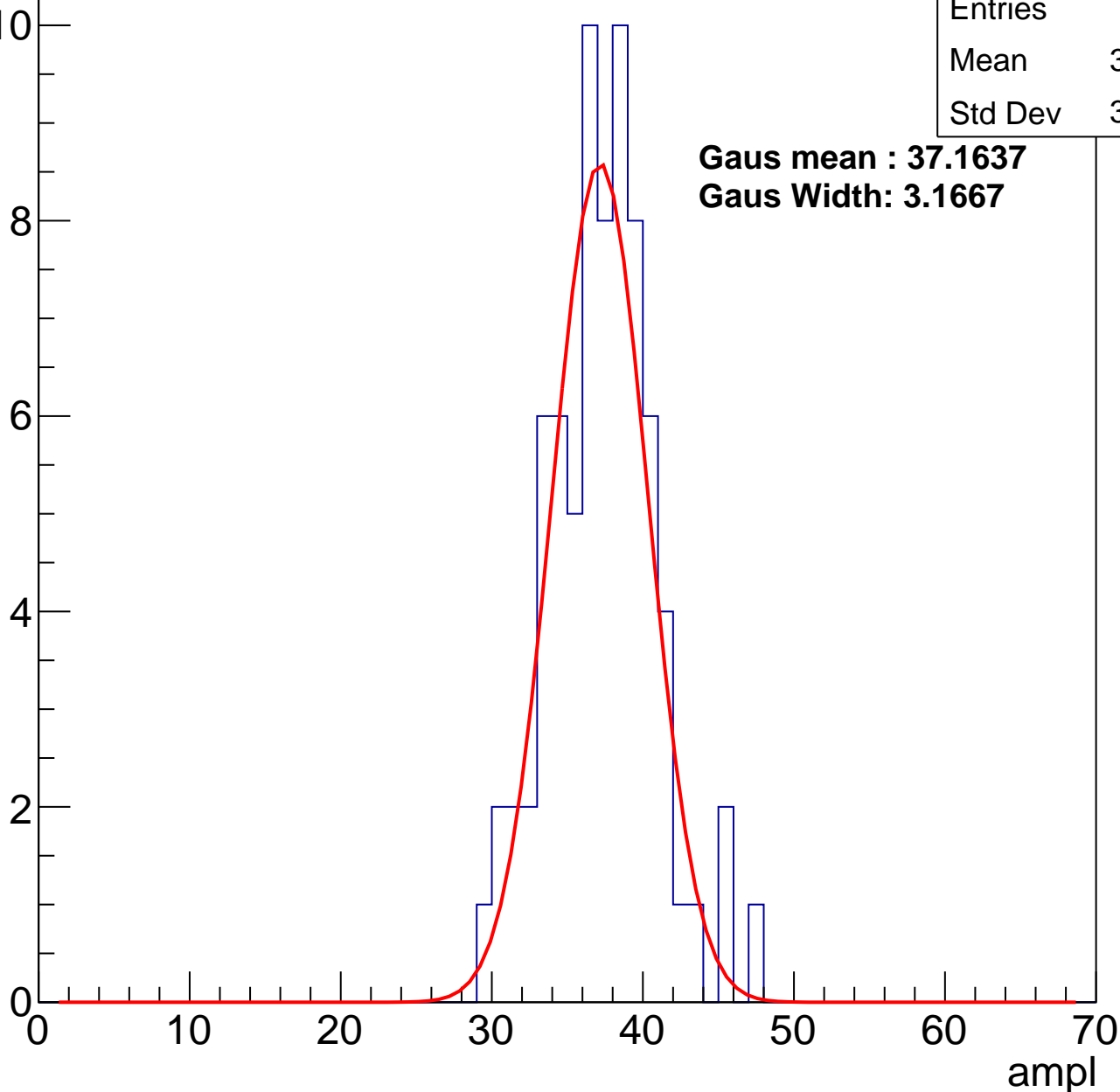
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 36.88 |
| Std Dev | 3.487 |

Gaus mean : 37.1637

Gaus Width: 3.1667



B1L100S, U9-ch64, adc2

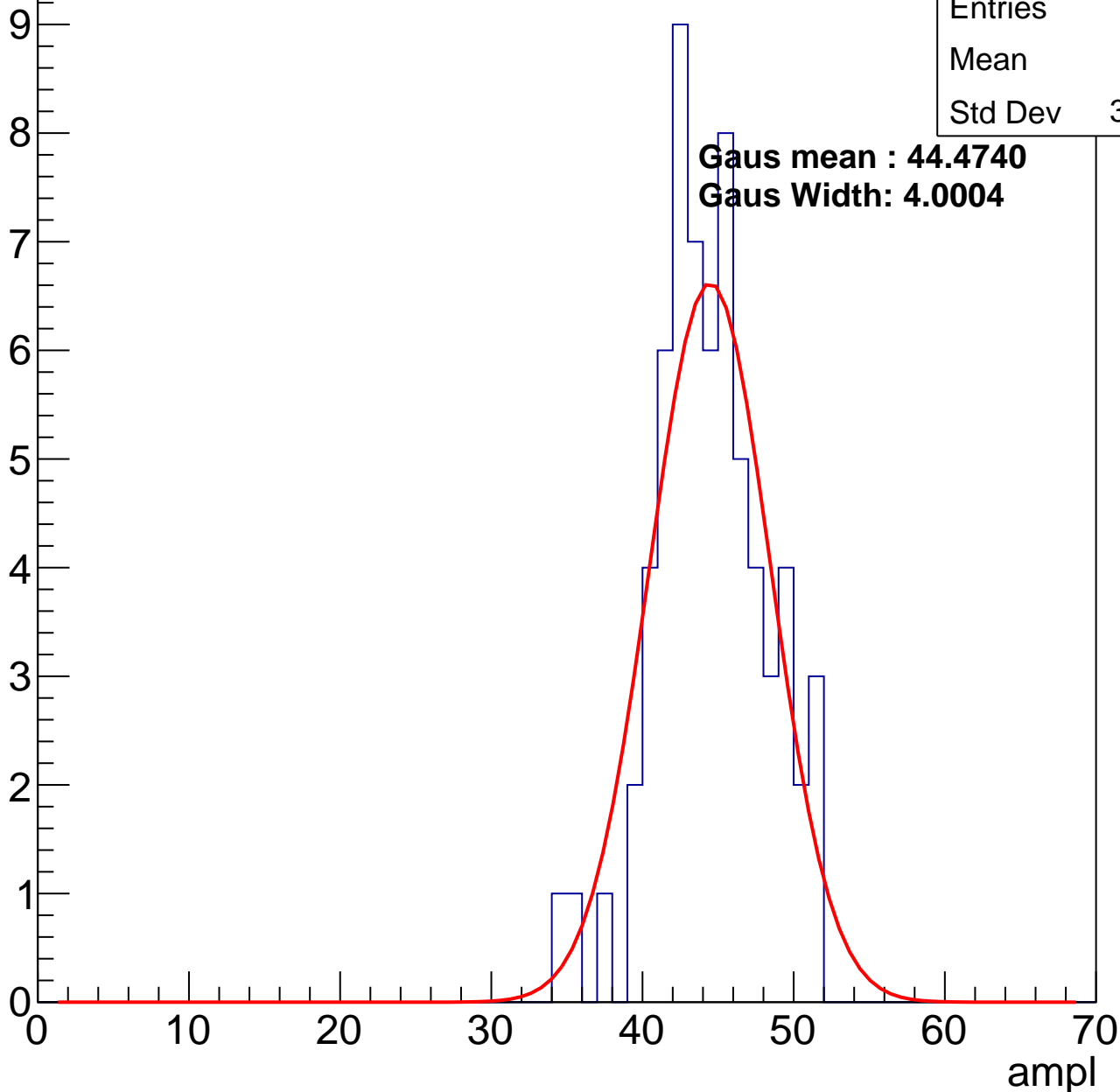
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44 |
| Std Dev | 3.635 |

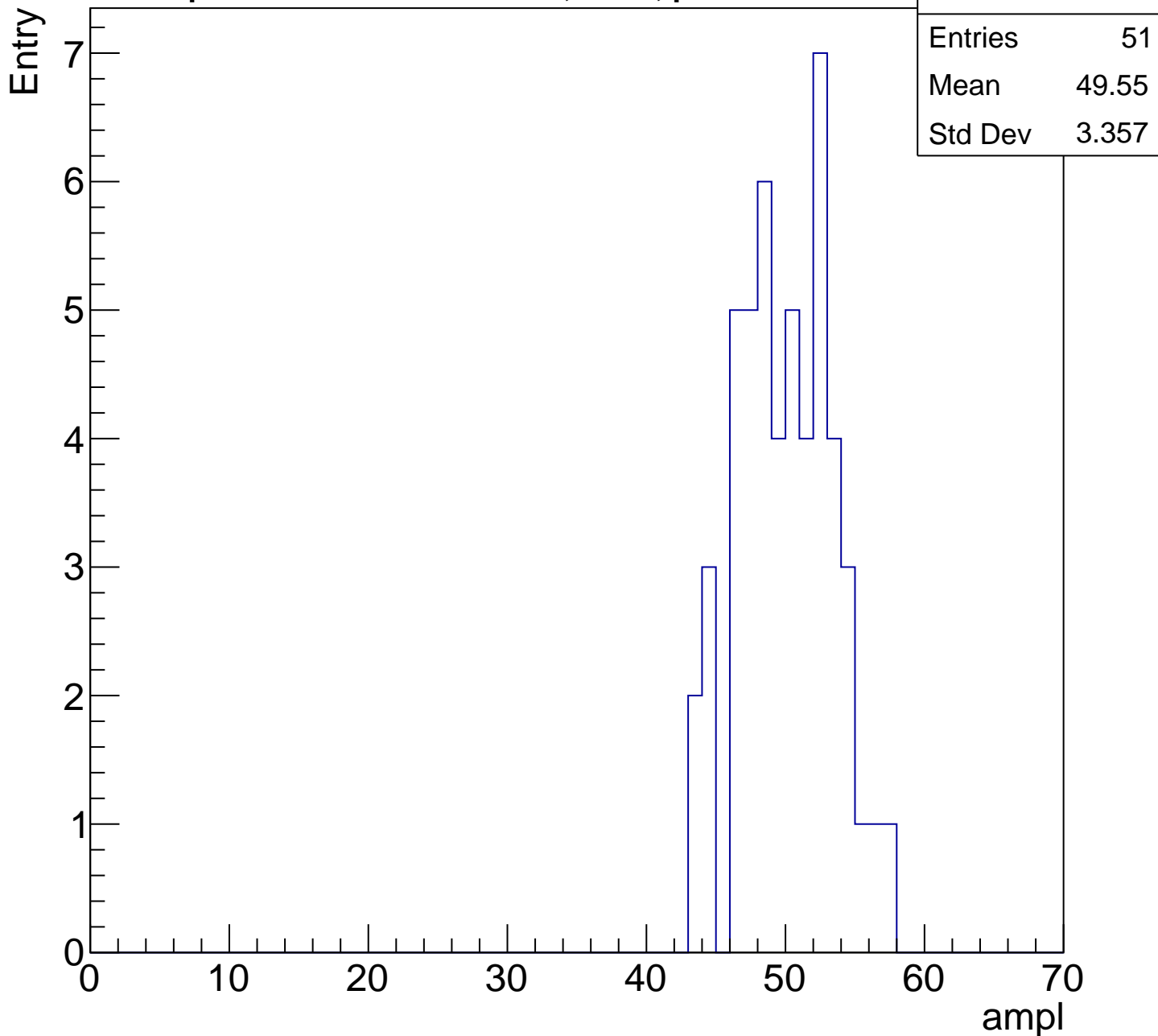
Gaus mean : 44.4740

Gaus Width: 4.0004



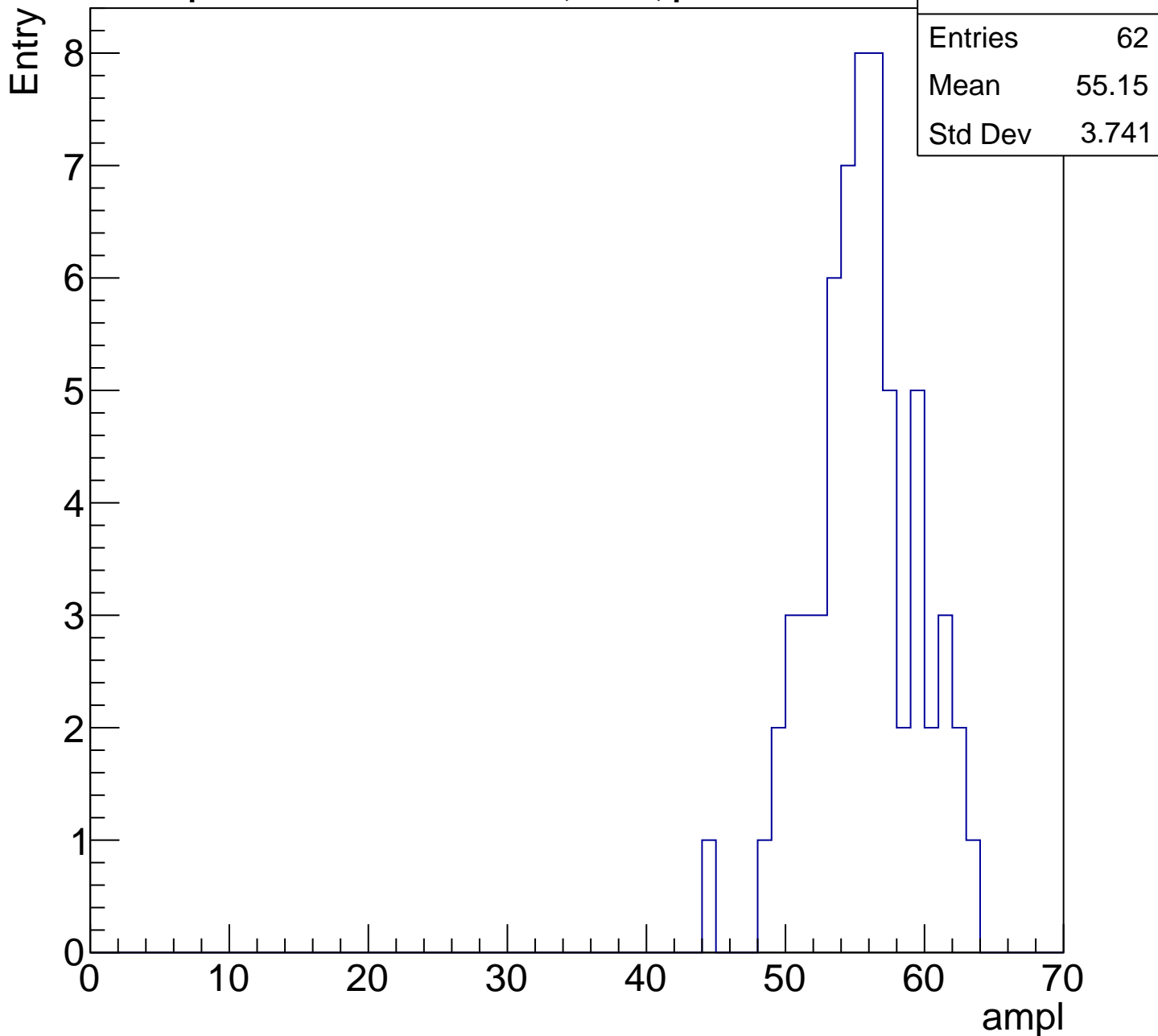
B1L100S, U9-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 60.13 |
| Std Dev | 2.163 |

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

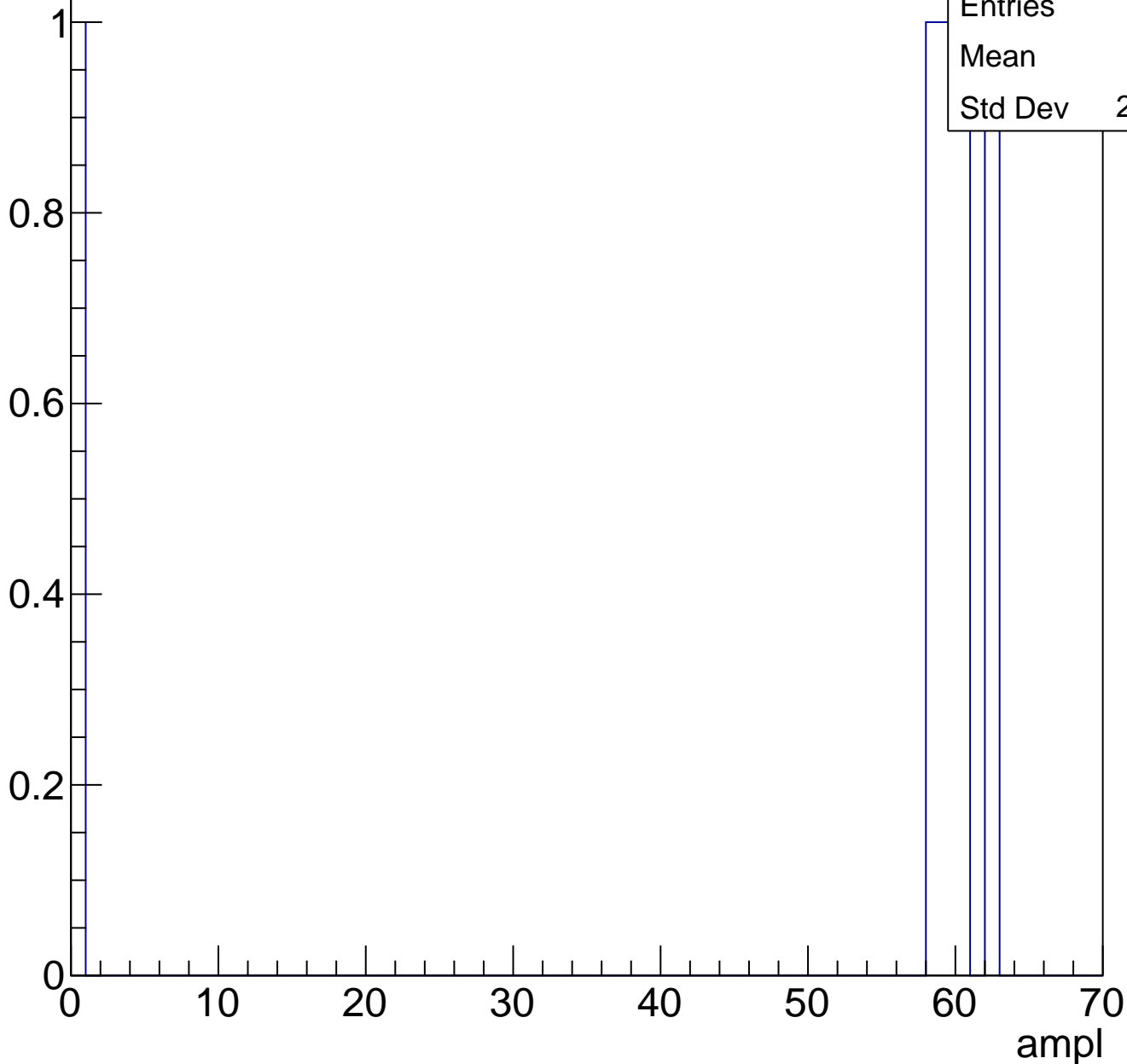
7

8

B1L100S, U9-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch65, adc0

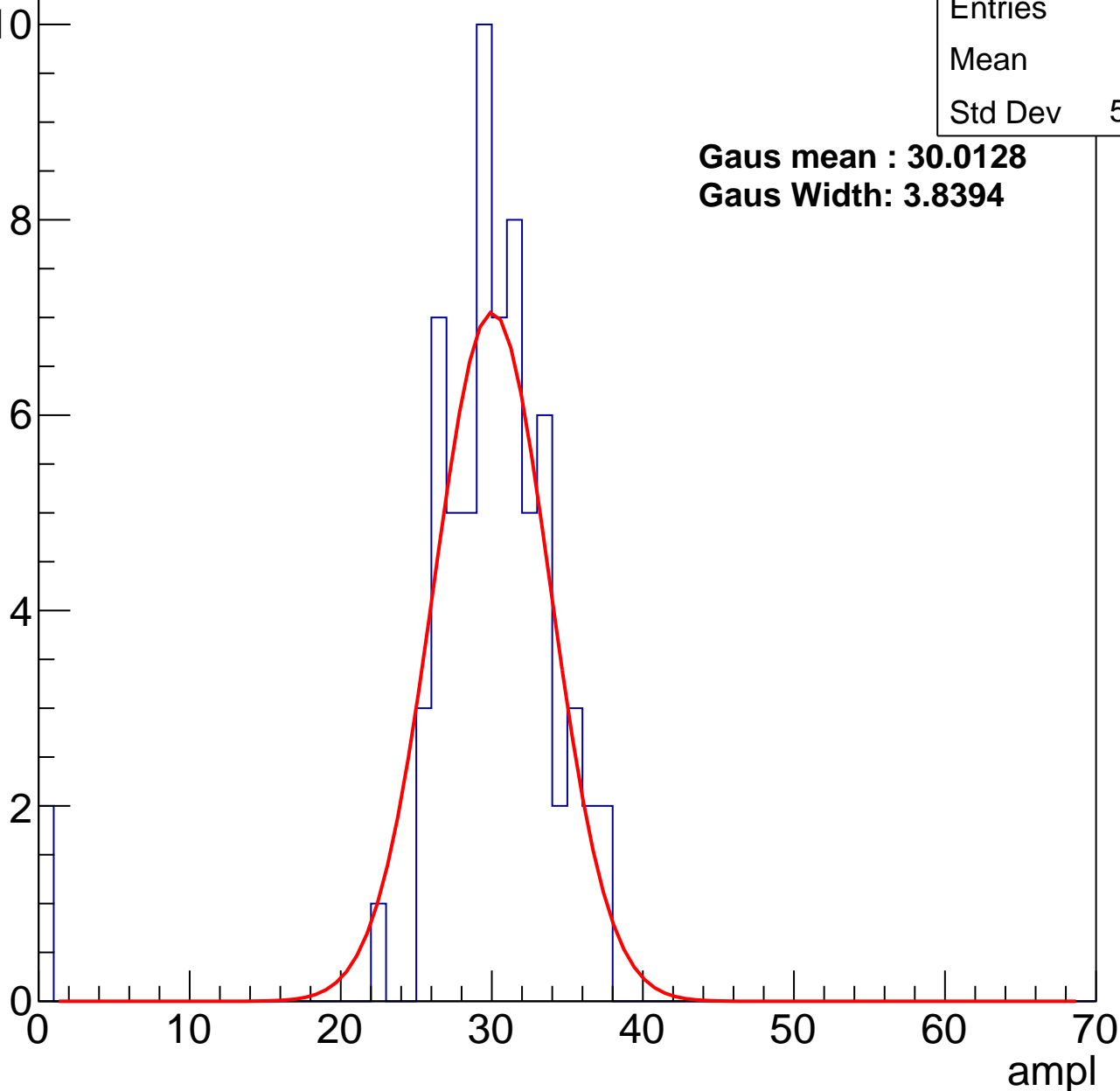
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.1 |
| Std Dev | 5.978 |

Gaus mean : 30.0128

Gaus Width: 3.8394



B1L100S, U9-ch65, adc1

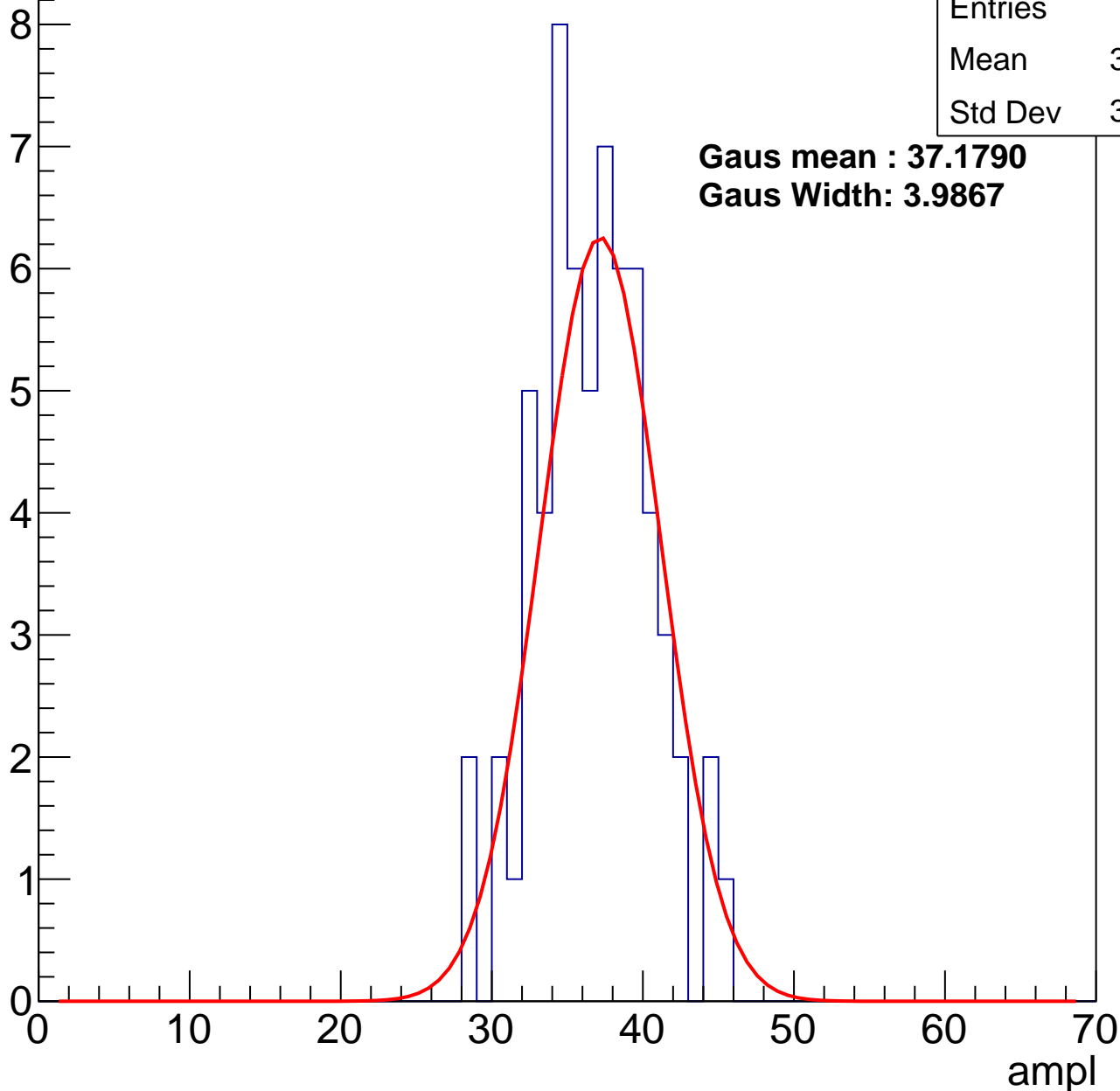
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.28 |
| Std Dev | 3.706 |

Gaus mean : 37.1790

Gaus Width: 3.9867



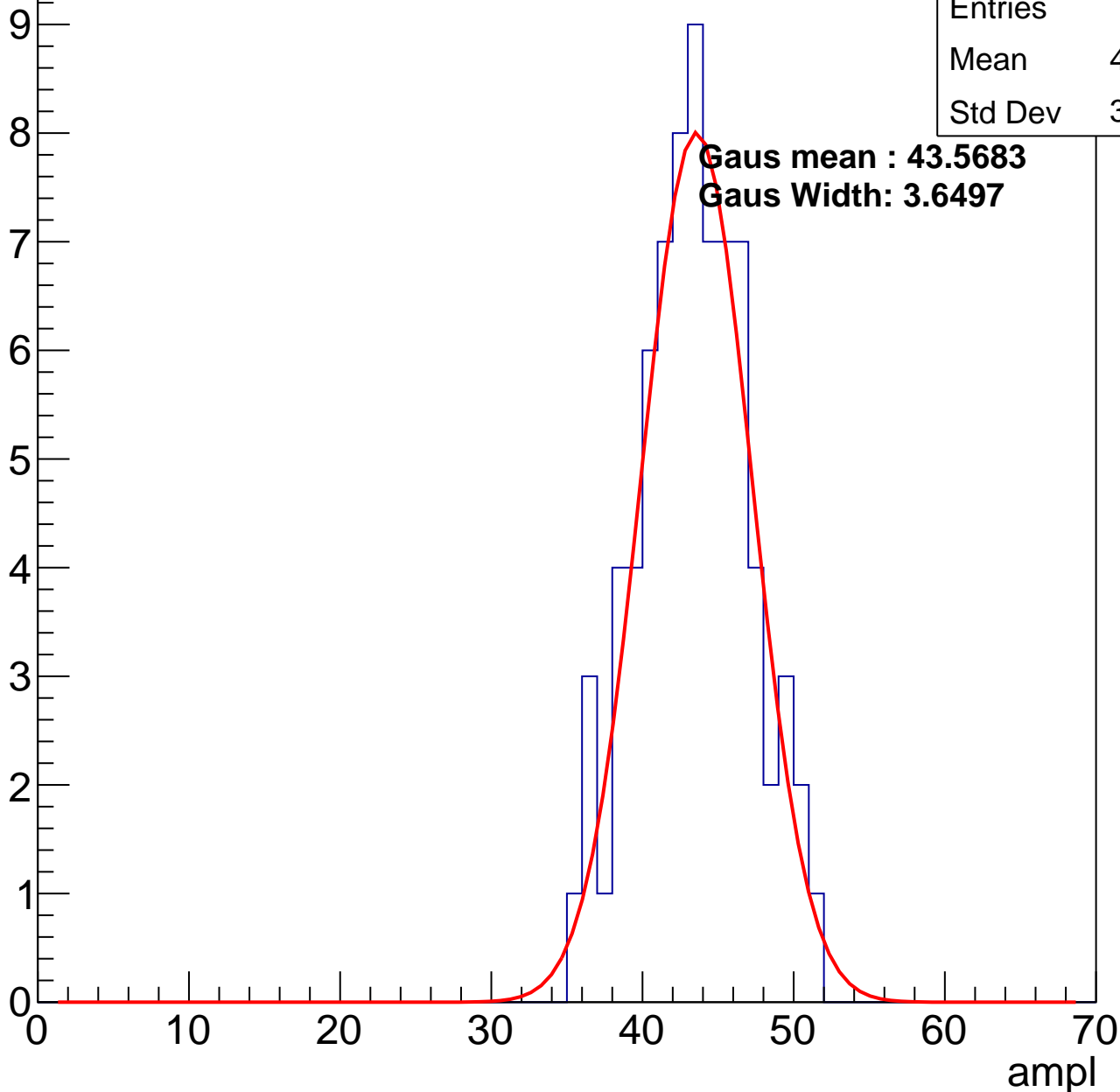
B1L100S, U9-ch65, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

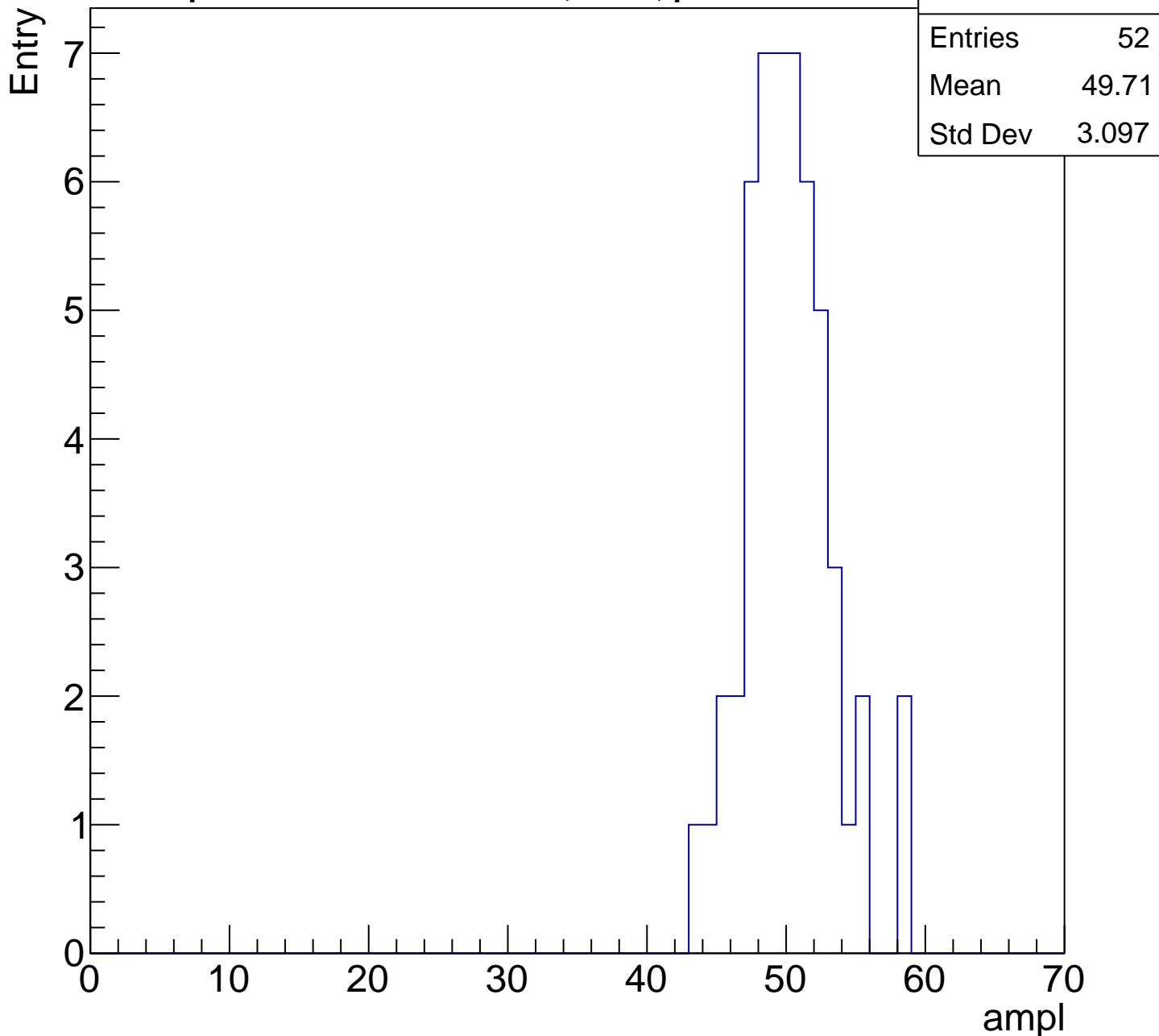
| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.96 |
| Std Dev | 3.596 |

Gaus mean : 43.5683
Gaus Width: 3.6497



B1L100S, U9-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

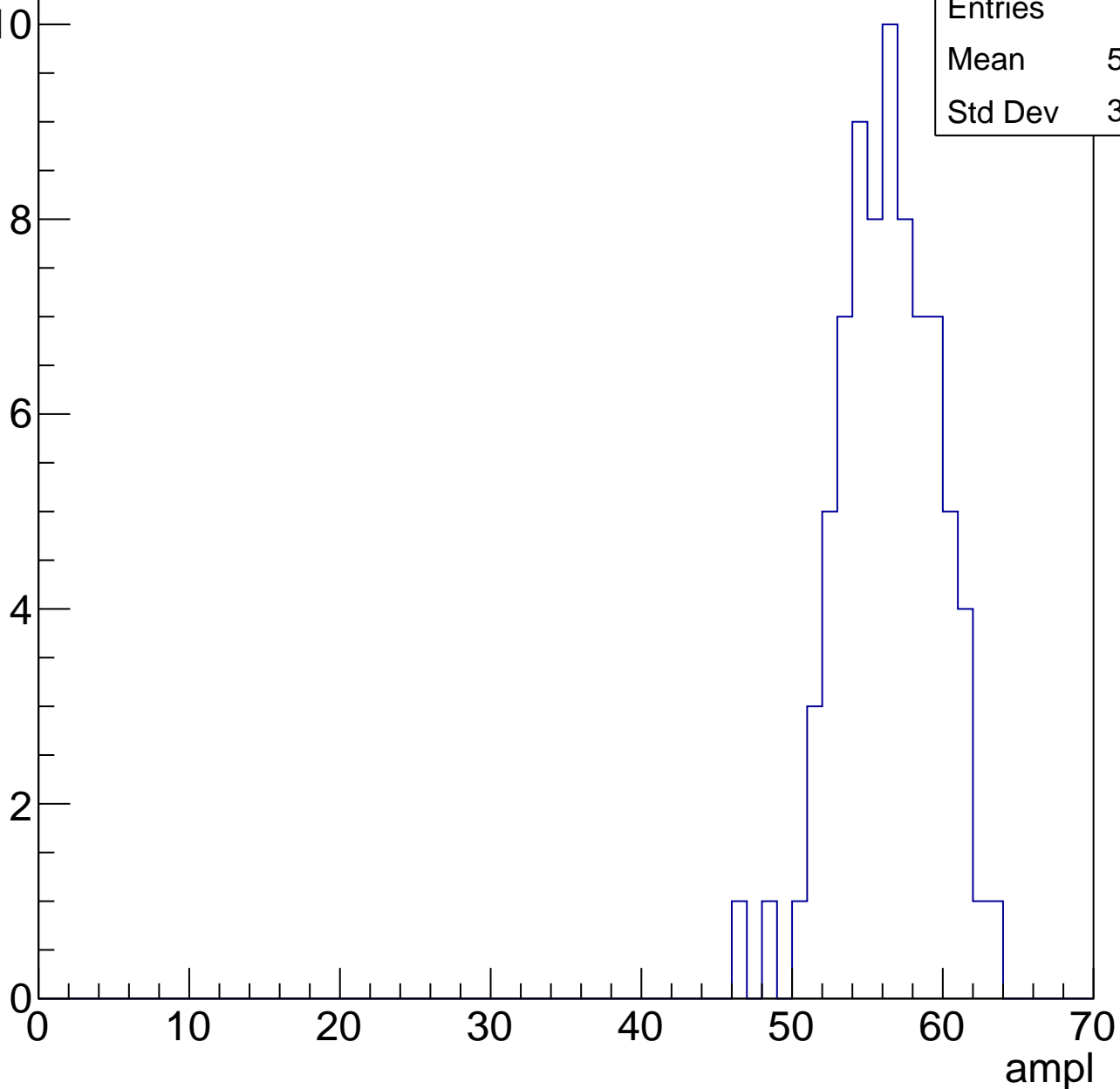


B1L100S, U9-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 55.87 |
| Std Dev | 3.252 |

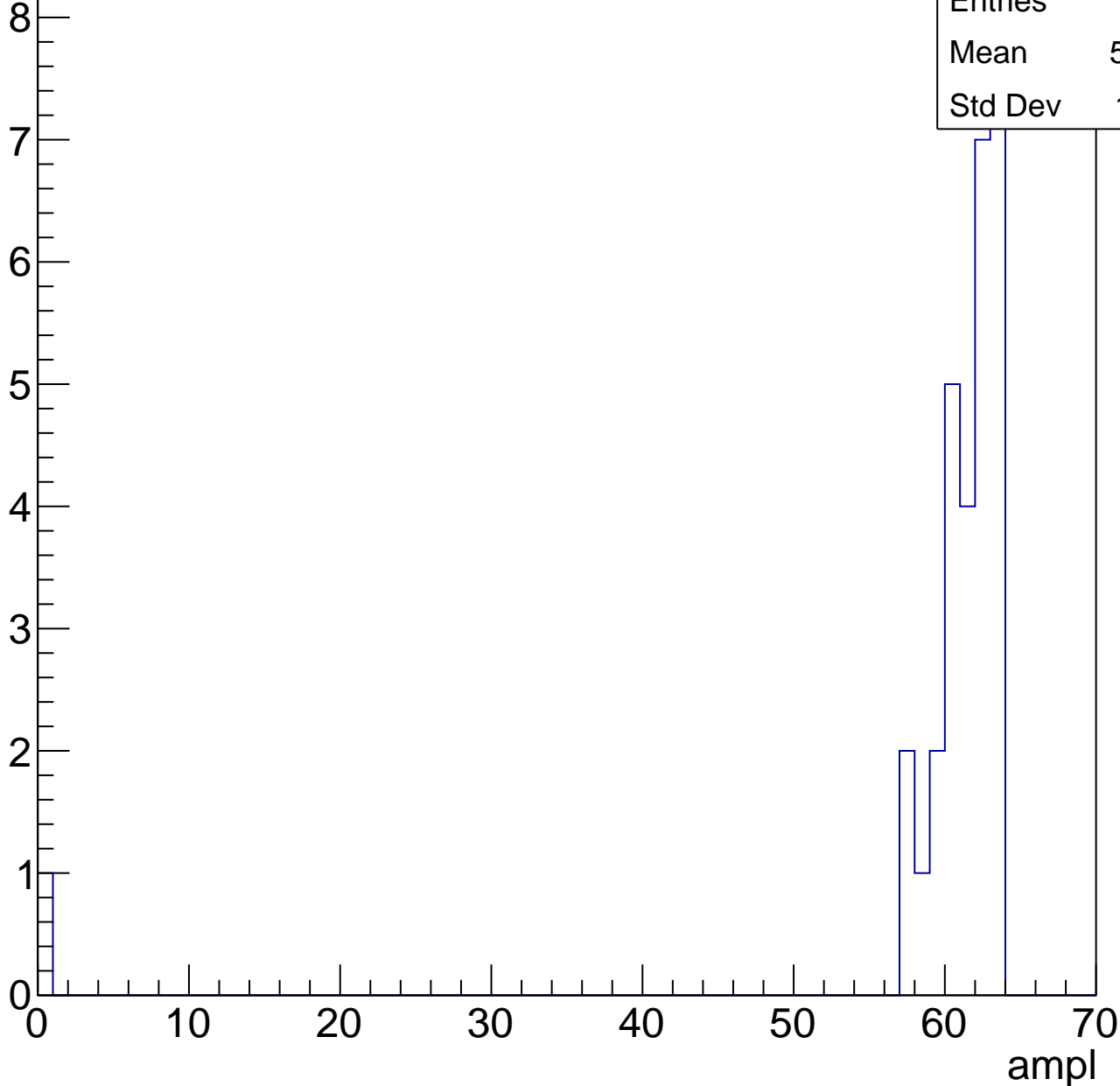


B1L100S, U9-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 30 |
| Mean | 59.07 |
| Std Dev | 11.11 |



B1L100S, U9-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.33 |
| Std Dev | 1.247 |

ampl

60

B1L100S, U9-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch66, adc0

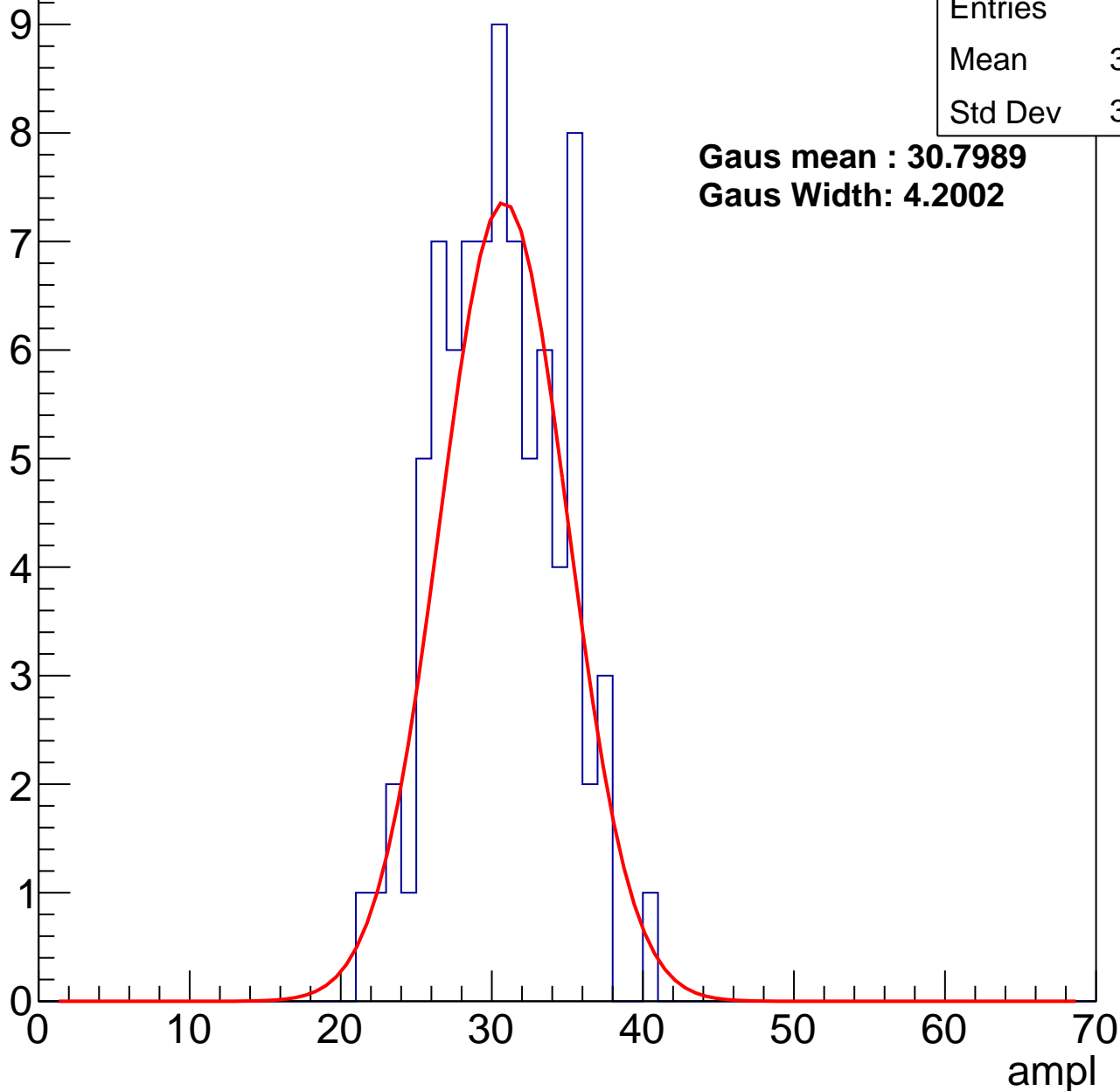
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 30.06 |
| Std Dev | 3.933 |

Gaus mean : 30.7989

Gaus Width: 4.2002



B1L100S, U9-ch66, adc1

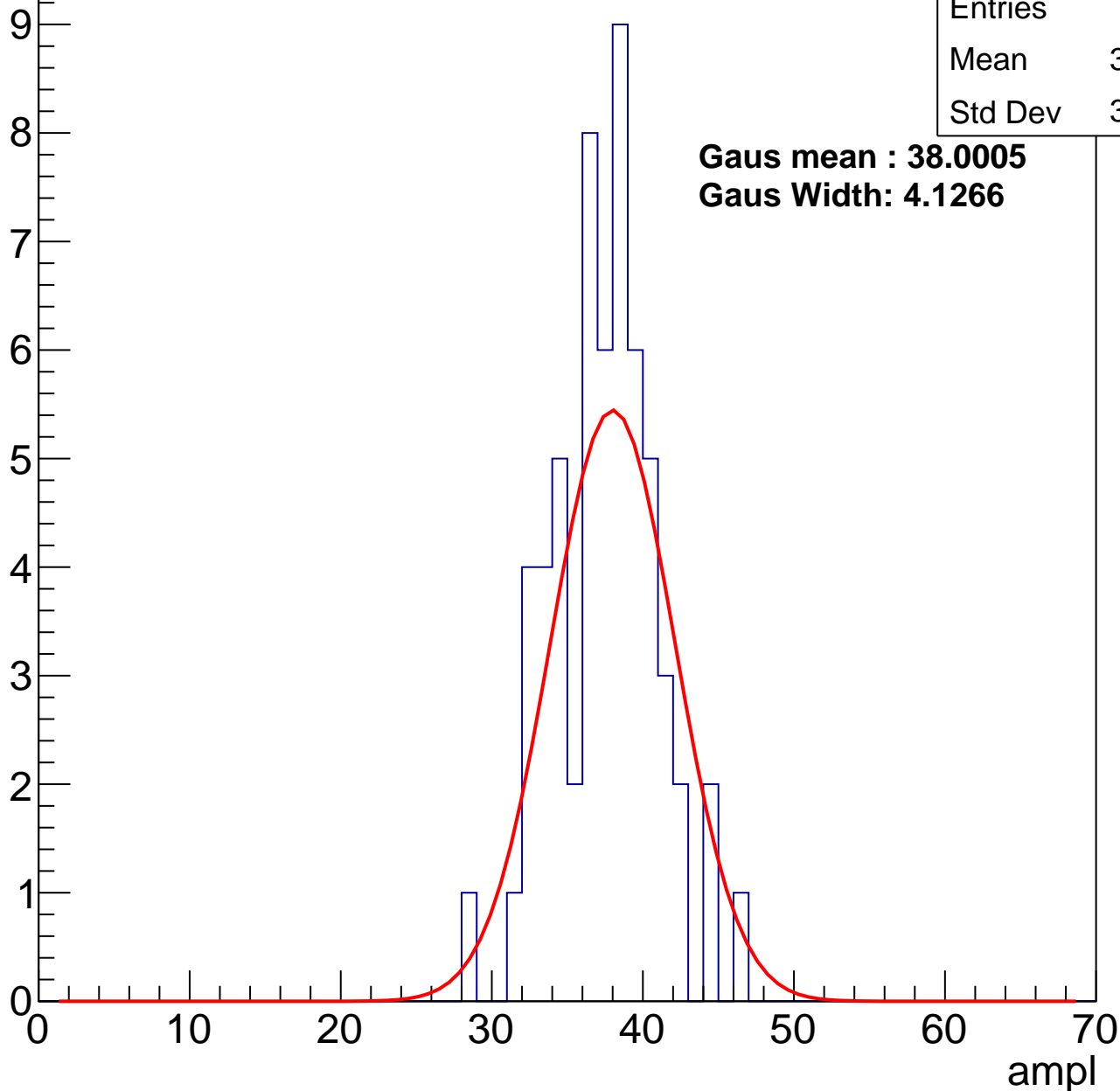
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 37.05 |
| Std Dev | 3.437 |

Gaus mean : 38.0005

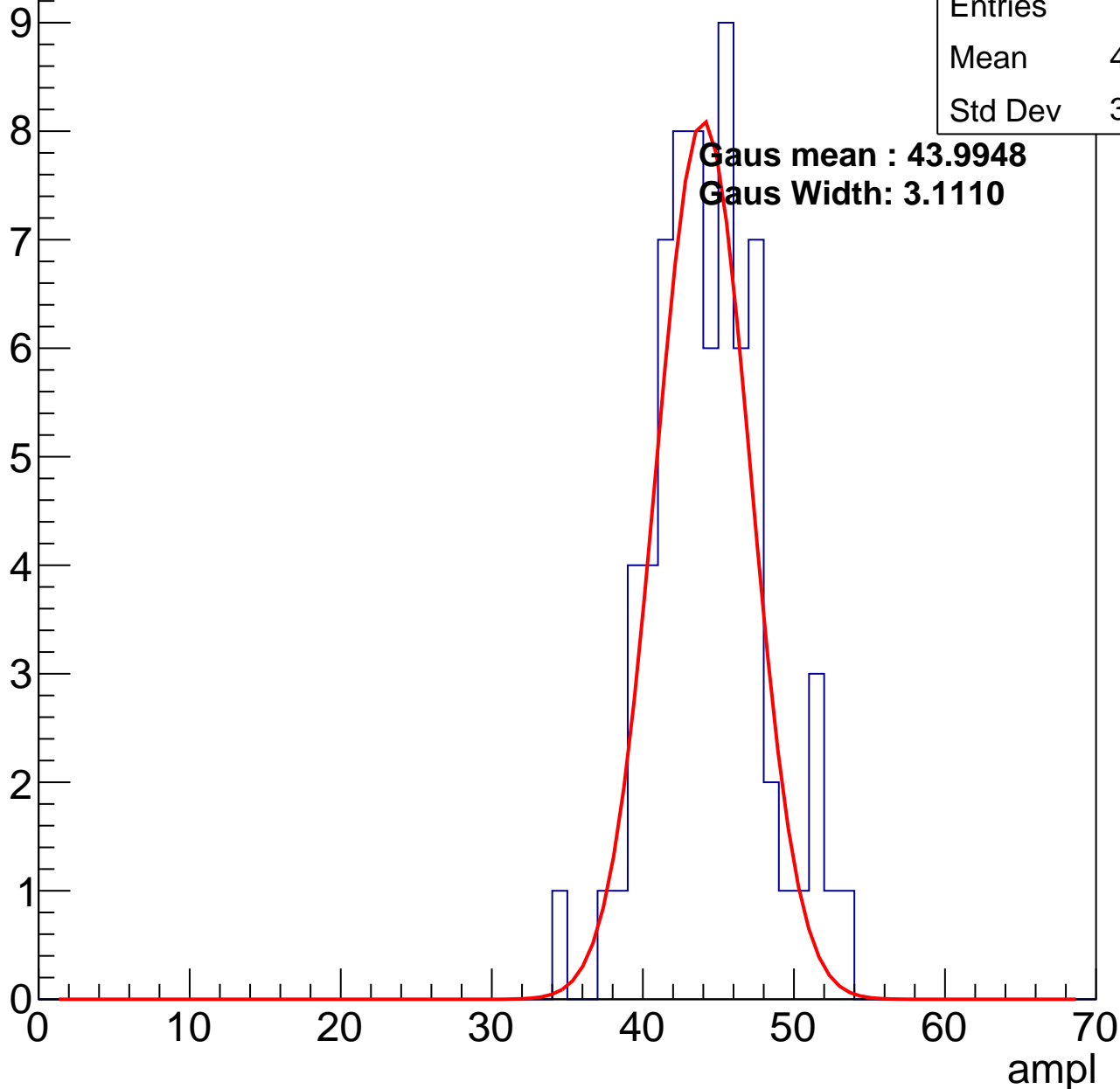
Gaus Width: 4.1266



B1L100S, U9-ch66, adc2

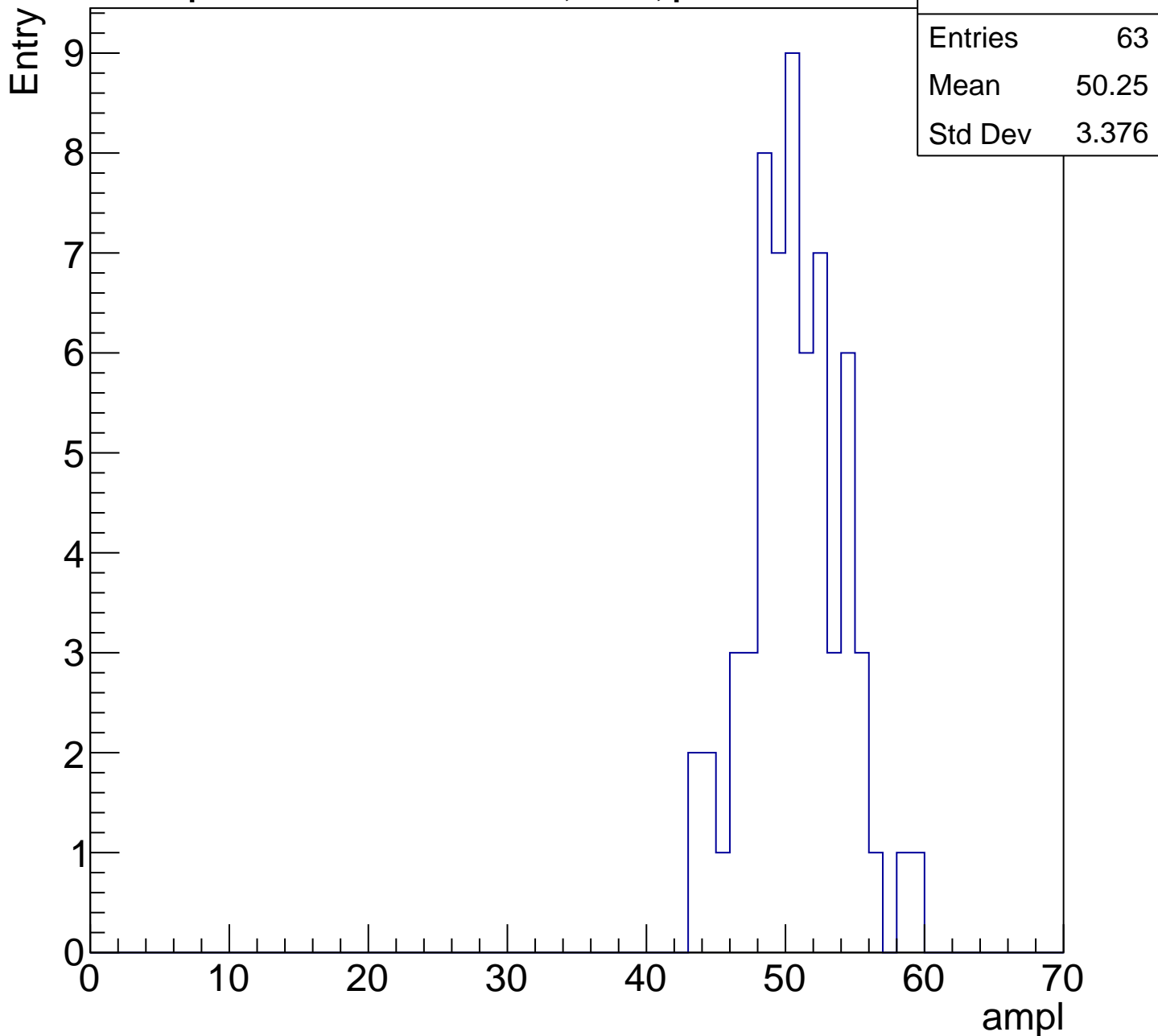
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

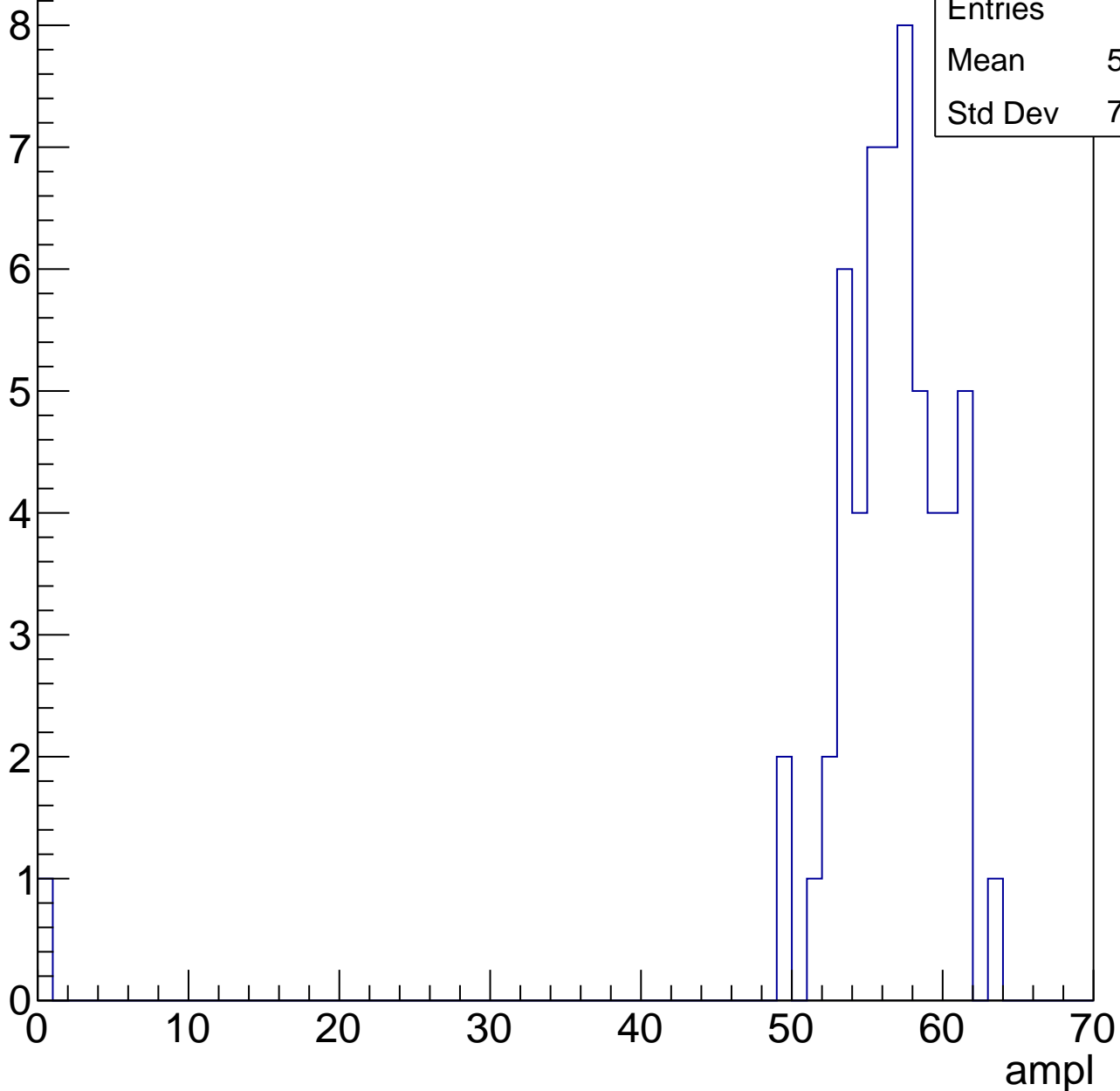


B1L100S, U9-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

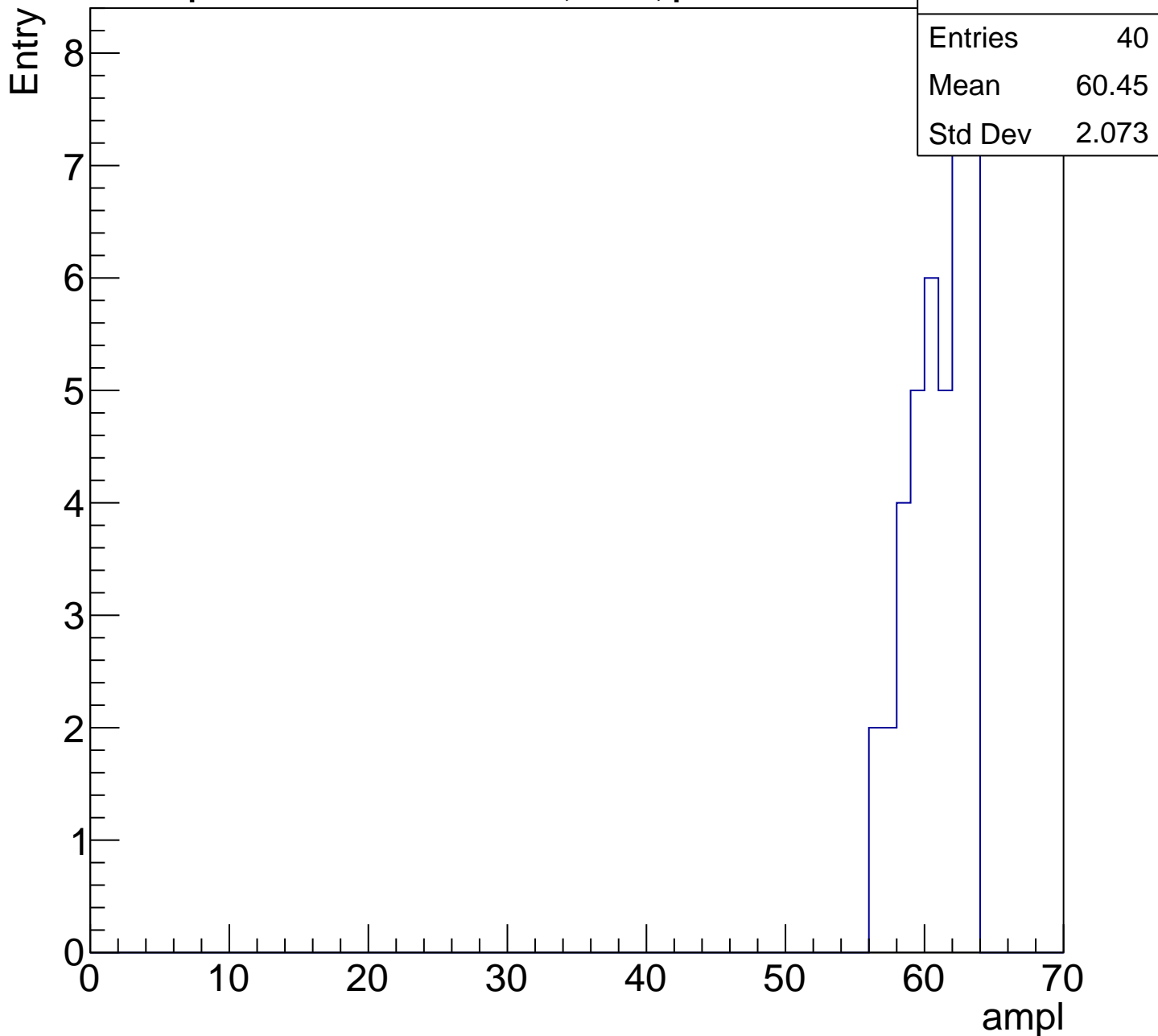
Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.33 |
| Std Dev | 7.996 |



B1L100S, U9-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch67, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 87 |
| Mean | 28.24 |
| Std Dev | 3.68 |

Gaus mean : 28.7899

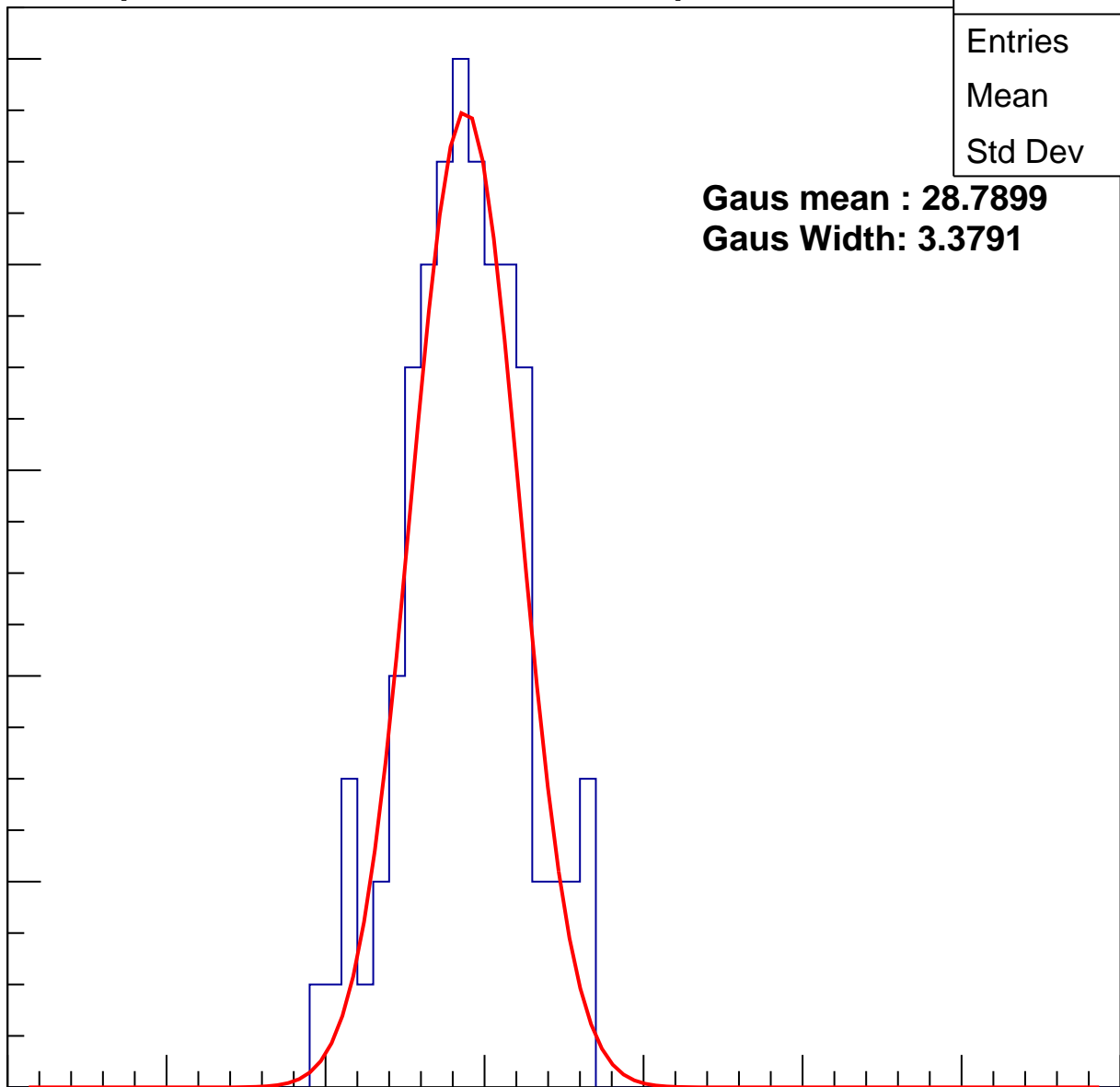
Gaus Width: 3.3791

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch67, adc1

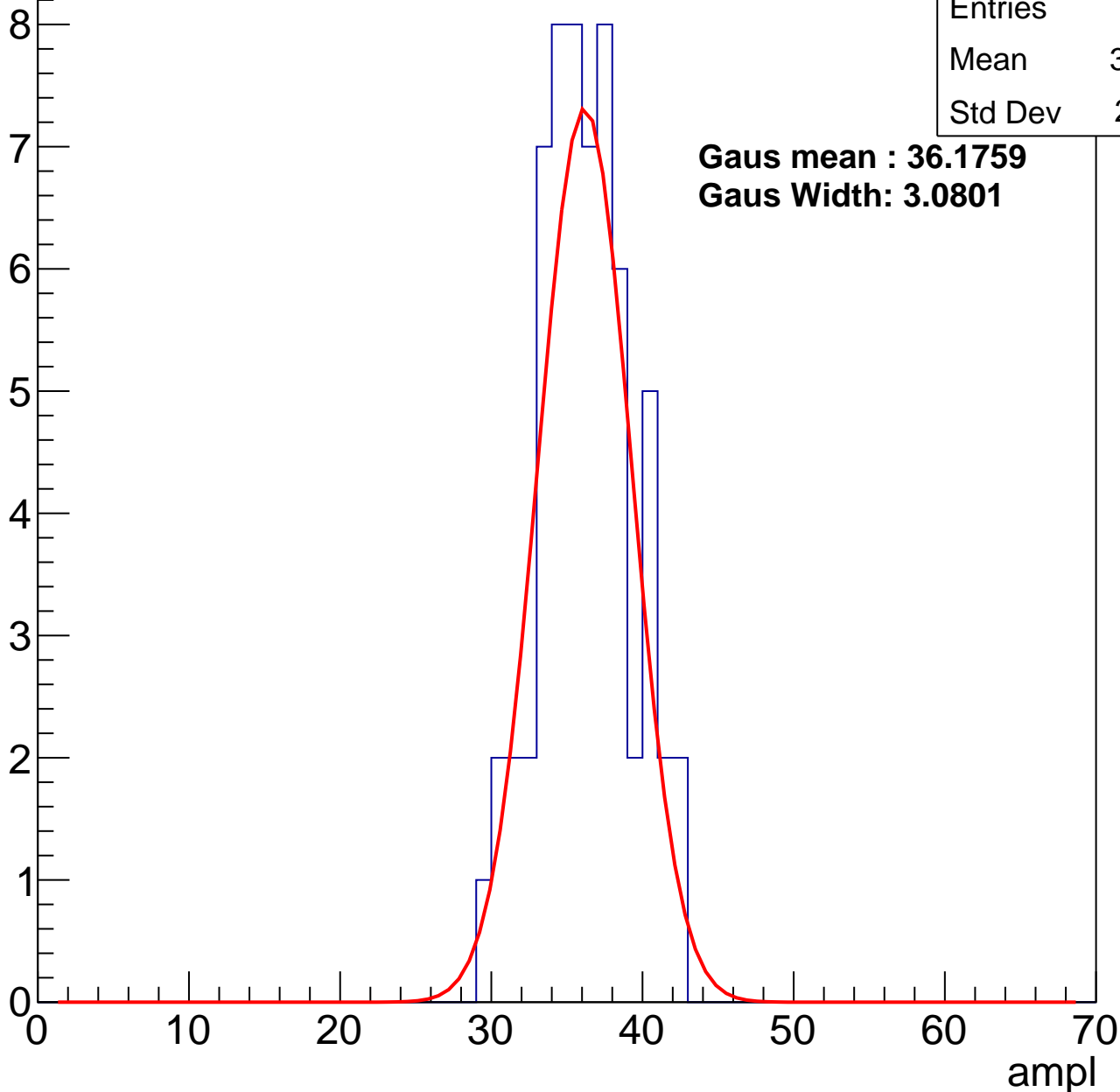
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.77 |
| Std Dev | 2.991 |

Gaus mean : 36.1759

Gaus Width: 3.0801



B1L100S, U9-ch67, adc2

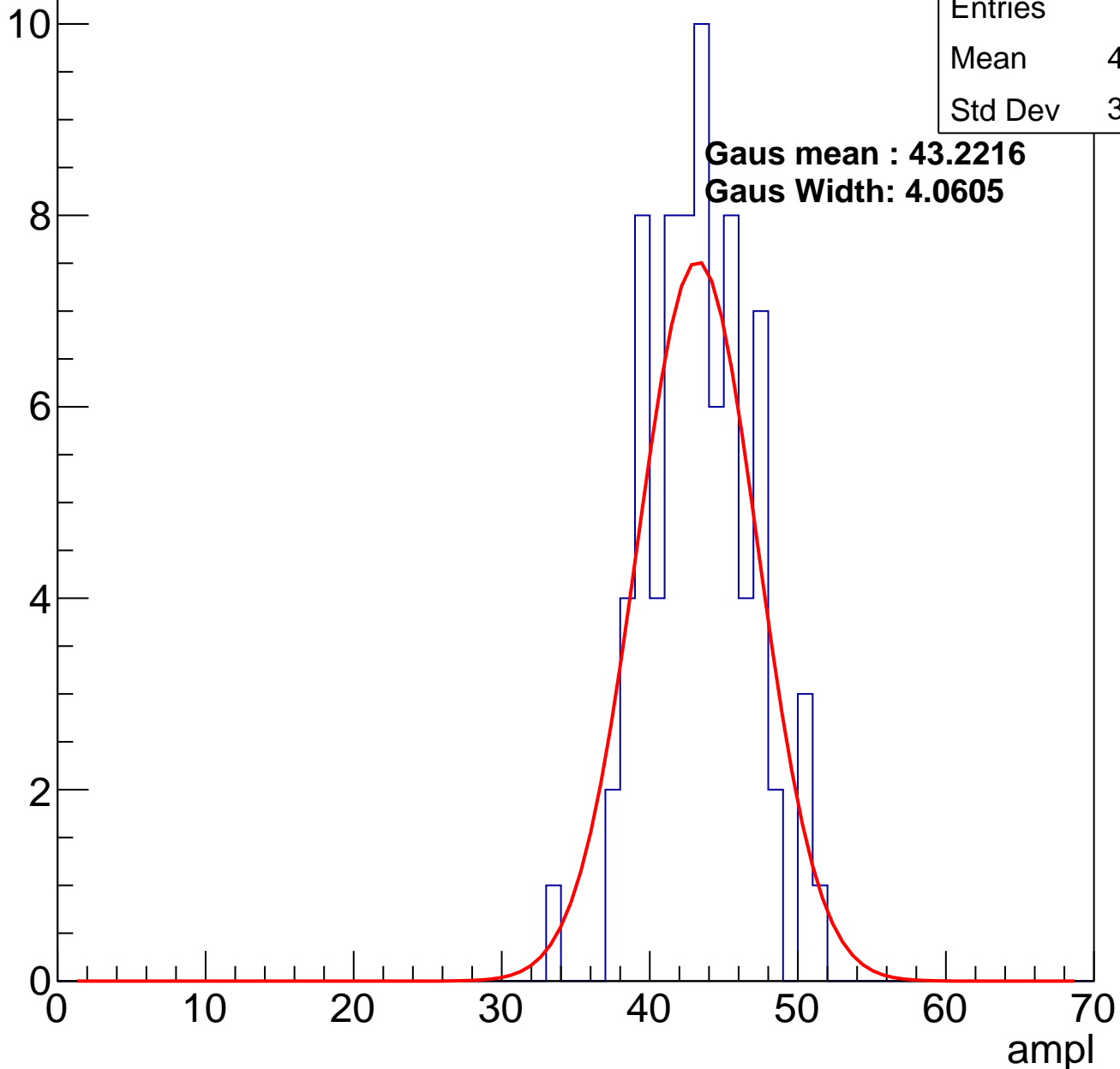
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.88 |
| Std Dev | 3.483 |

Gaus mean : 43.2216

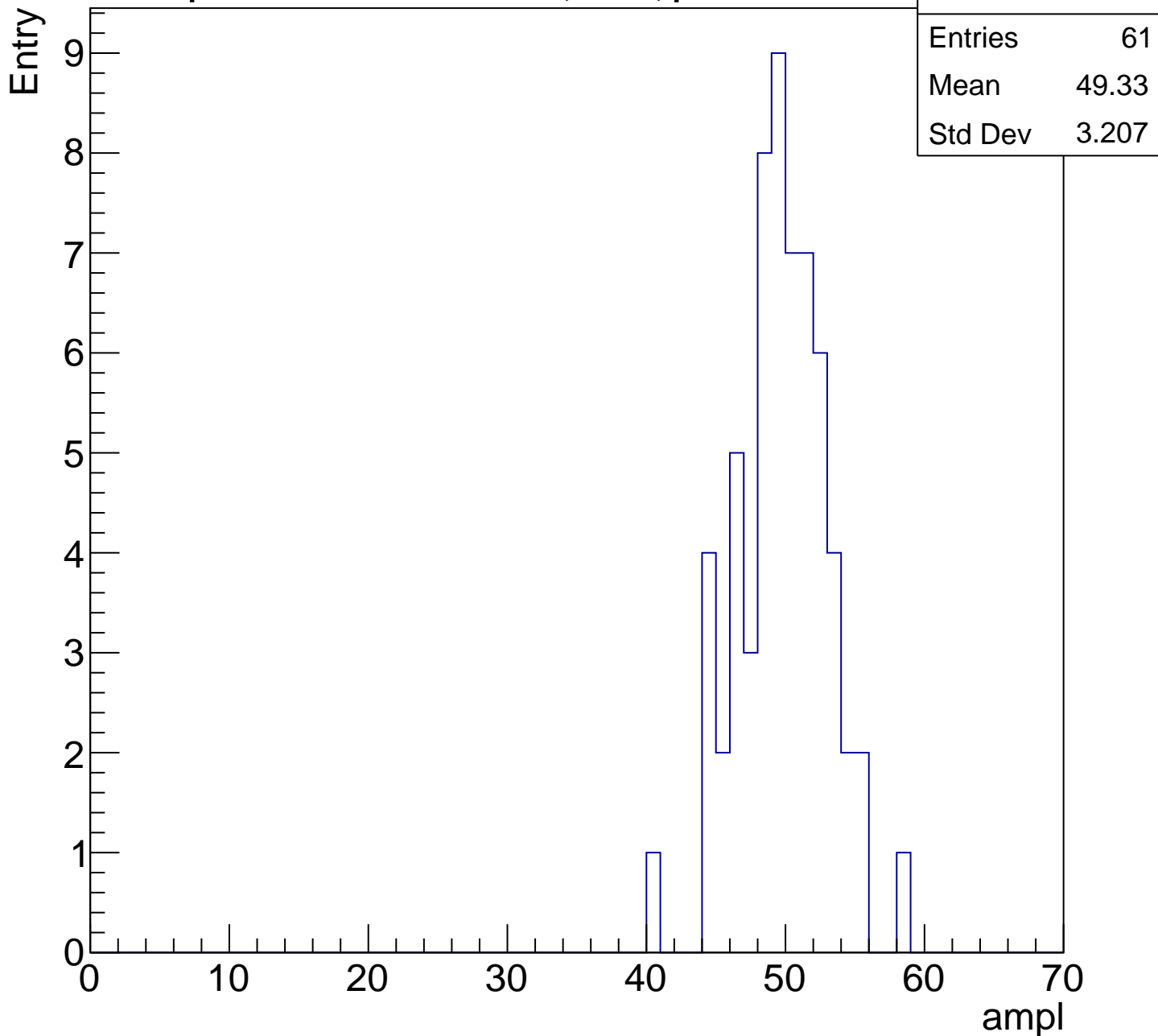
Gaus Width: 4.0605

Entry



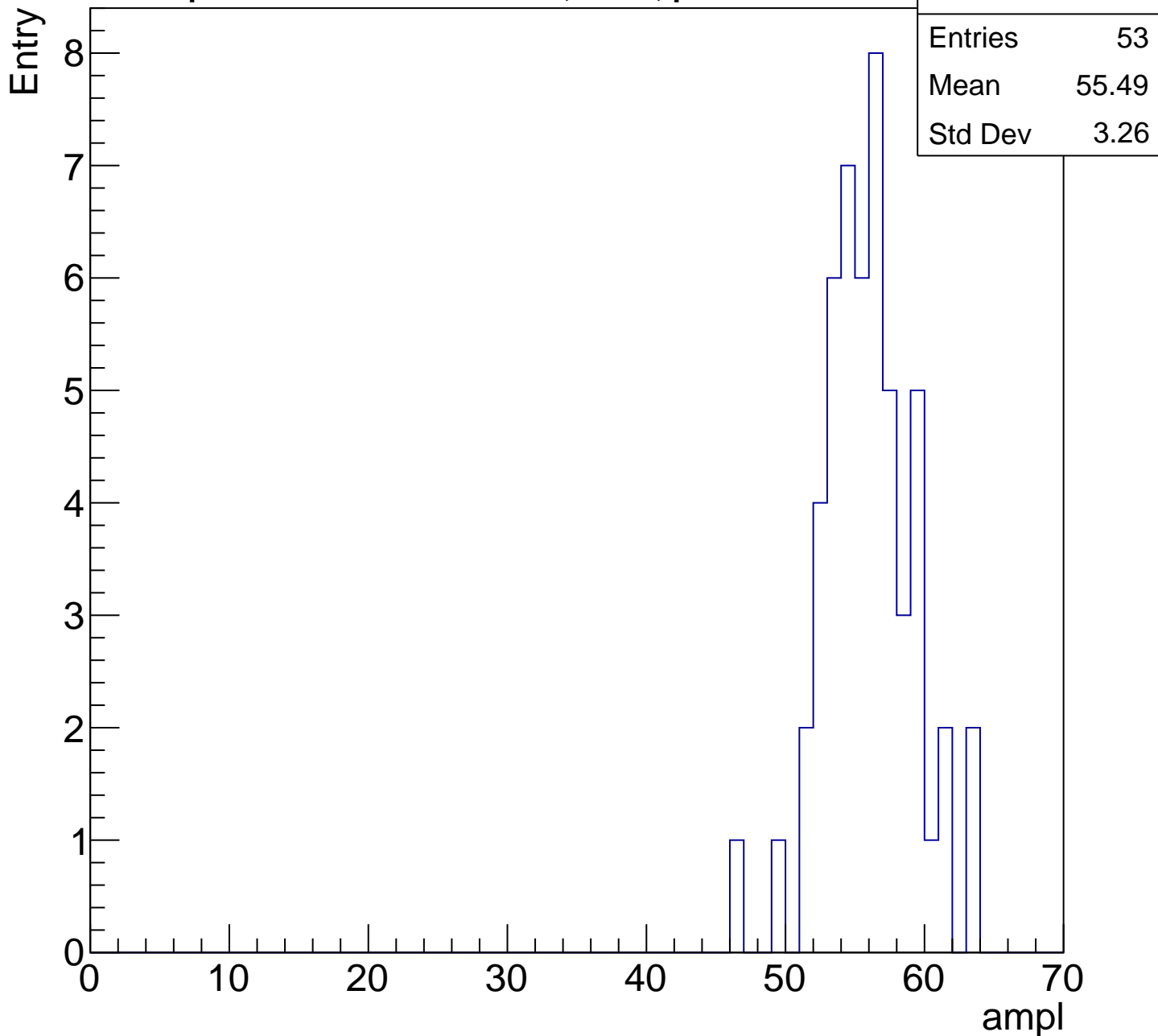
B1L100S, U9-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

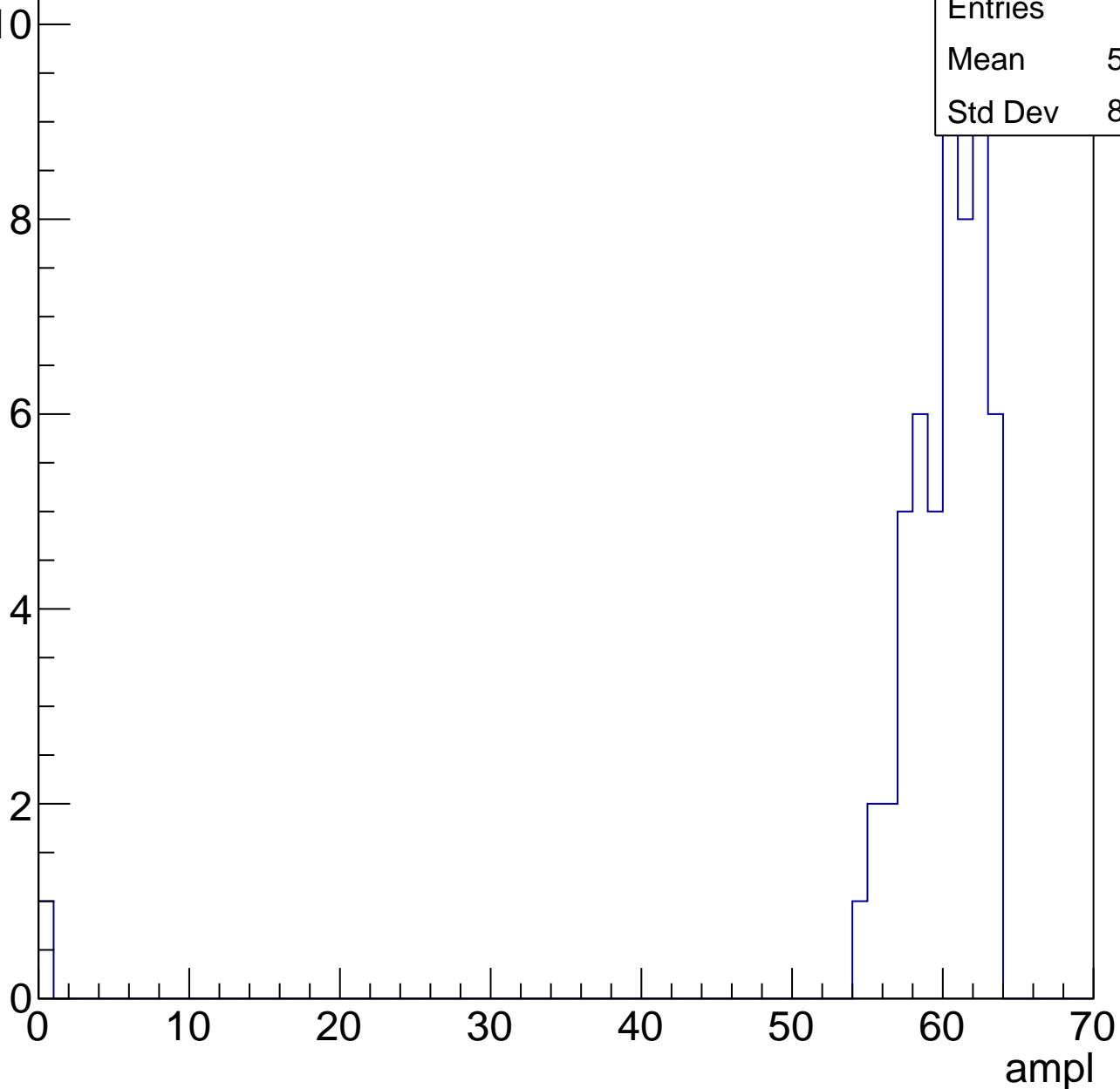


B1L100S, U9-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.73 |
| Std Dev | 8.317 |



B1L100S, U9-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch68, adc0

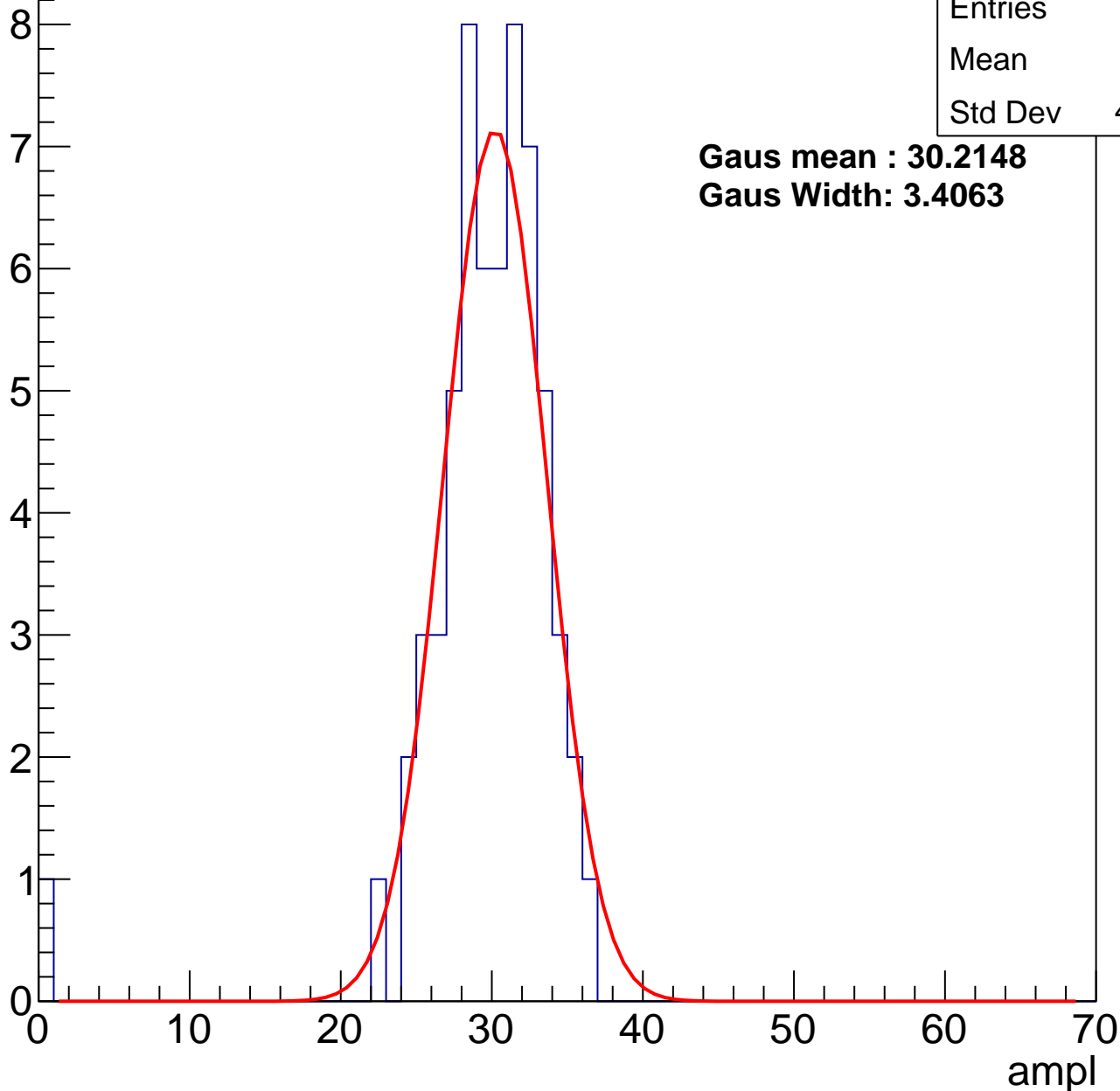
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.2 |
| Std Dev | 4.821 |

Gaus mean : 30.2148

Gaus Width: 3.4063



B1L100S, U9-ch68, adc1

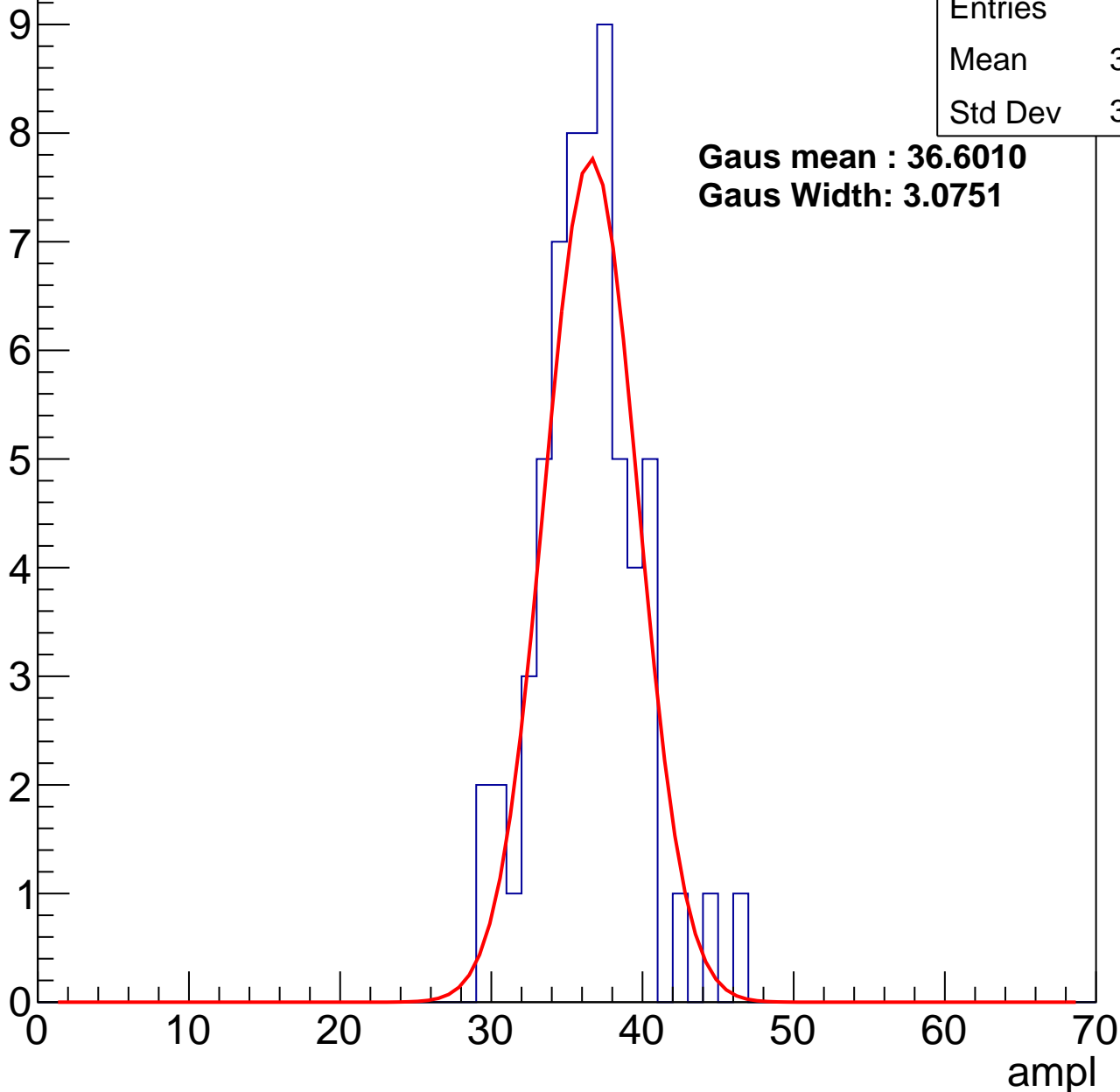
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.92 |
| Std Dev | 3.284 |

Gaus mean : 36.6010

Gaus Width: 3.0751



B1L100S, U9-ch68, adc2

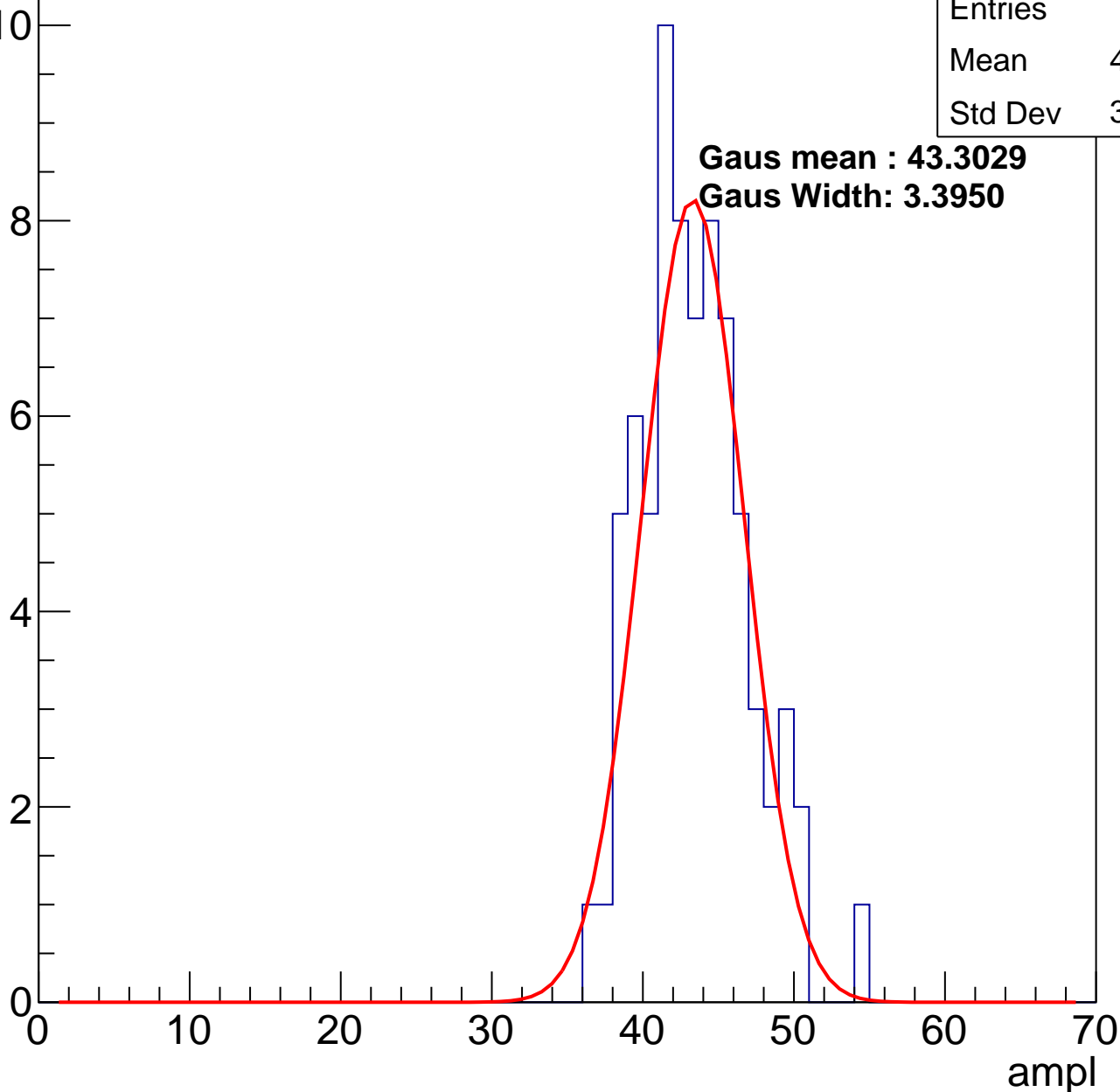
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 42.96 |
| Std Dev | 3.497 |

Gaus mean : 43.3029

Gaus Width: 3.3950

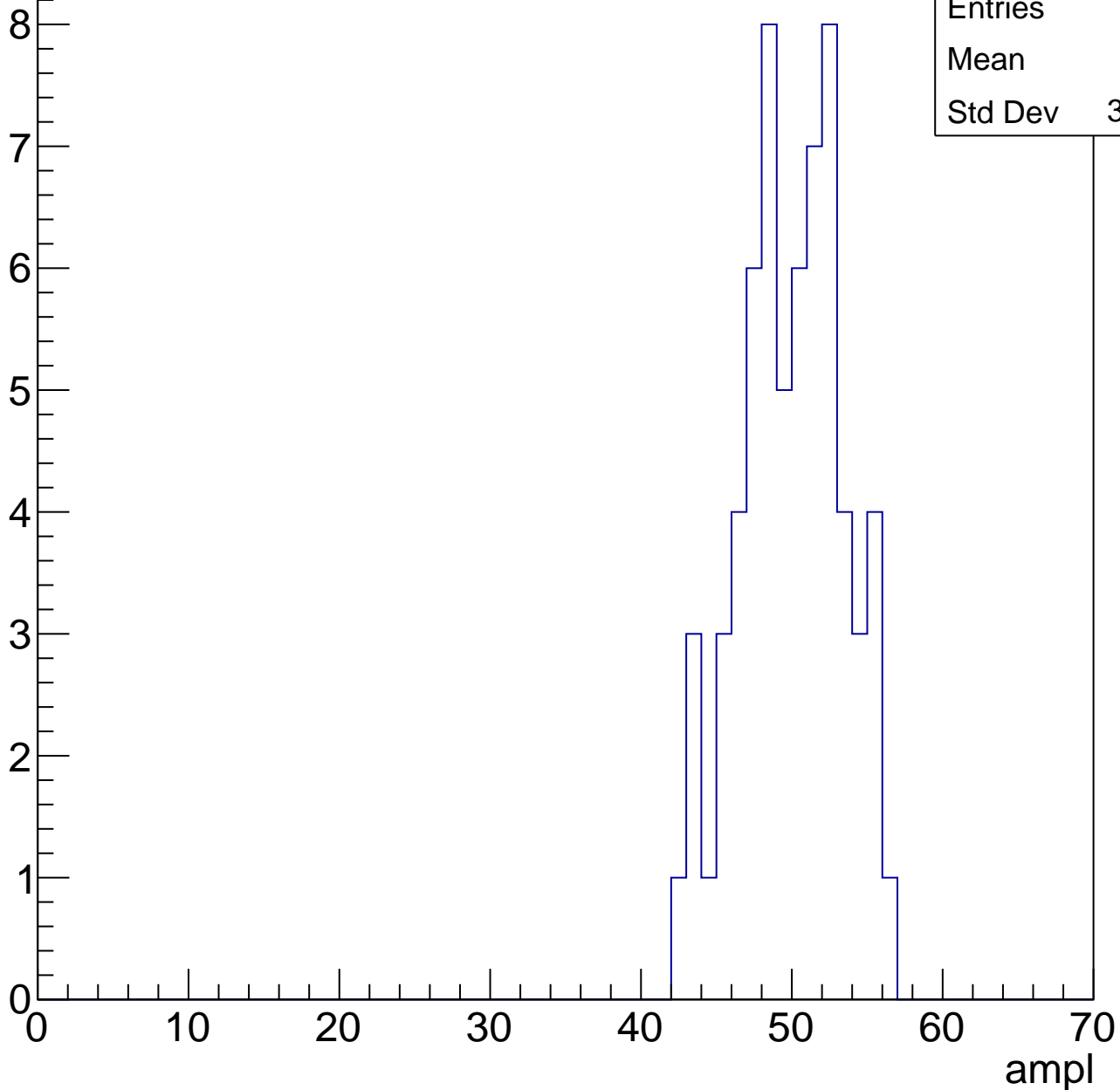


B1L100S, U9-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.5 |
| Std Dev | 3.354 |

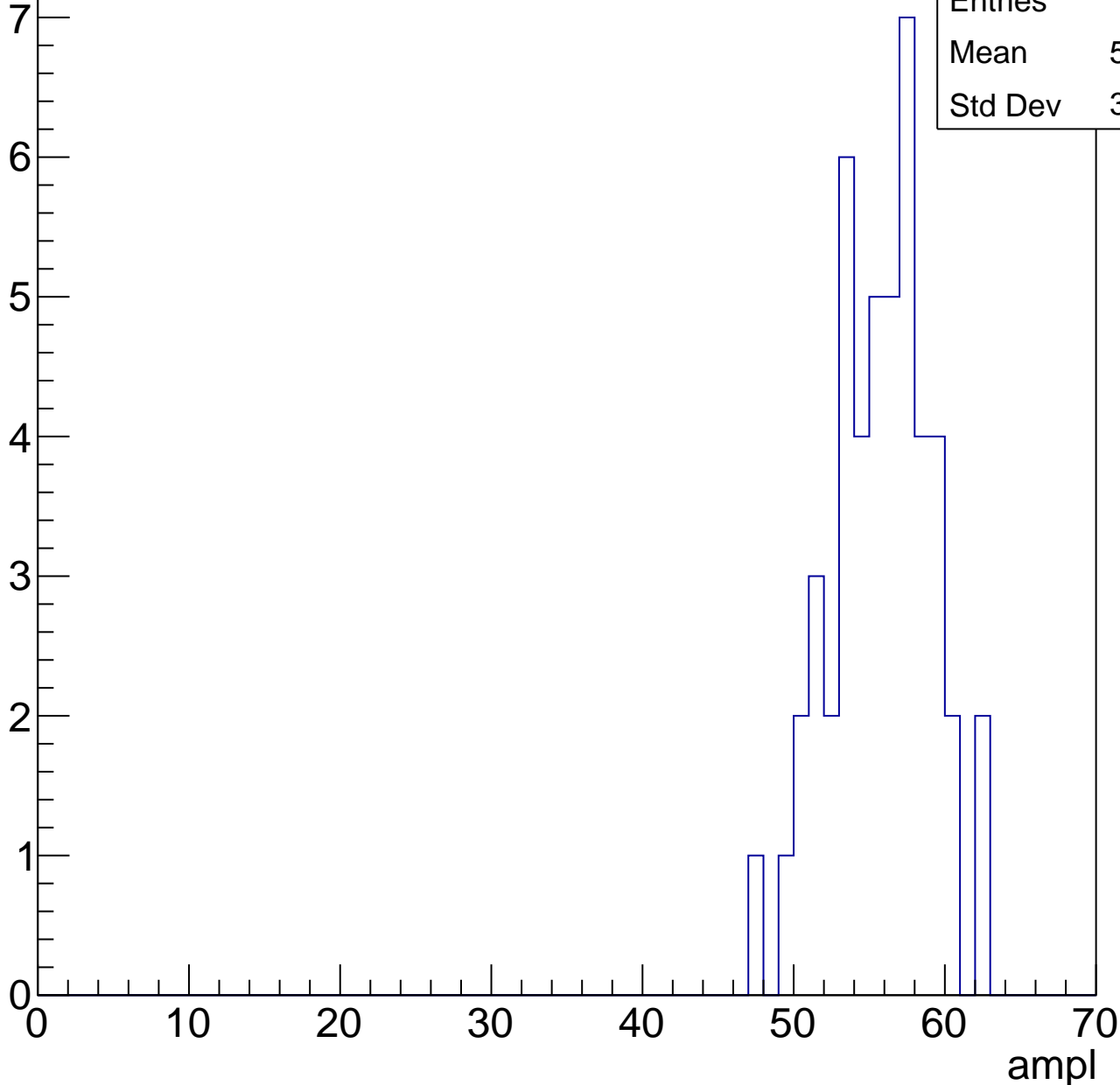


B1L100S, U9-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 55.27 |
| Std Dev | 3.296 |

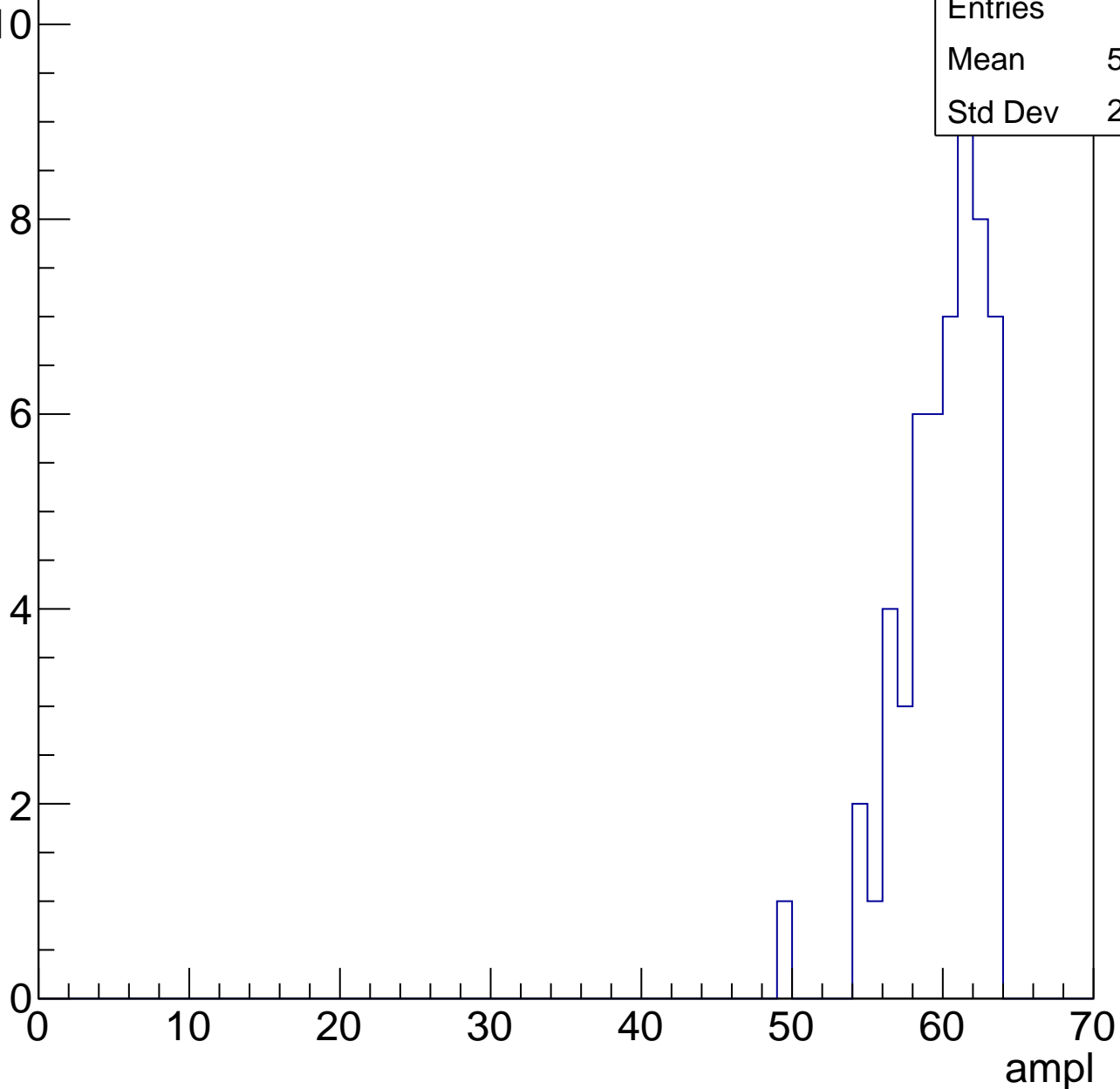


B1L100S, U9-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

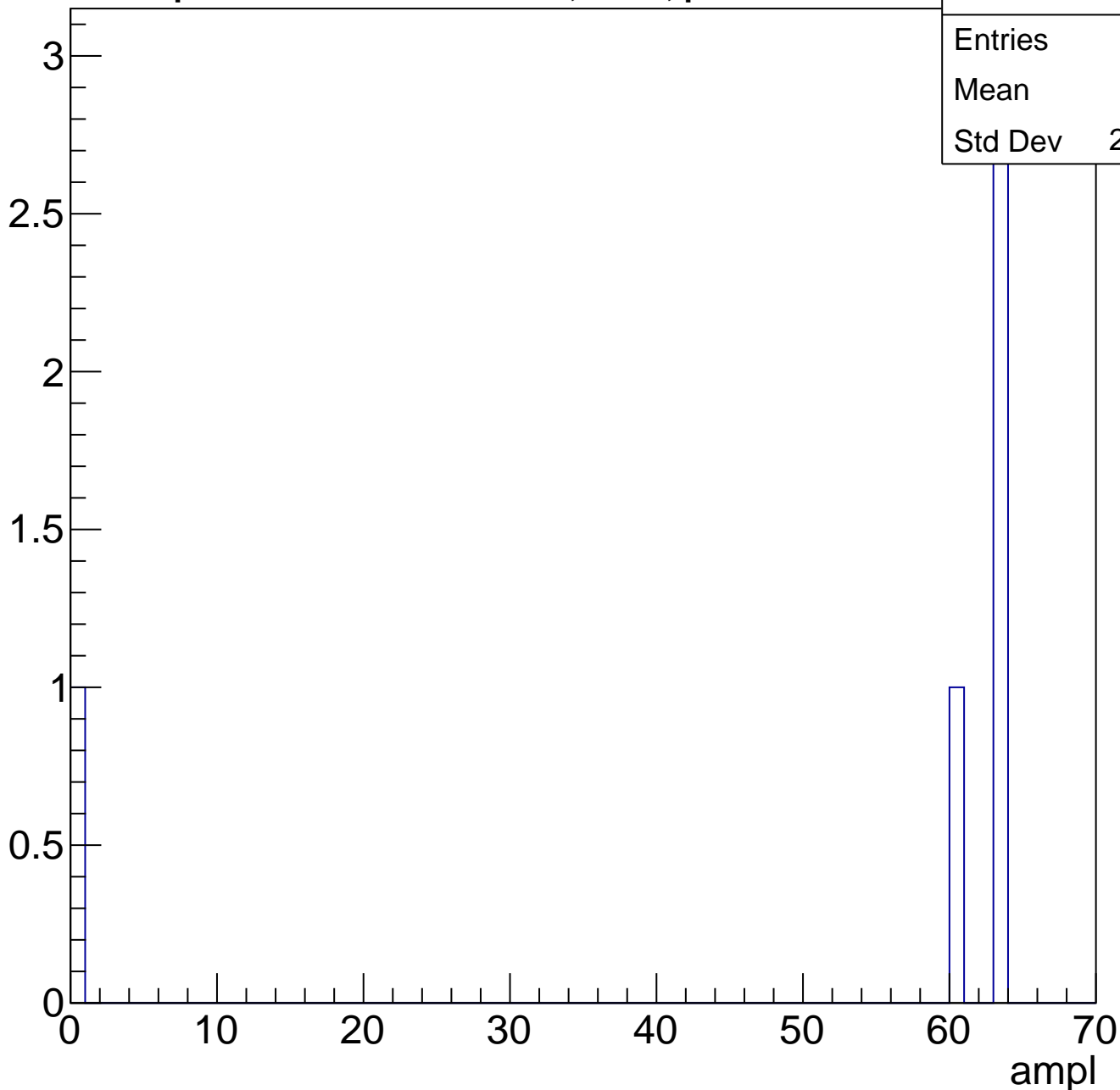
| | |
|---------|-------|
| Entries | 55 |
| Mean | 59.56 |
| Std Dev | 2.808 |



B1L100S, U9-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch69, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 53 |
| Mean | 26.85 |
| Std Dev | 4.768 |

Gaus mean : 27.8069

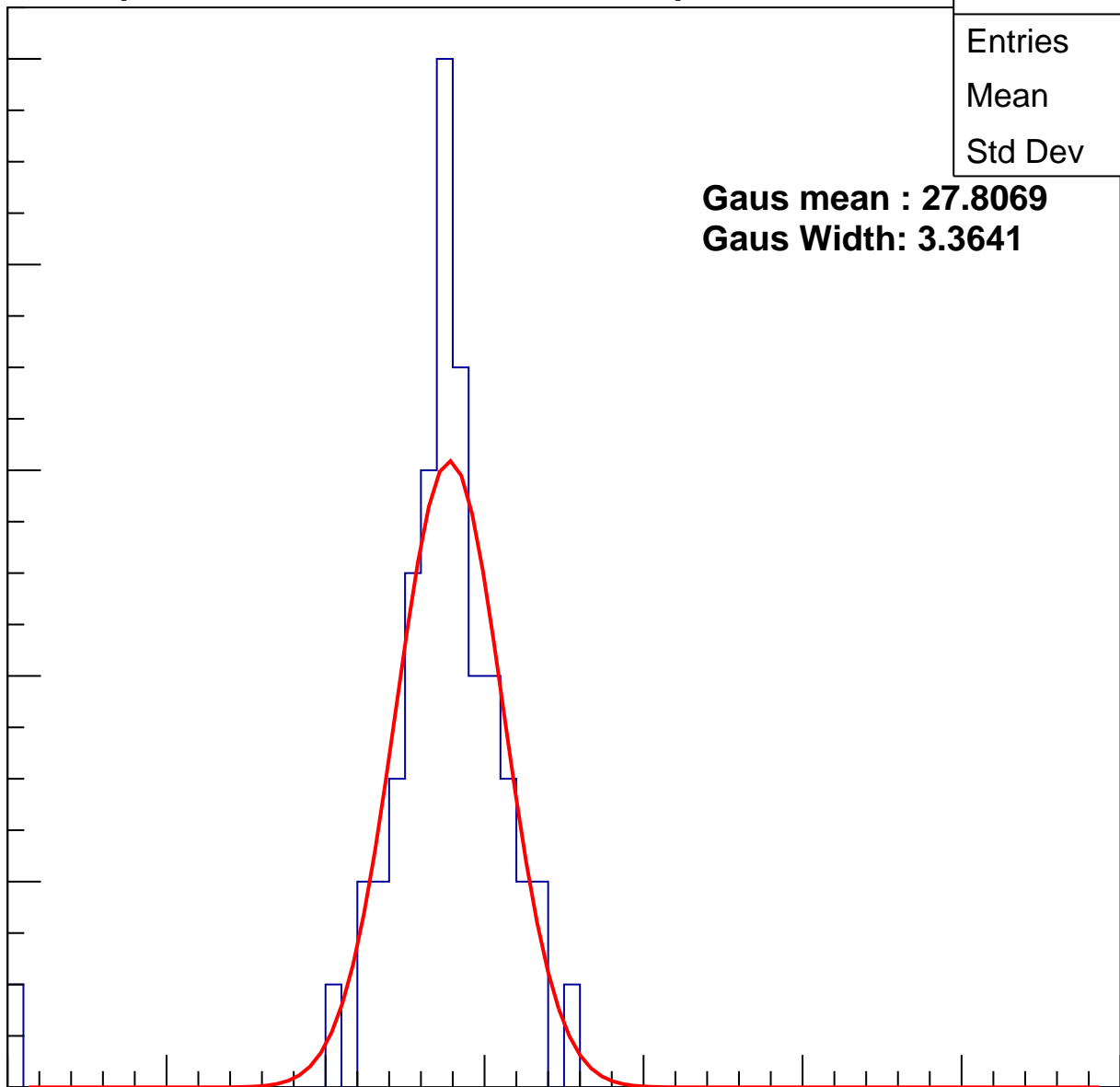
Gaus Width: 3.3641

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch69, adc1

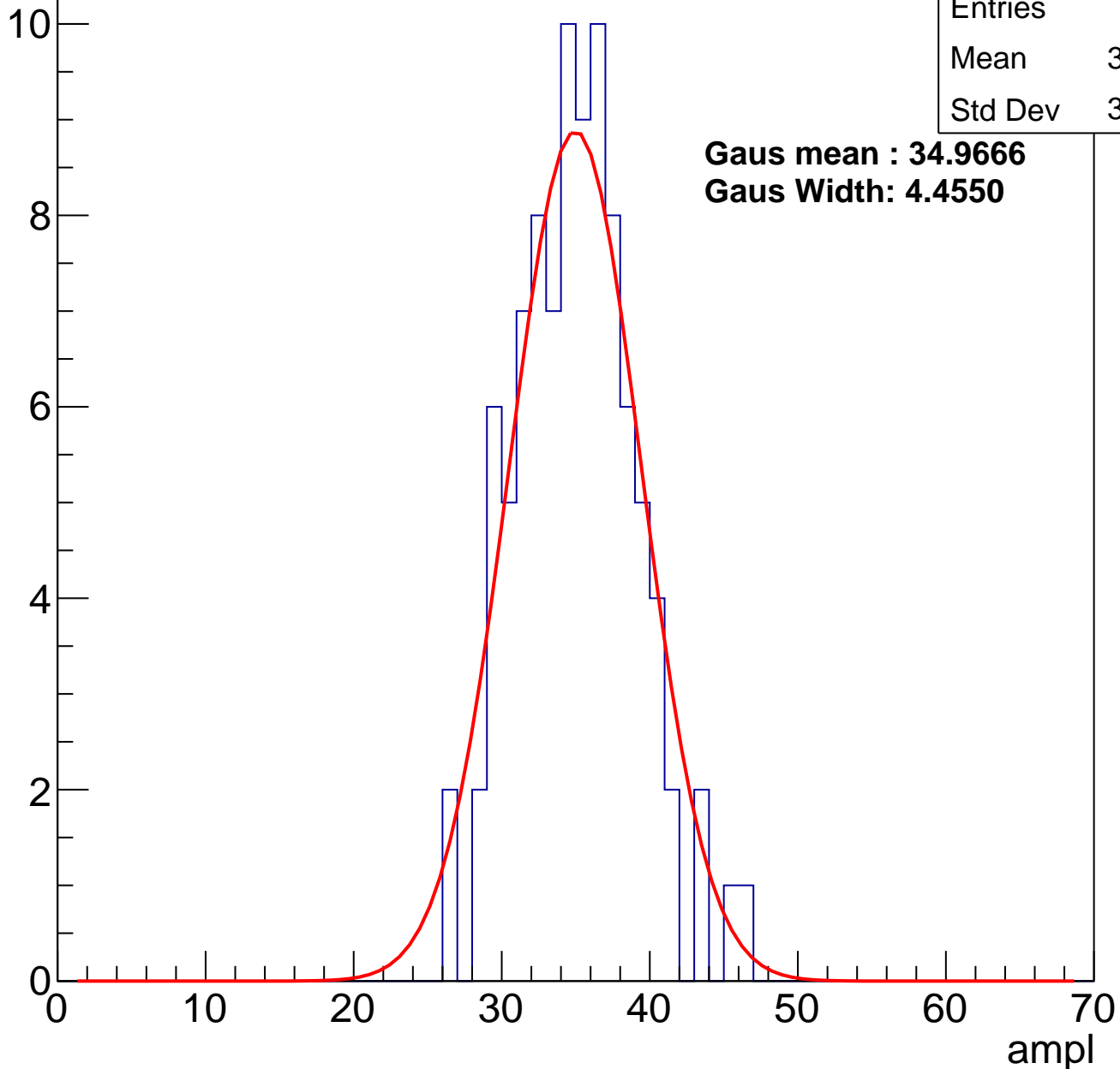
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 95 |
| Mean | 34.62 |
| Std Dev | 3.982 |

Gaus mean : 34.9666

Gaus Width: 4.4550

Entry



B1L100S, U9-ch69, adc2

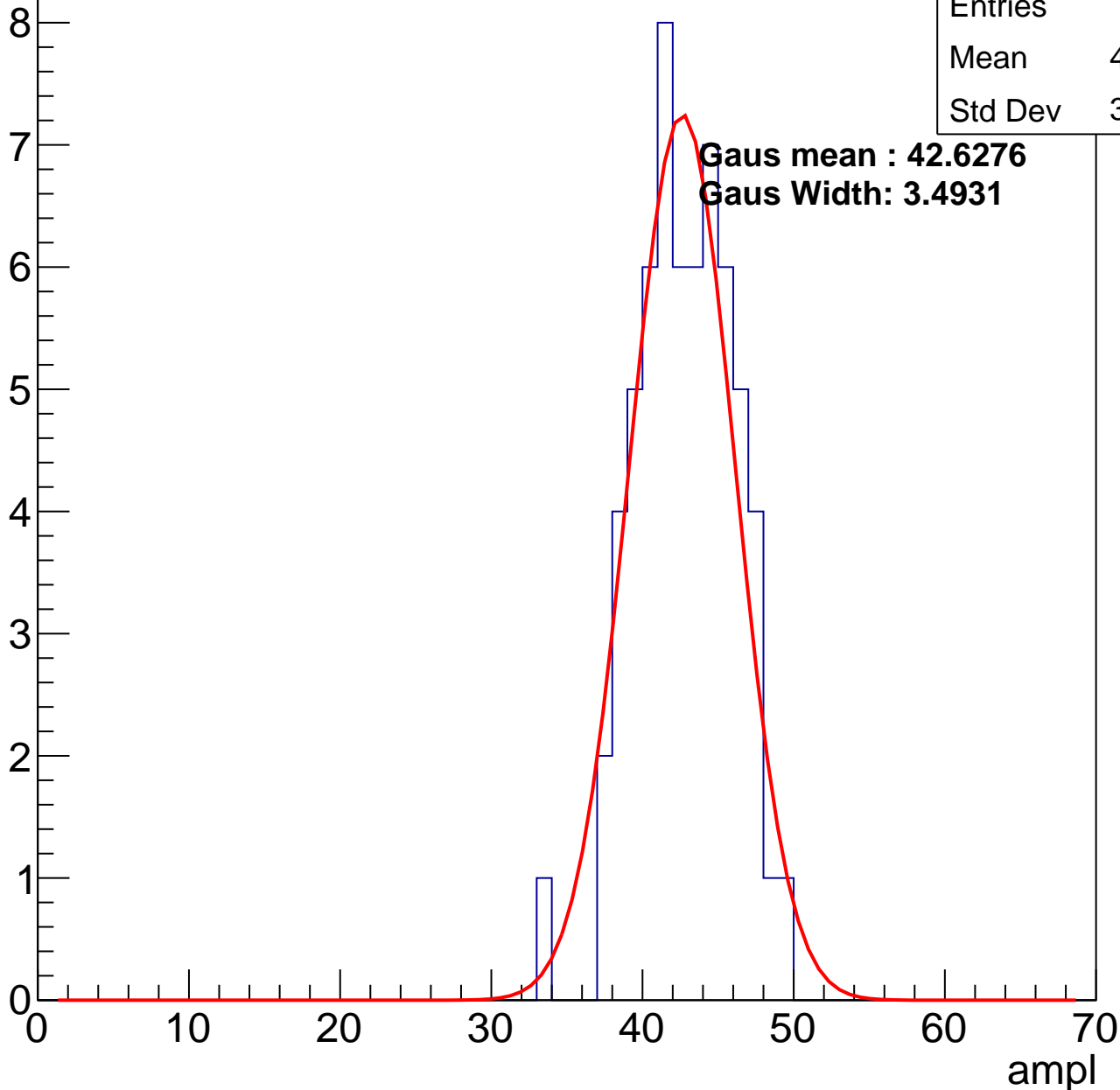
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.34 |
| Std Dev | 3.157 |

Gaus mean : 42.6276

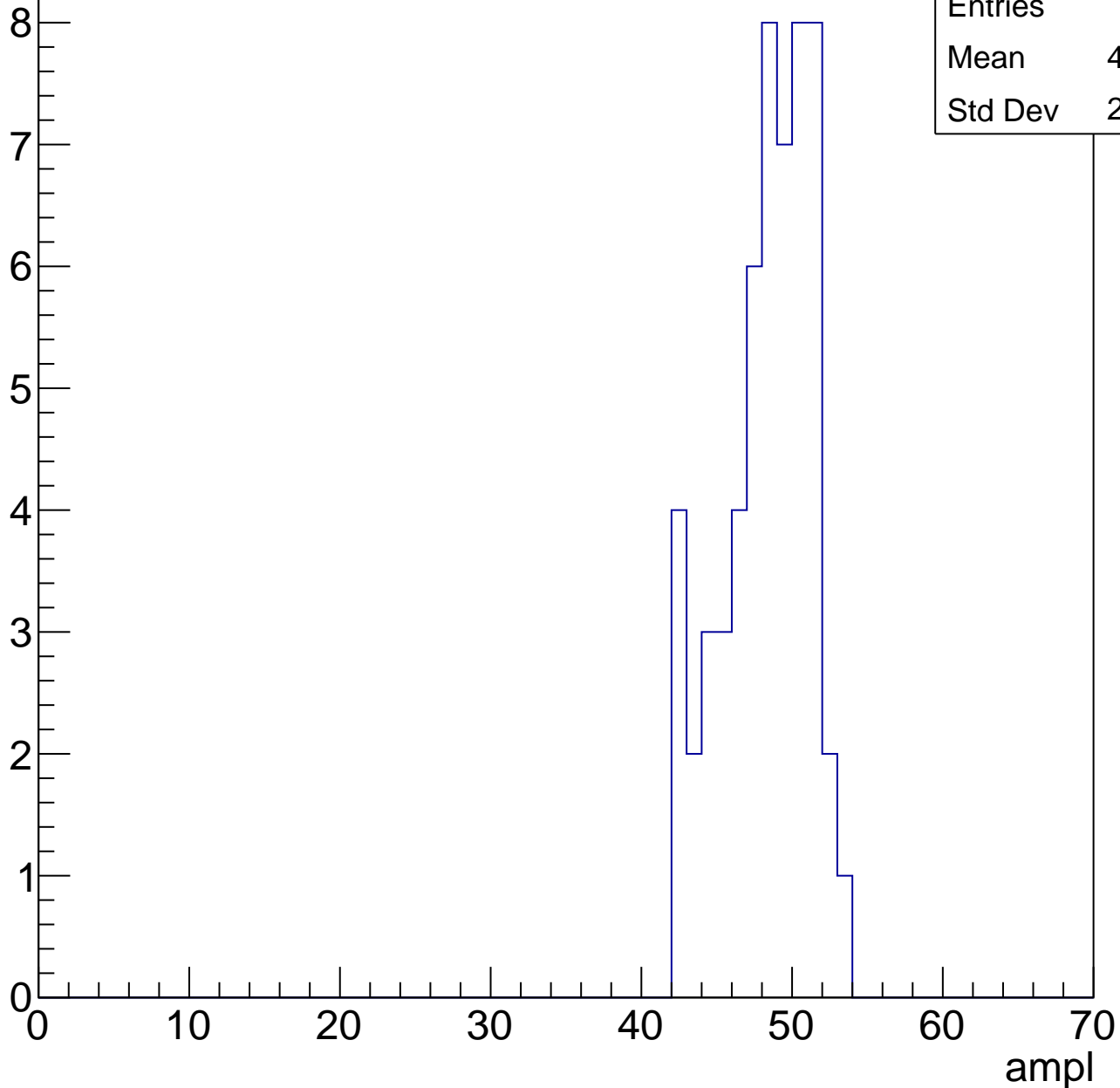
Gaus Width: 3.4931



B1L100S, U9-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

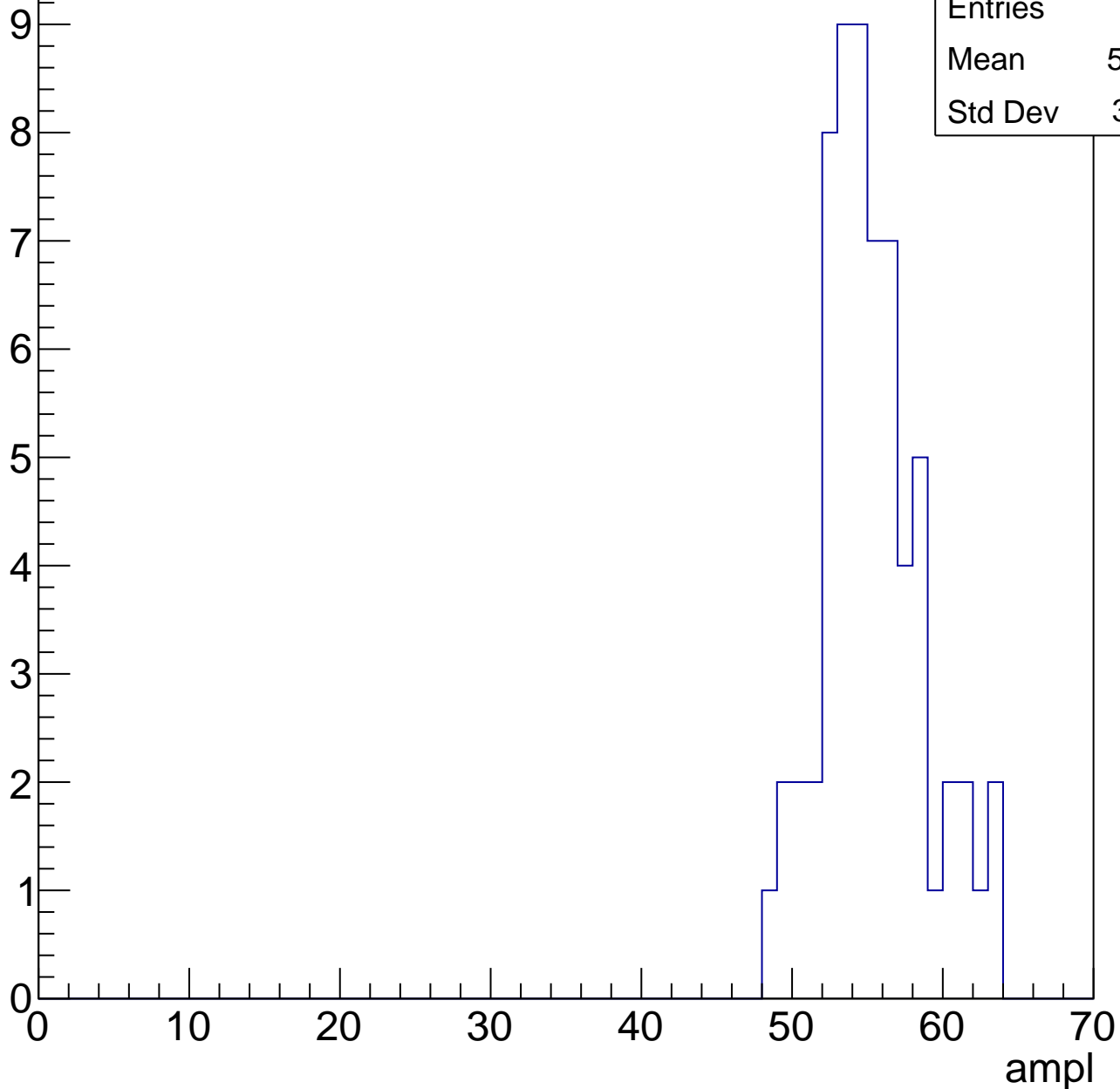


| | |
|---------|-------|
| Entries | 56 |
| Mean | 47.84 |
| Std Dev | 2.858 |

B1L100S, U9-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

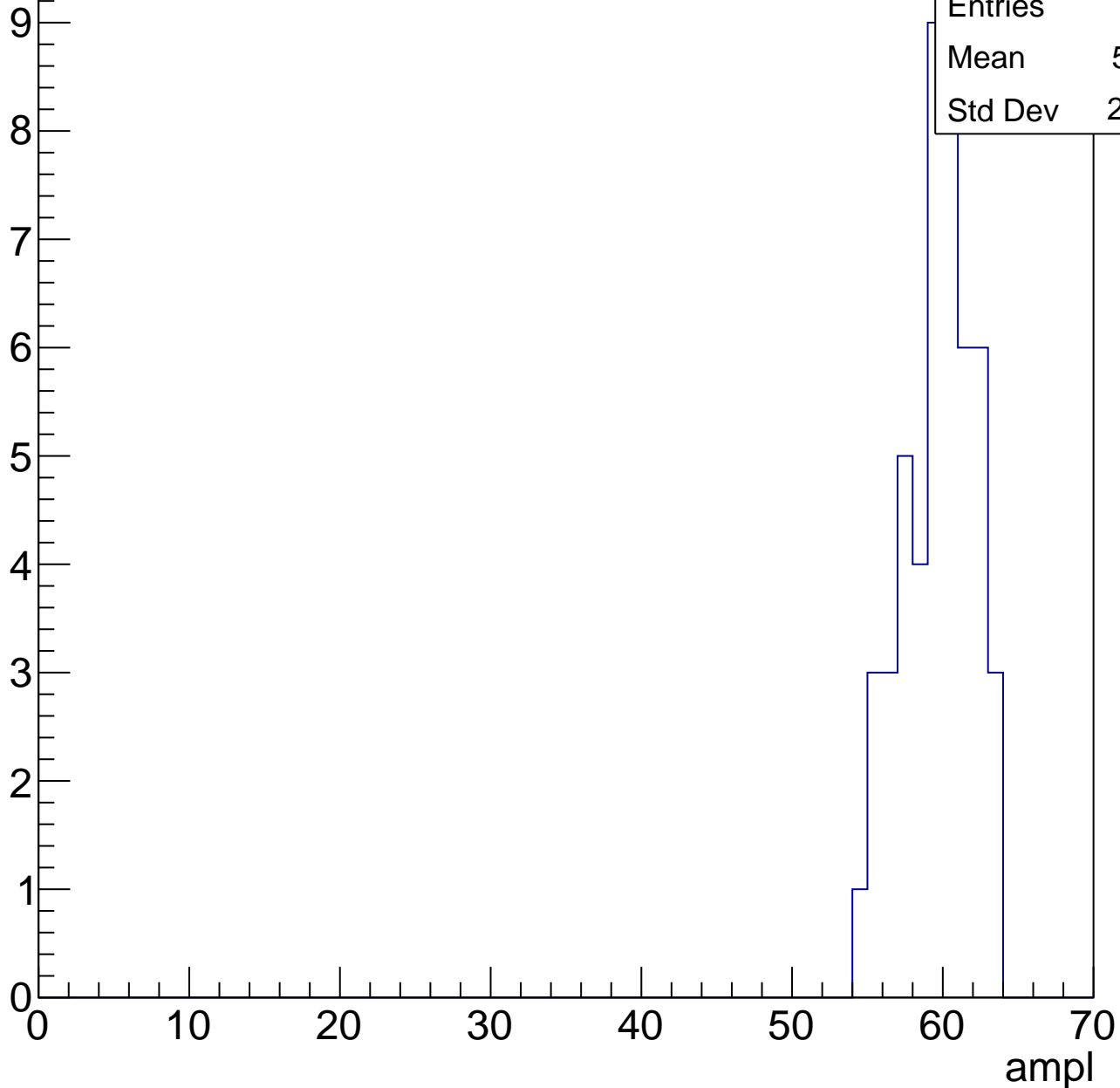
Entry



B1L100S, U9-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



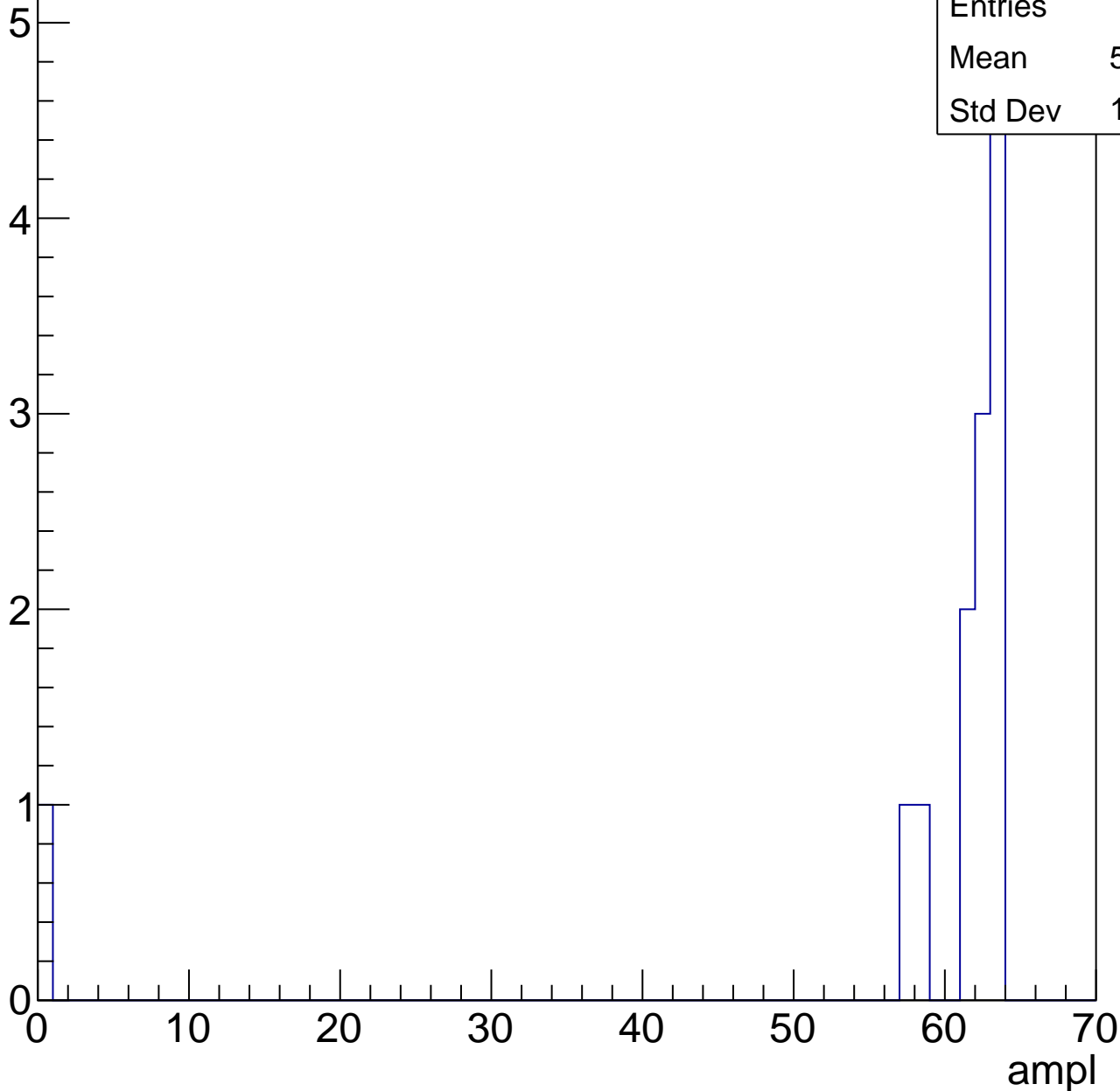
| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.21 |
| Std Dev | 2.309 |

B1L100S, U9-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

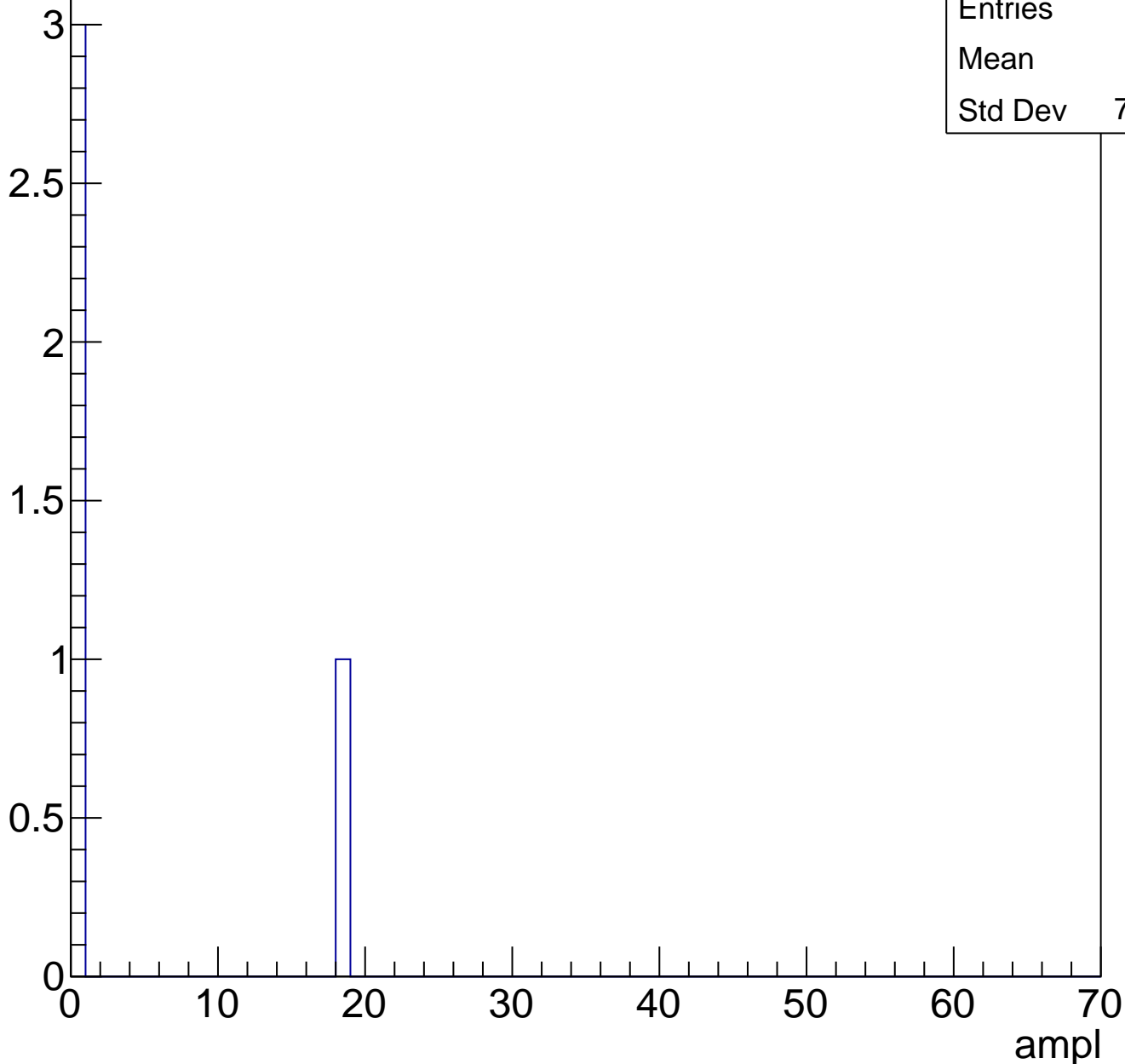
| | |
|---------|-------|
| Entries | 13 |
| Mean | 56.77 |
| Std Dev | 16.49 |



B1L100S, U9-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch70, adc0

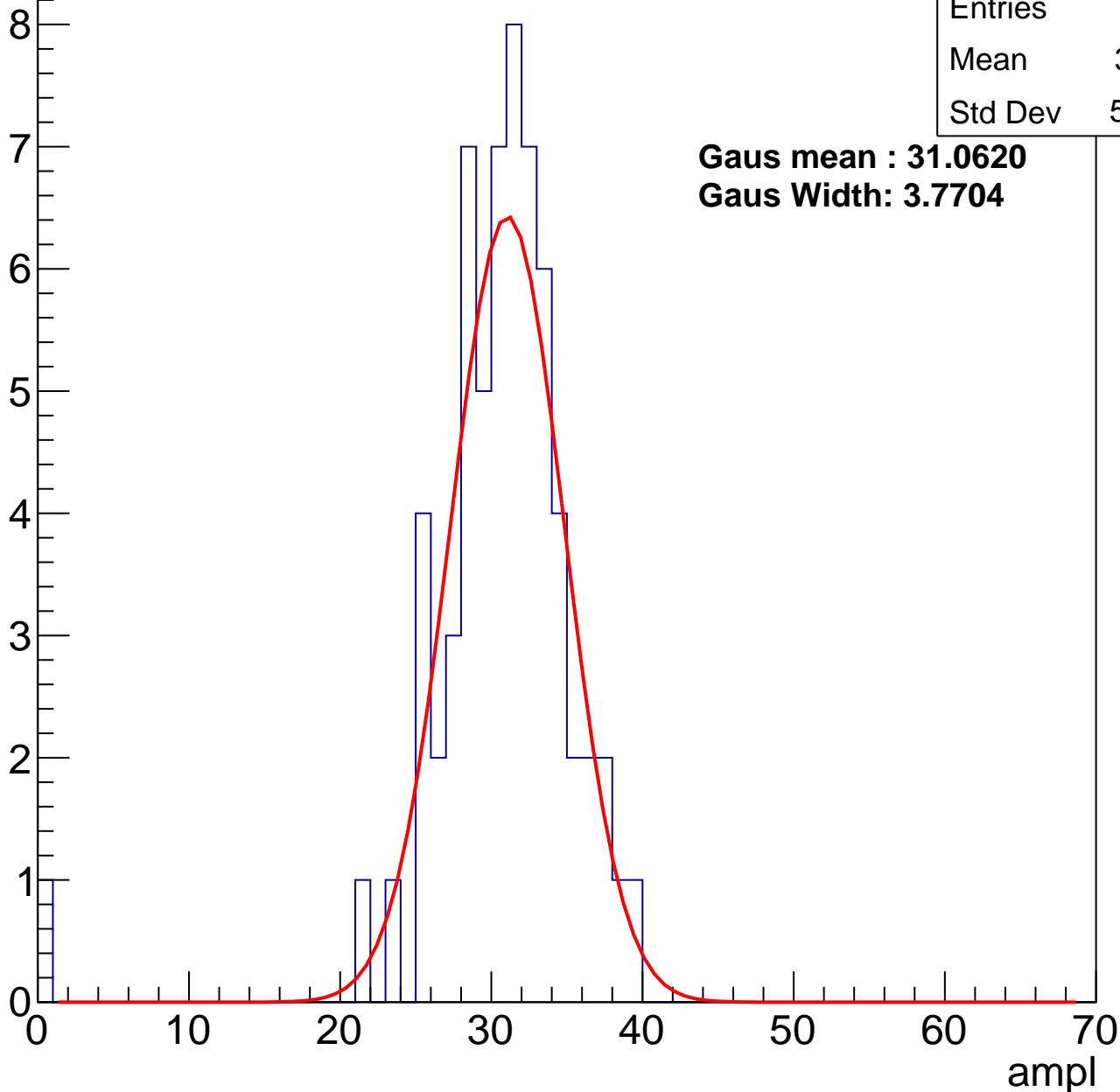
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.11 |
| Std Dev | 5.215 |

Gaus mean : 31.0620

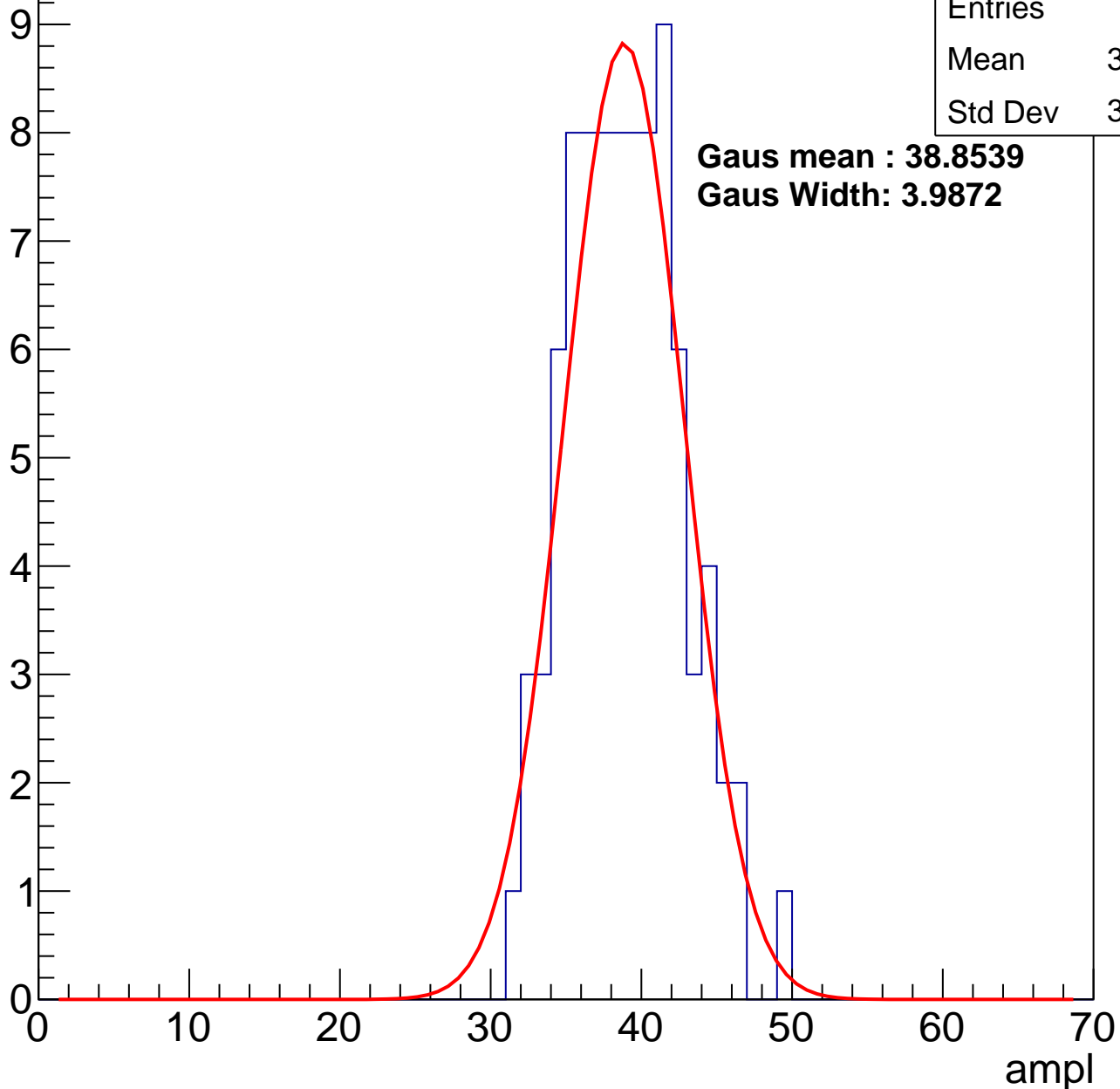
Gaus Width: 3.7704



B1L100S, U9-ch70, adc1

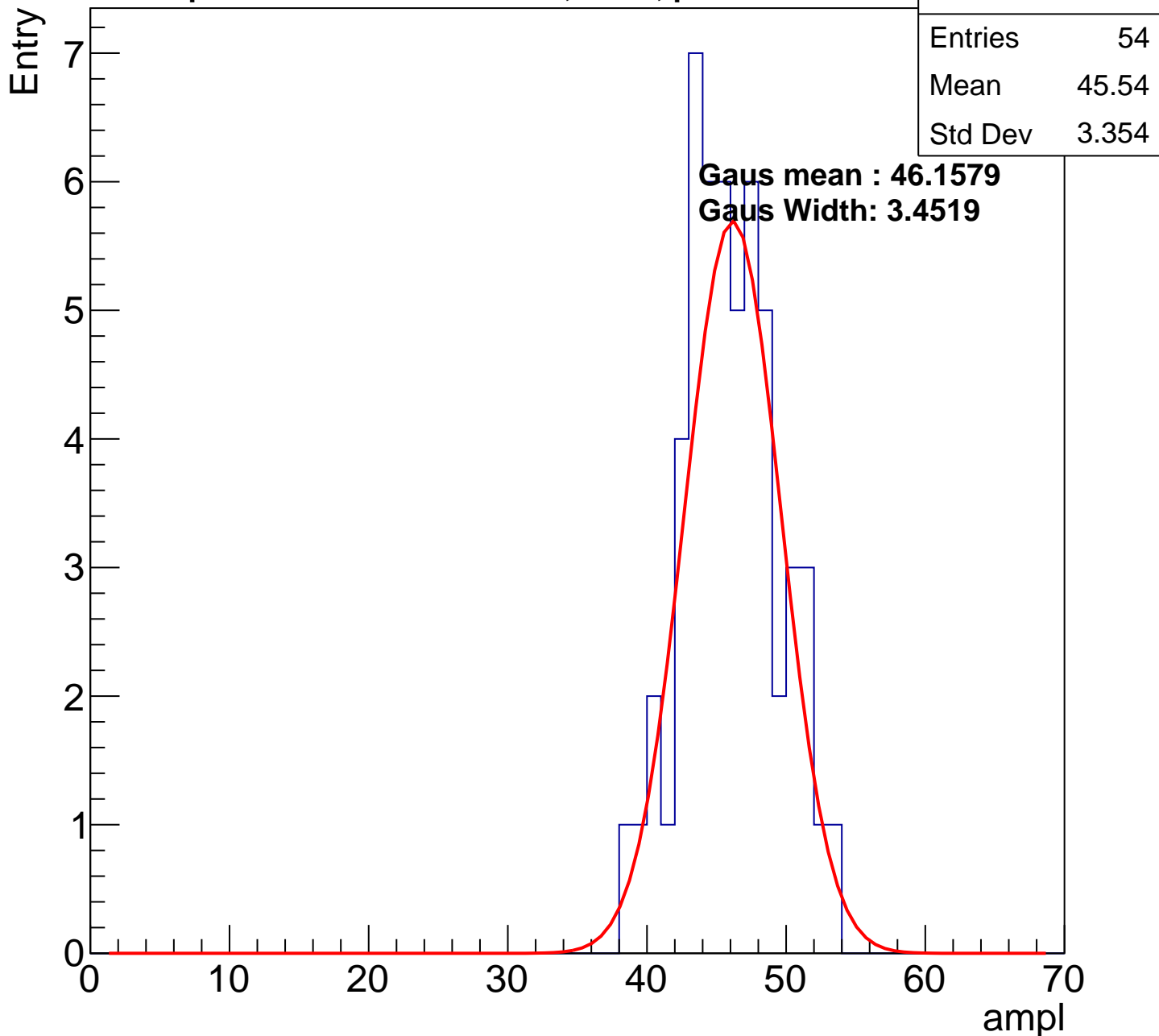
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch70, adc2

calib_packv5_042523_0143.root, FC#4, port A2

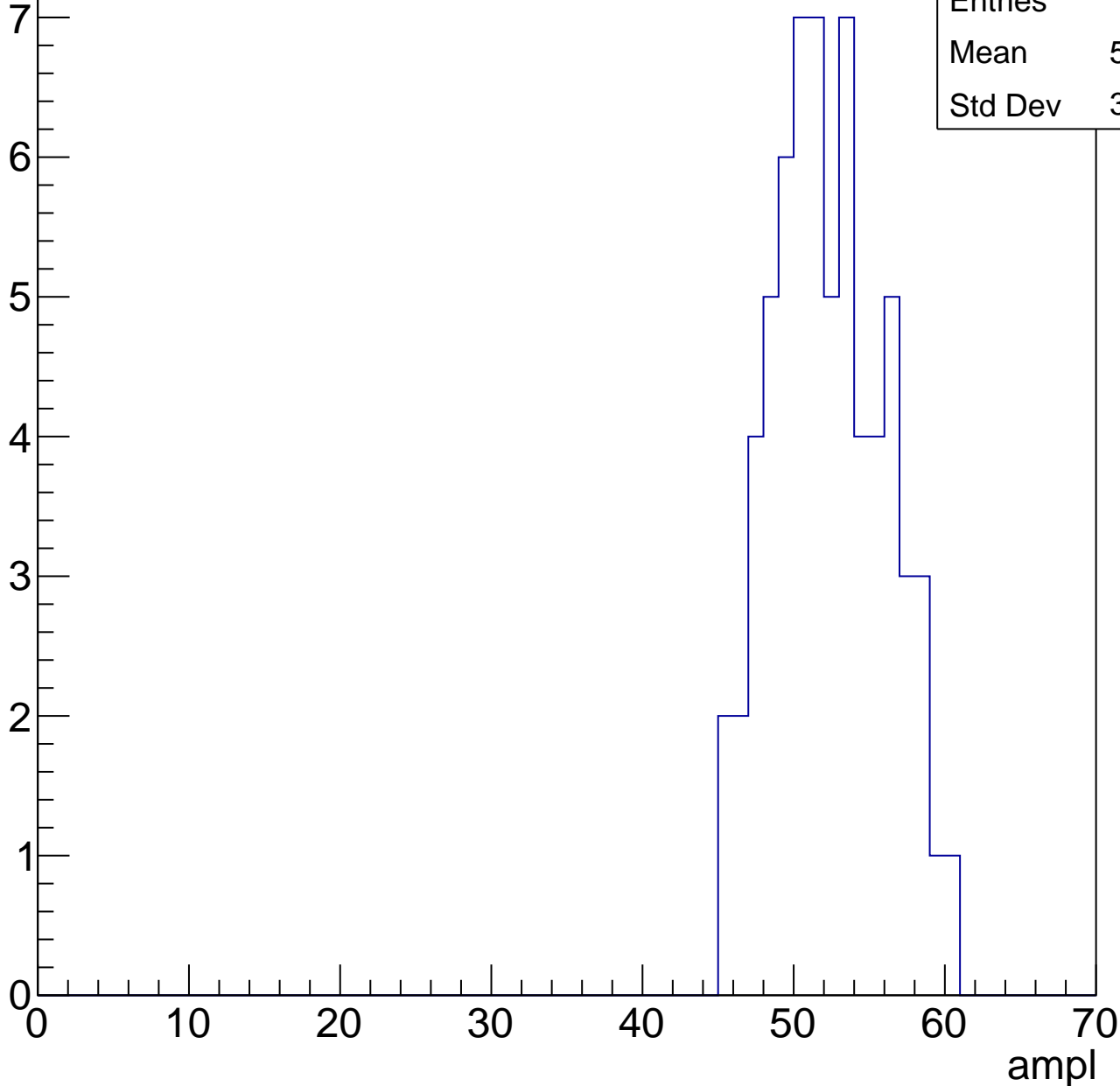


B1L100S, U9-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 51.85 |
| Std Dev | 3.636 |

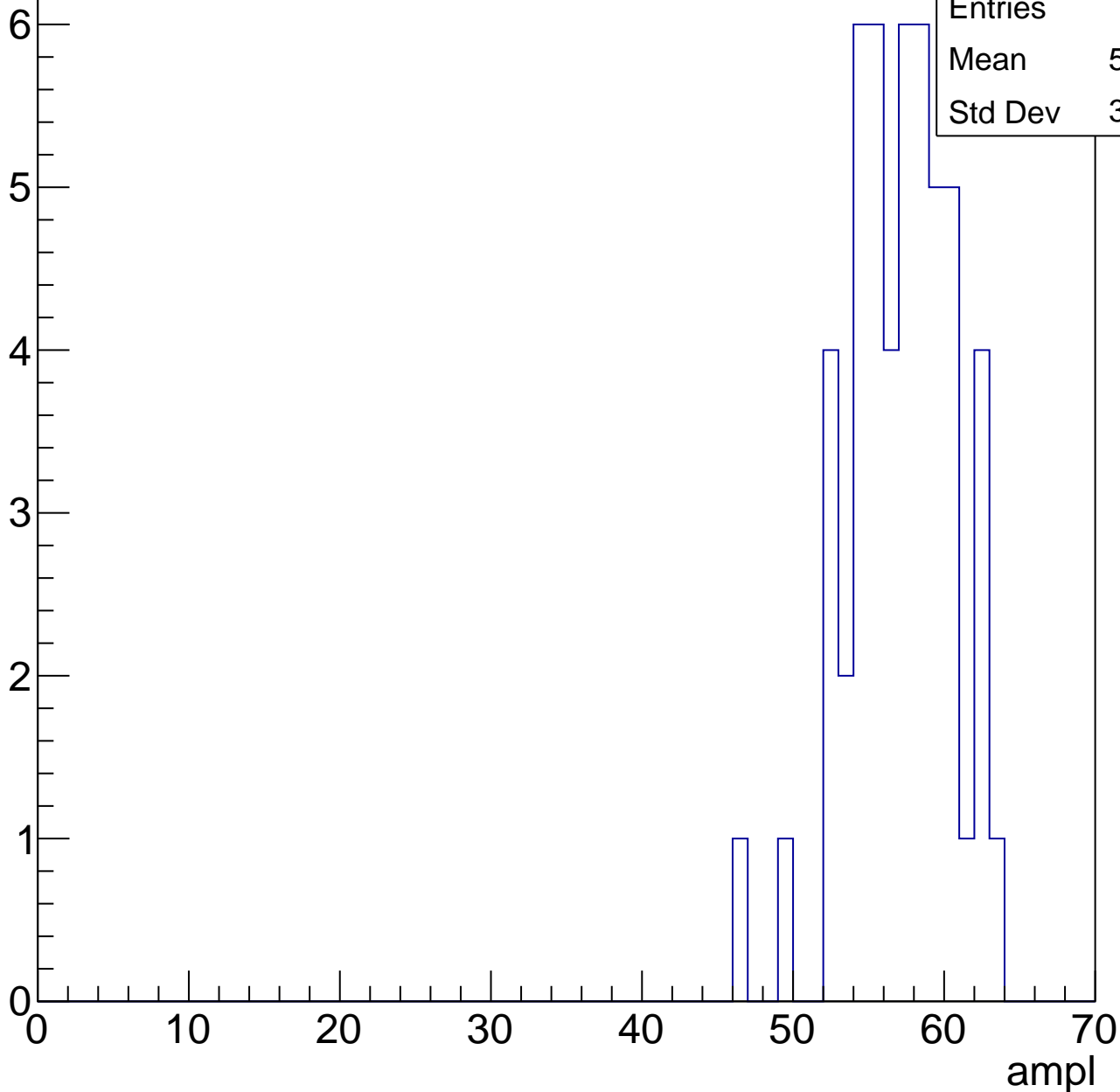


B1L100S, U9-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

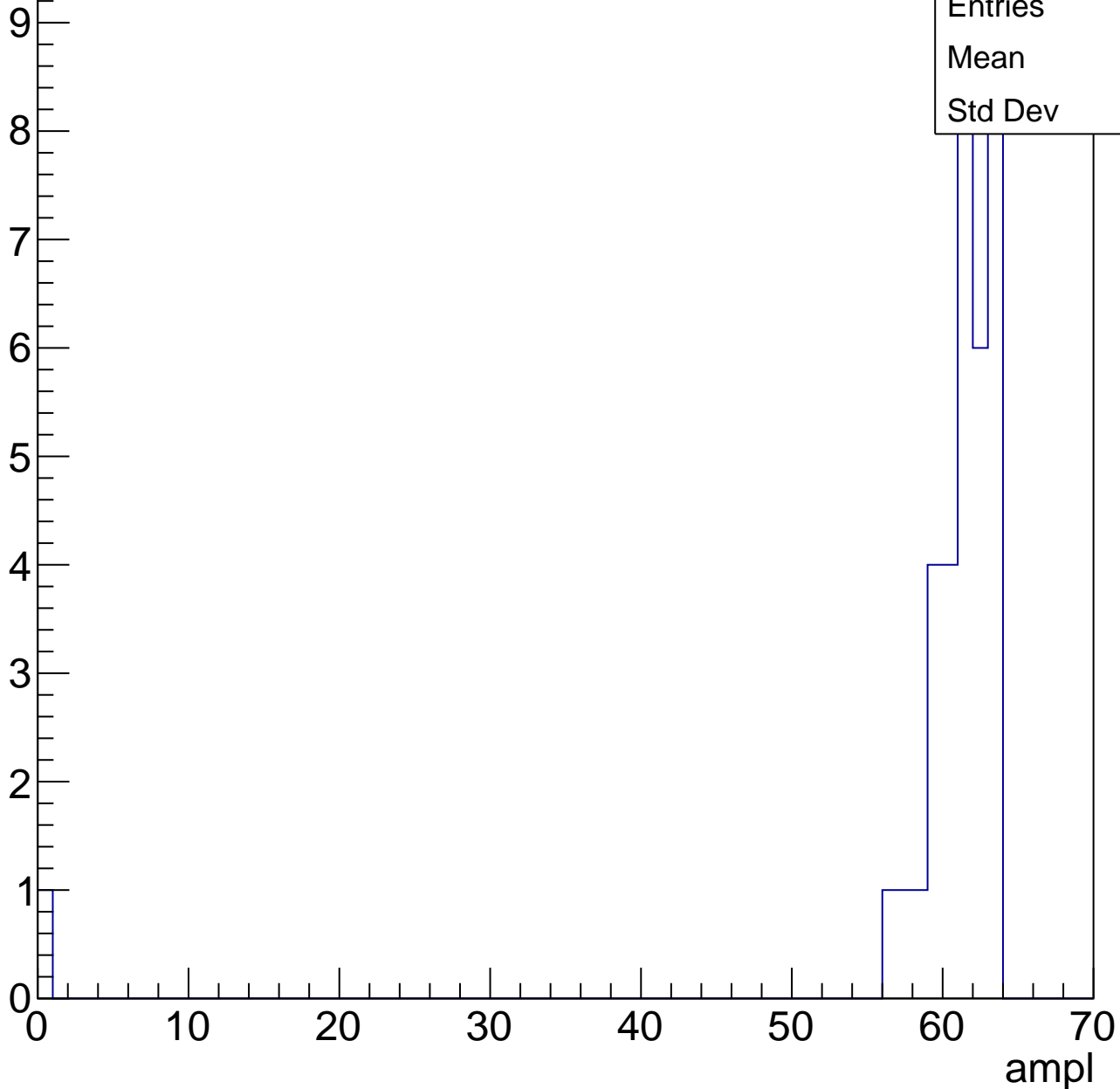
| | |
|---------|-------|
| Entries | 52 |
| Mean | 56.62 |
| Std Dev | 3.437 |



B1L100S, U9-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch71, adc0

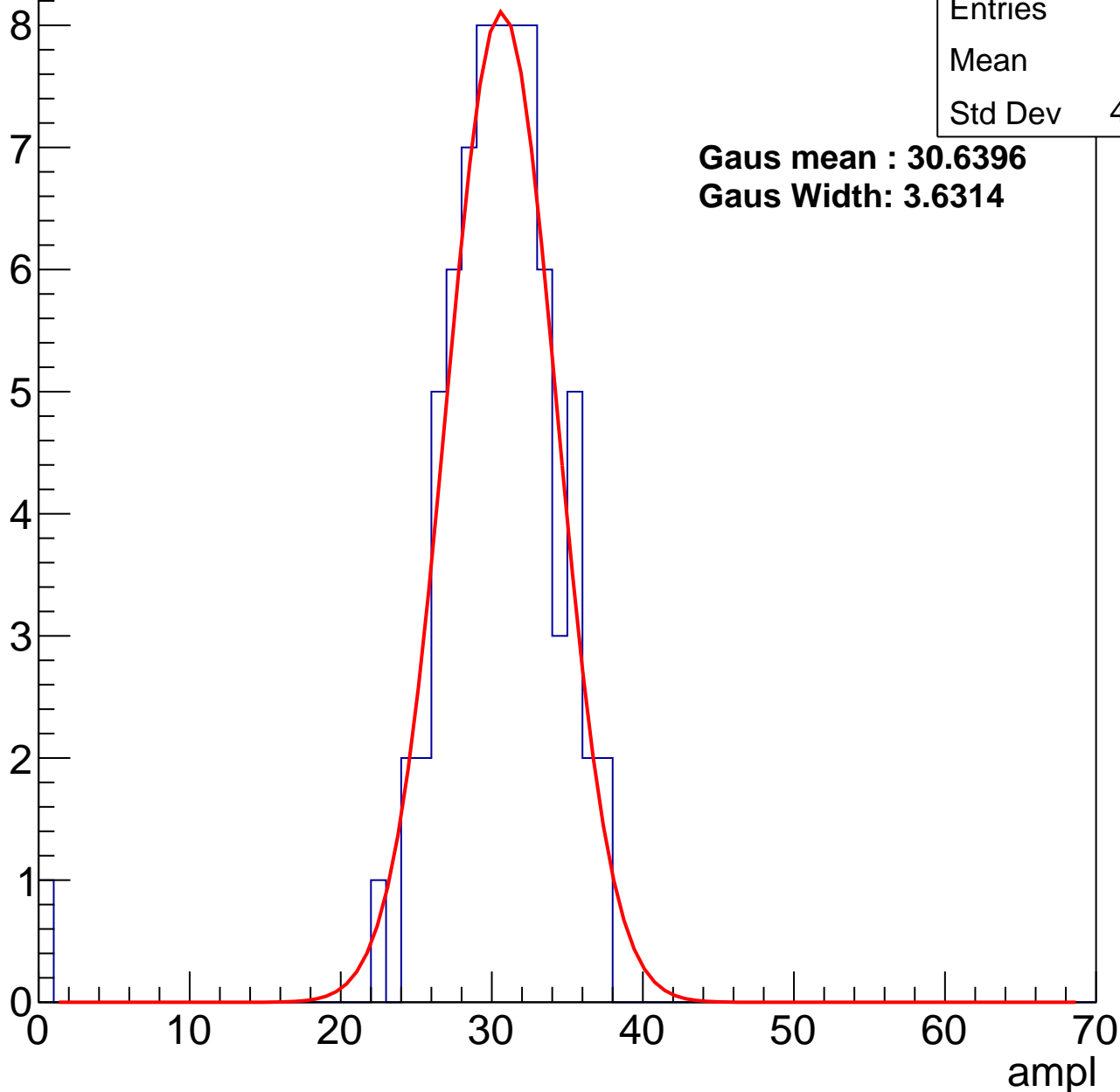
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.8 |
| Std Dev | 4.779 |

Gaus mean : 30.6396

Gaus Width: 3.6314



B1L100S, U9-ch71, adc1

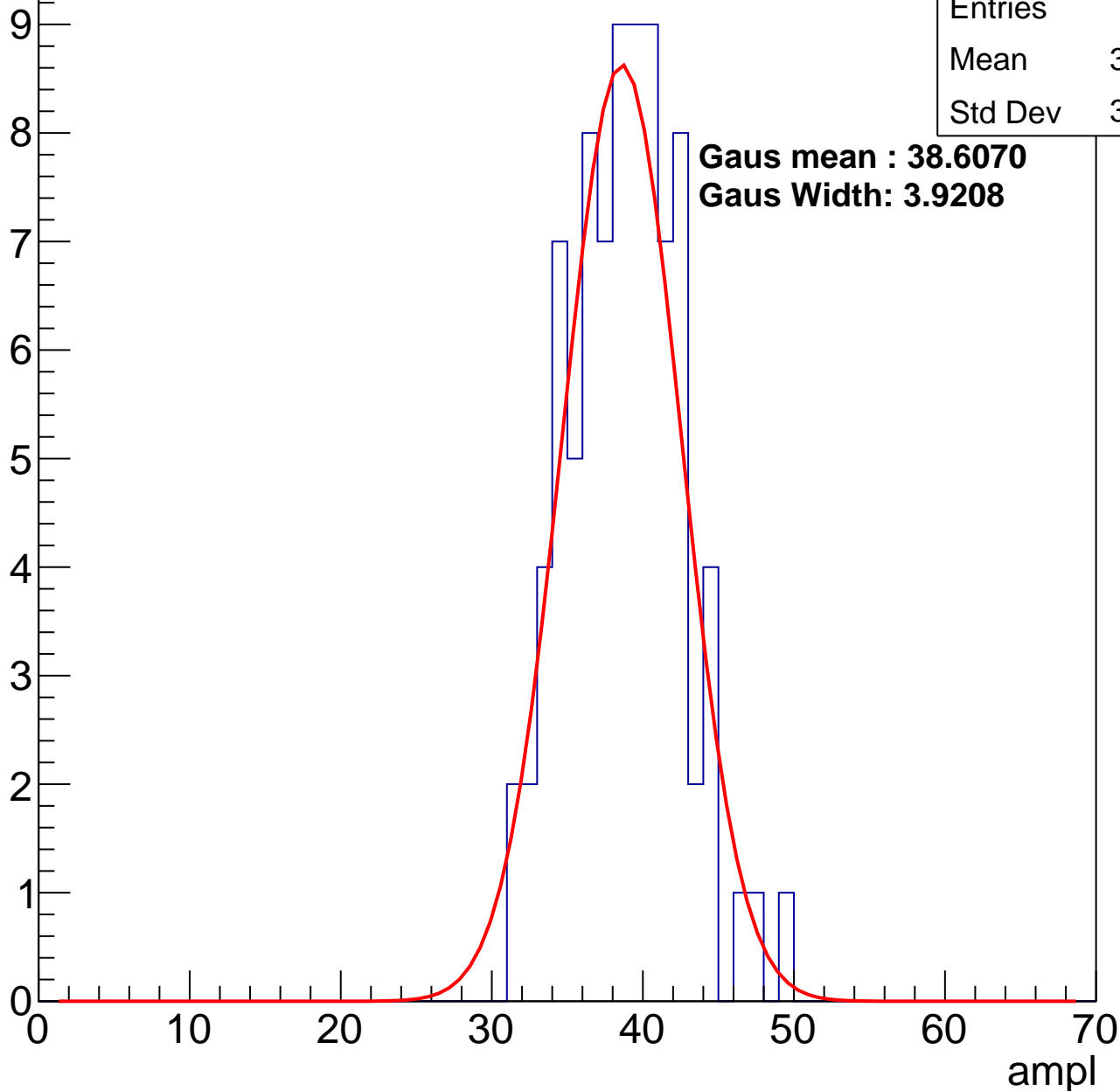
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 38.35 |
| Std Dev | 3.659 |

Gaus mean : 38.6070

Gaus Width: 3.9208



B1L100S, U9-ch71, adc2

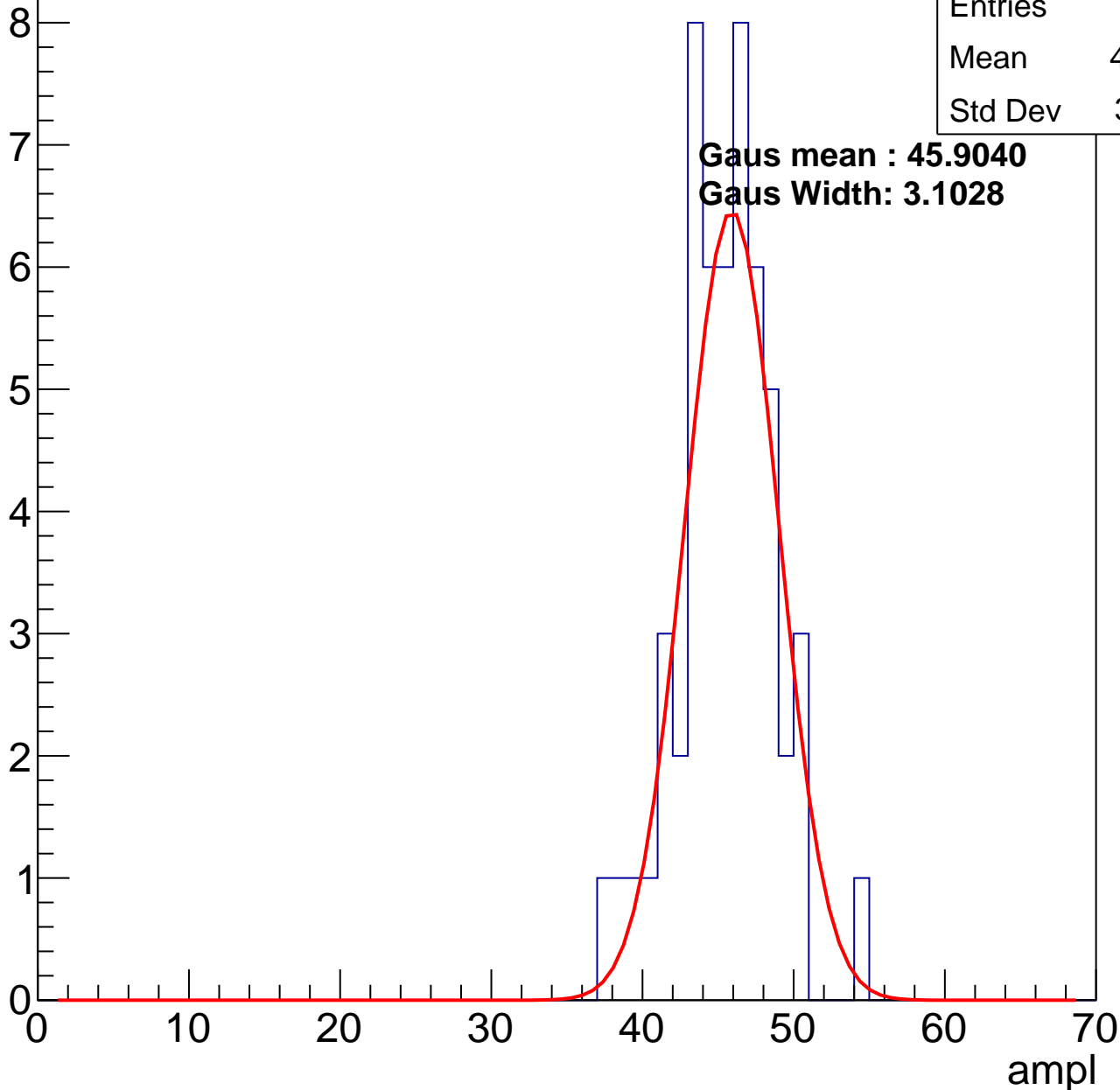
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 45.02 |
| Std Dev | 3.171 |

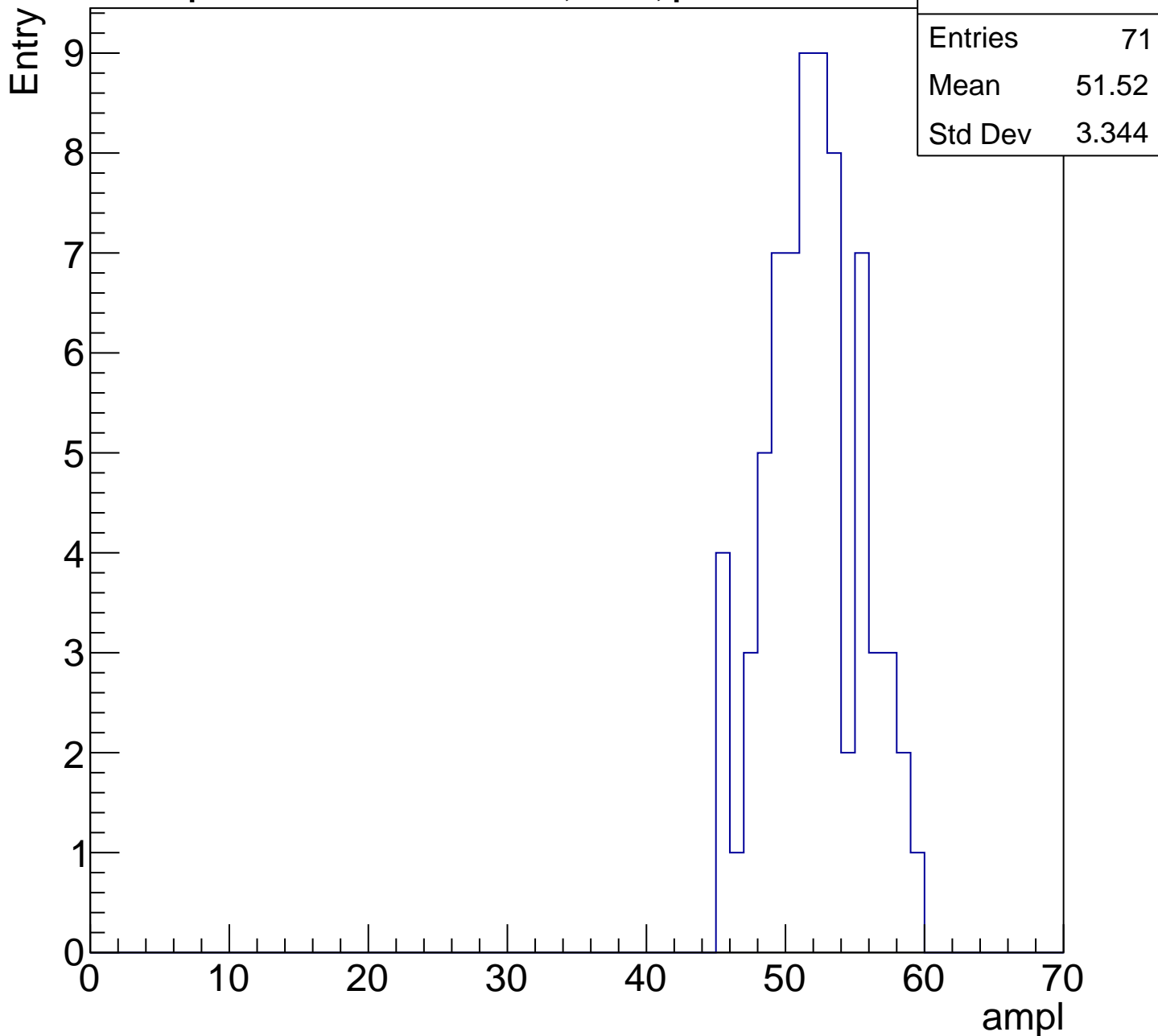
Gaus mean : 45.9040

Gaus Width: 3.1028



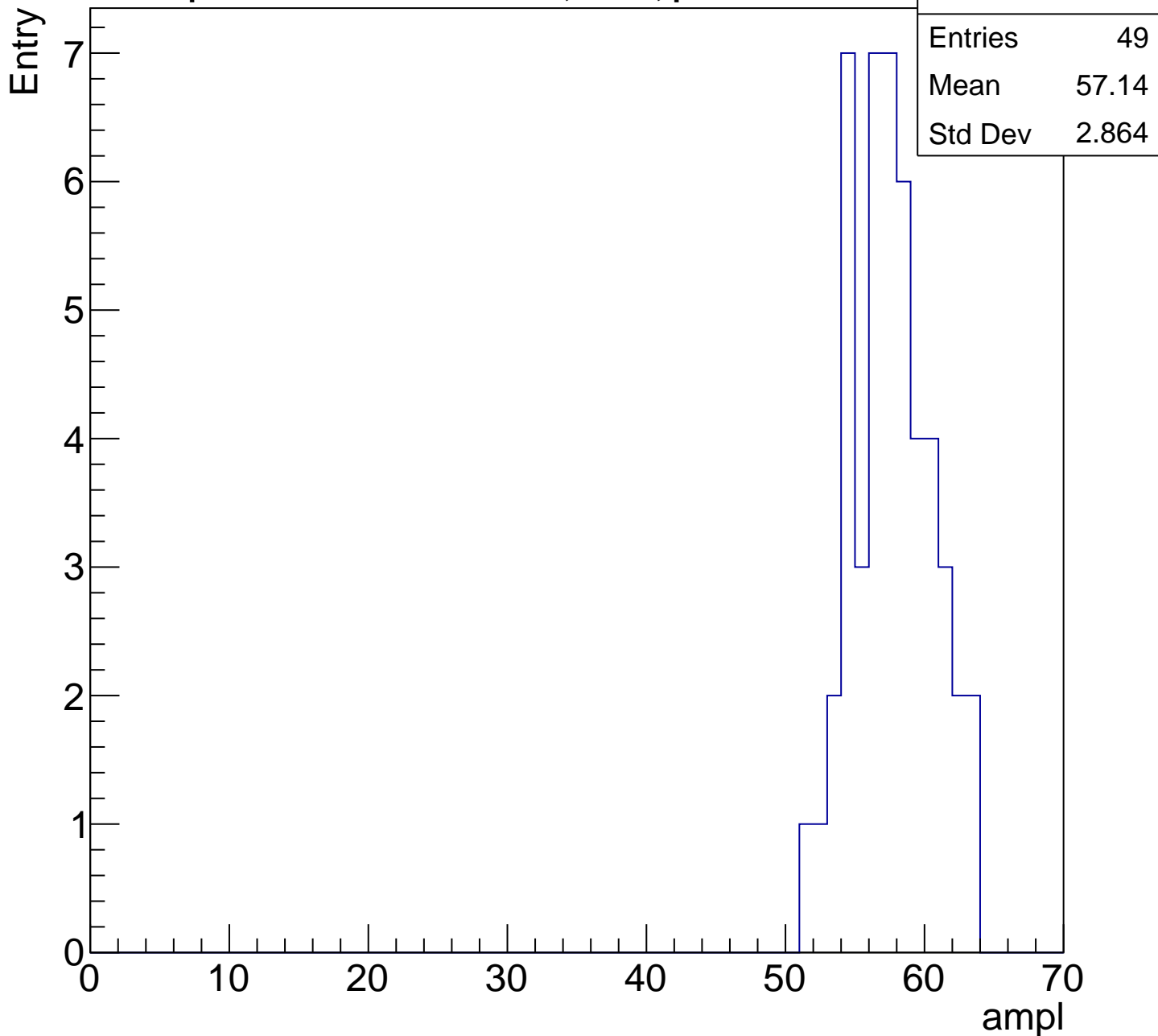
B1L100S, U9-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

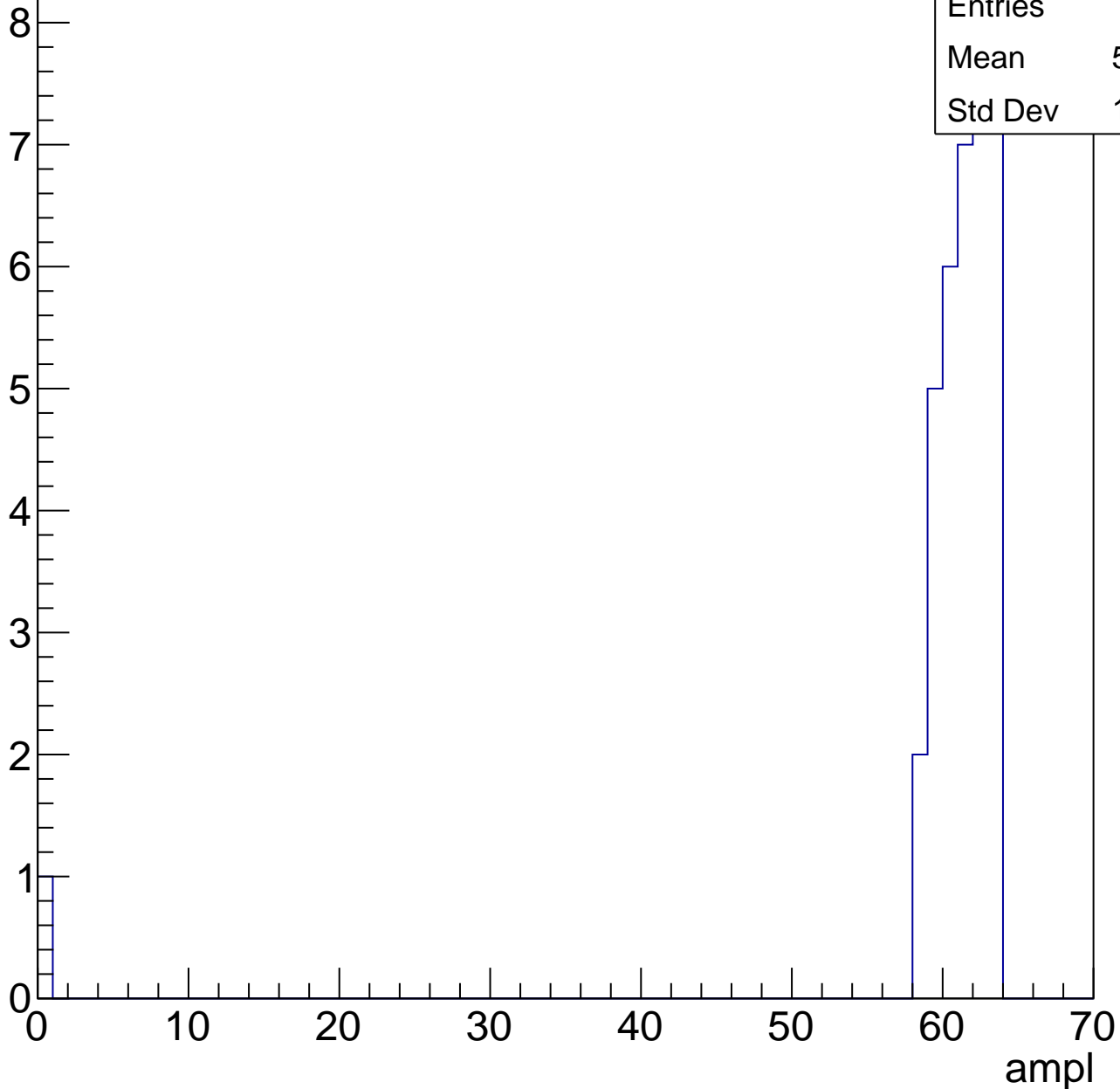


B1L100S, U9-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 59.41 |
| Std Dev | 10.01 |



B1L100S, U9-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch72, adc0

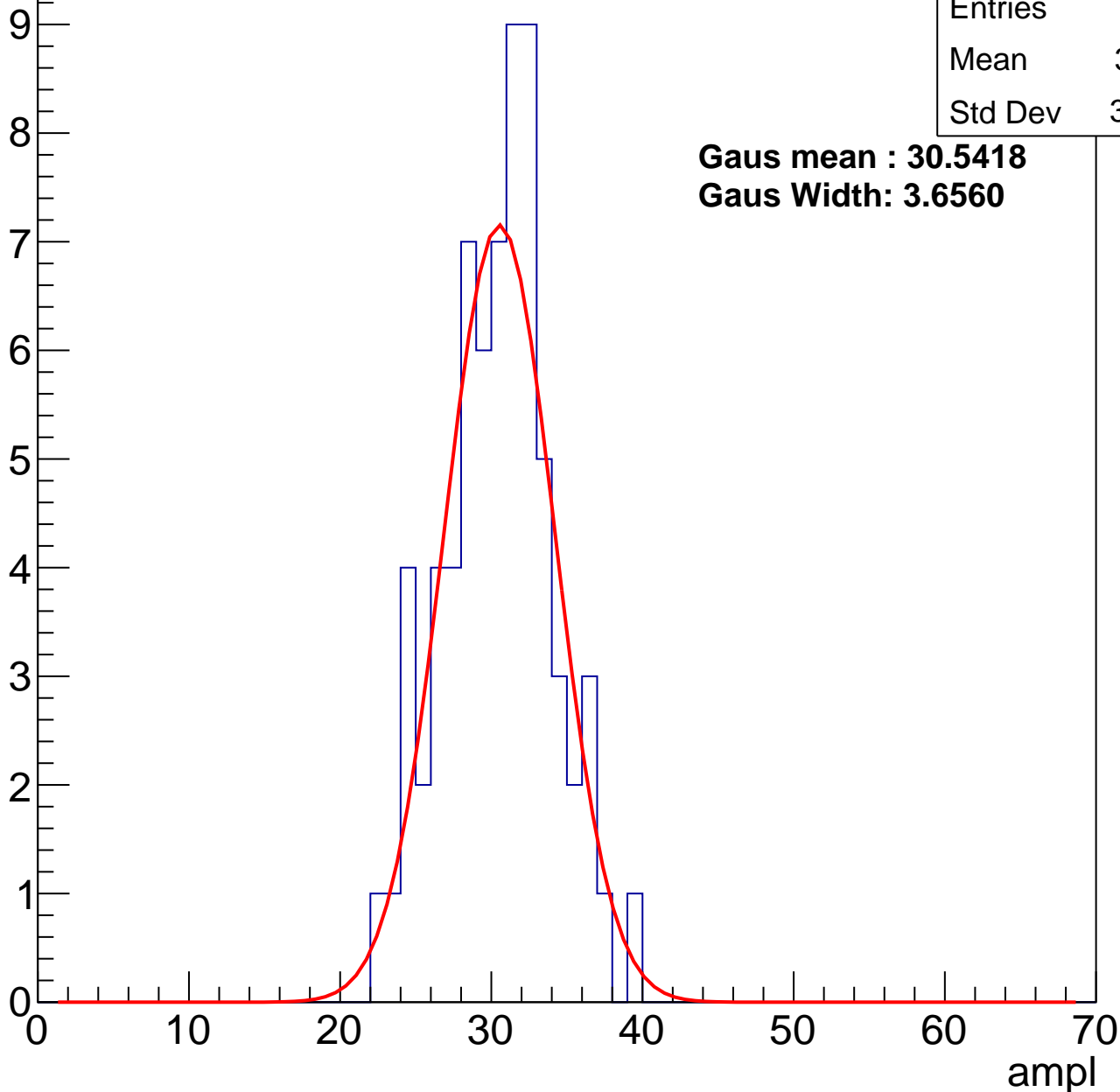
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 30.01 |
| Std Dev | 3.545 |

Gaus mean : 30.5418

Gaus Width: 3.6560



B1L100S, U9-ch72, adc1

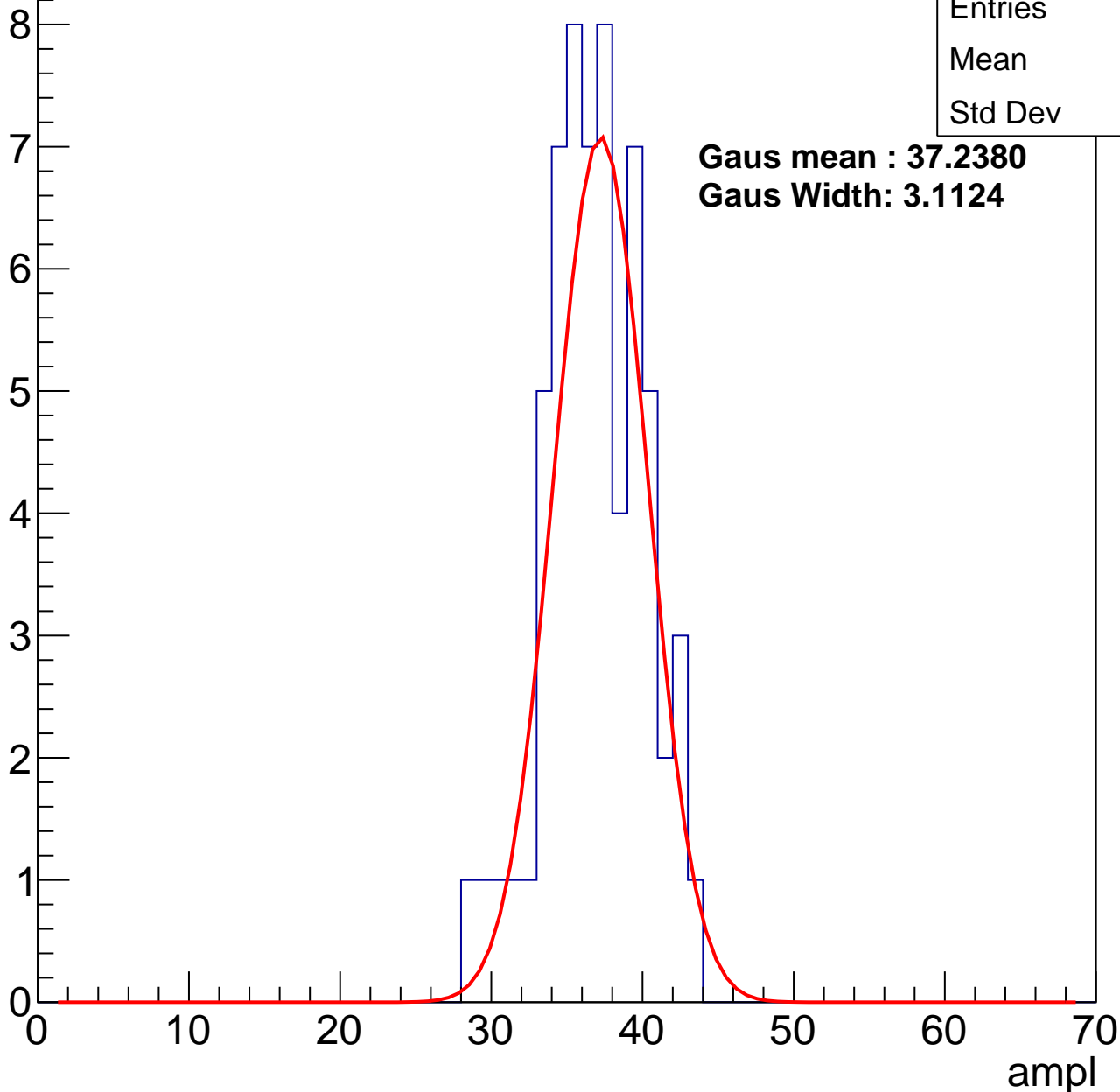
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 62 |
| Mean | 36.4 |
| Std Dev | 3.2 |

Gaus mean : 37.2380

Gaus Width: 3.1124



B1L100S, U9-ch72, adc2

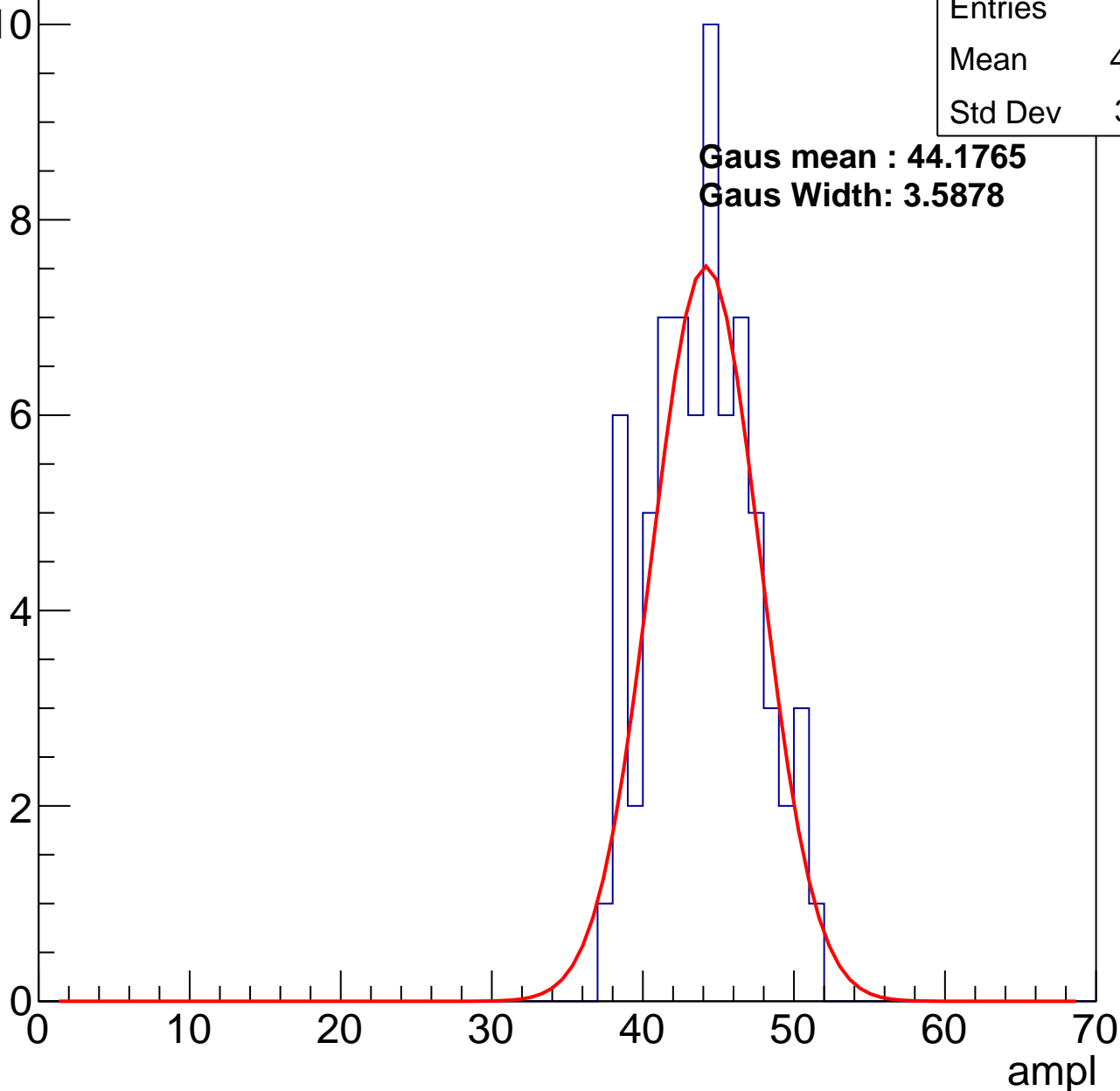
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 43.55 |
| Std Dev | 3.381 |

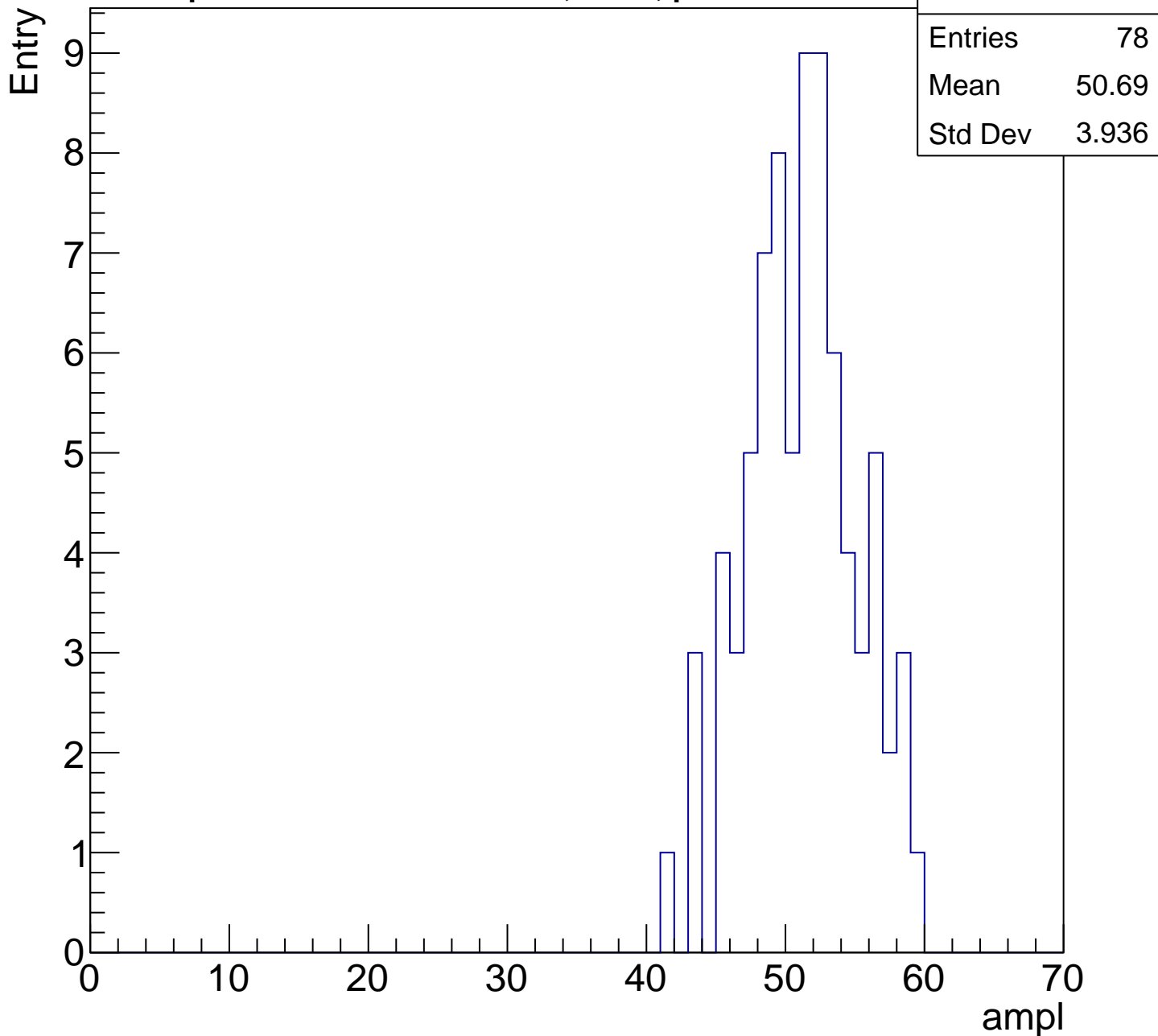
Gaus mean : 44.1765

Gaus Width: 3.5878



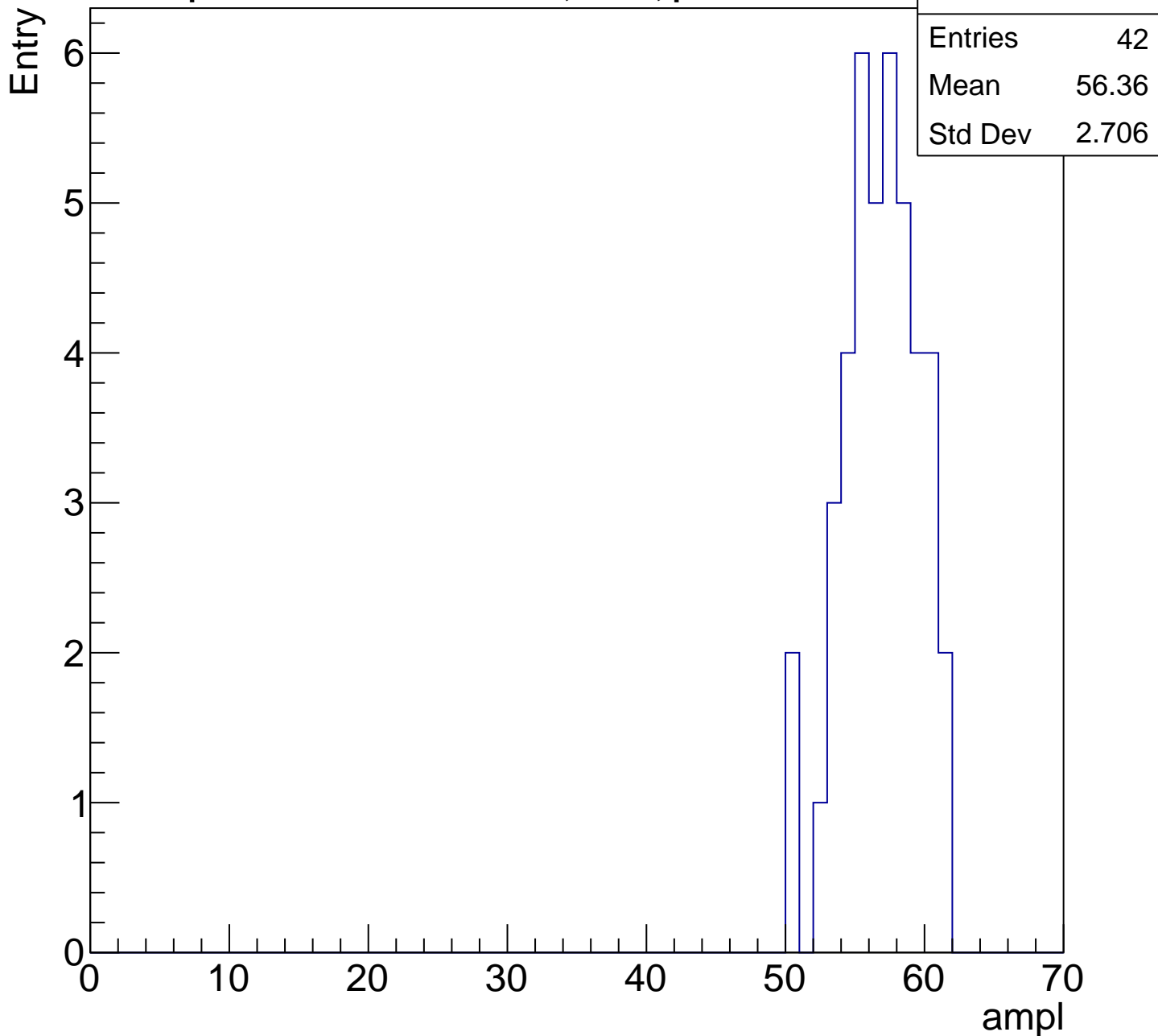
B1L100S, U9-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch72, adc5

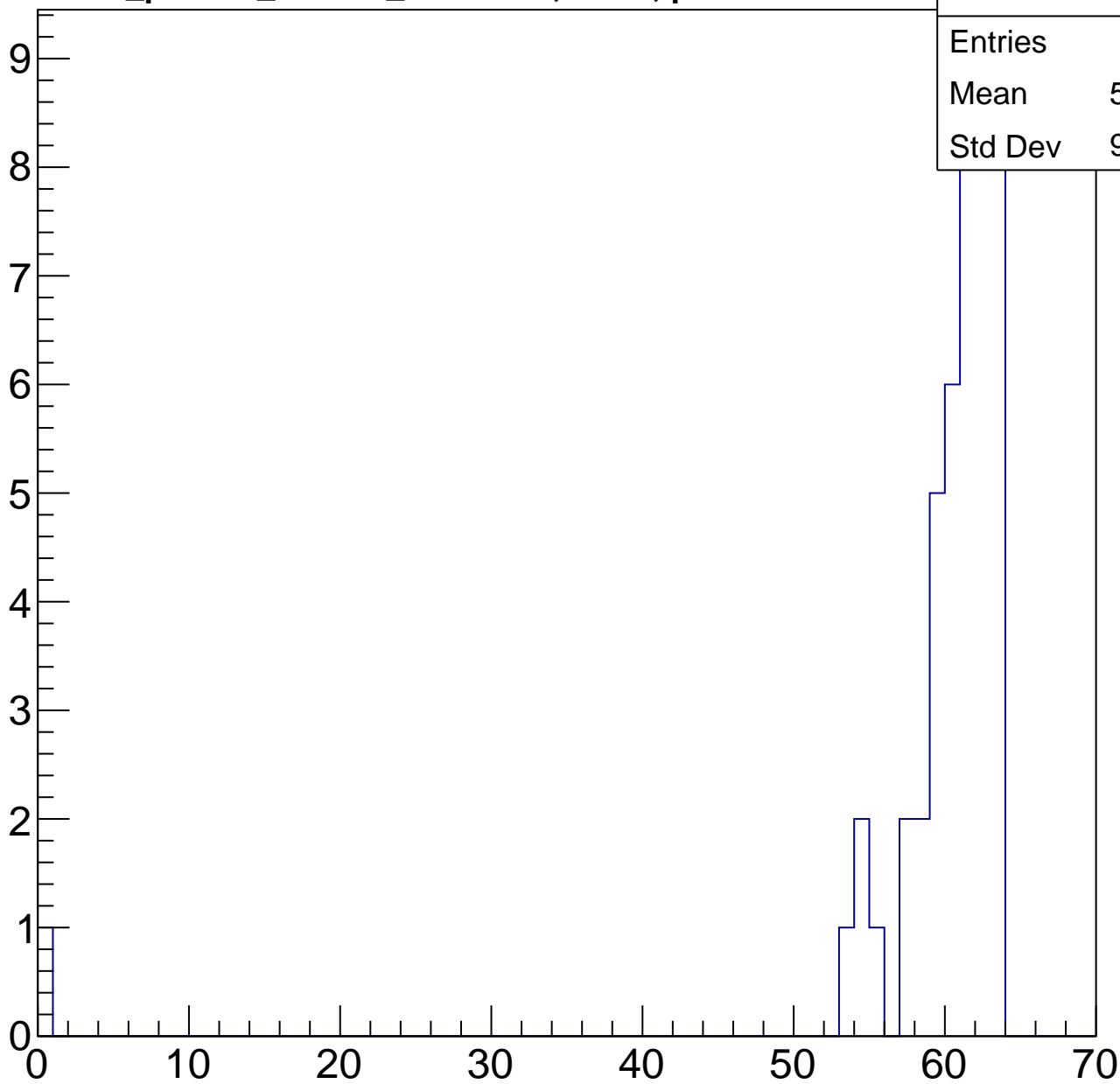
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.93 |
| Std Dev | 9.246 |

ampl



B1L100S, U9-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch73, adc0

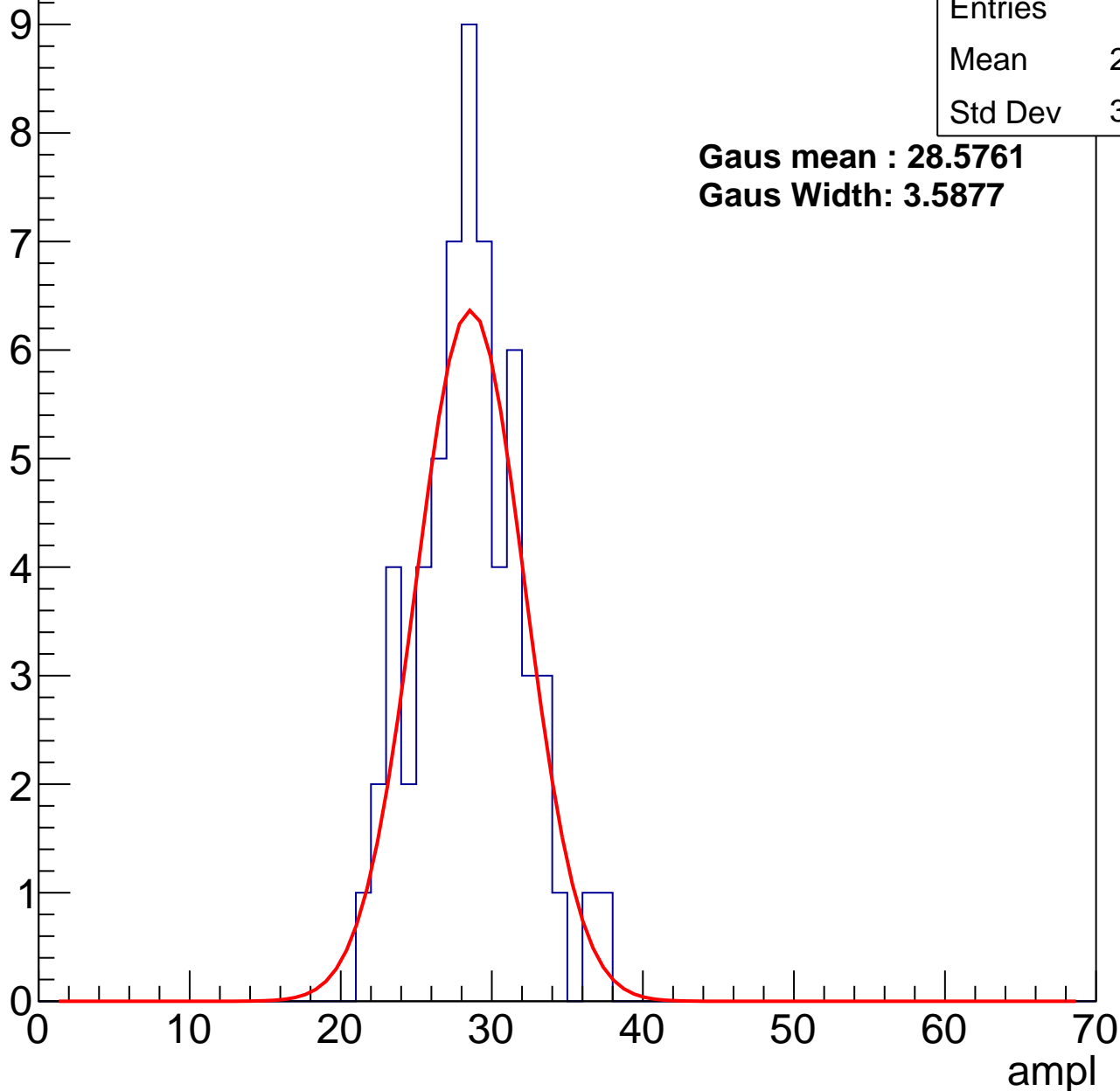
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 28.12 |
| Std Dev | 3.406 |

Gaus mean : 28.5761

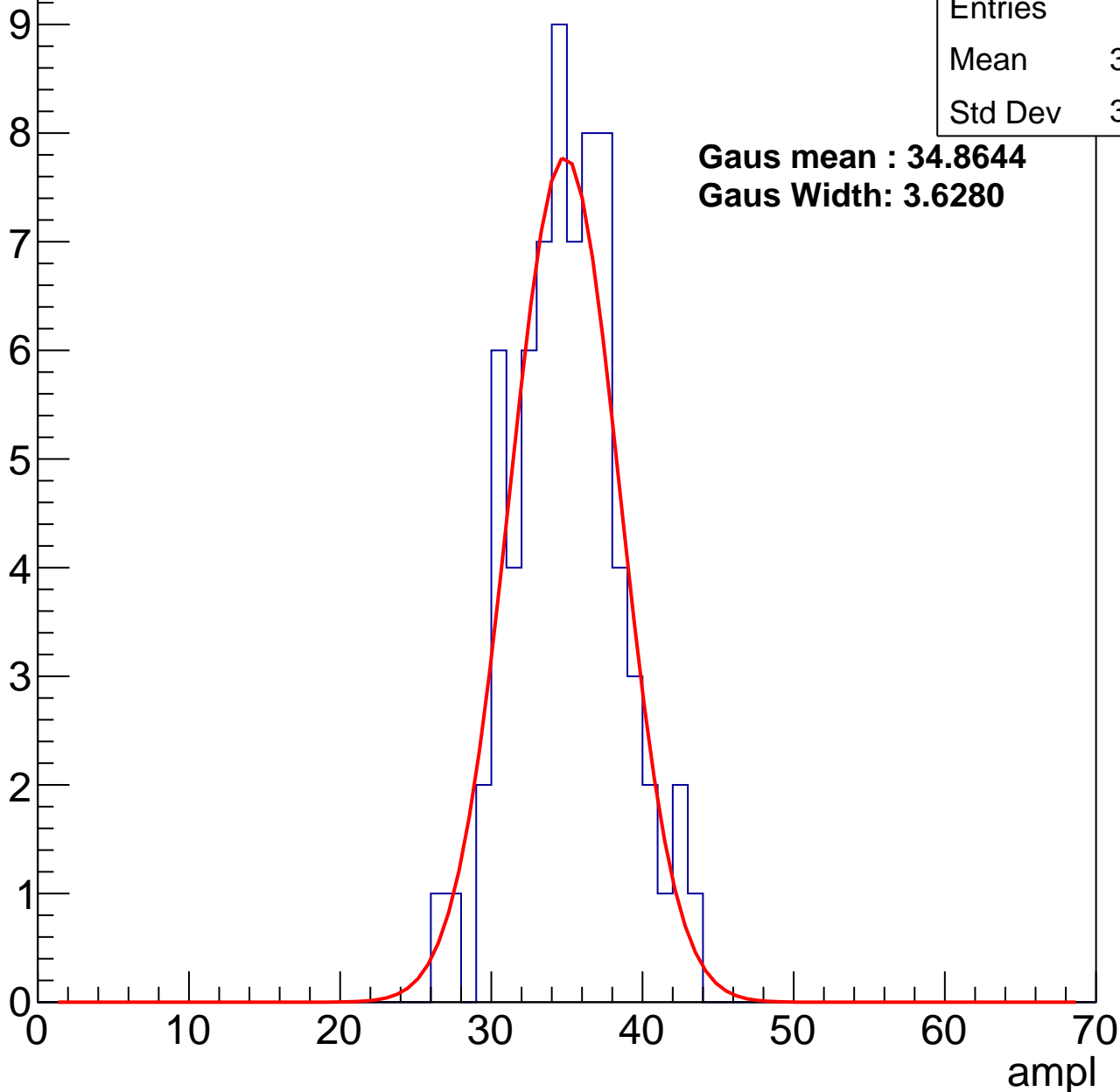
Gaus Width: 3.5877



B1L100S, U9-ch73, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch73, adc2

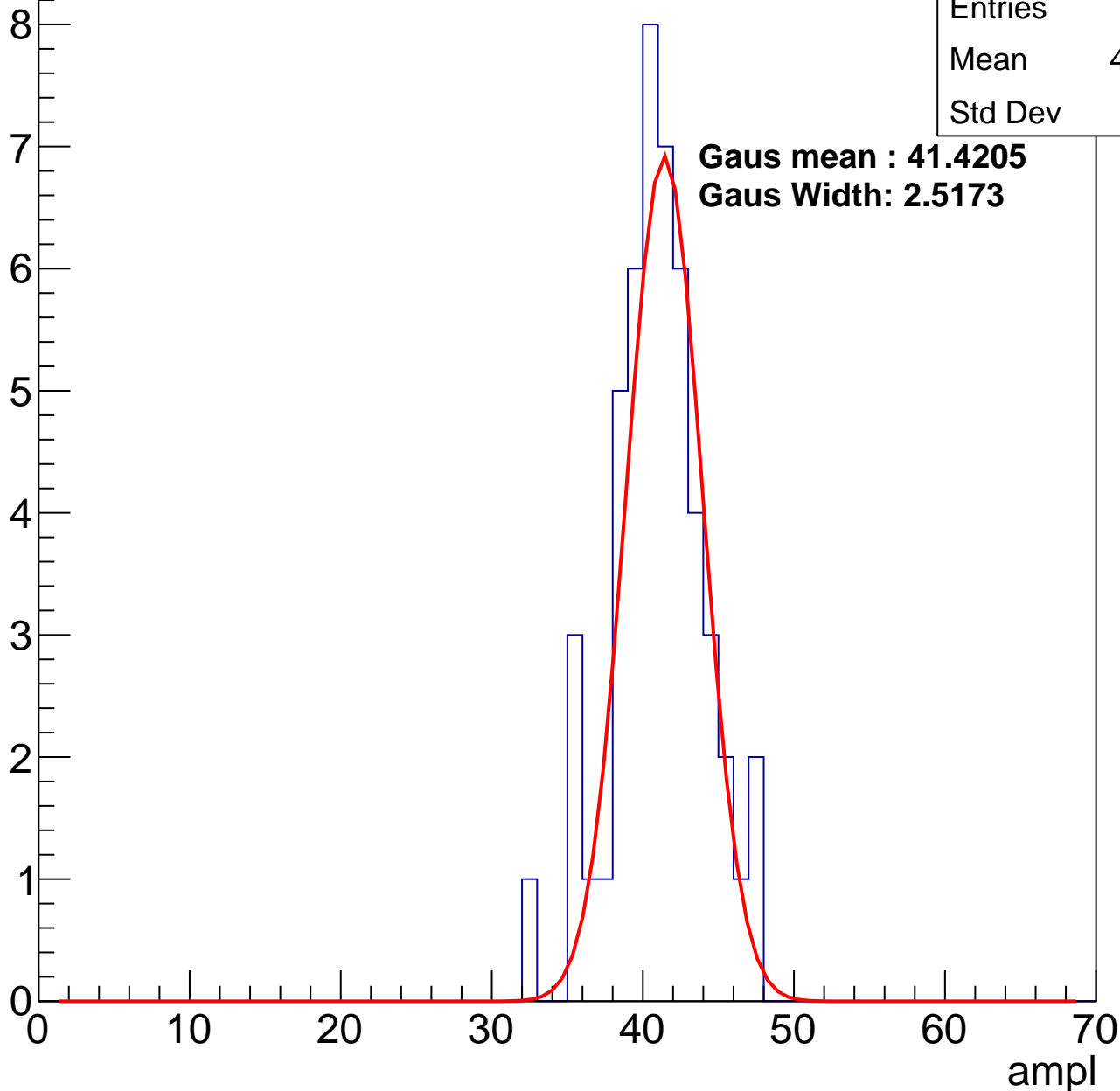
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 40.54 |
| Std Dev | 3.08 |

Gaus mean : 41.4205

Gaus Width: 2.5173

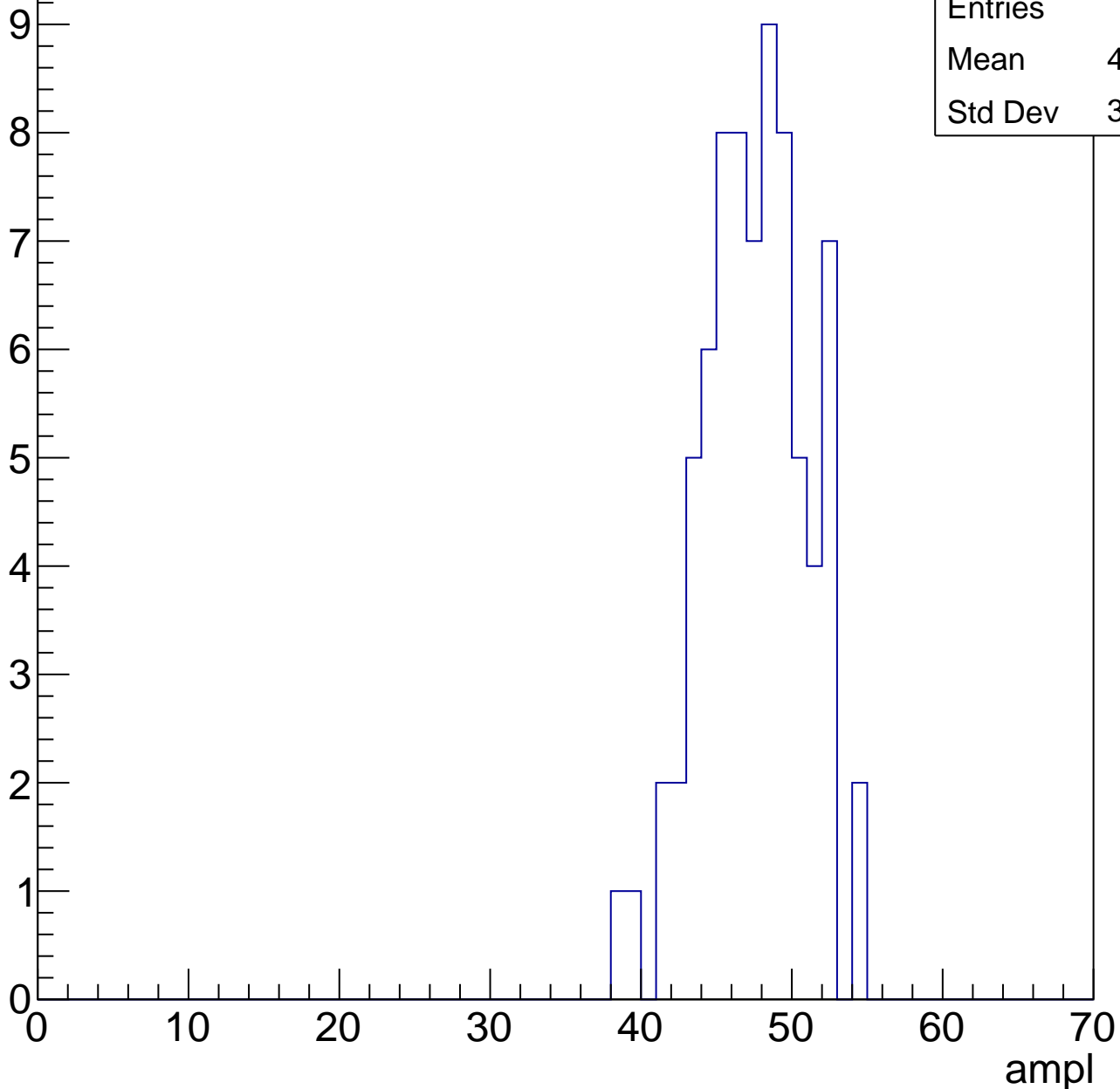


B1L100S, U9-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

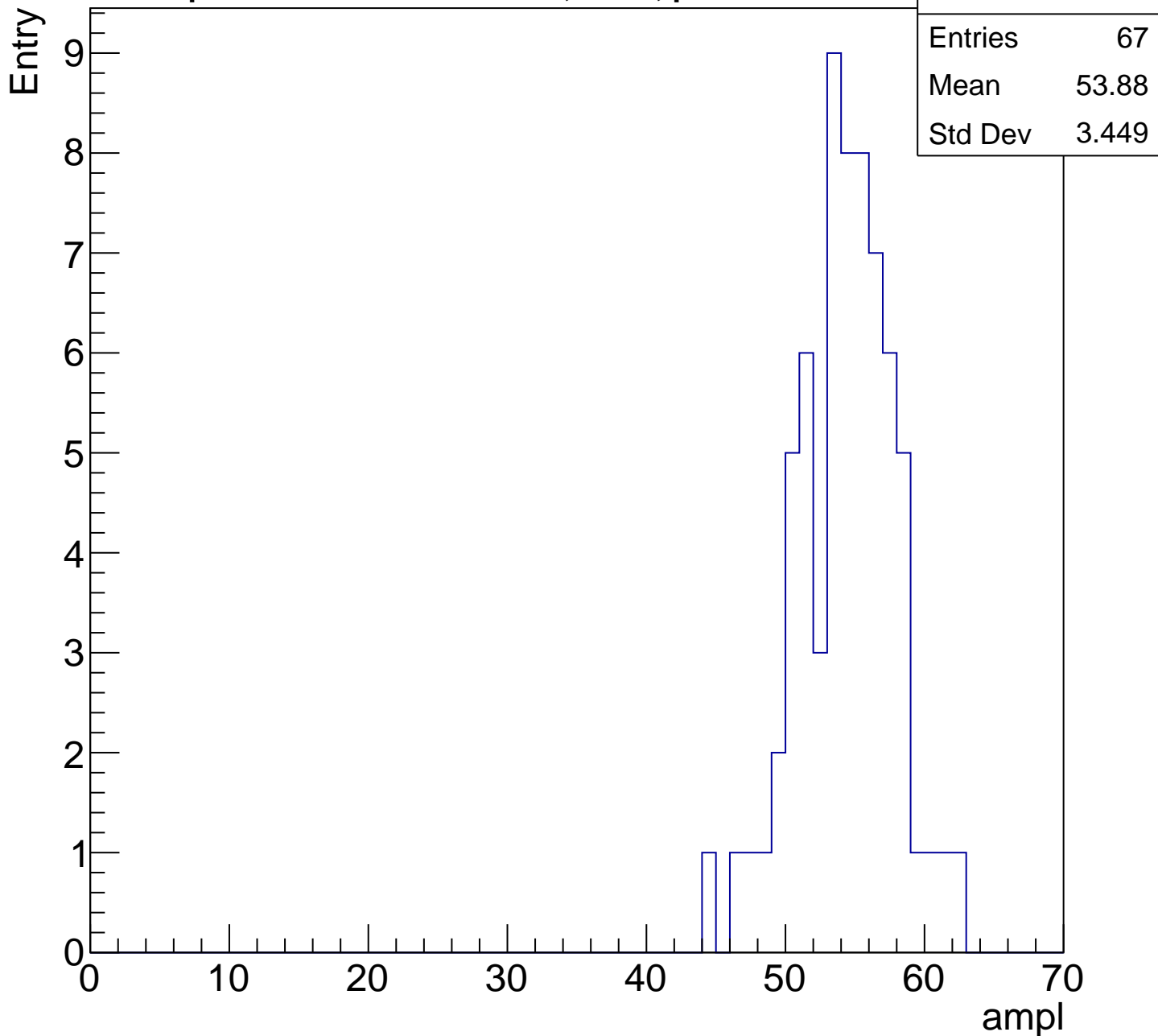
Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 47.05 |
| Std Dev | 3.394 |



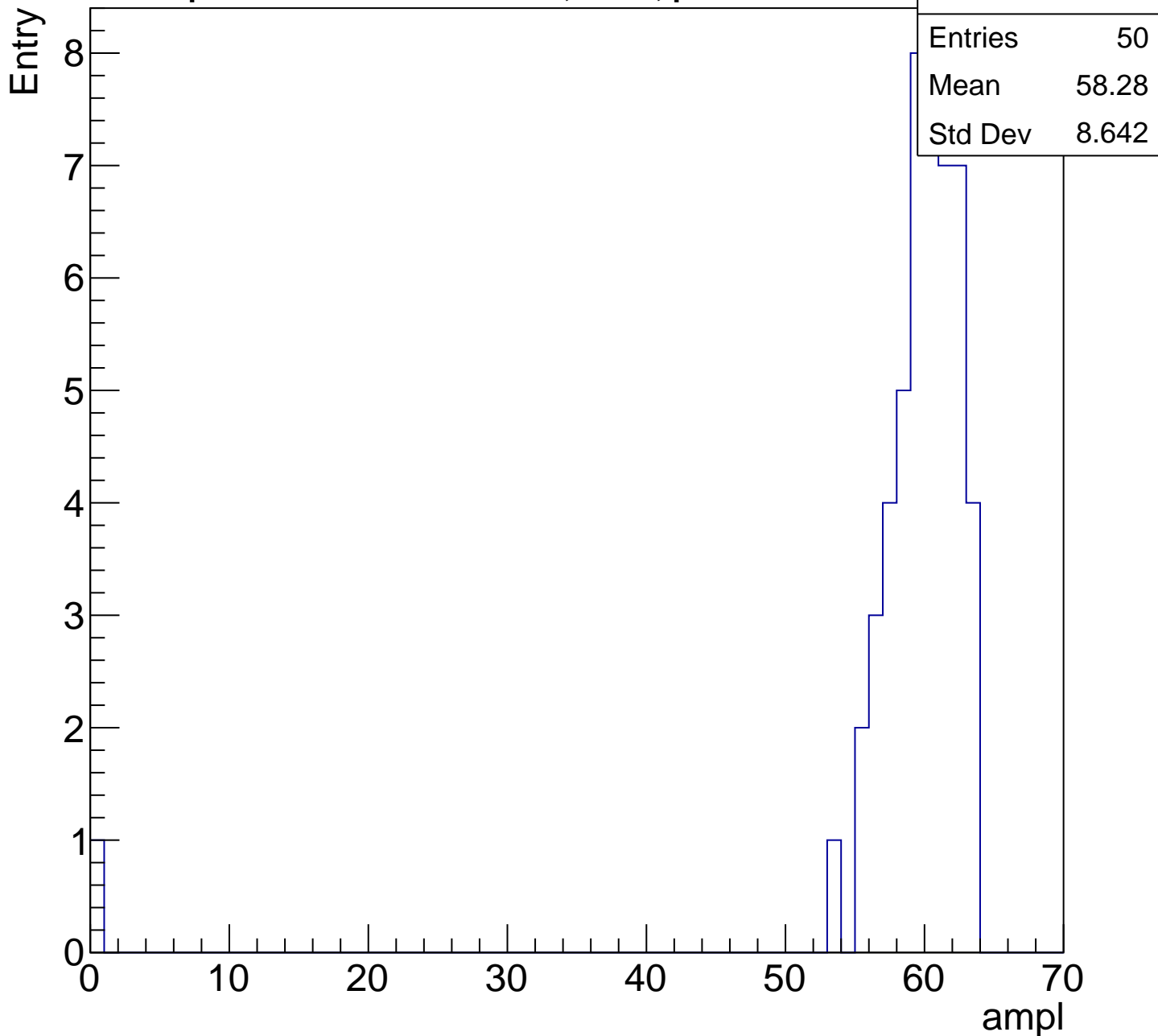
B1L100S, U9-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

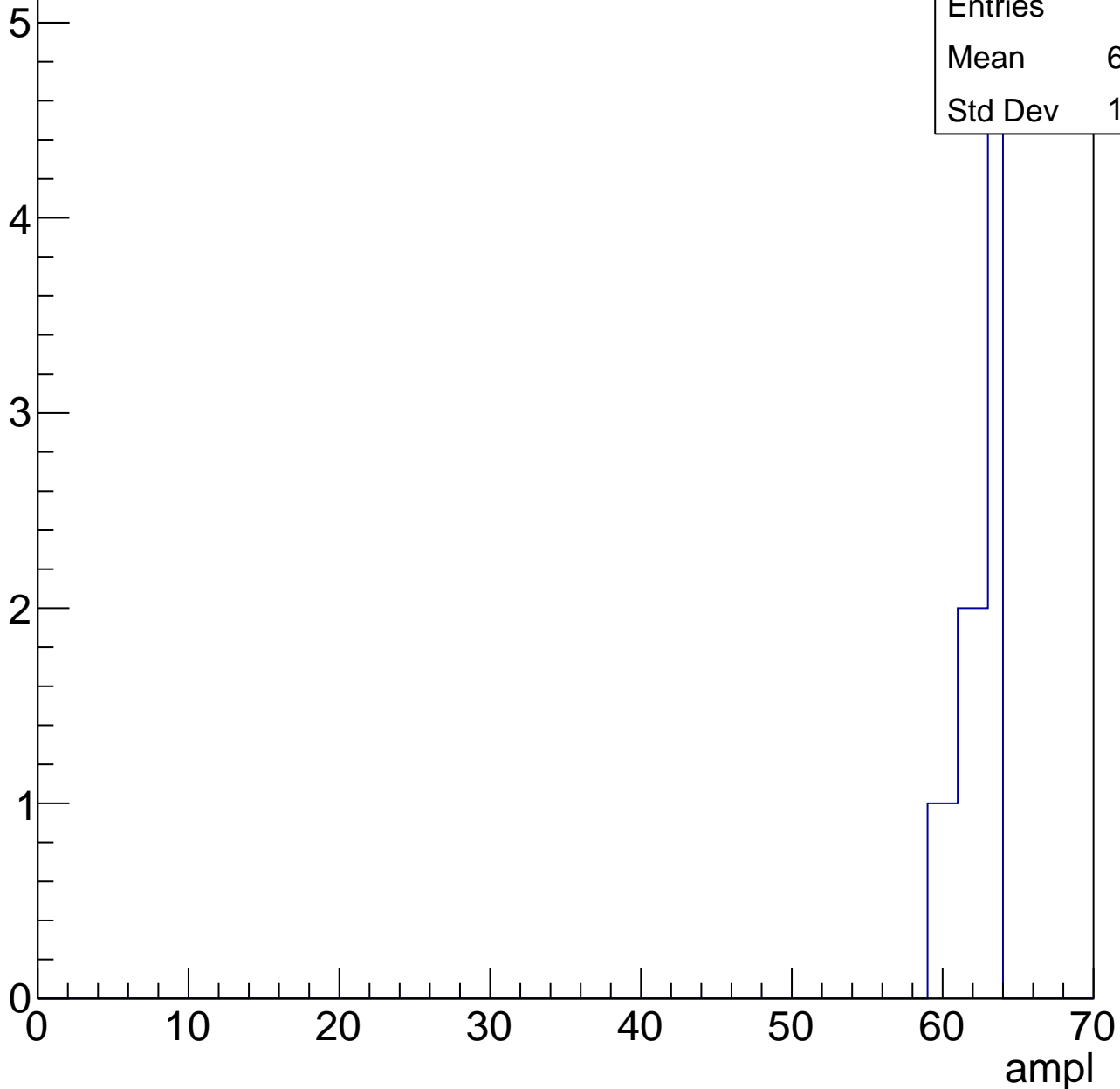


B1L100S, U9-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

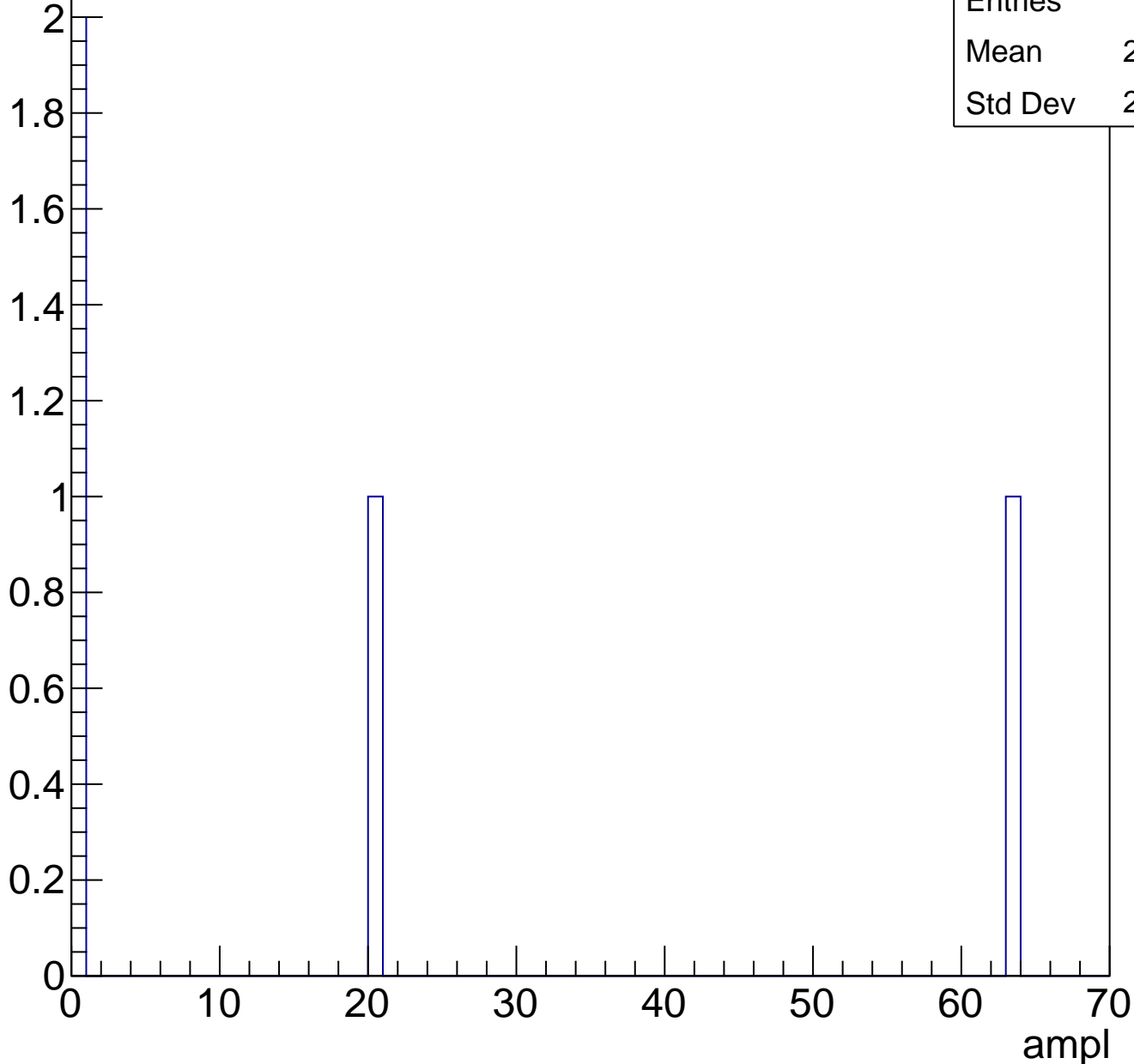
| | |
|---------|-------|
| Entries | 11 |
| Mean | 61.82 |
| Std Dev | 1.336 |



B1L100S, U9-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 20.75 |
| Std Dev | 25.72 |

B1L100S, U9-ch74, adc0

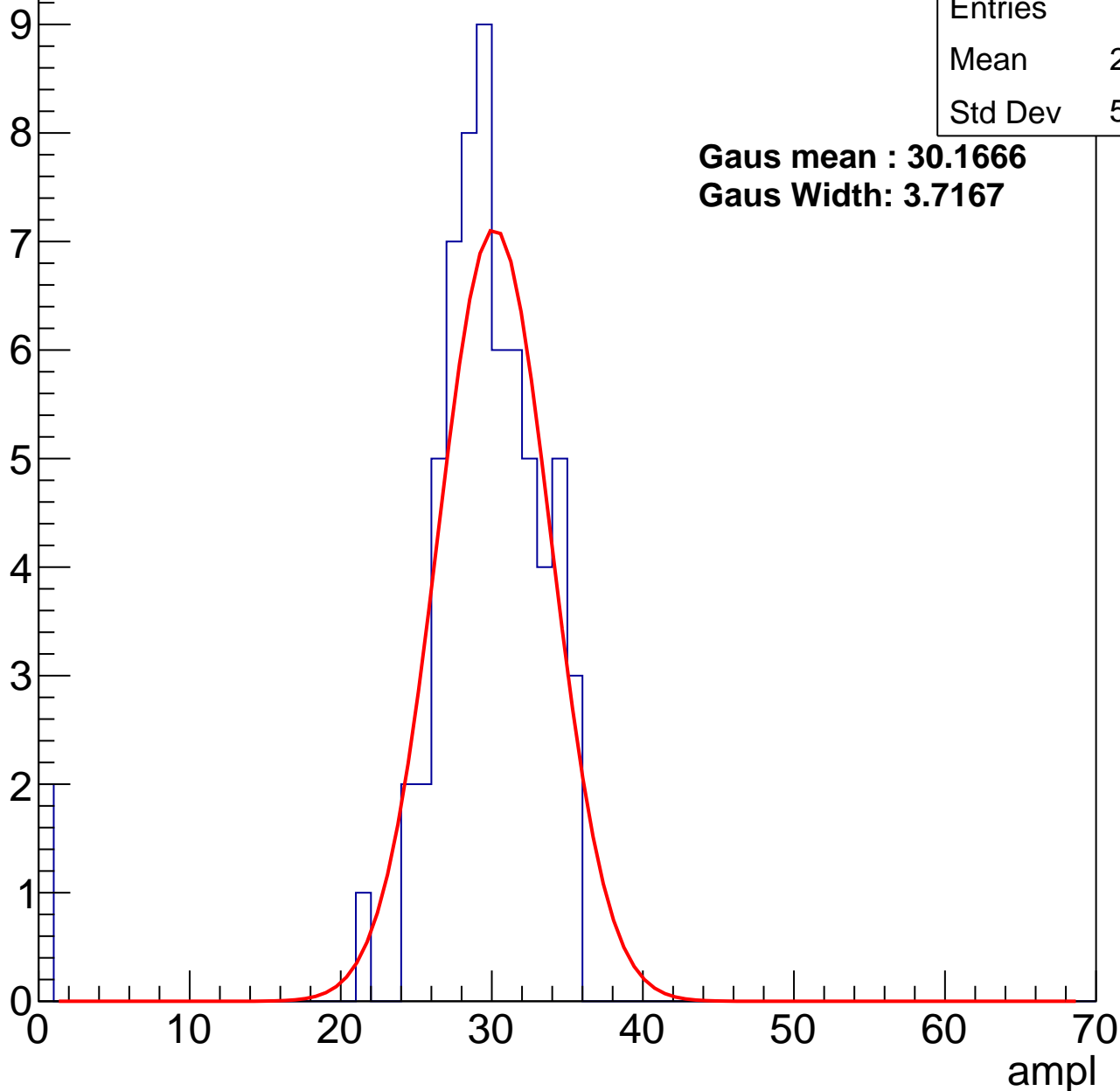
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 28.55 |
| Std Dev | 5.907 |

Gaus mean : 30.1666

Gaus Width: 3.7167



B1L100S, U9-ch74, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 36.09 |
| Std Dev | 3.658 |

Gaus mean : 36.6620

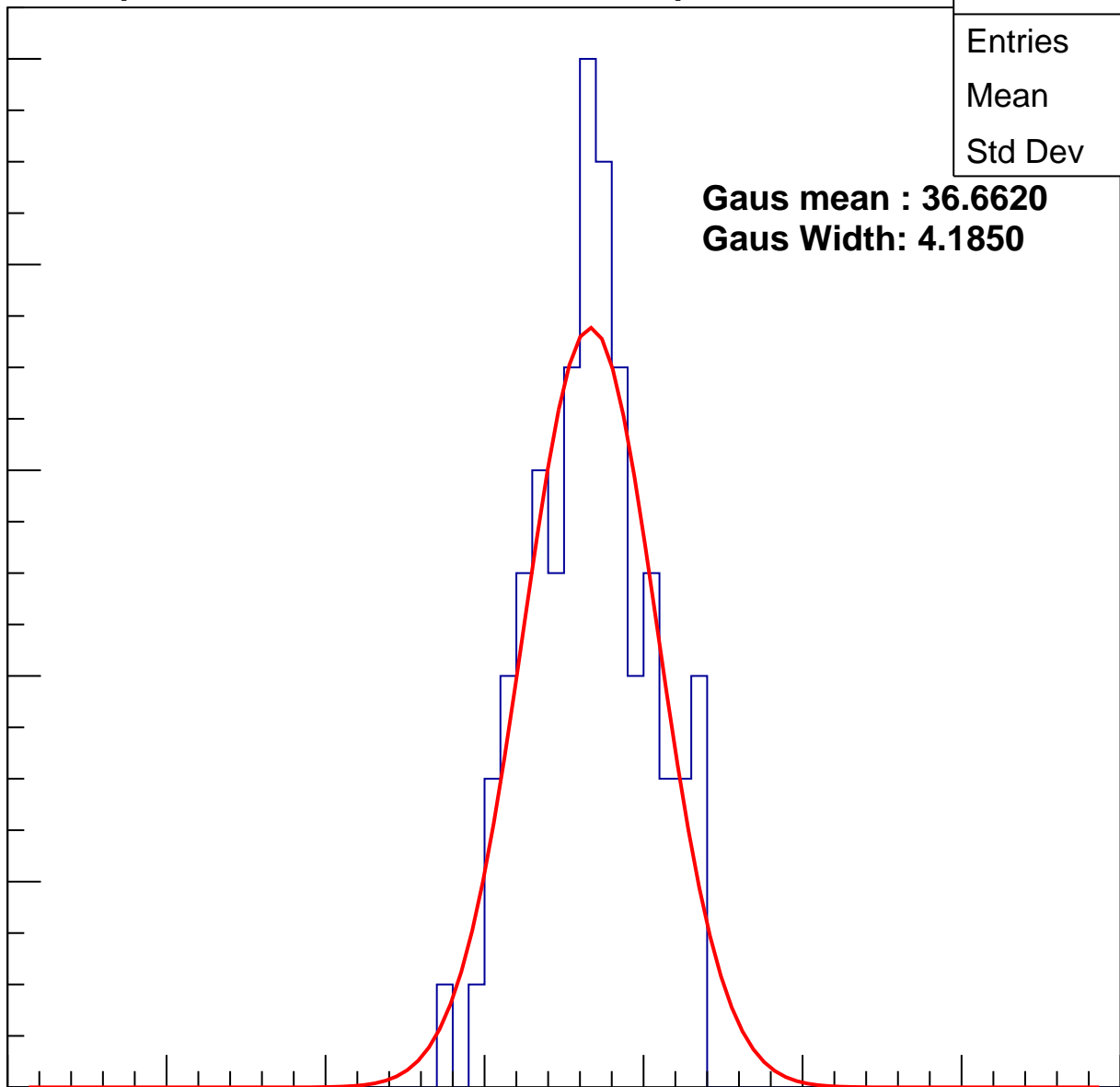
Gaus Width: 4.1850

Entry

10
8
6
4
2
0

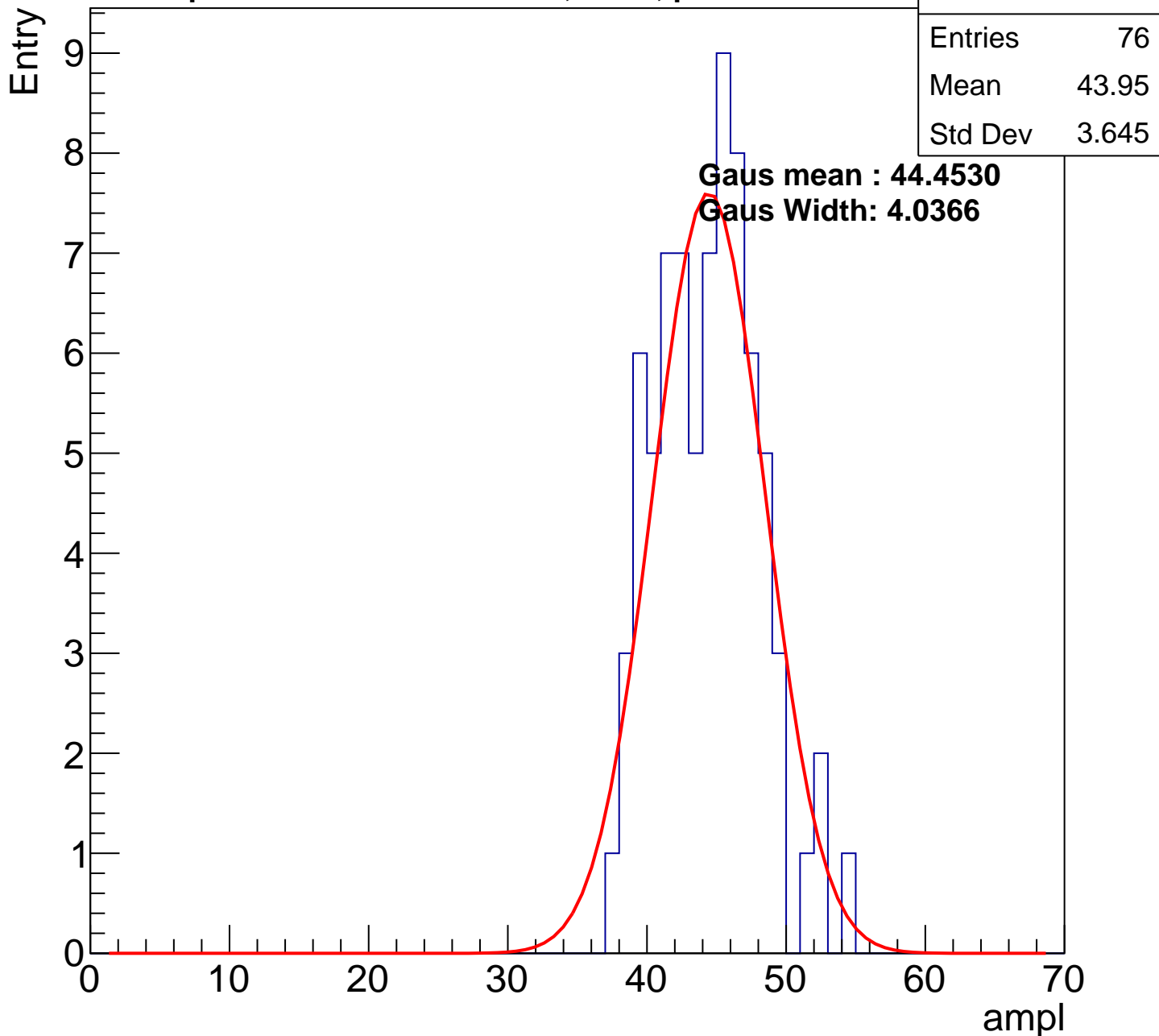
0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch74, adc2

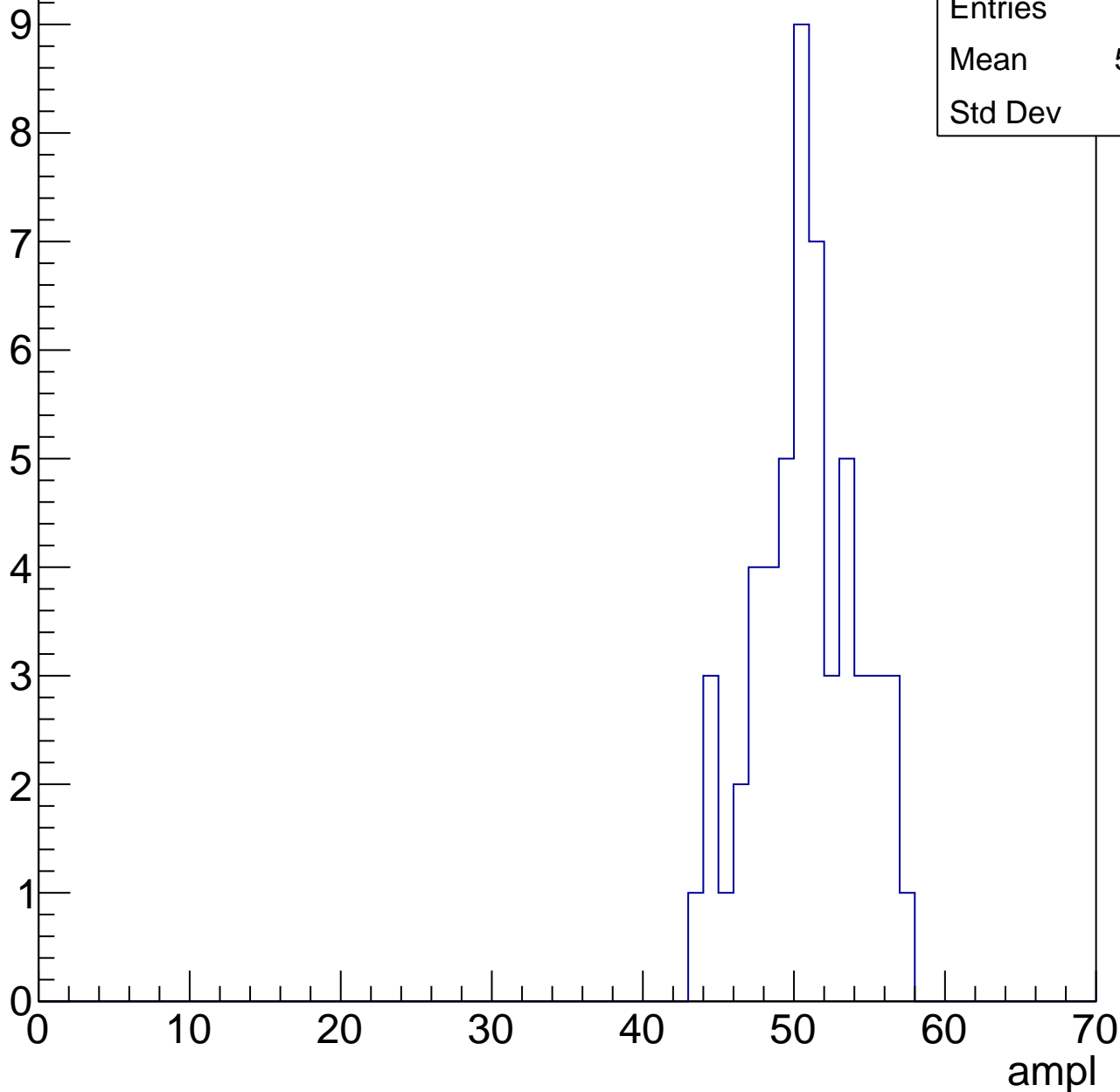
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

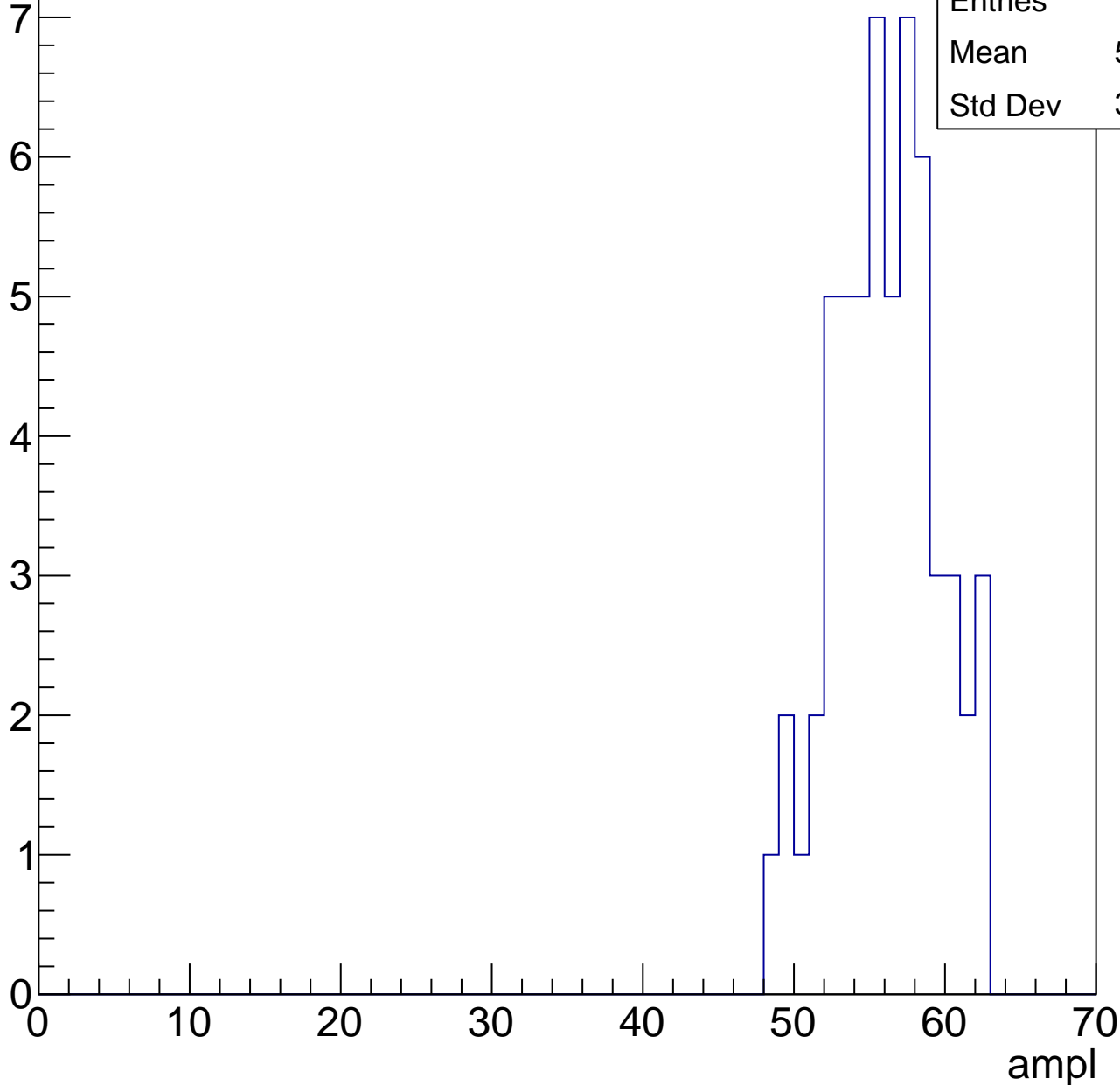


| | |
|---------|-------|
| Entries | 54 |
| Mean | 50.31 |
| Std Dev | 3.36 |

B1L100S, U9-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.61 |
| Std Dev | 3.391 |

B1L100S, U9-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.87 |
| Std Dev | 9.028 |

ampl

0

10

20

30

40

50

60

70

B1L100S, U9-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch75, adc0

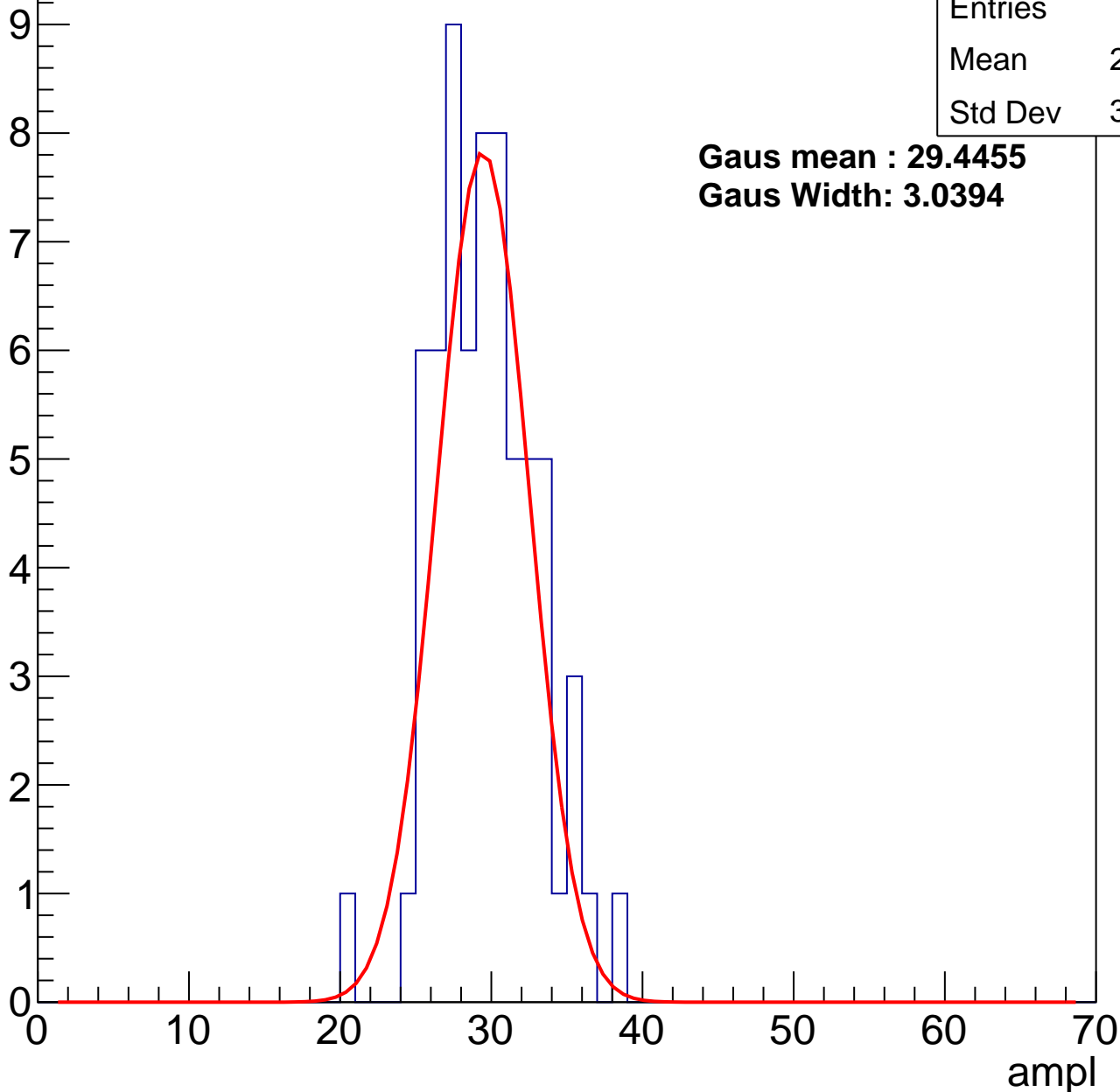
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 29.18 |
| Std Dev | 3.284 |

Gaus mean : 29.4455

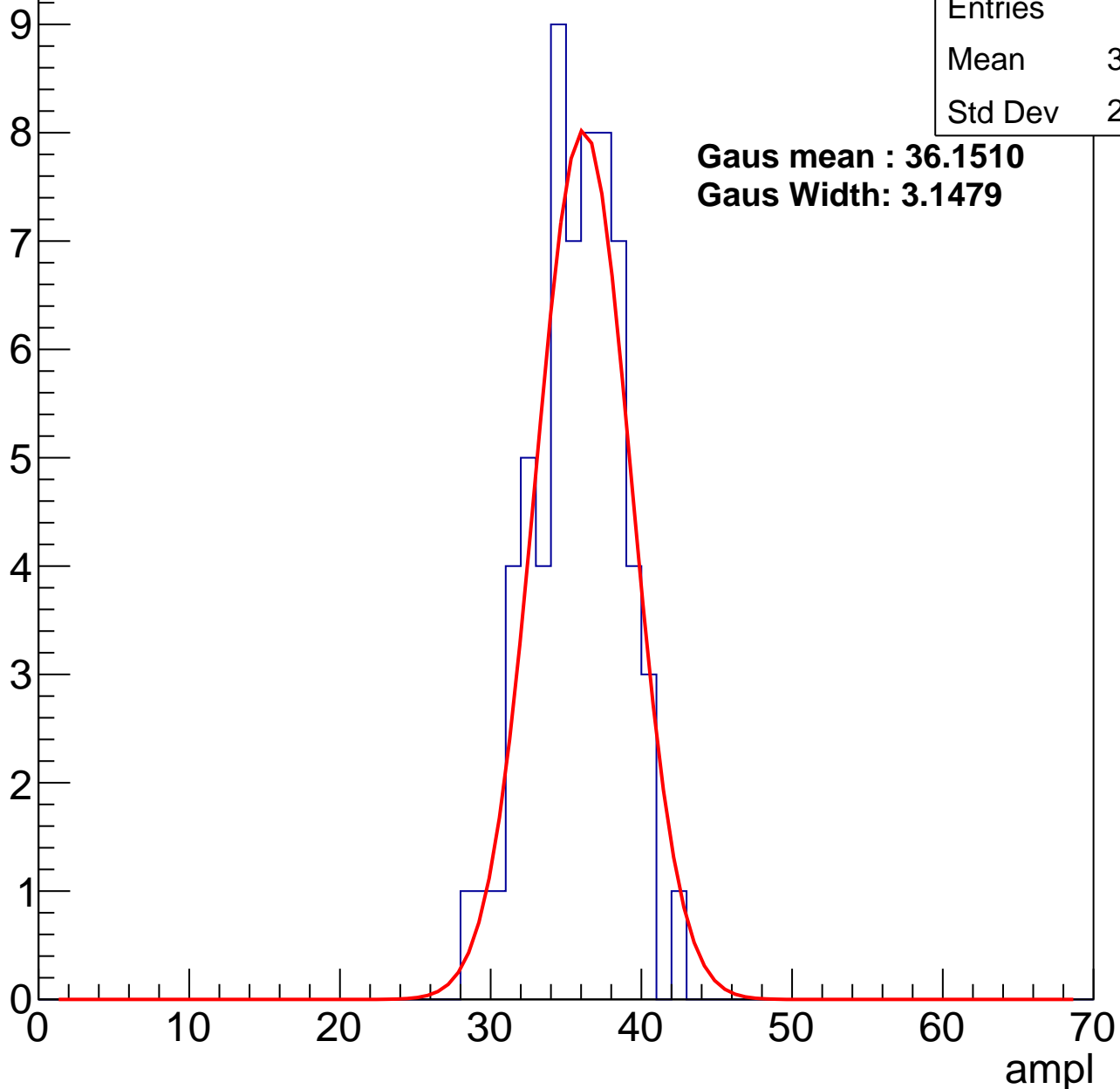
Gaus Width: 3.0394



B1L100S, U9-ch75, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch75, adc2

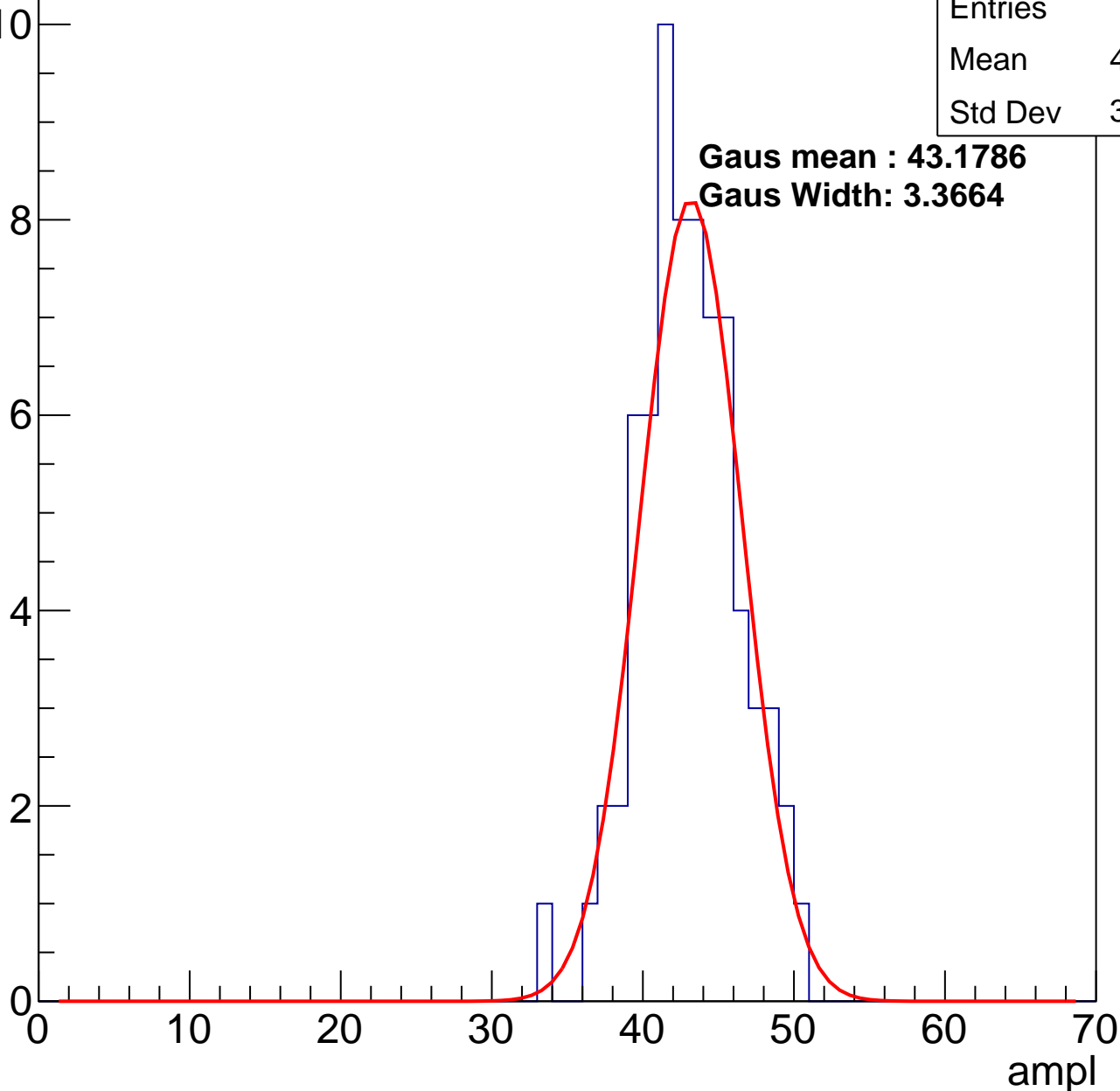
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.58 |
| Std Dev | 3.296 |

Gaus mean : 43.1786

Gaus Width: 3.3664

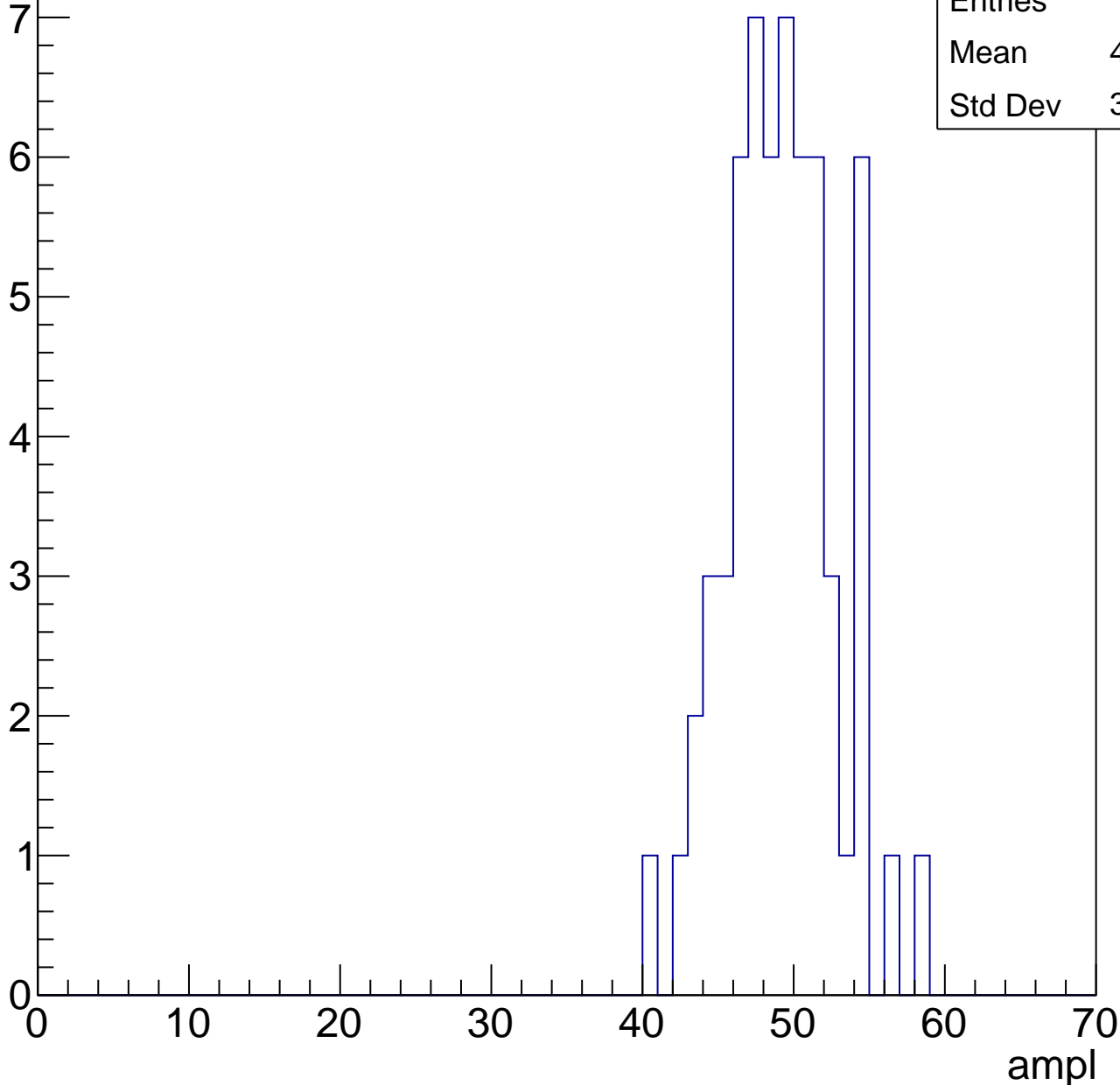


B1L100S, U9-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.73 |
| Std Dev | 3.563 |

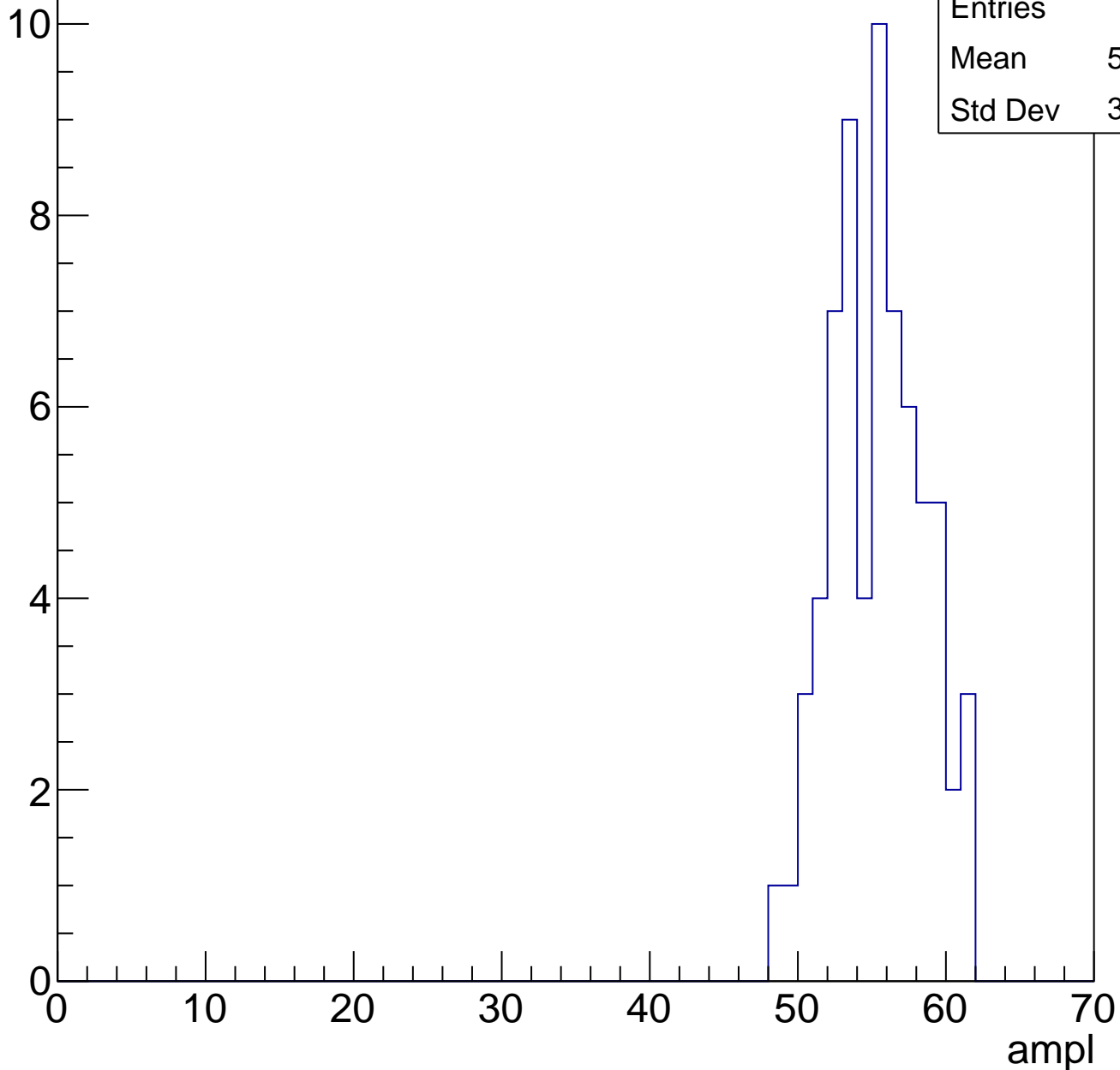


B1L100S, U9-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 54.93 |
| Std Dev | 3.092 |

Entry



B1L100S, U9-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

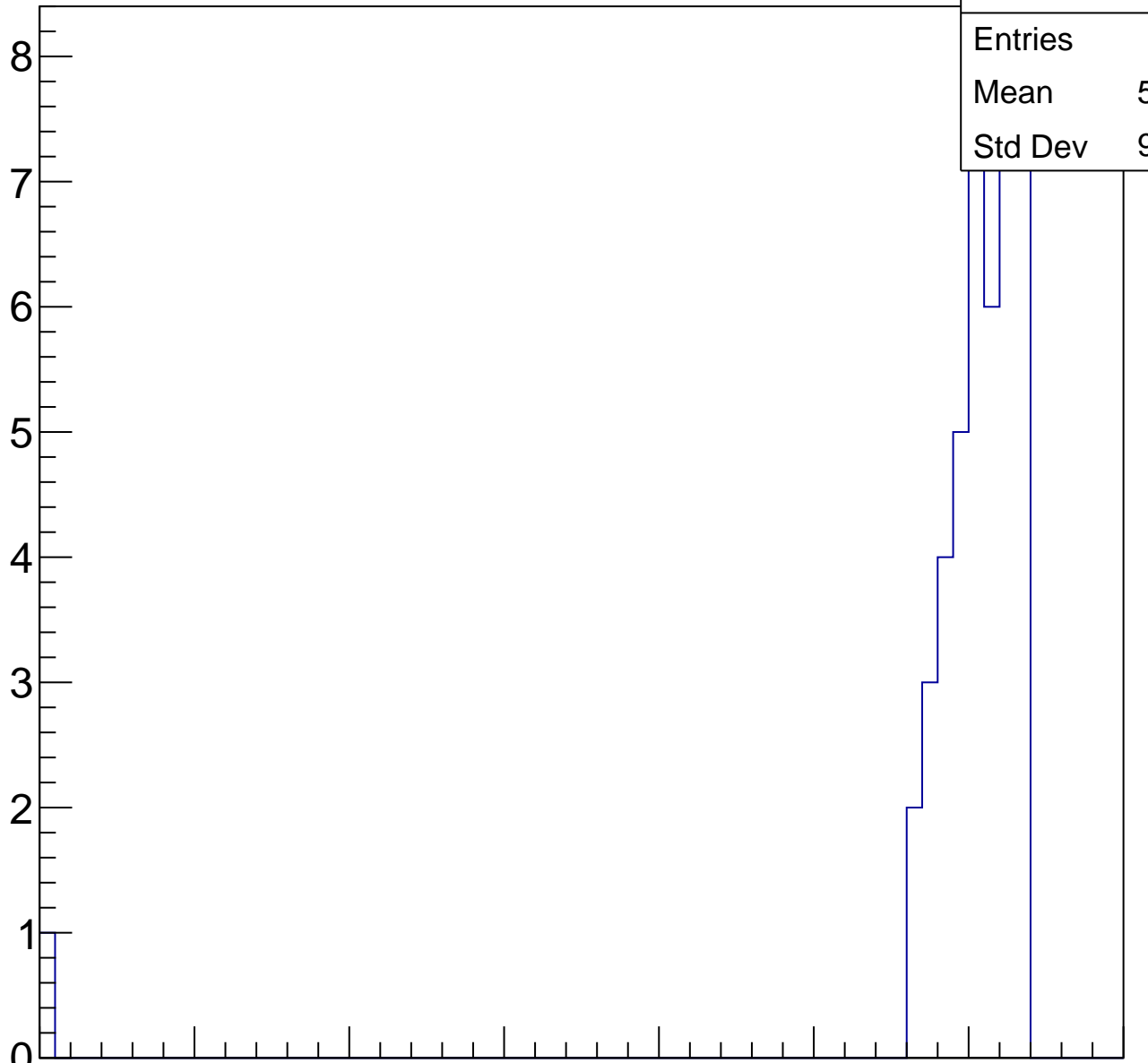
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.02 |
| Std Dev | 9.125 |

ampl

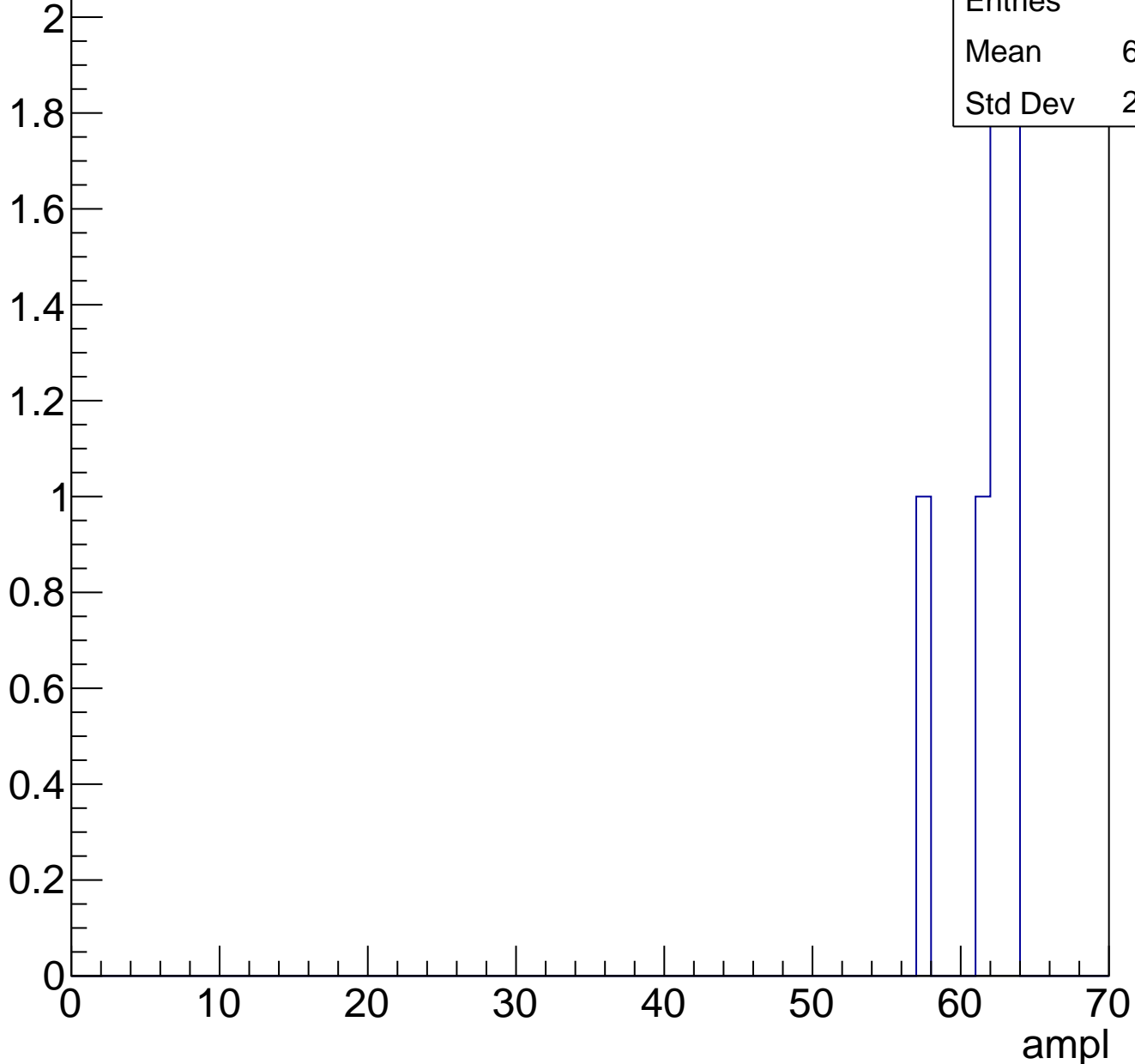
0 10 20 30 40 50 60 70



B1L100S, U9-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch76, adc0

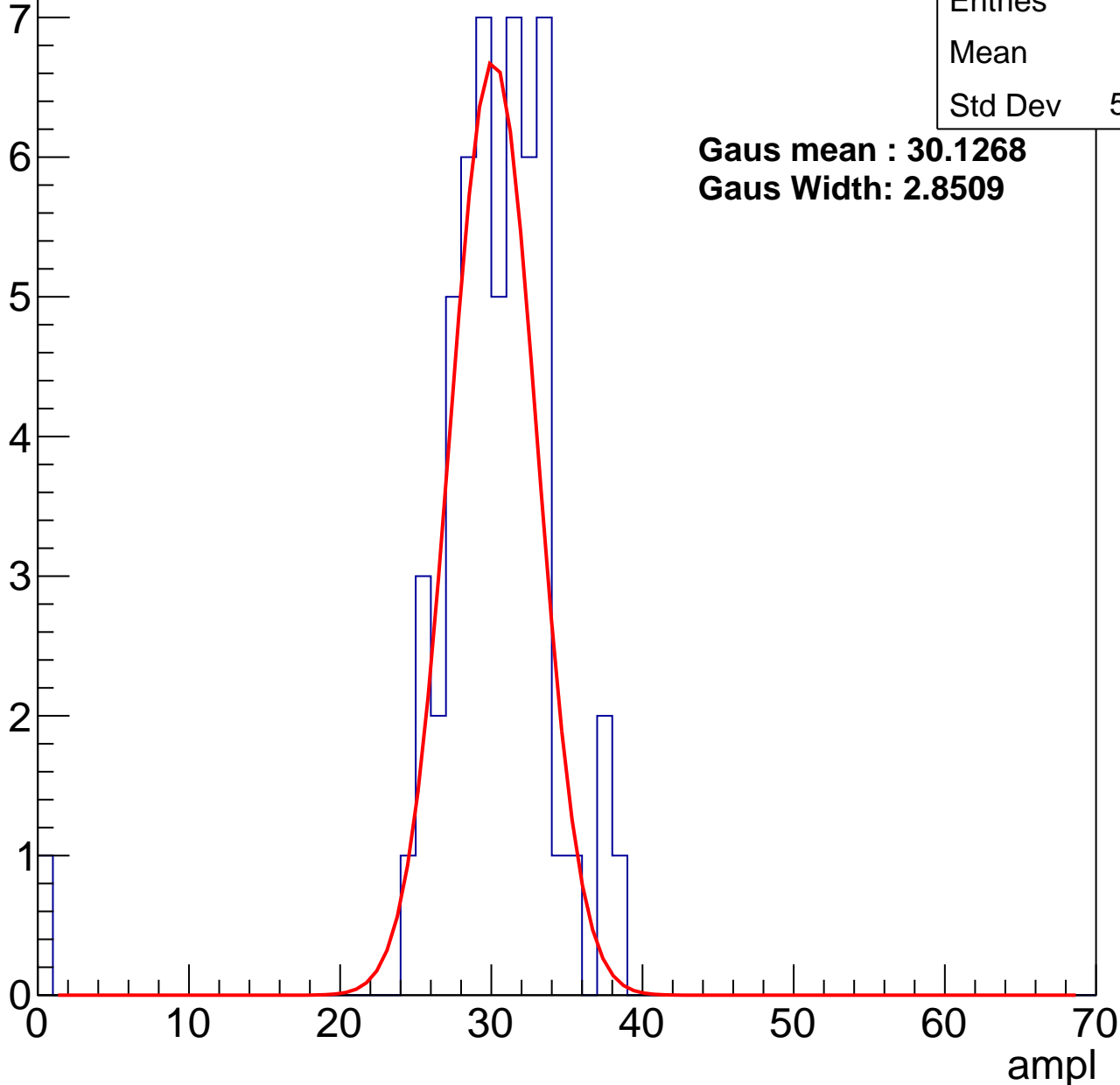
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 29.6 |
| Std Dev | 5.058 |

Gaus mean : 30.1268

Gaus Width: 2.8509



B1L100S, U9-ch76, adc1

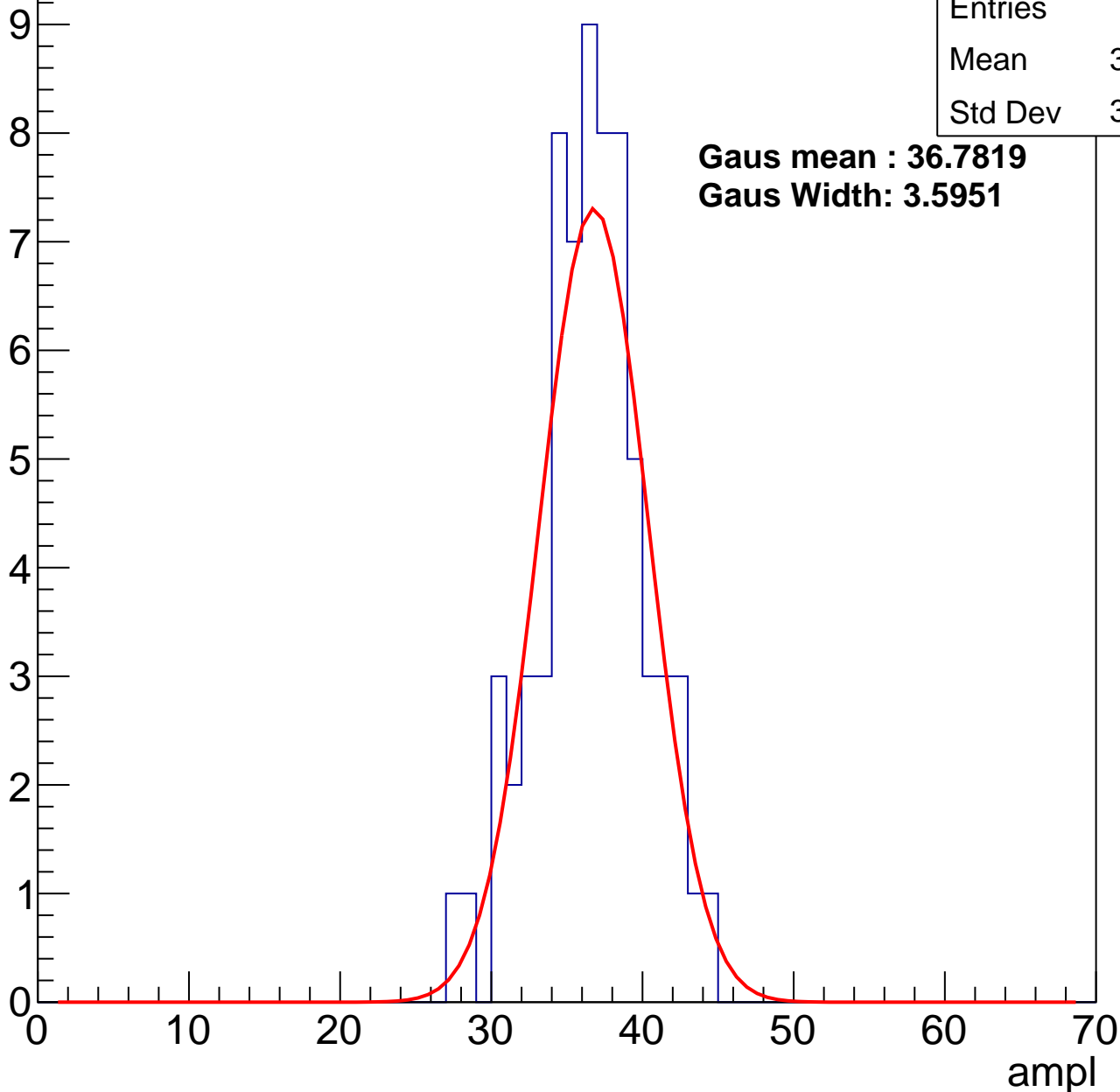
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.14 |
| Std Dev | 3.499 |

Gaus mean : 36.7819

Gaus Width: 3.5951



B1L100S, U9-ch76, adc2

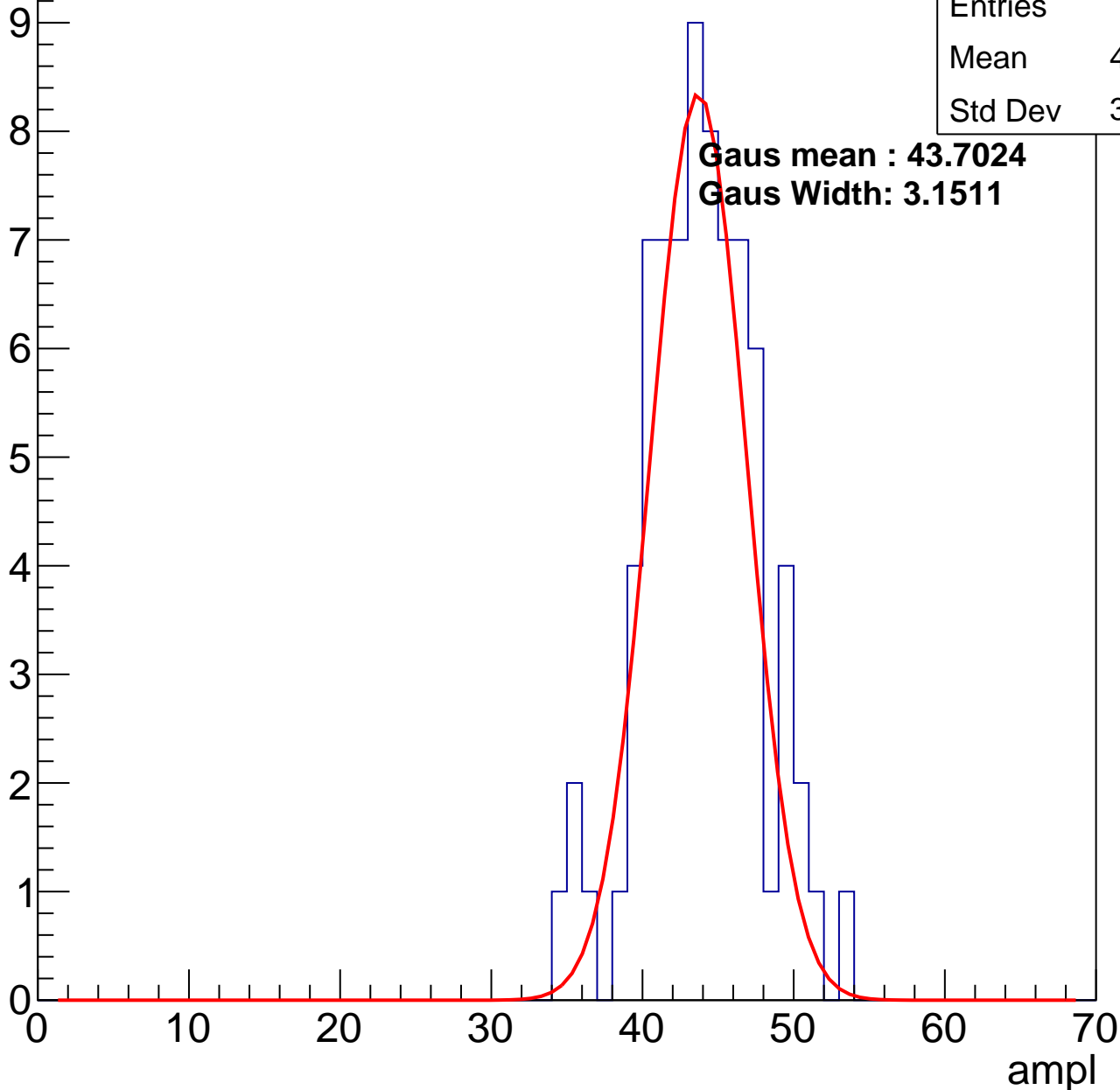
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 43.43 |
| Std Dev | 3.729 |

Gaus mean : 43.7024

Gaus Width: 3.1511

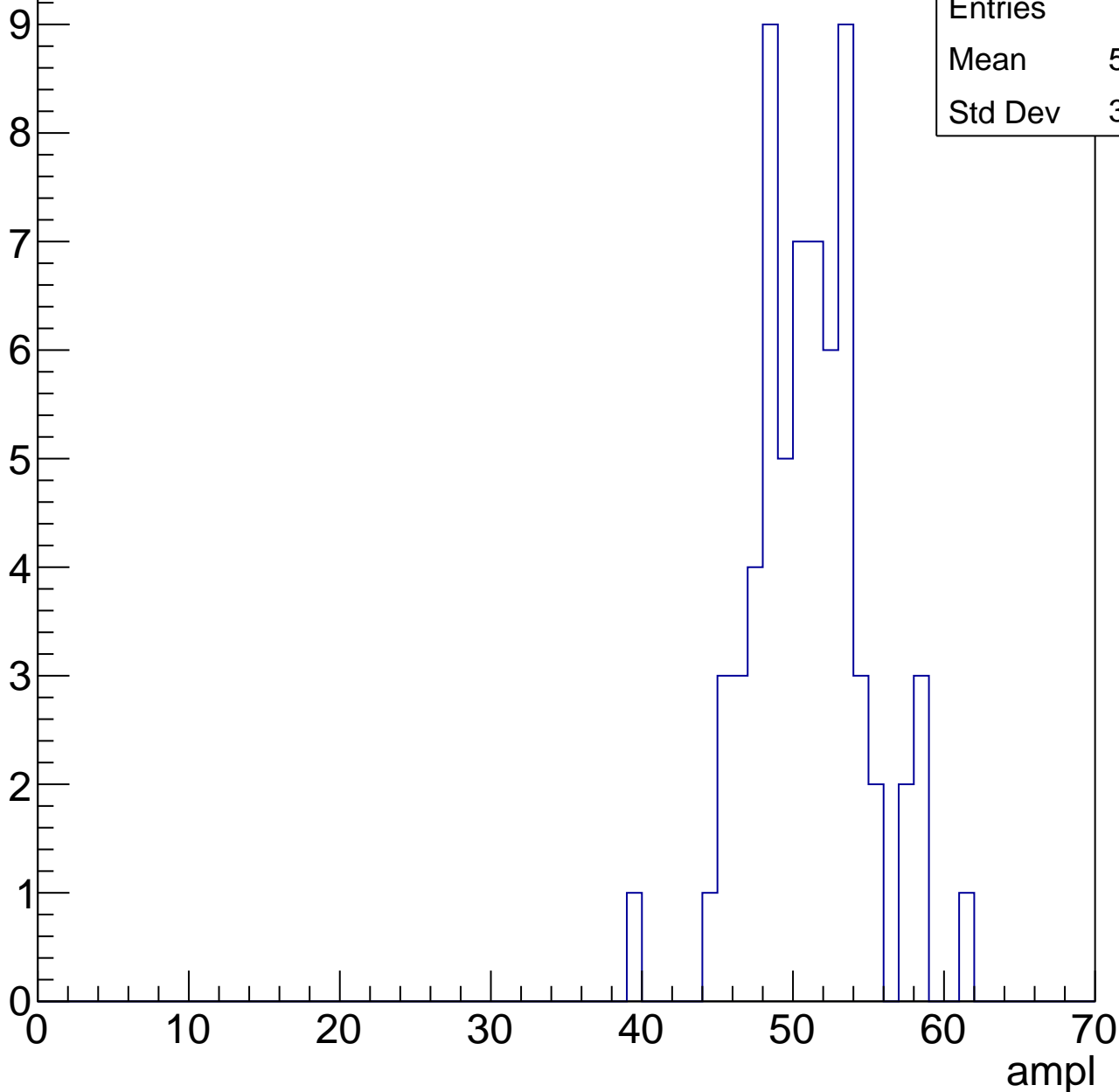


B1L100S, U9-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

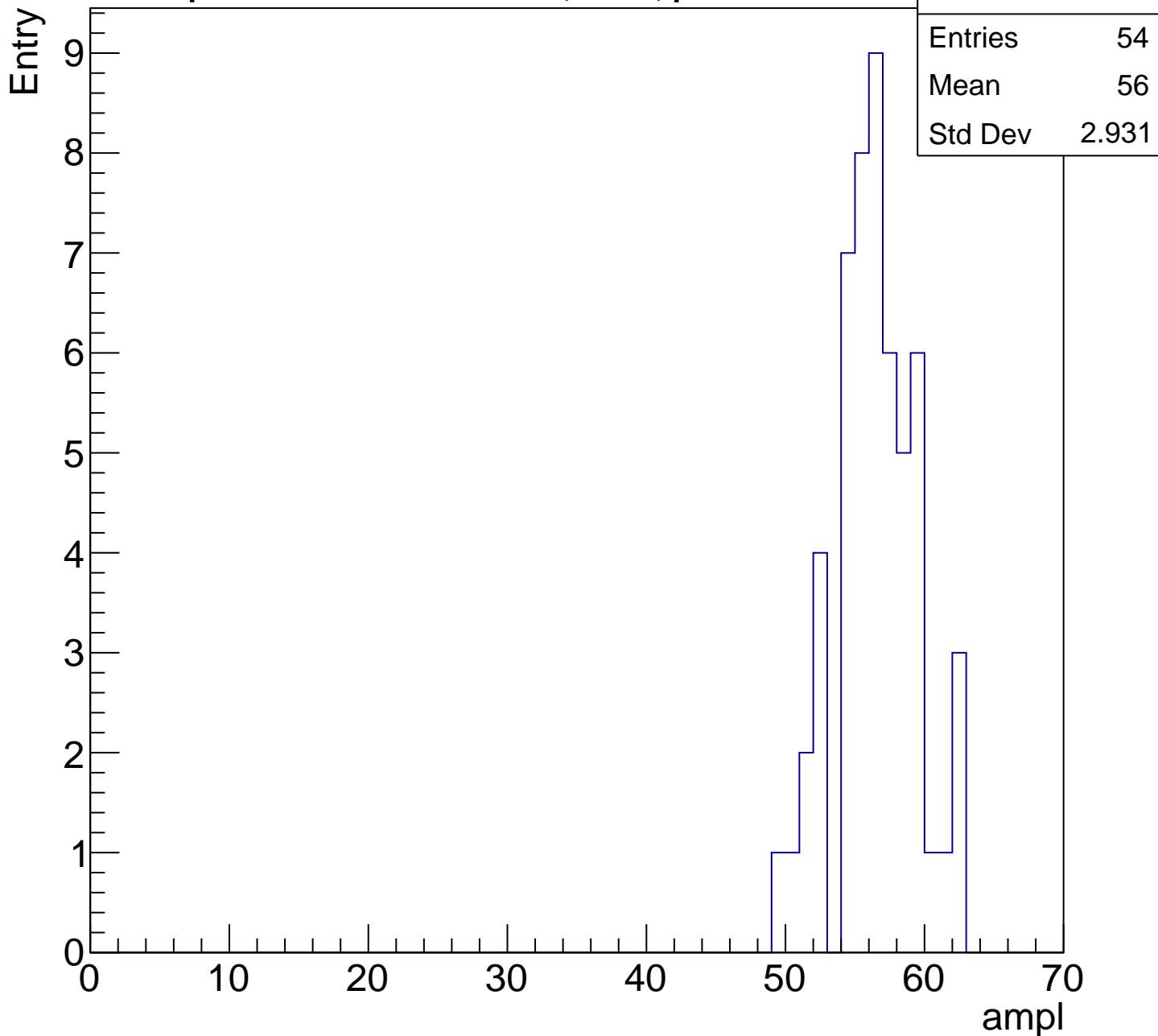
Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 50.58 |
| Std Dev | 3.806 |



B1L100S, U9-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch76, adc5

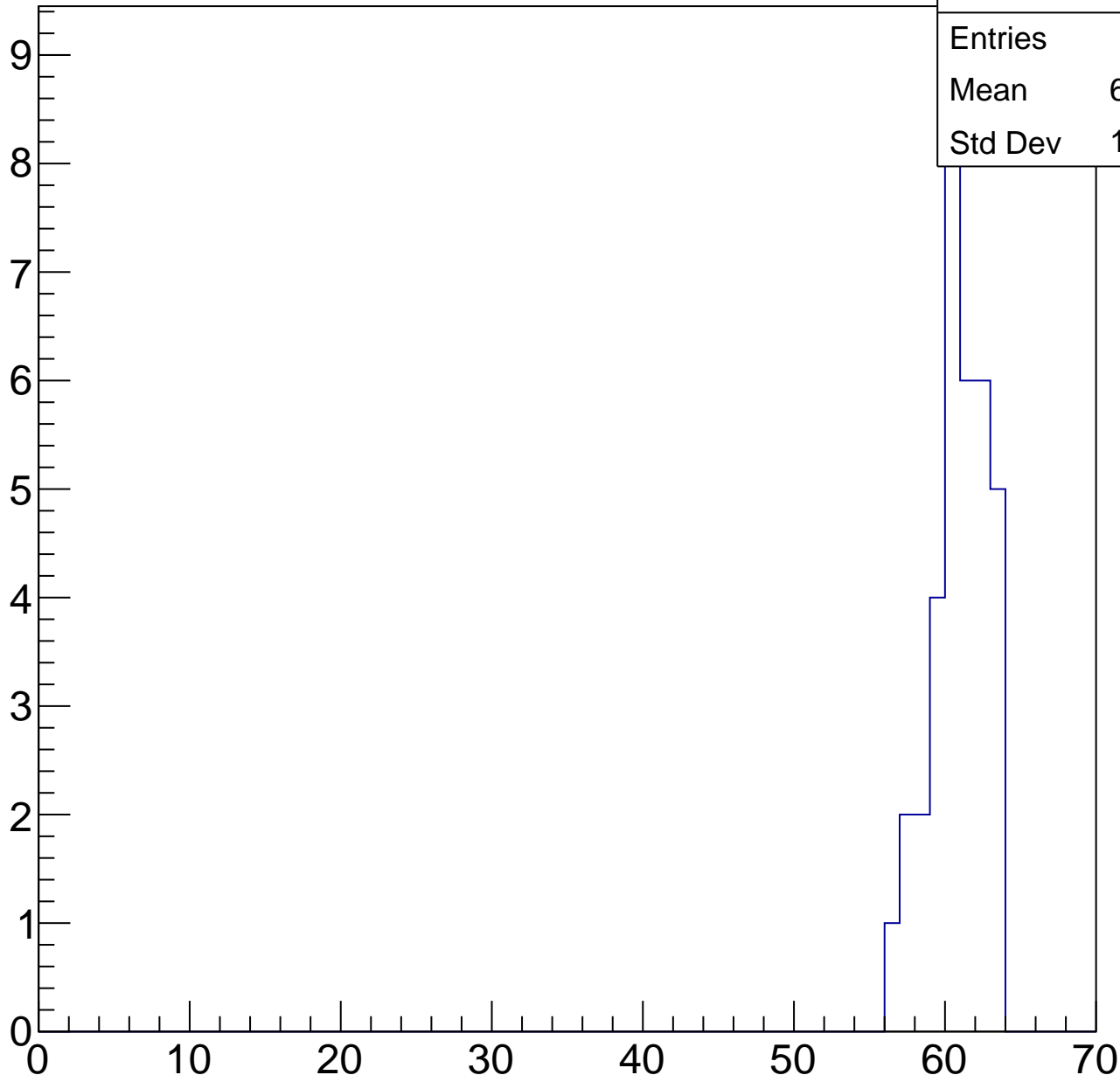
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.43 |
| Std Dev | 1.809 |

ampl

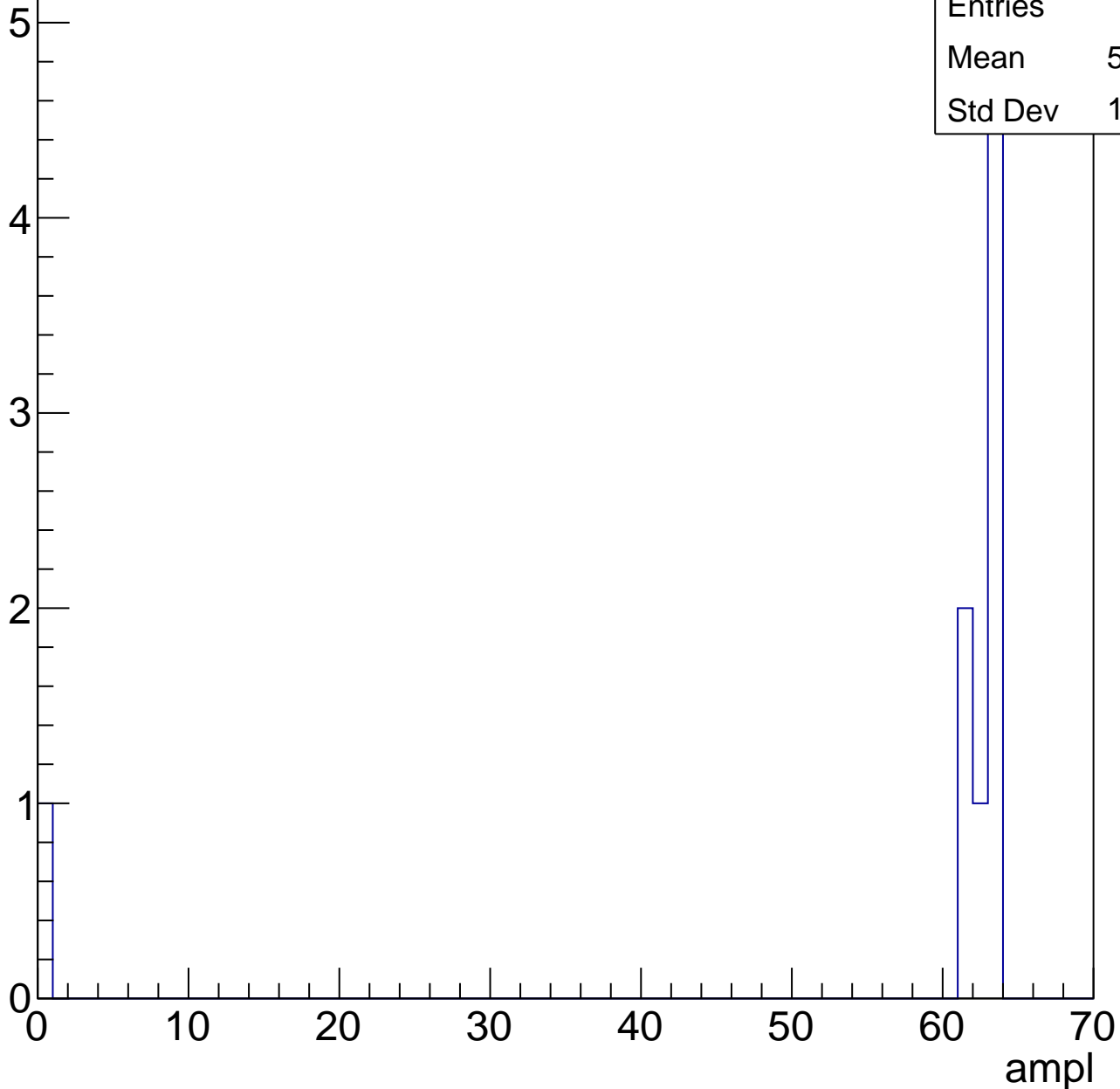


B1L100S, U9-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 55.44 |
| Std Dev | 19.62 |



B1L100S, U9-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch77, adc0

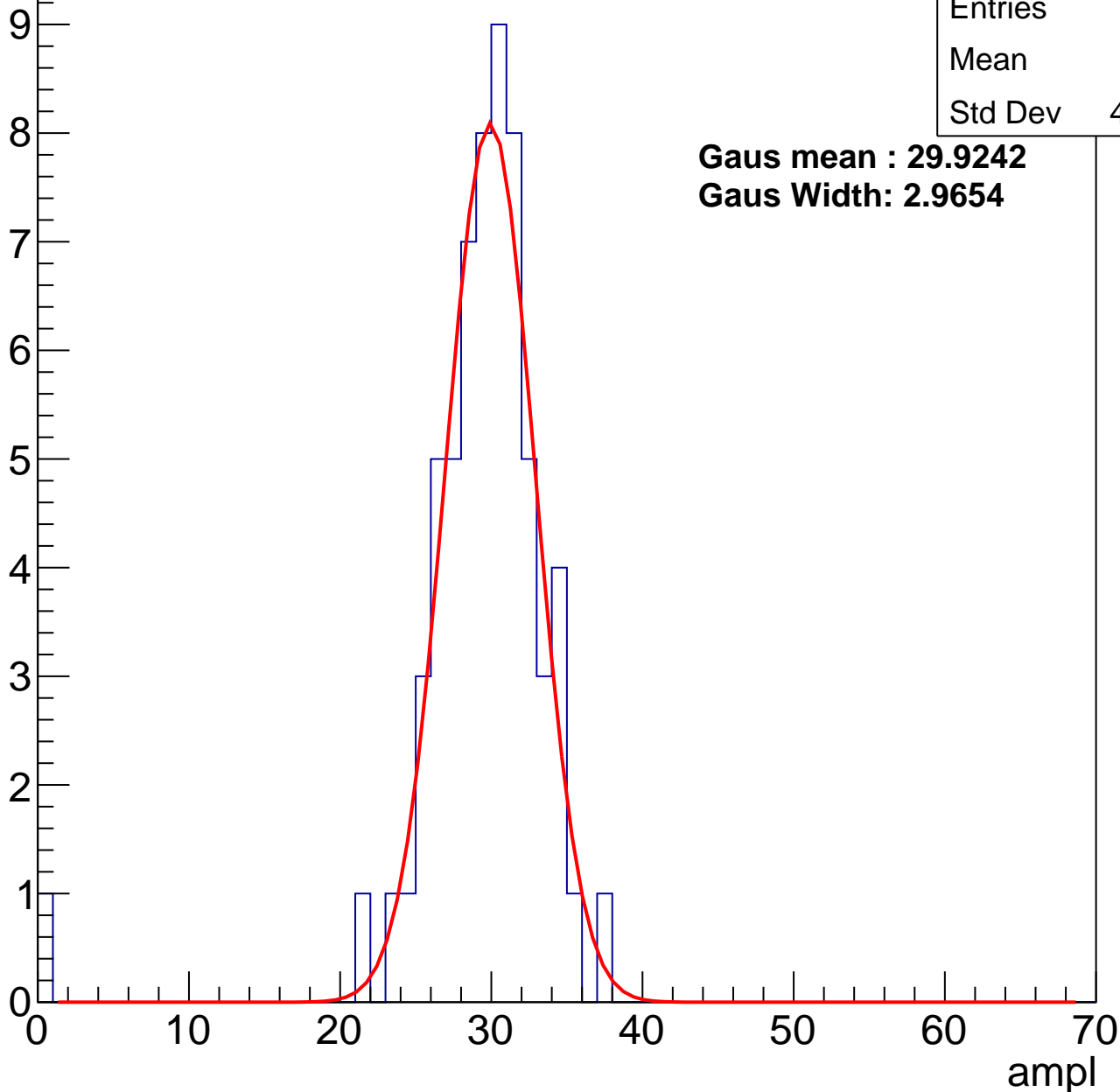
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.9 |
| Std Dev | 4.747 |

Gaus mean : 29.9242

Gaus Width: 2.9654



B1L100S, U9-ch77, adc1

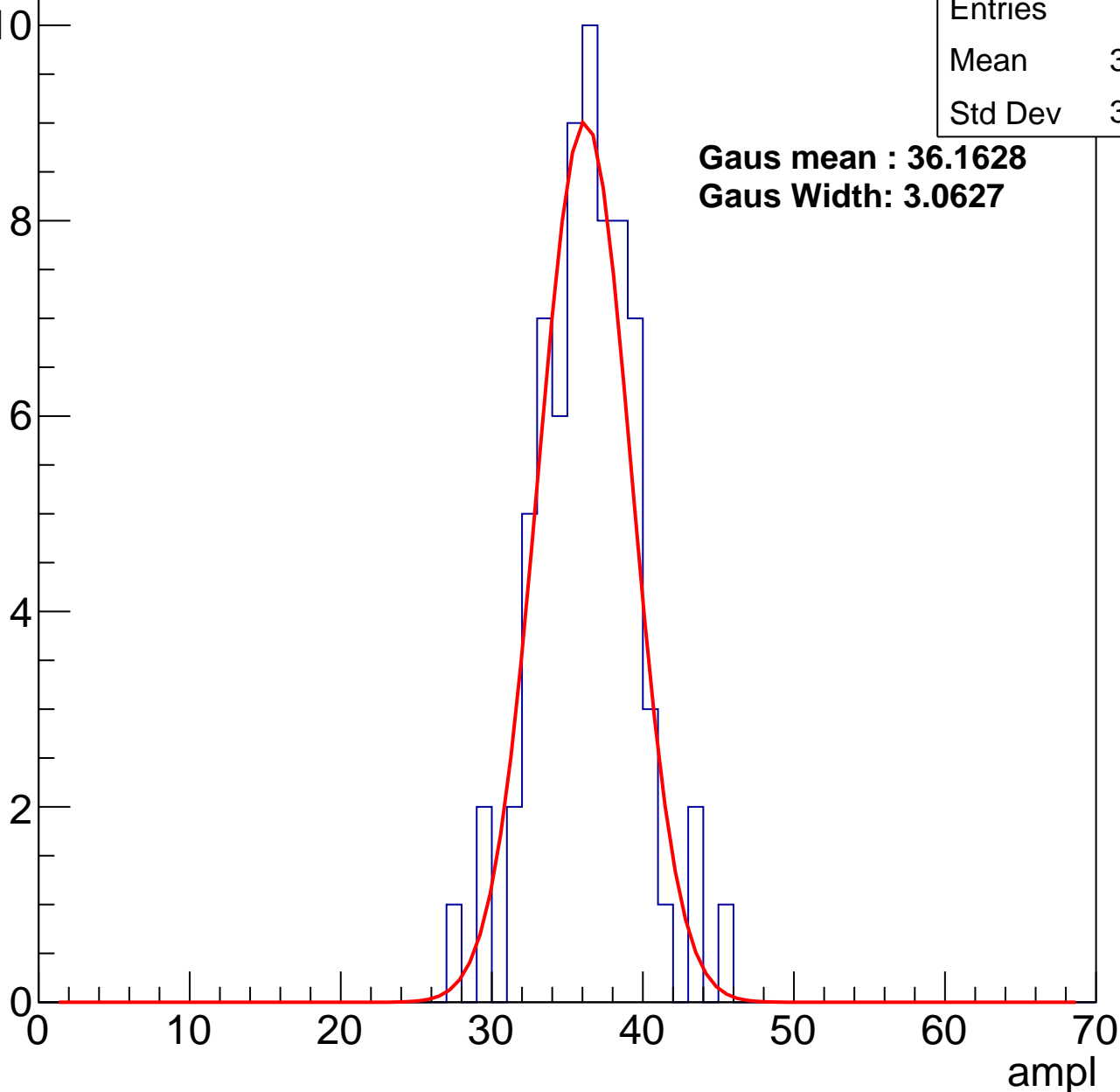
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.86 |
| Std Dev | 3.246 |

Gaus mean : 36.1628

Gaus Width: 3.0627



B1L100S, U9-ch77, adc2

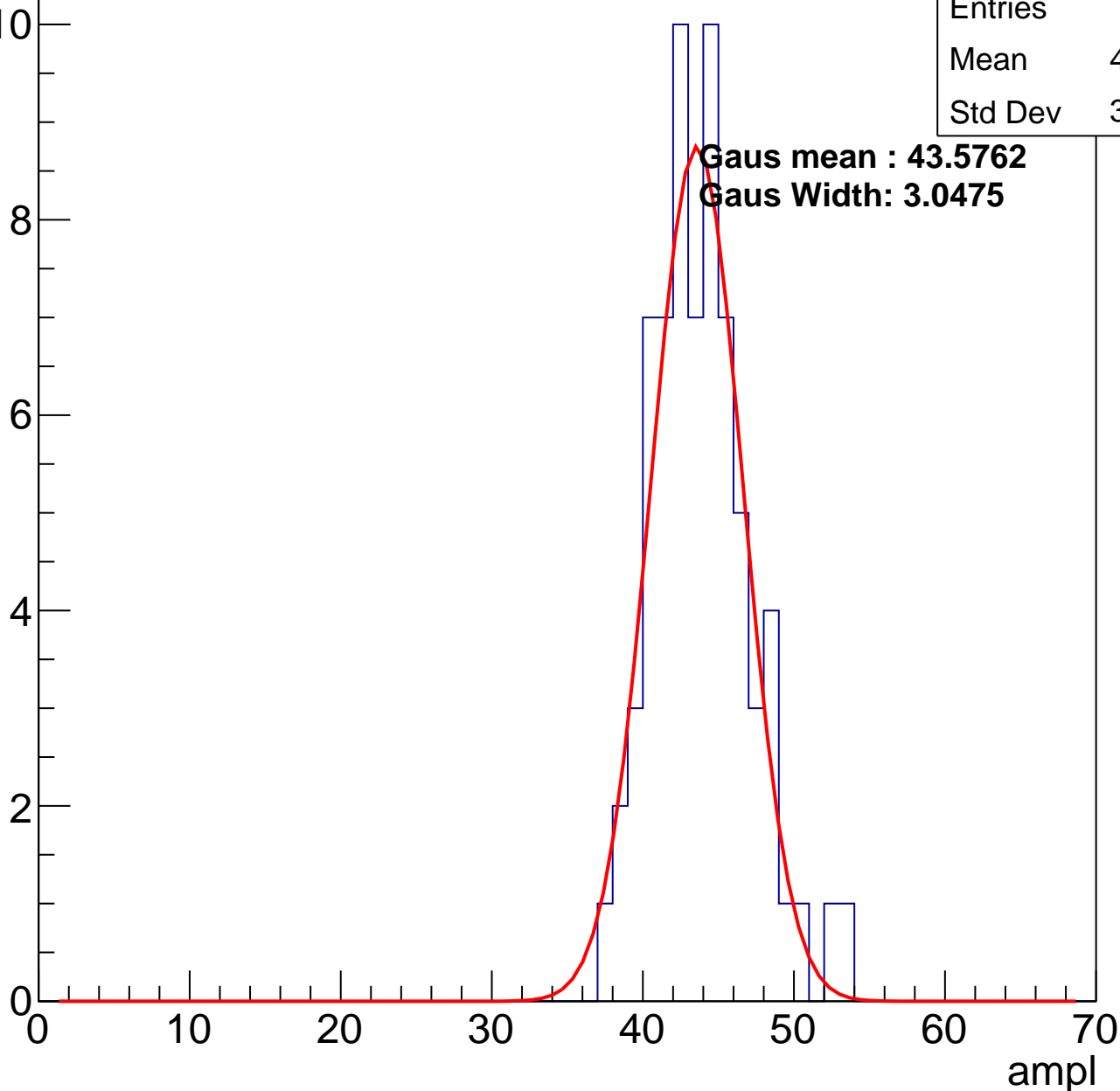
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 43.43 |
| Std Dev | 3.205 |

Entry

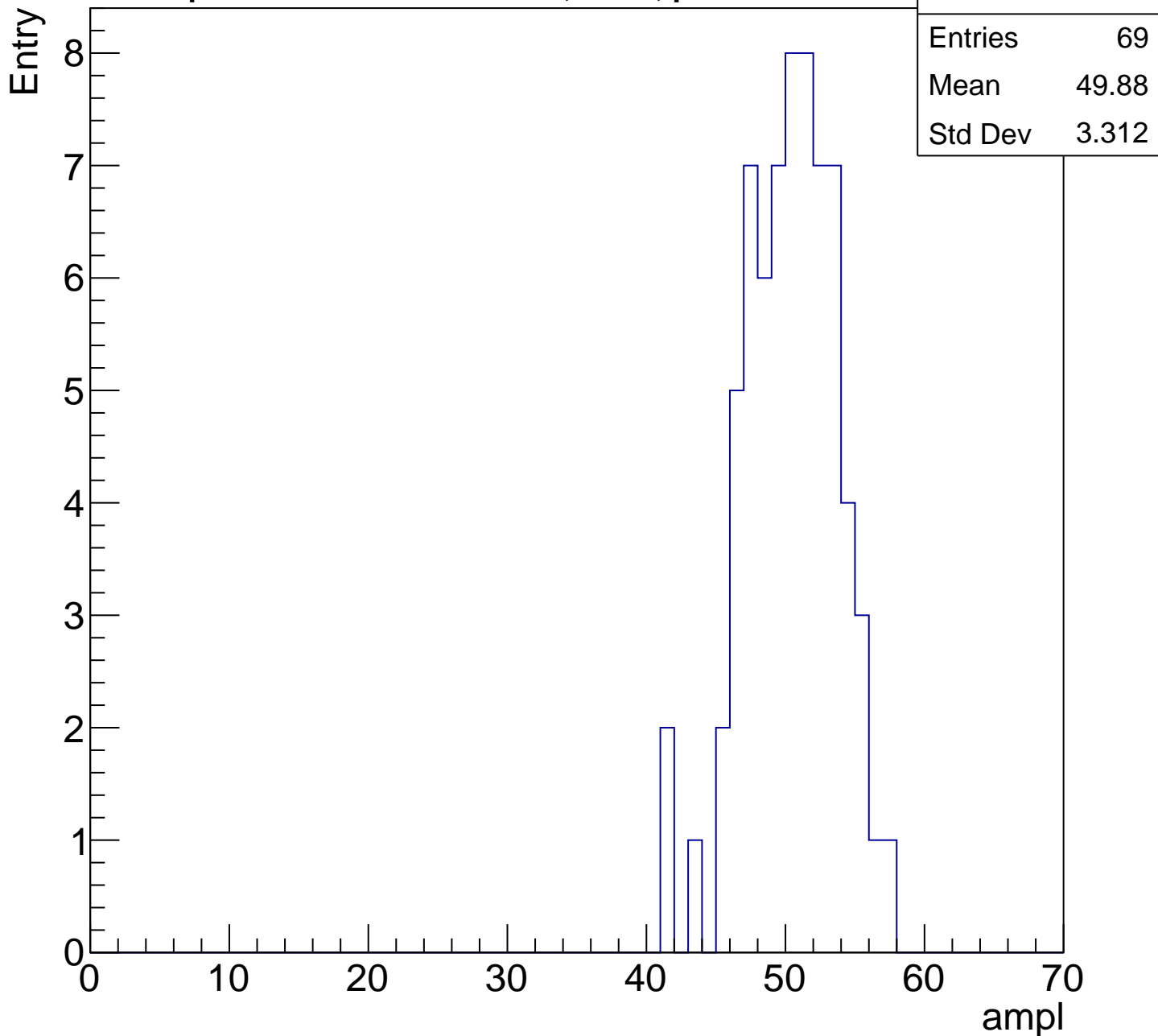
Gaus mean : 43.5762

Gaus Width: 3.0475



B1L100S, U9-ch77, adc3

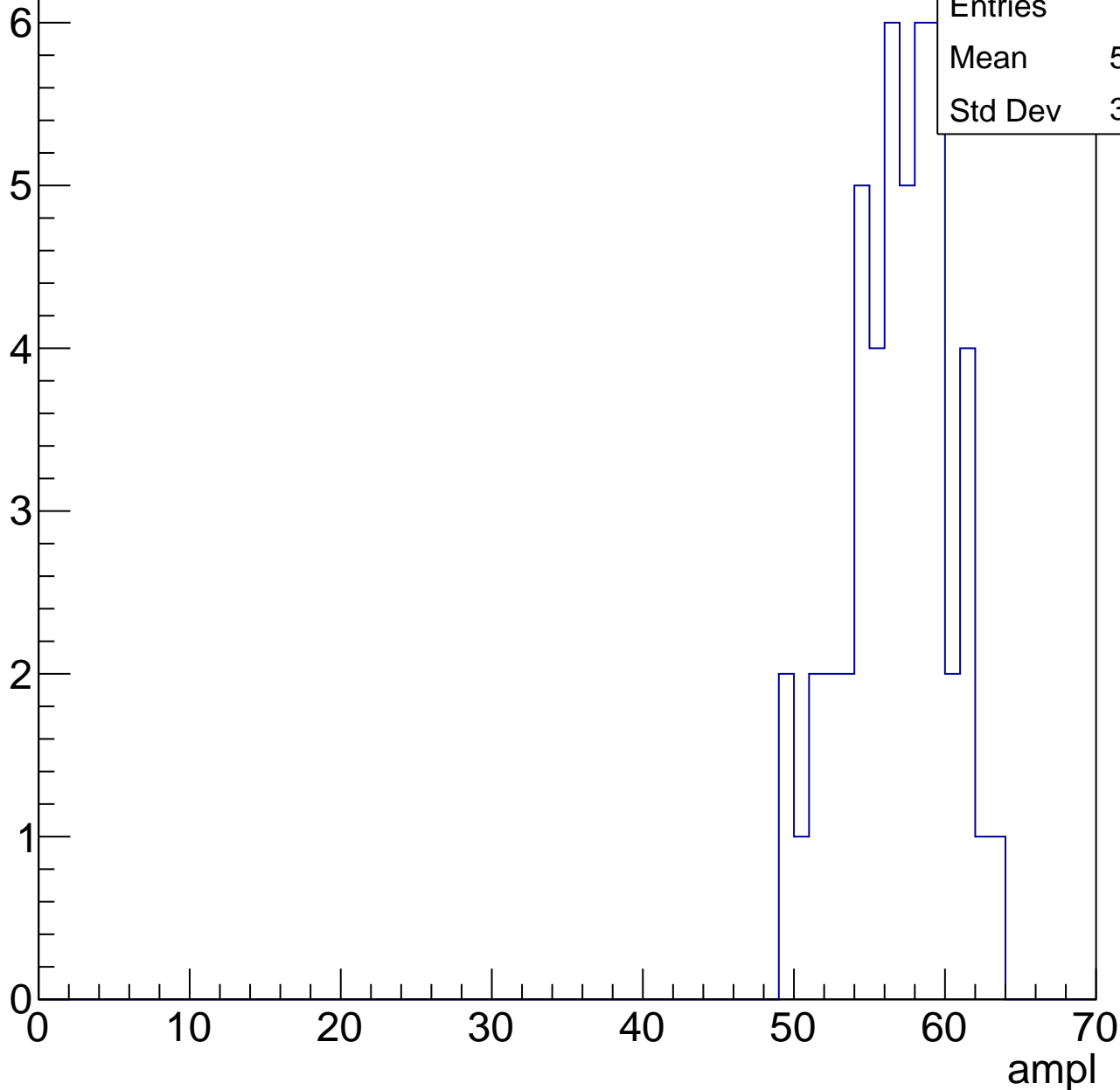
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch77, adc5

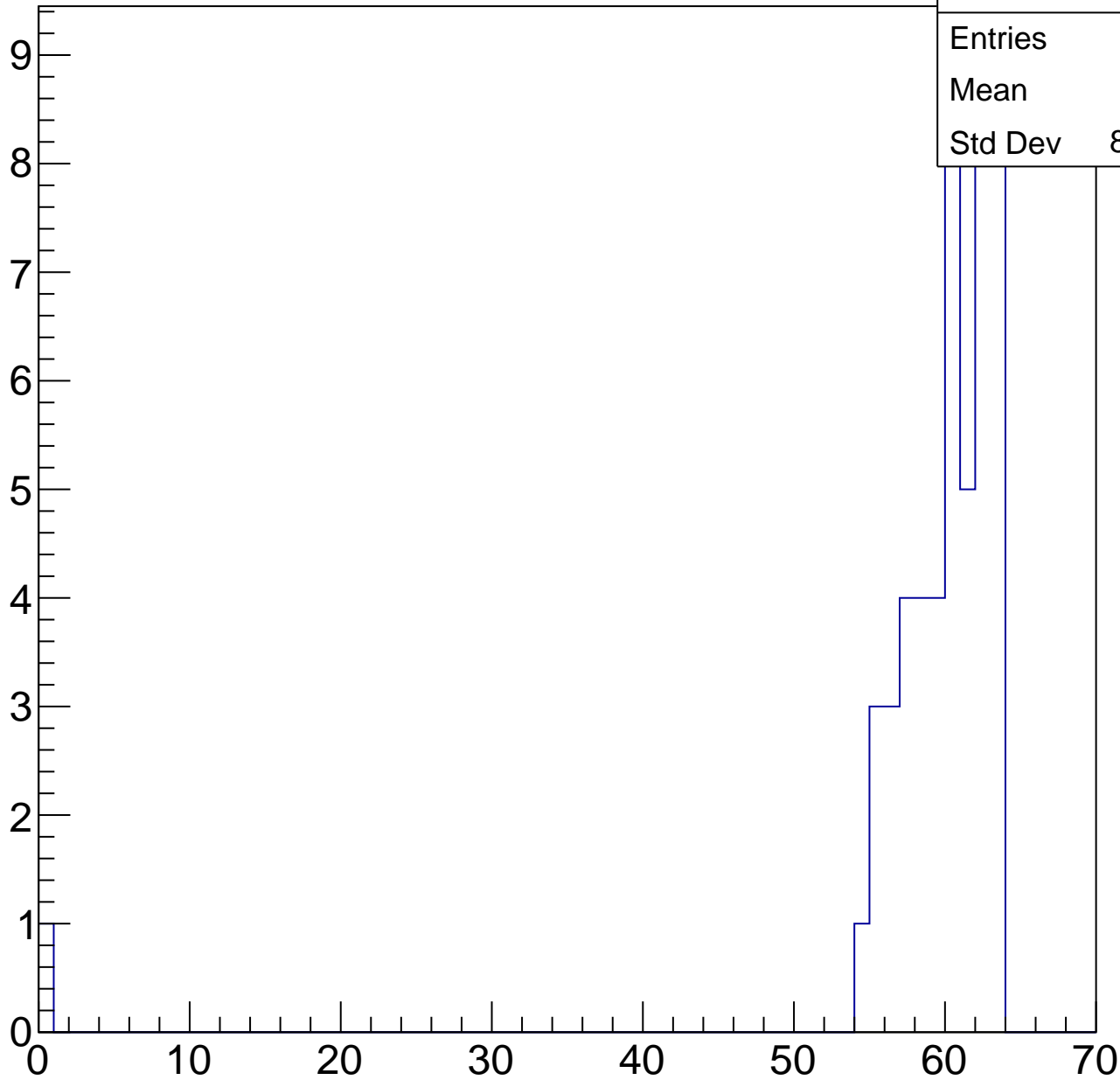
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.6 |
| Std Dev | 8.752 |

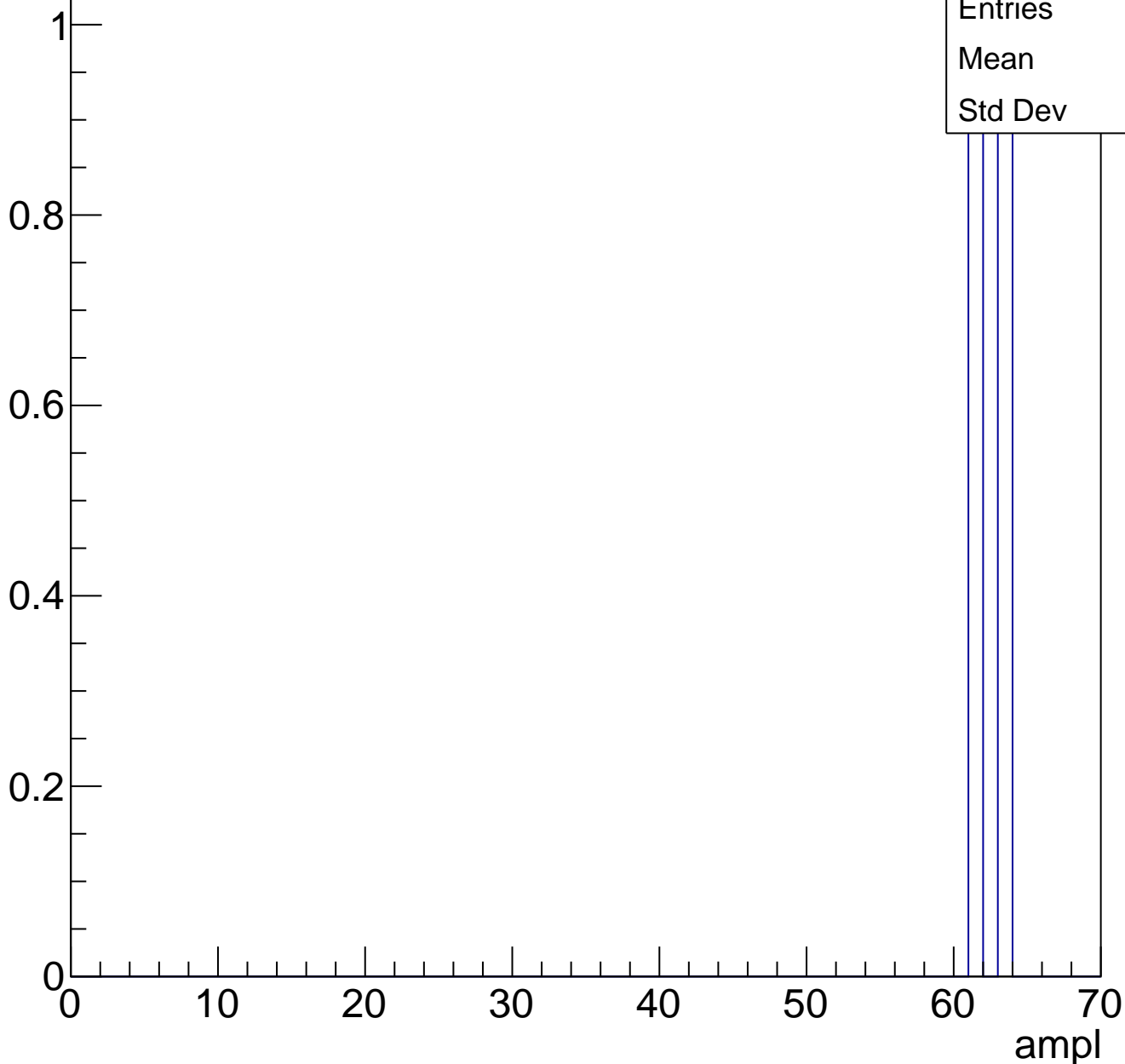
ampl



B1L100S, U9-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch78, adc0

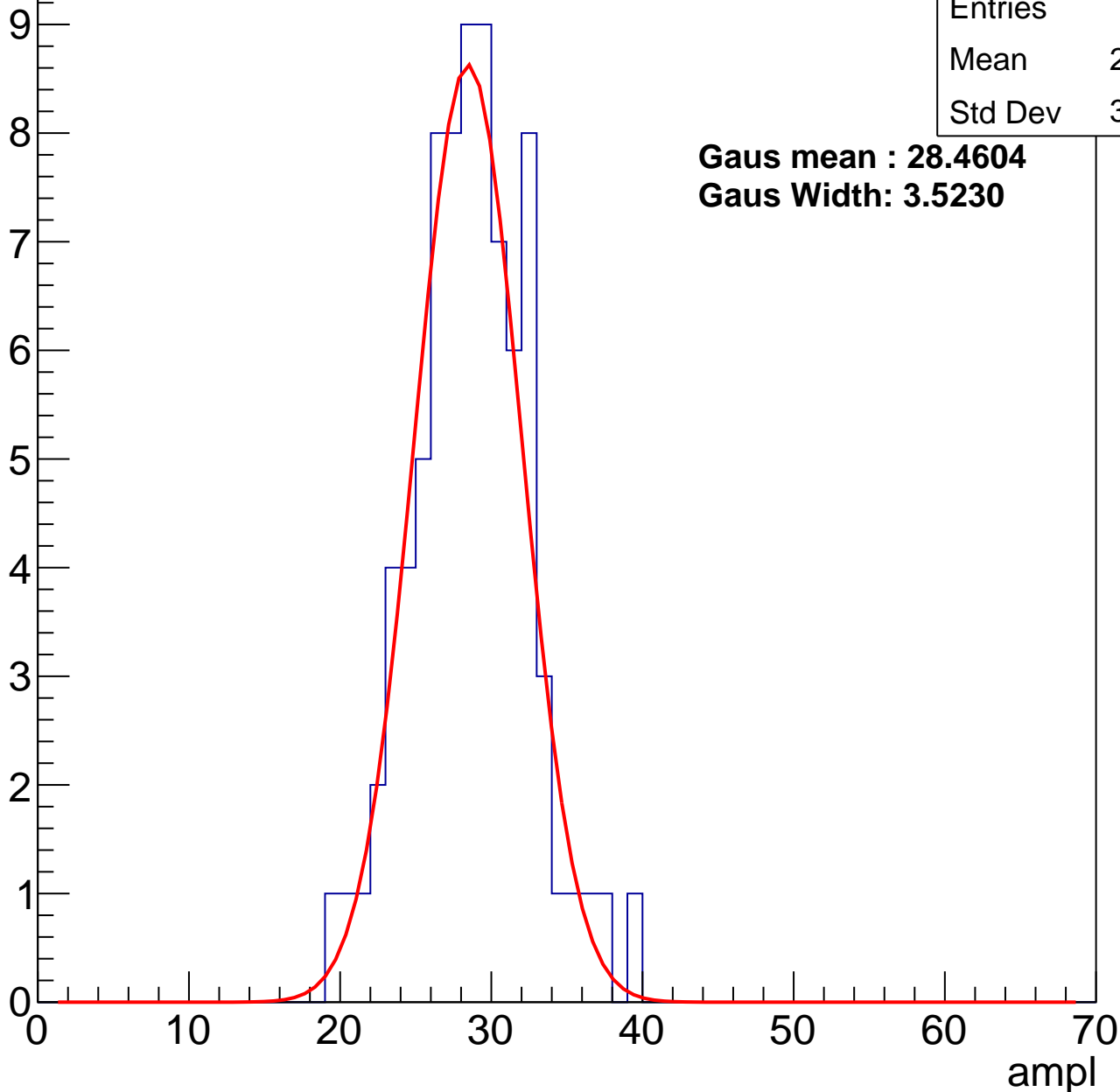
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 28.22 |
| Std Dev | 3.778 |

Gaus mean : 28.4604

Gaus Width: 3.5230



B1L100S, U9-ch78, adc1

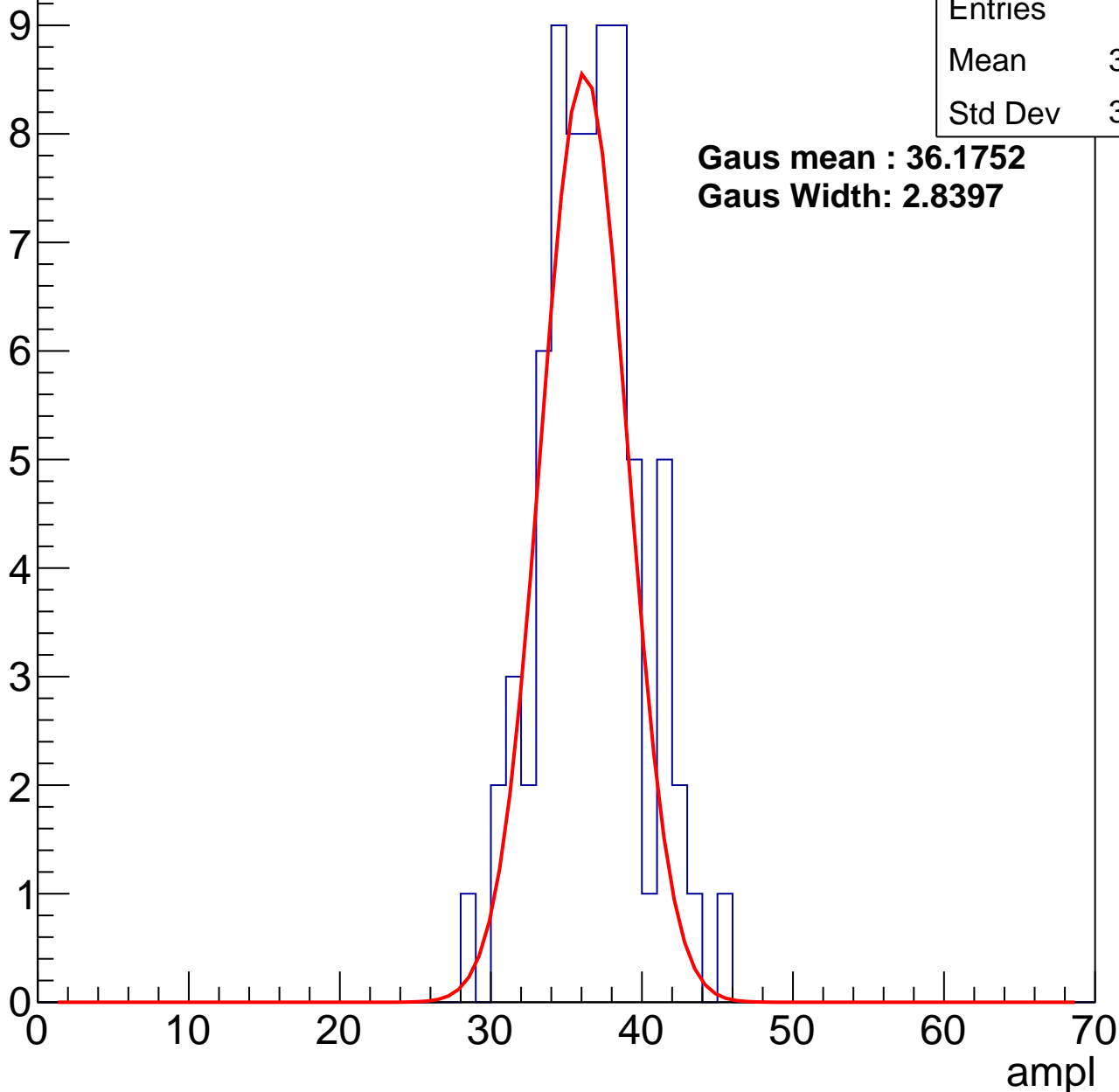
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.17 |
| Std Dev | 3.274 |

Gaus mean : 36.1752

Gaus Width: 2.8397



B1L100S, U9-ch78, adc2

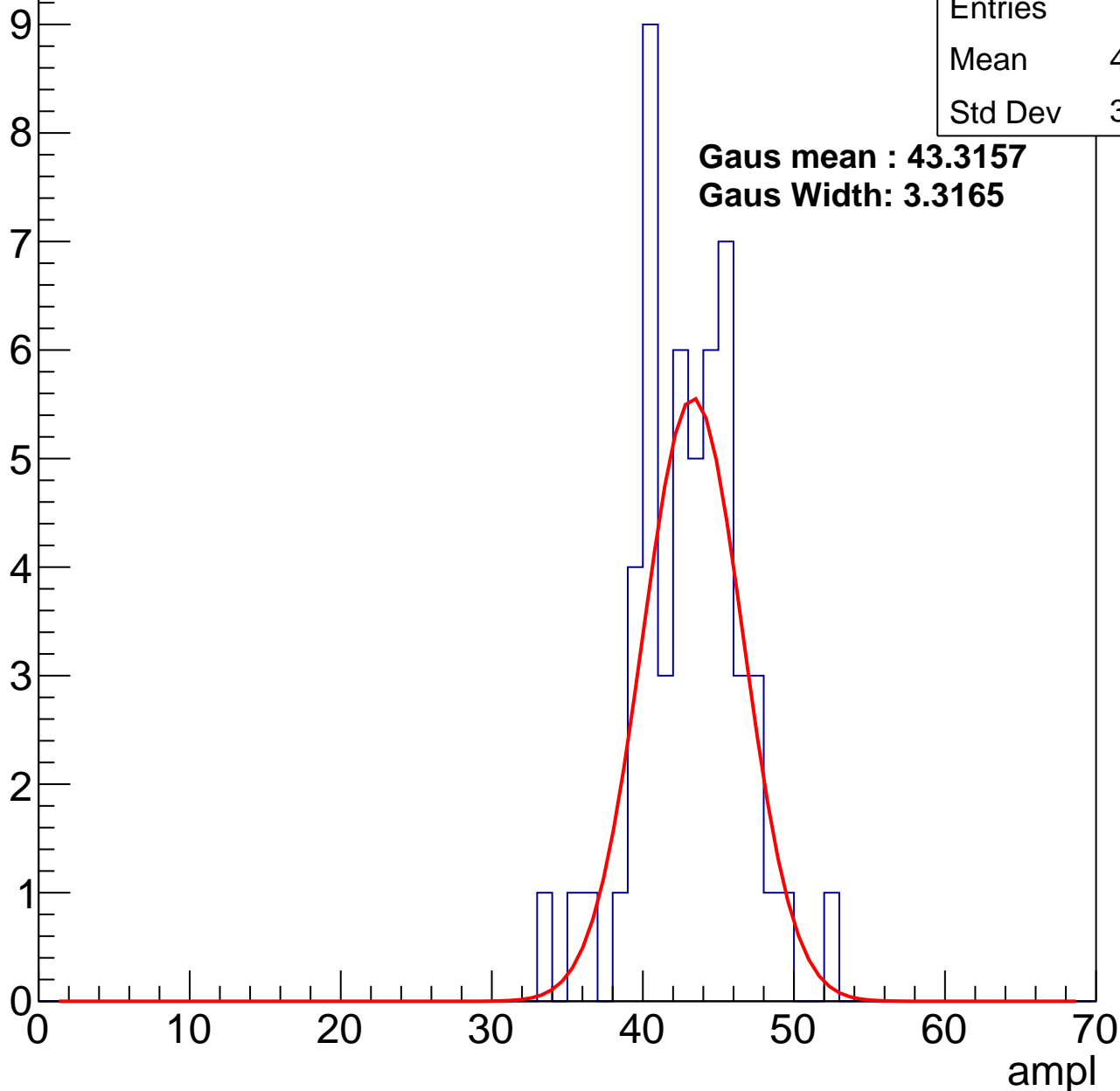
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 42.55 |
| Std Dev | 3.478 |

Gaus mean : 43.3157

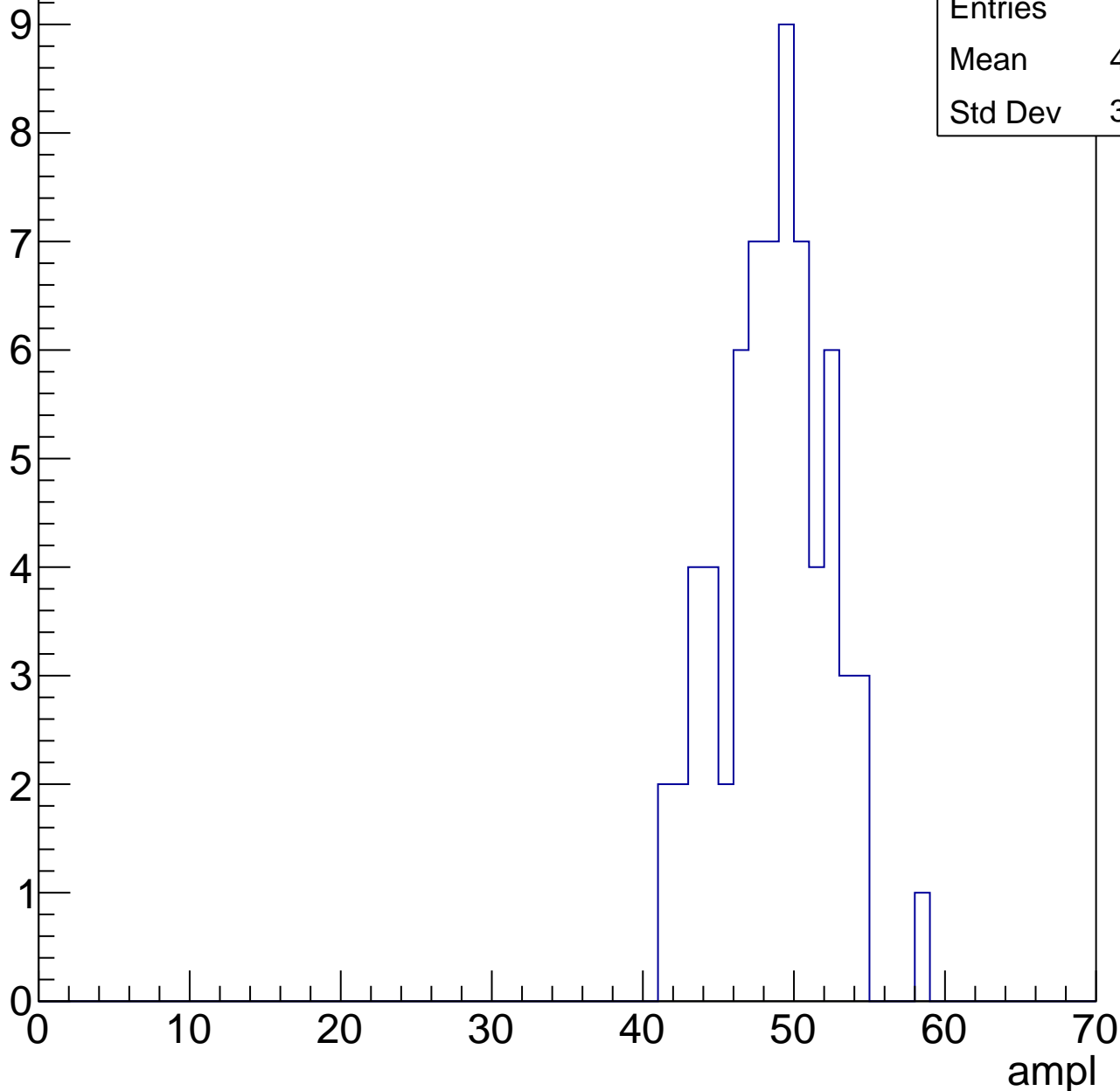
Gaus Width: 3.3165



B1L100S, U9-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



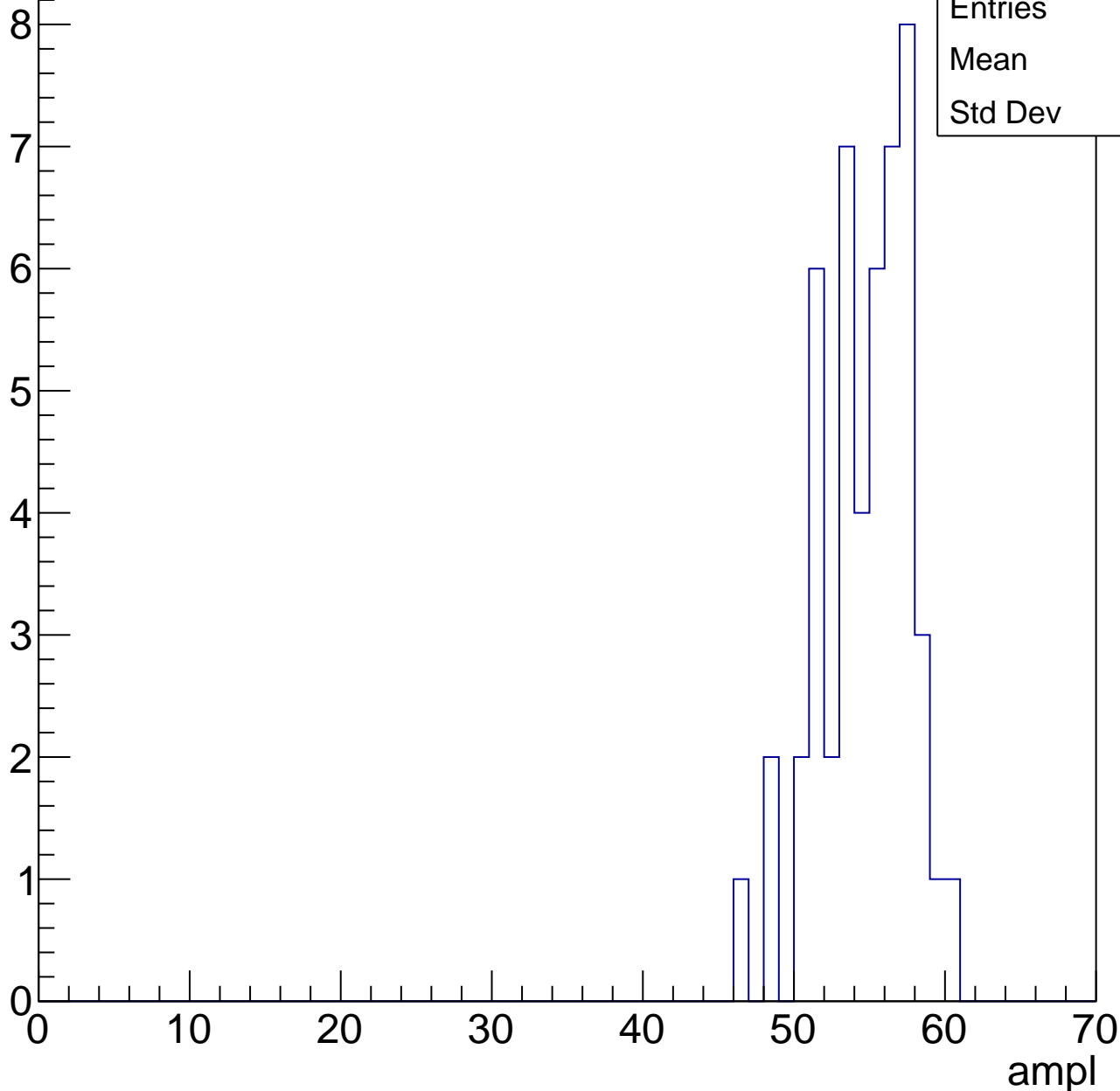
| | |
|---------|-------|
| Entries | 67 |
| Mean | 48.22 |
| Std Dev | 3.523 |

B1L100S, U9-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 50 |
| Mean | 54.2 |
| Std Dev | 3 |

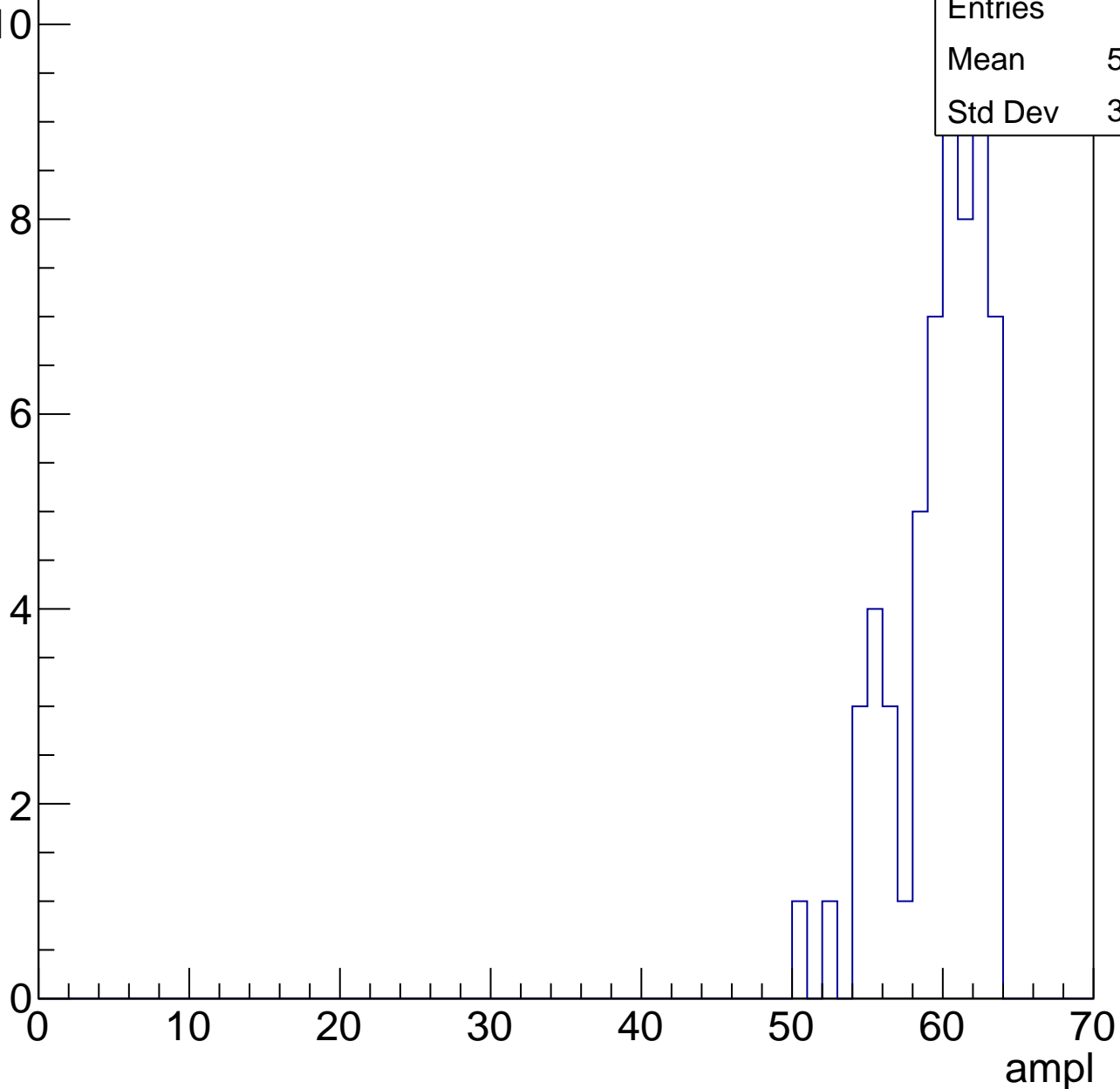


B1L100S, U9-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

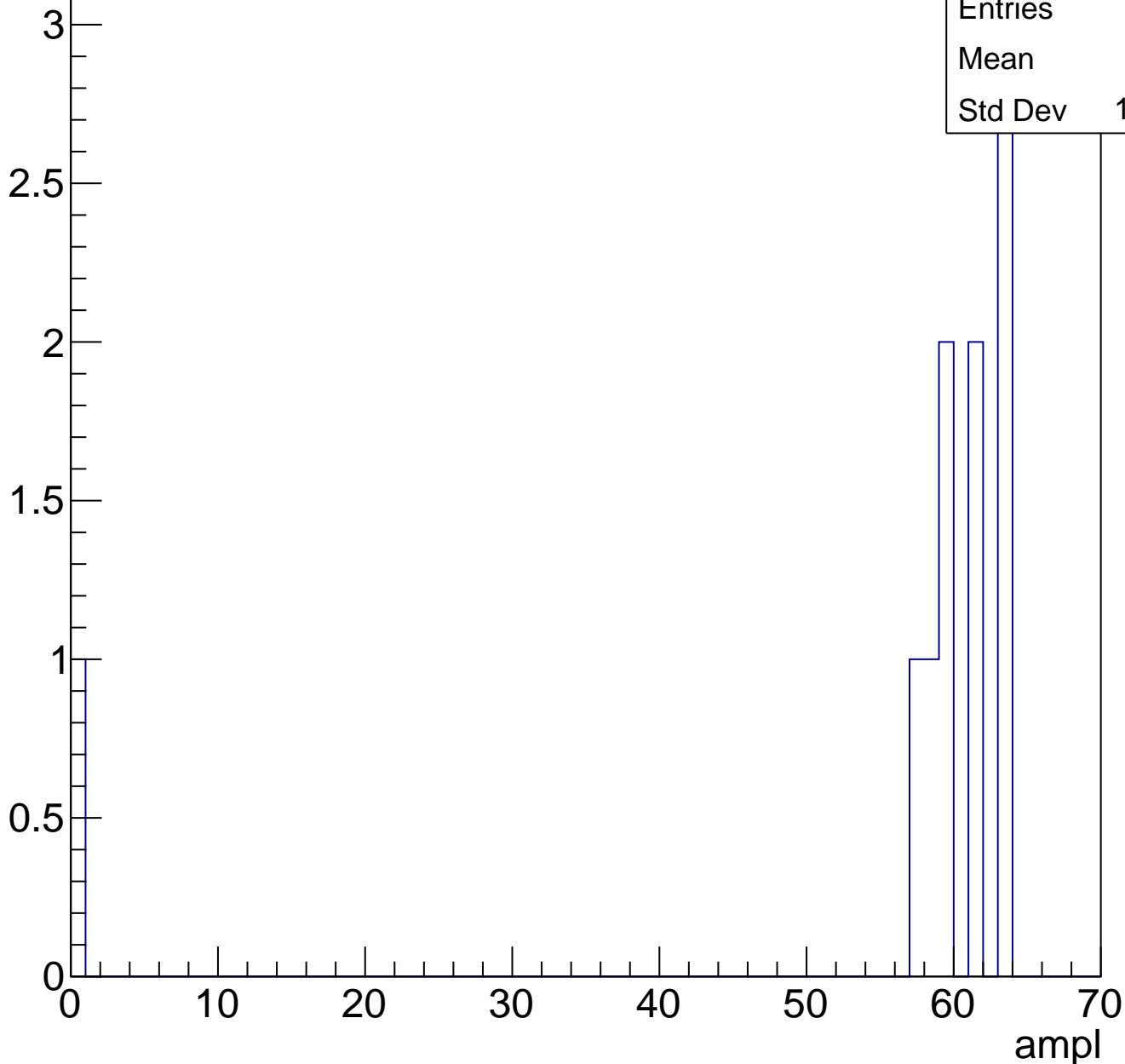
| | |
|---------|-------|
| Entries | 59 |
| Mean | 59.34 |
| Std Dev | 3.029 |



B1L100S, U9-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

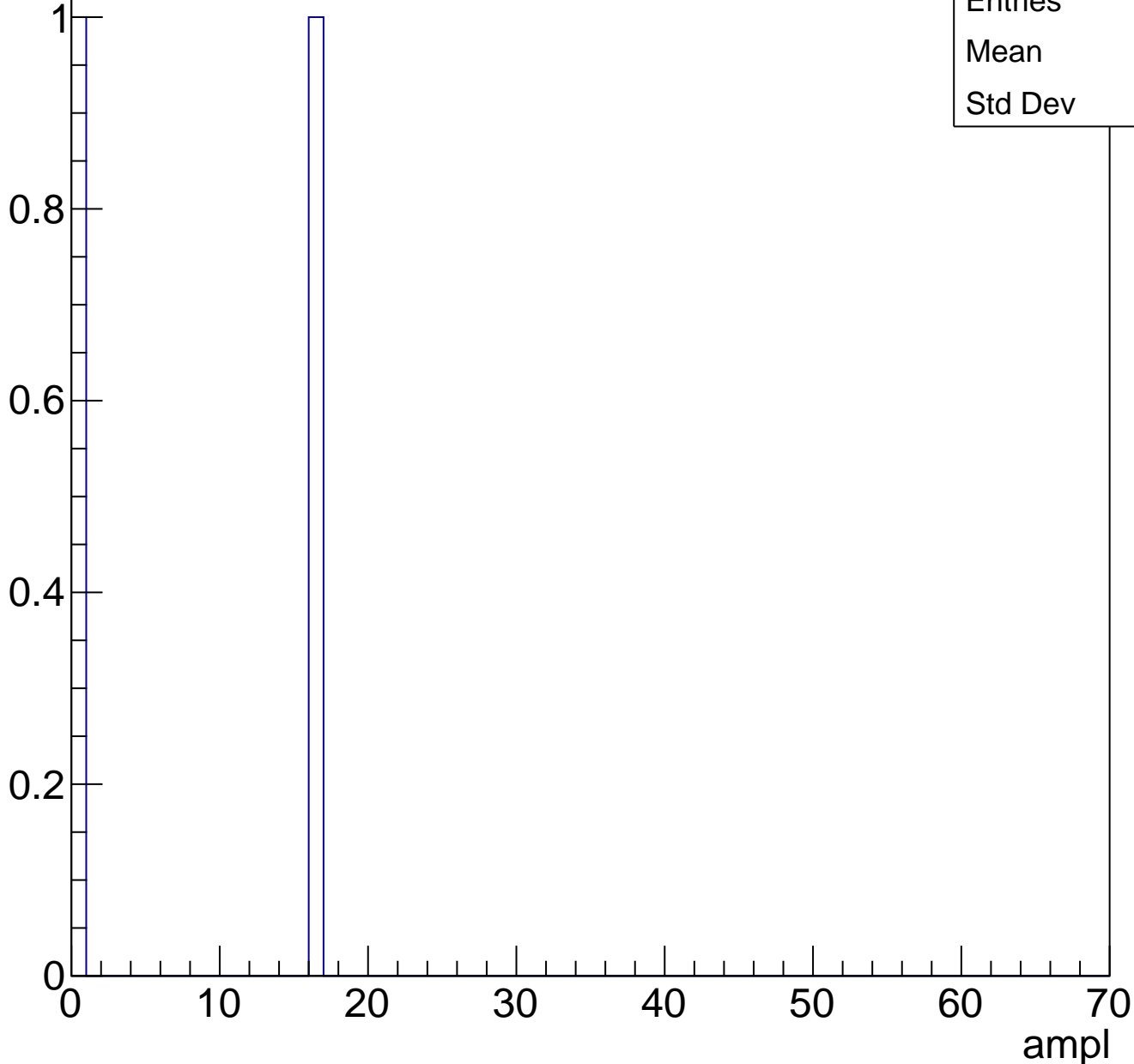
Entry



B1L100S, U9-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 8 |
| Std Dev | 8 |

B1L100S, U9-ch79, adc0

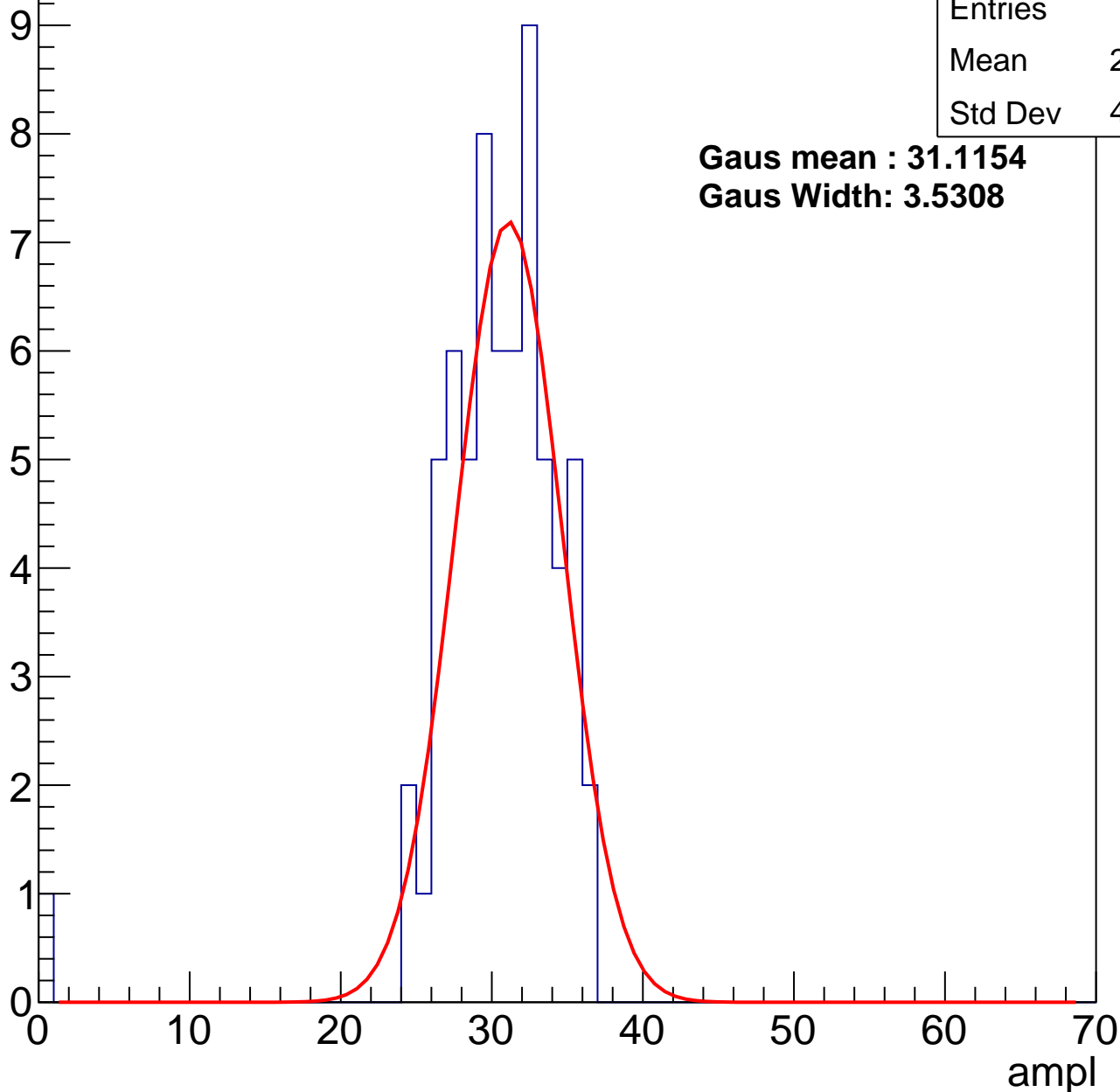
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 29.83 |
| Std Dev | 4.812 |

Gaus mean : 31.1154

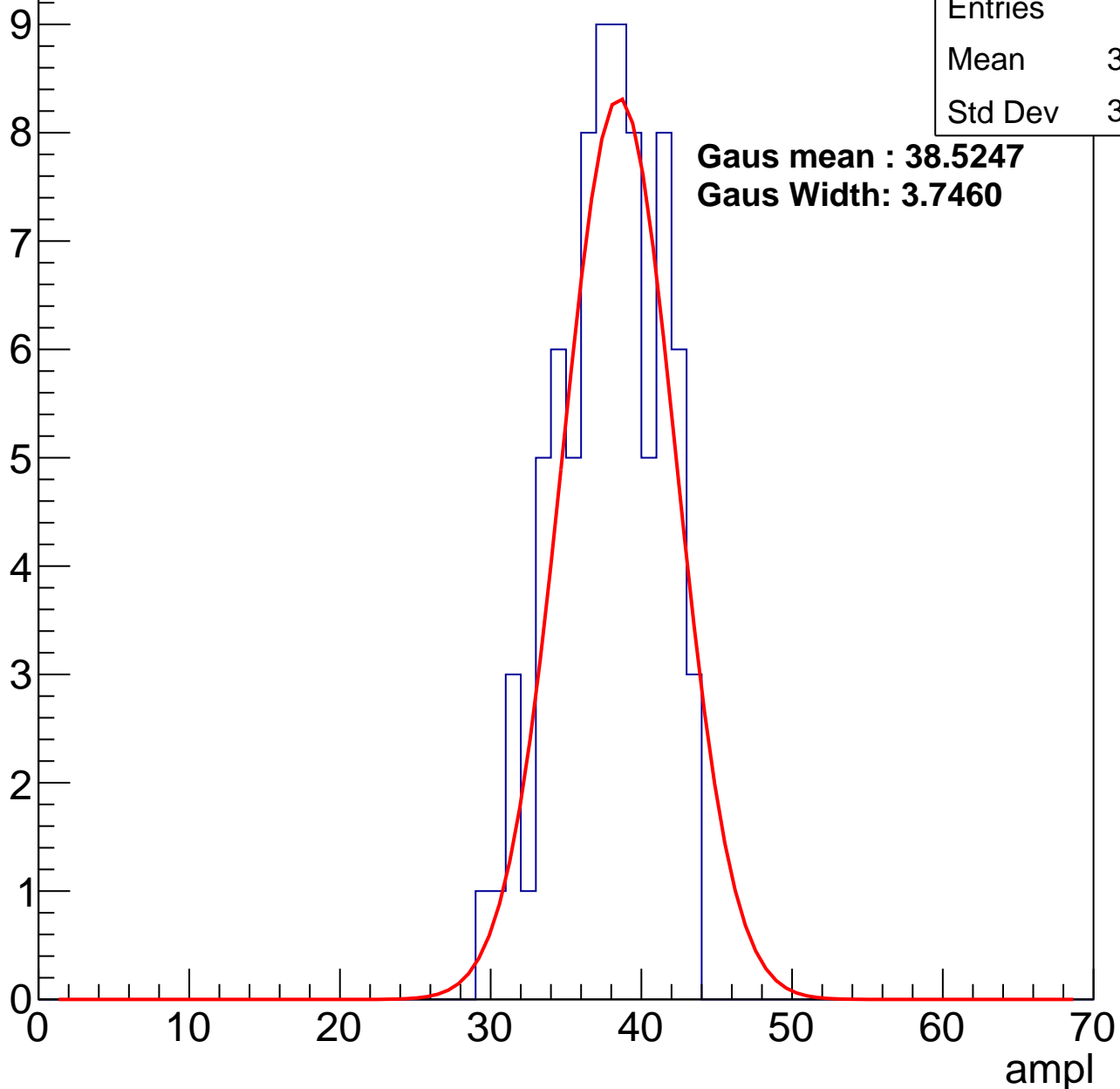
Gaus Width: 3.5308



B1L100S, U9-ch79, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch79, adc2

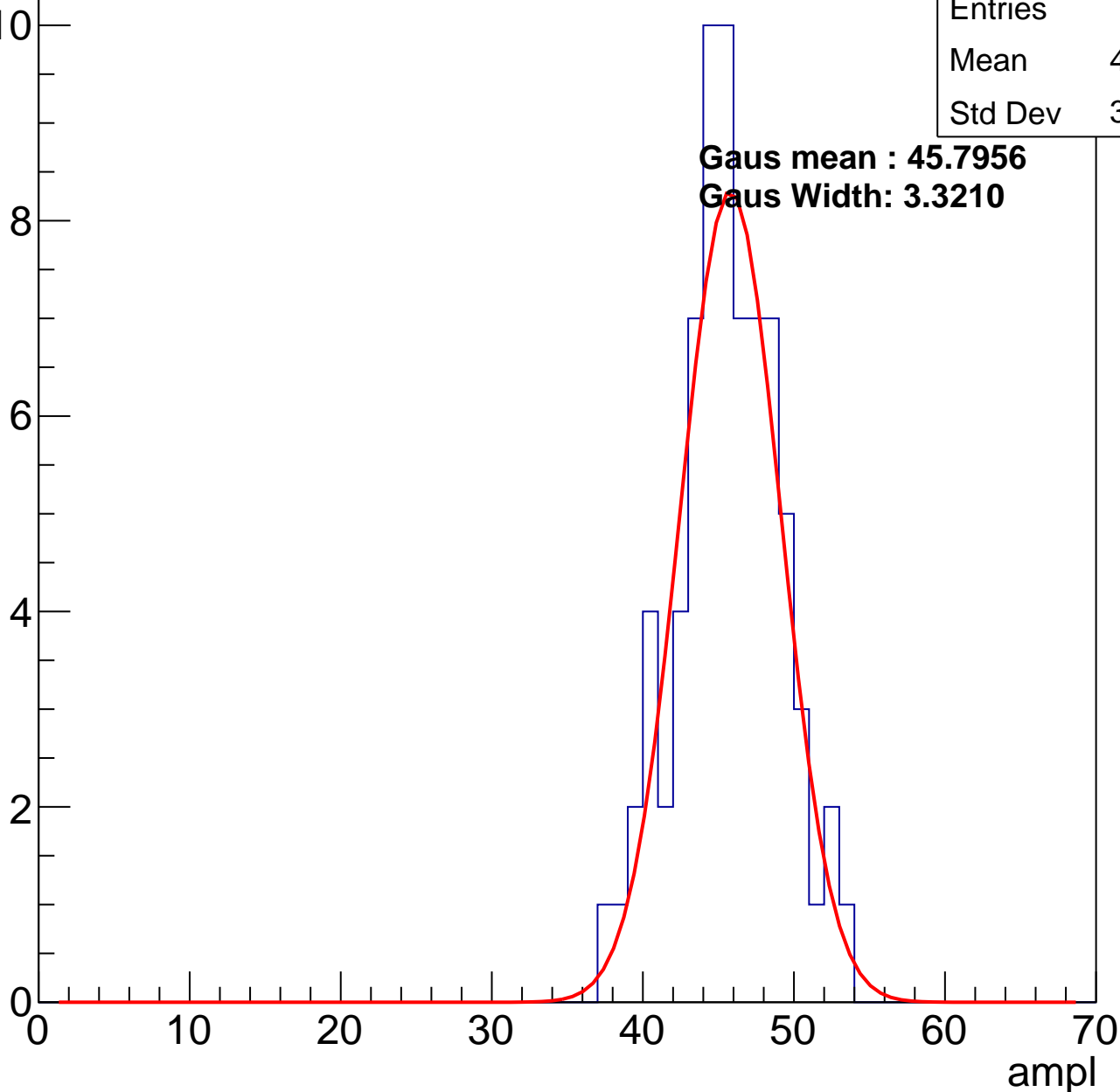
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 45.19 |
| Std Dev | 3.368 |

Gaus mean : 45.7956

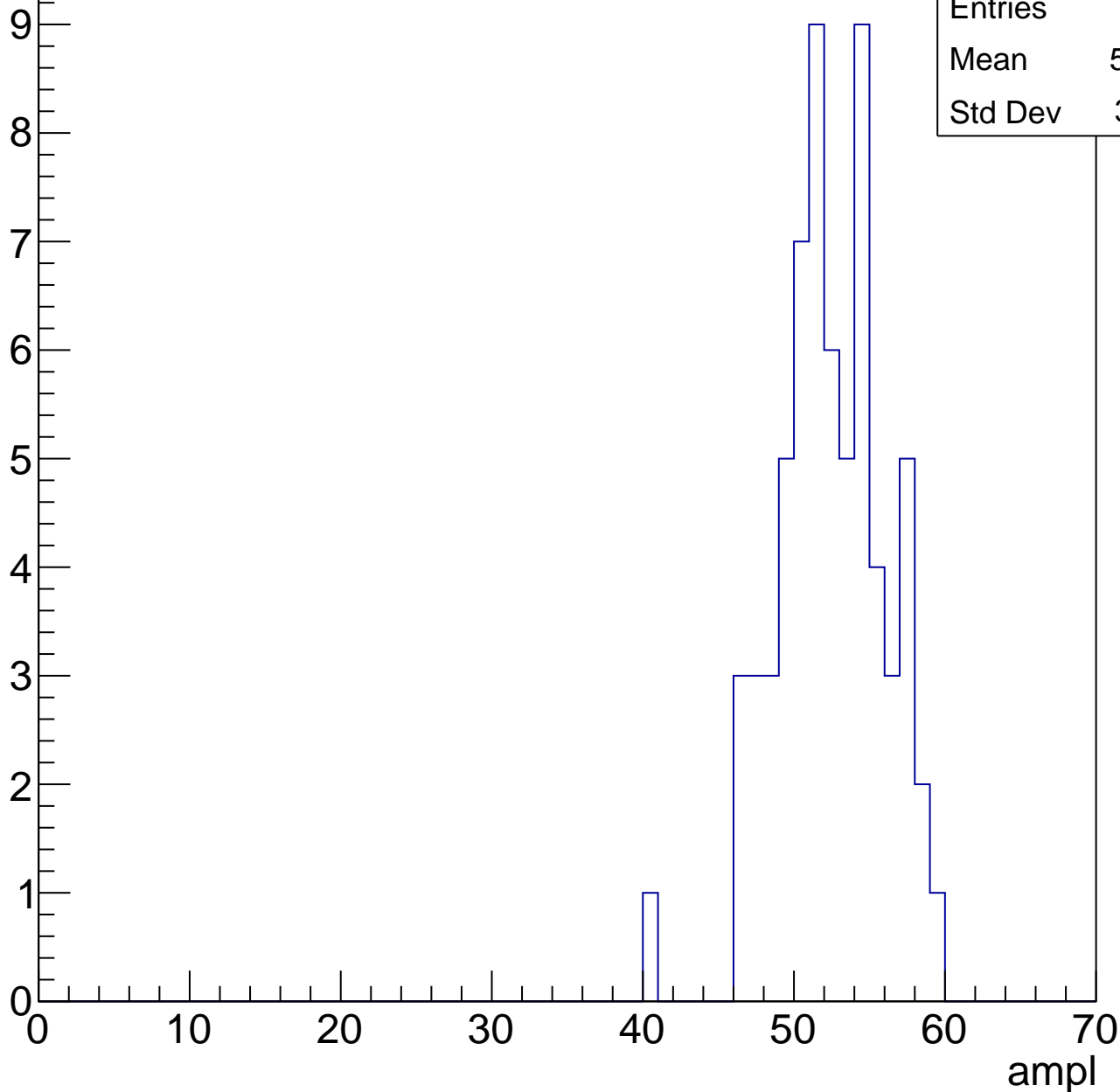
Gaus Width: 3.3210



B1L100S, U9-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.41 |
| Std Dev | 2.762 |

ampl

0

10

20

30

40

50

60

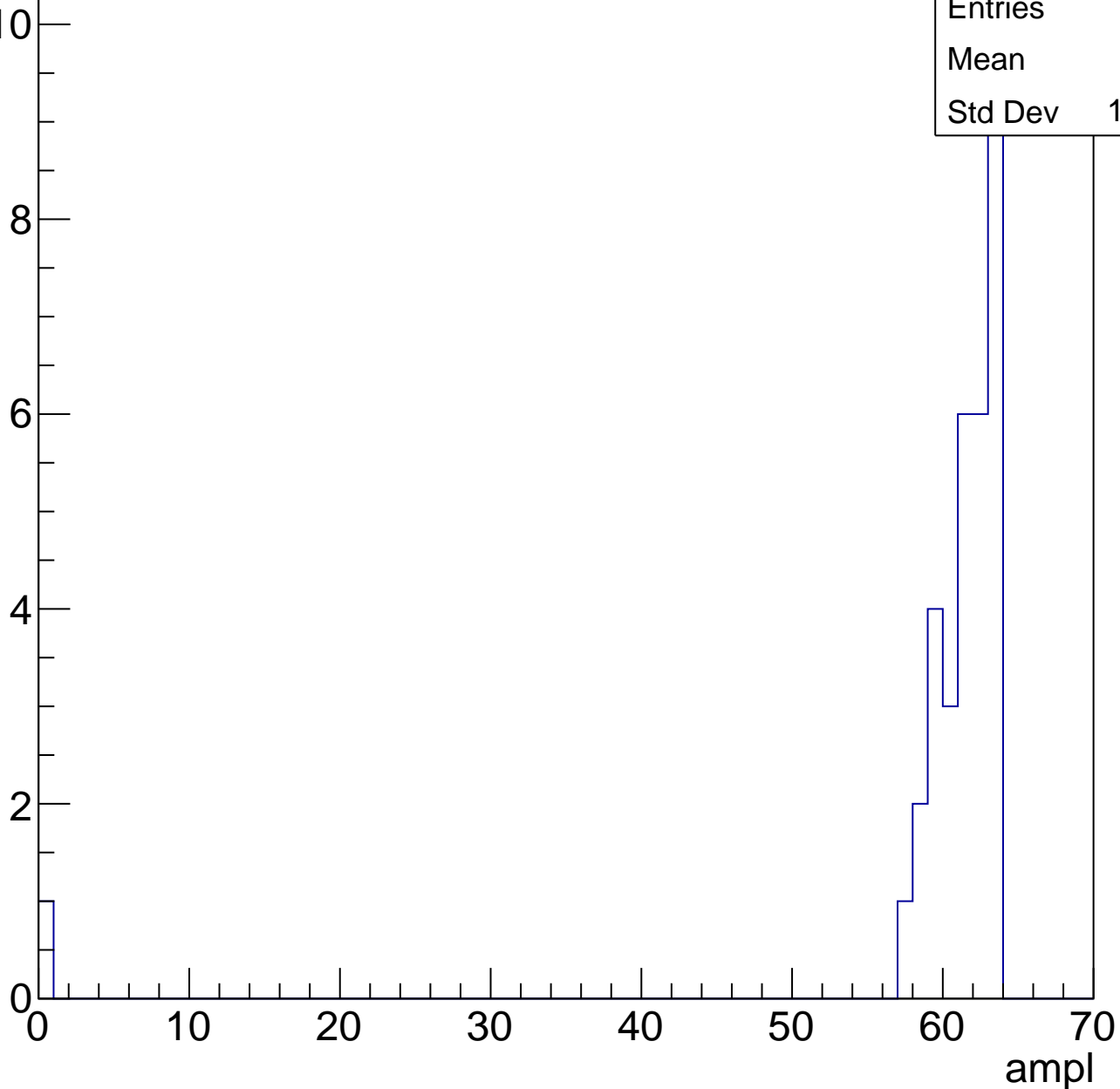
70

B1L100S, U9-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

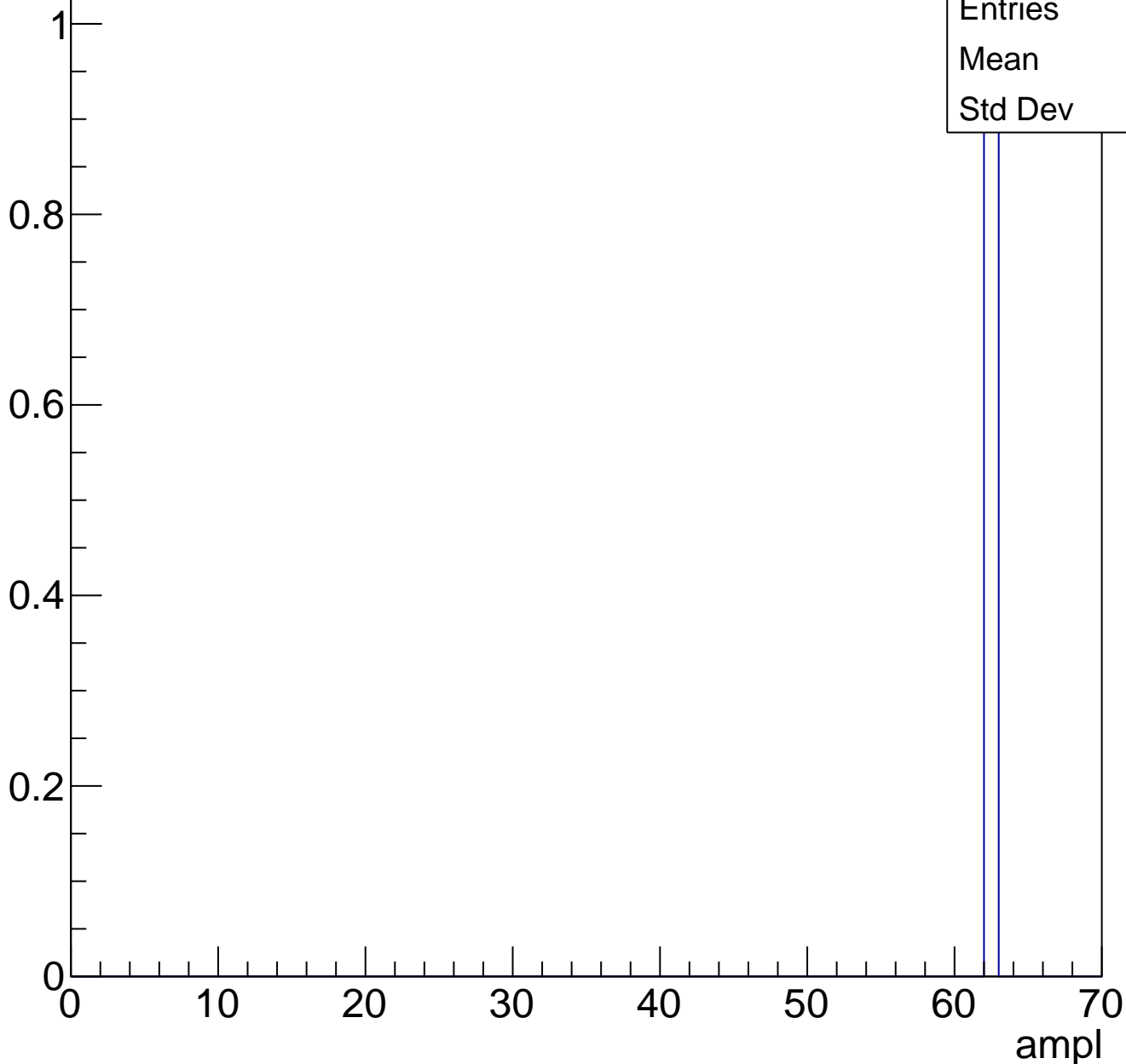
| | |
|---------|-------|
| Entries | 33 |
| Mean | 59.3 |
| Std Dev | 10.62 |



B1L100S, U9-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch80, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 87 |
| Mean | 29.71 |
| Std Dev | 3.42 |

Gaus mean : 30.2331

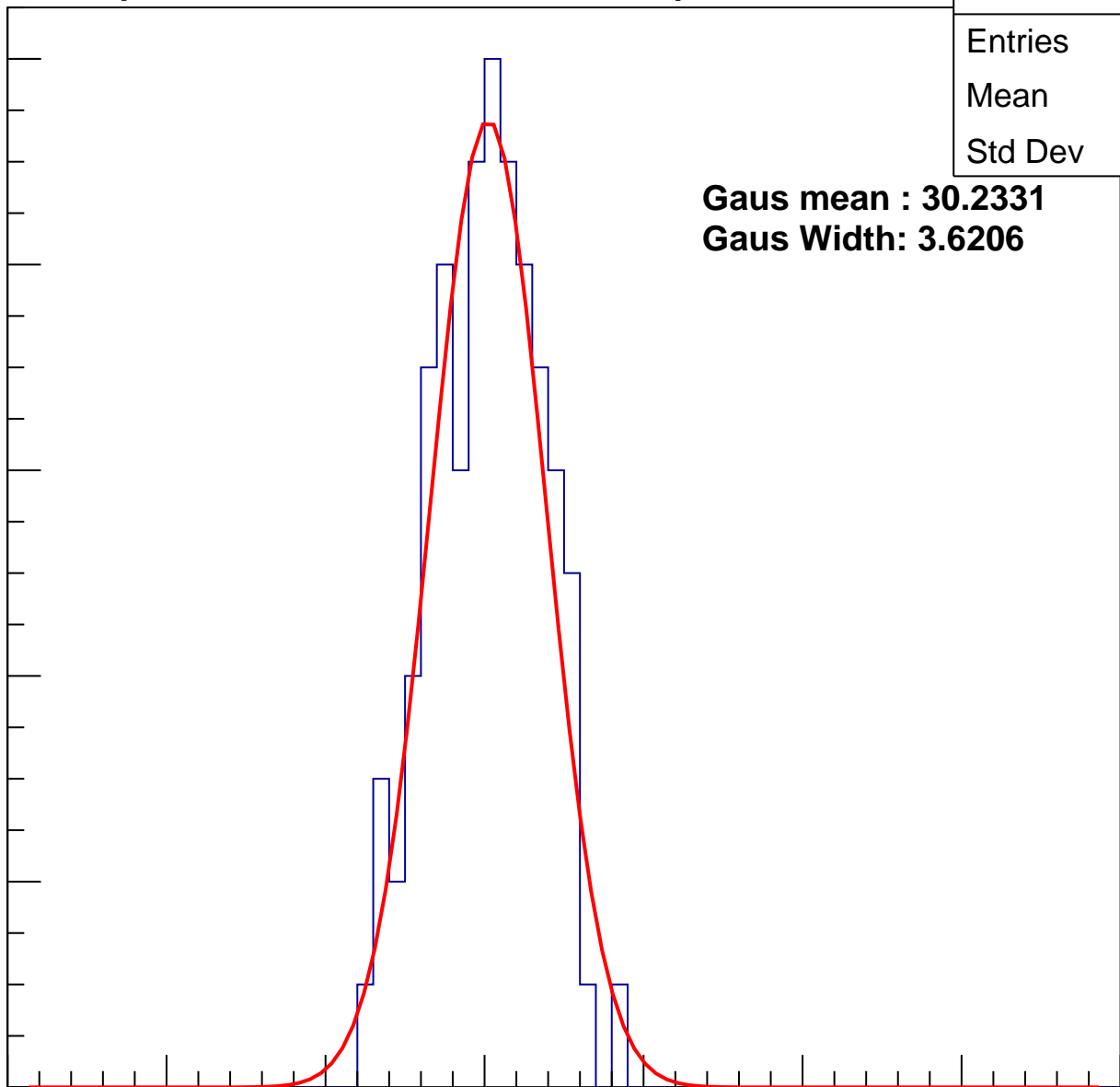
Gaus Width: 3.6206

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch80, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 61 |
| Mean | 37.44 |
| Std Dev | 2.725 |

Gaus mean : 37.3743

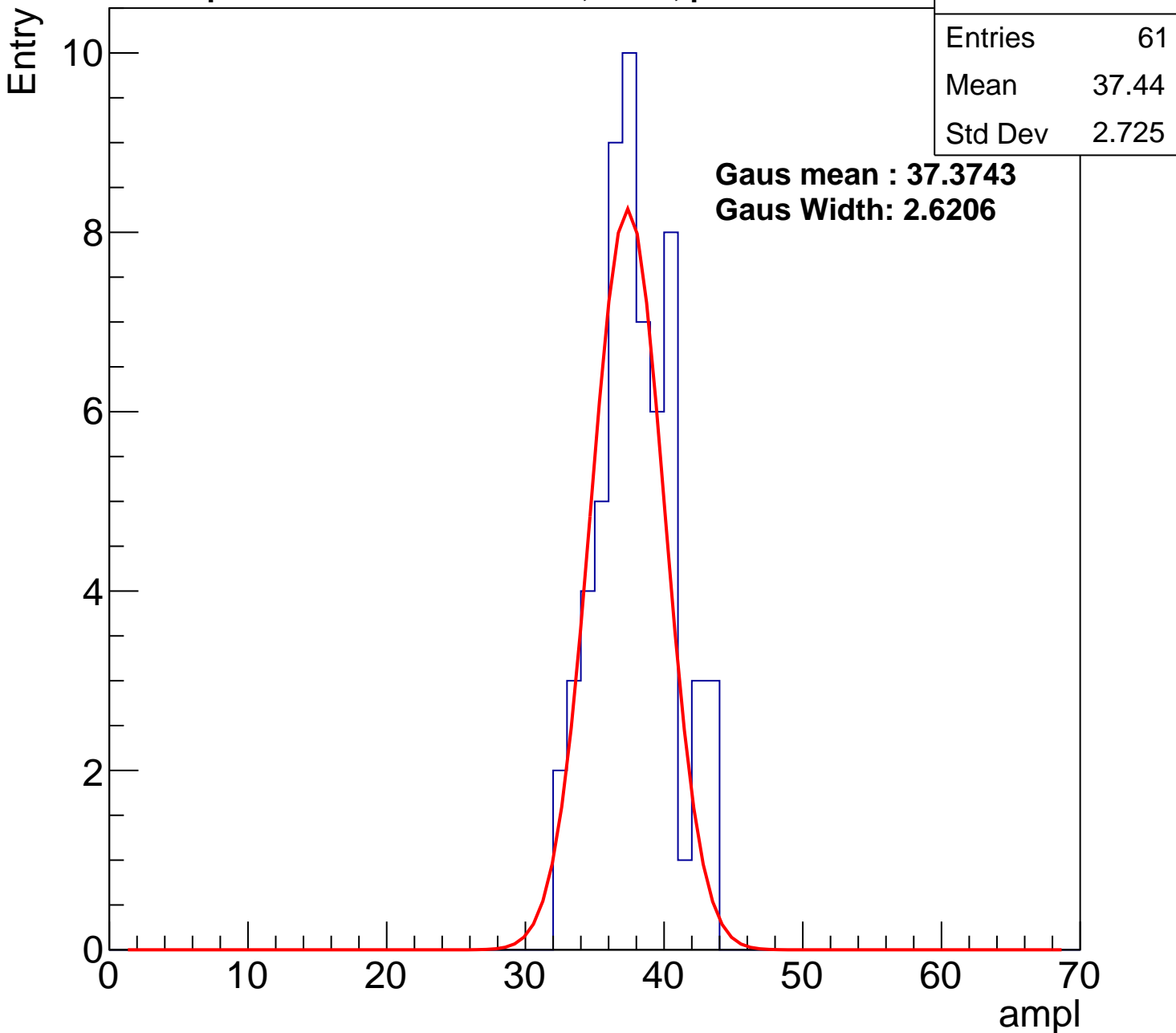
Gaus Width: 2.6206

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch80, adc2

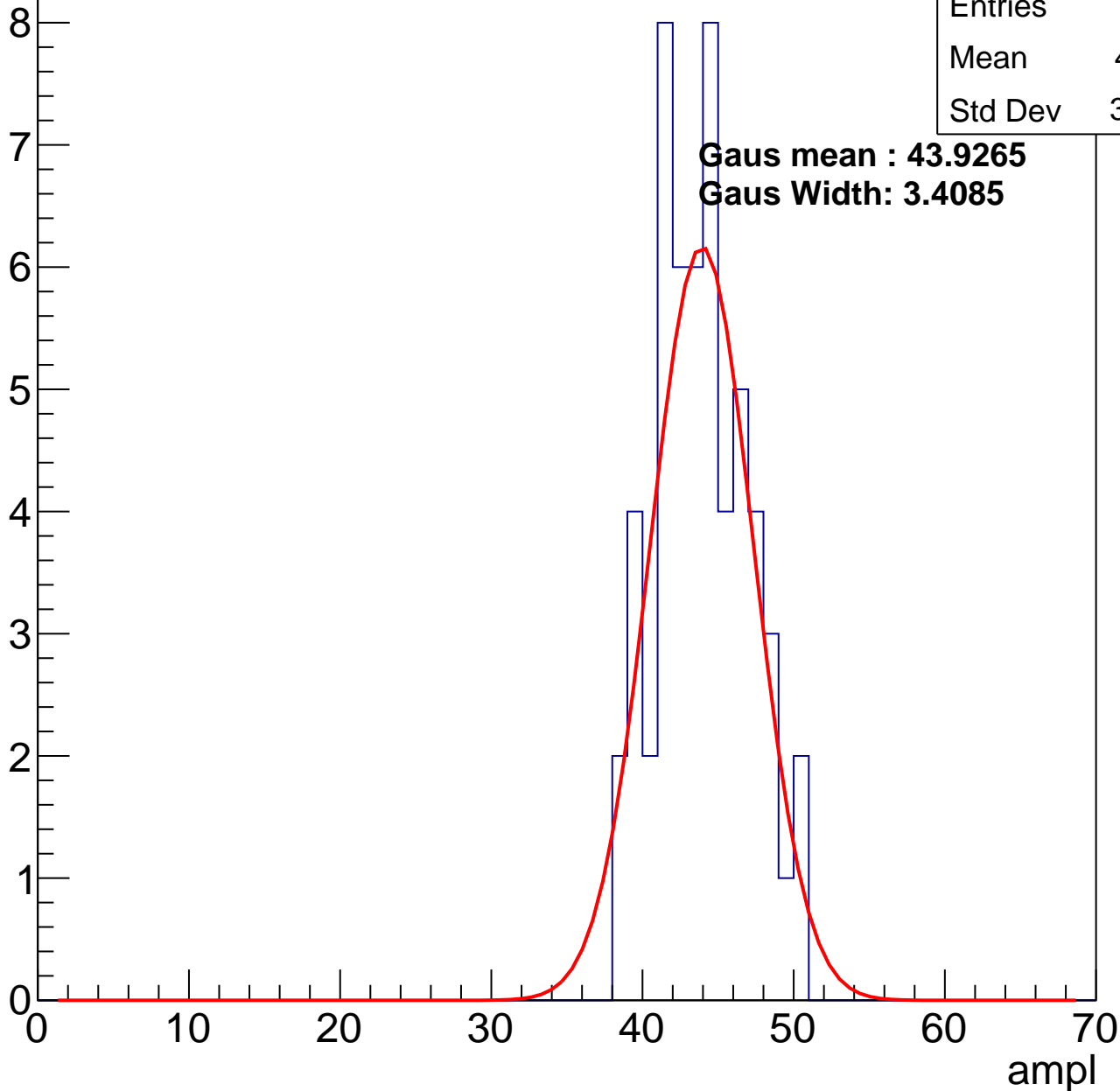
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.51 |
| Std Dev | 3.008 |

Gaus mean : 43.9265

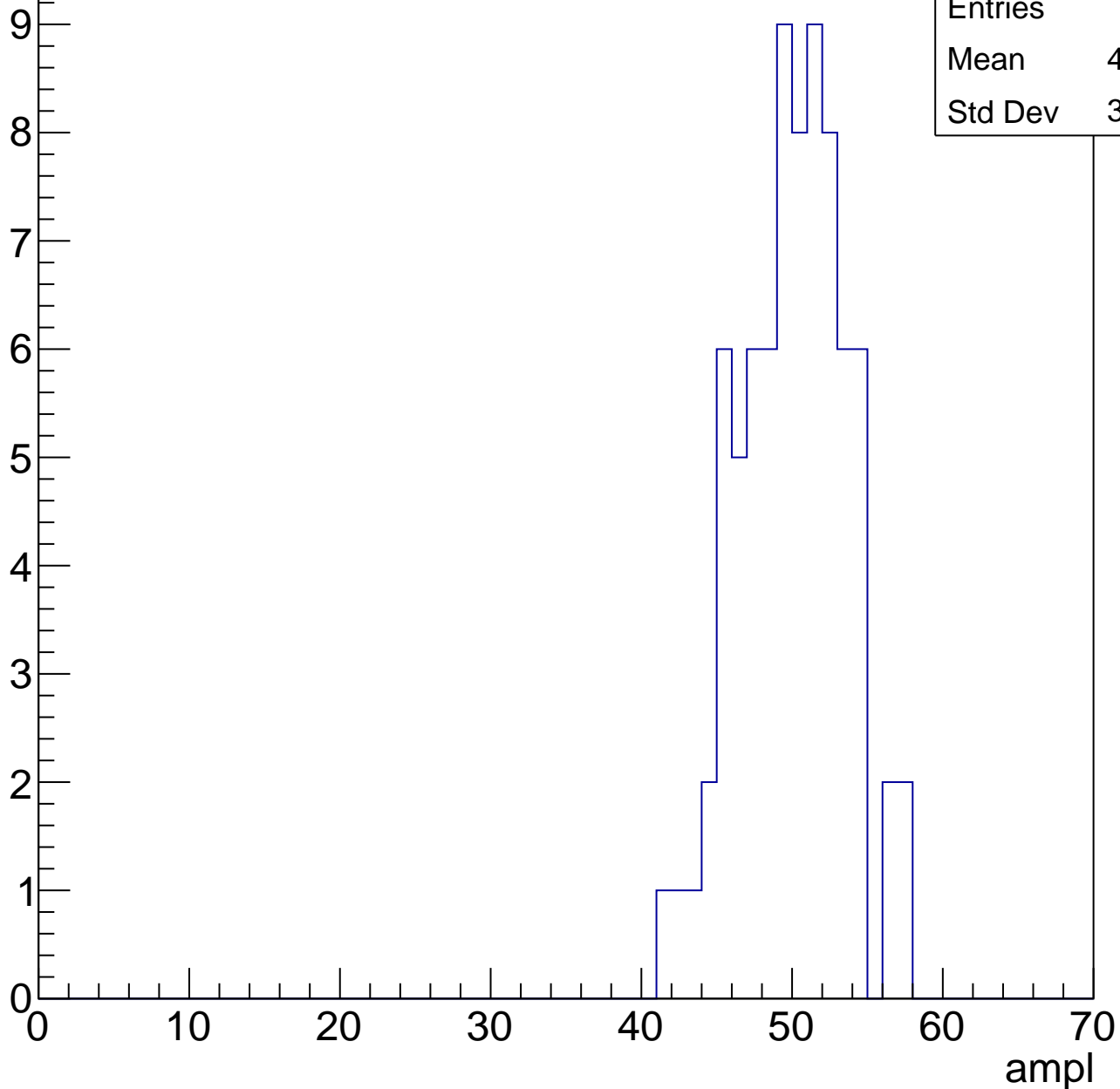
Gaus Width: 3.4085



B1L100S, U9-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

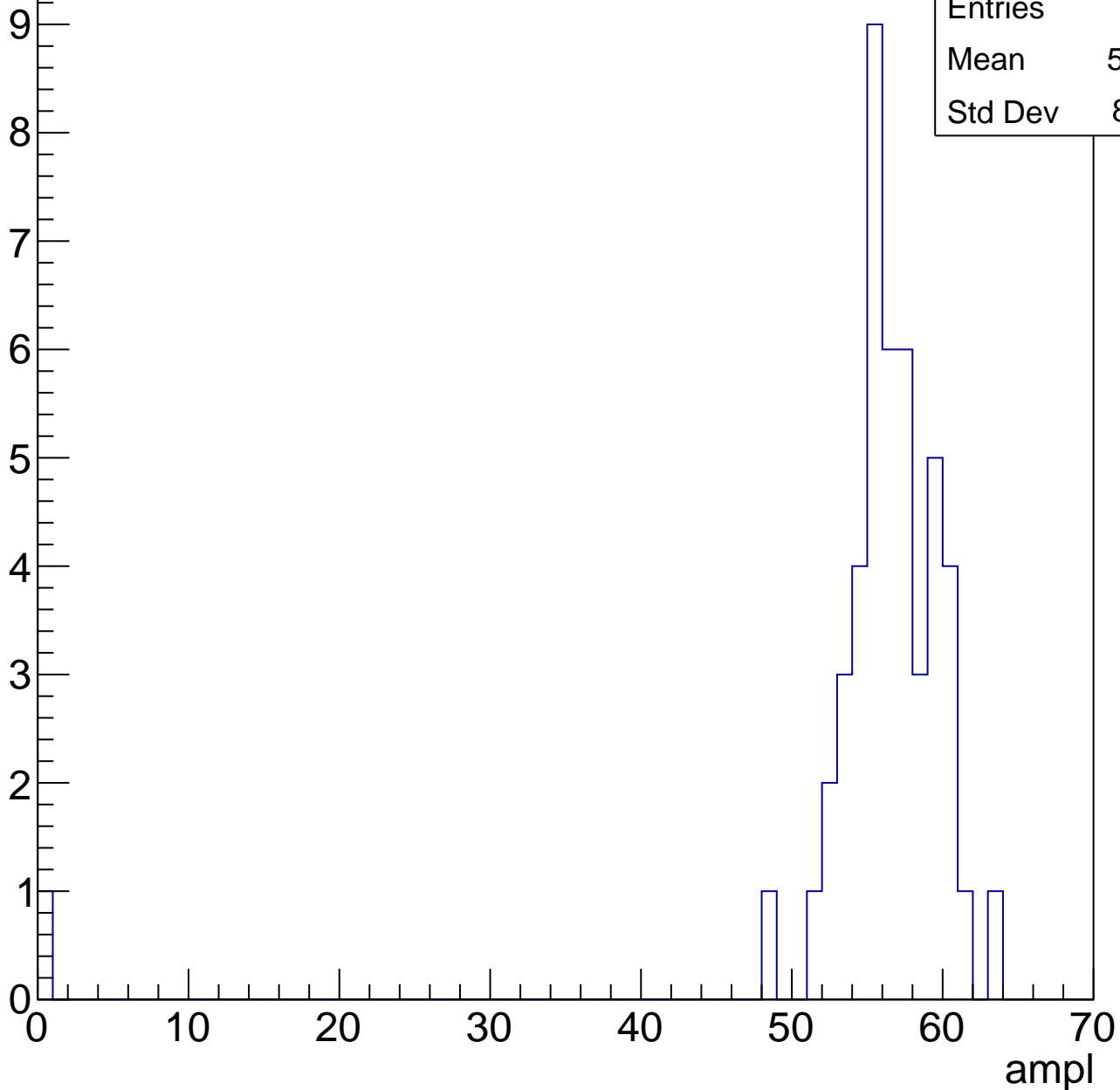


B1L100S, U9-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 54.98 |
| Std Dev | 8.581 |



B1L100S, U9-ch80, adc5

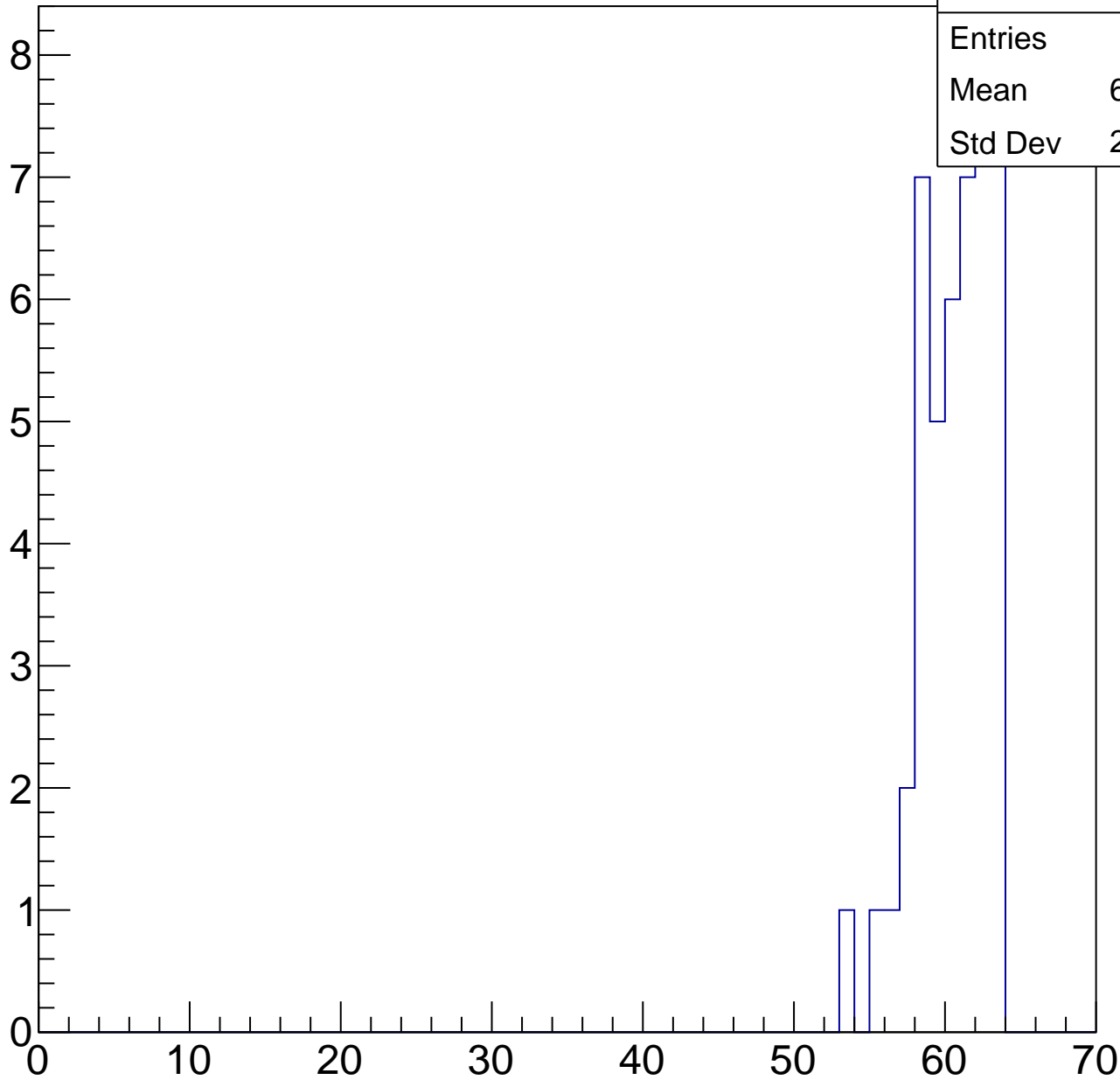
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 60.13 |
| Std Dev | 2.337 |

ampl



B1L100S, U9-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

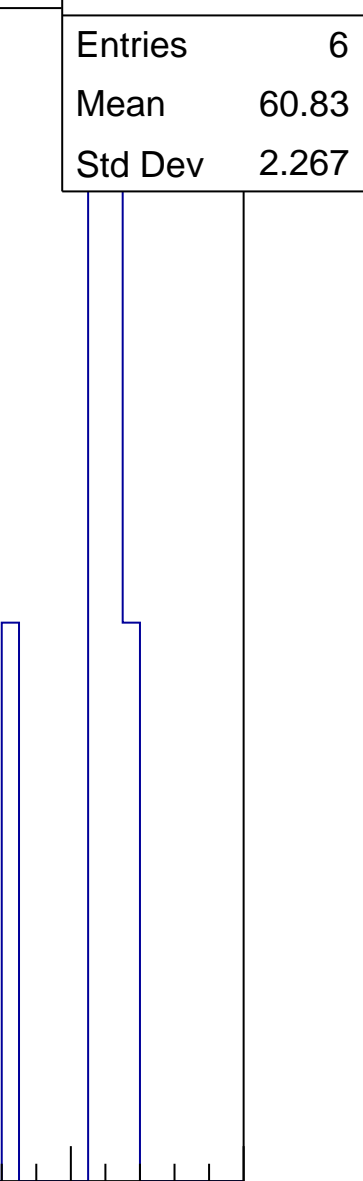
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 60.83 |
| Std Dev | 2.267 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch81, adc0

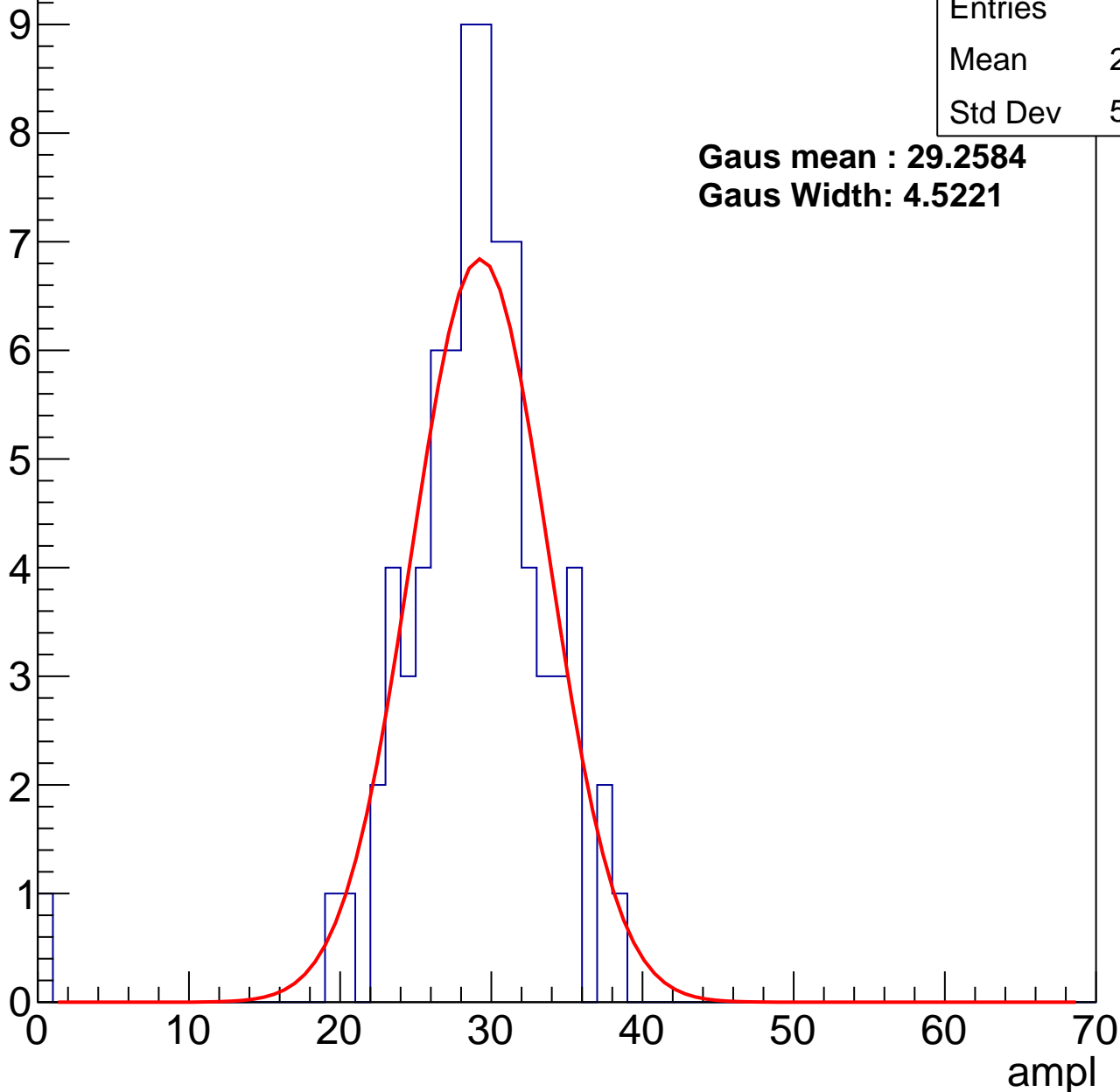
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 28.39 |
| Std Dev | 5.107 |

Gaus mean : 29.2584

Gaus Width: 4.5221



B1L100S, U9-ch81, adc1

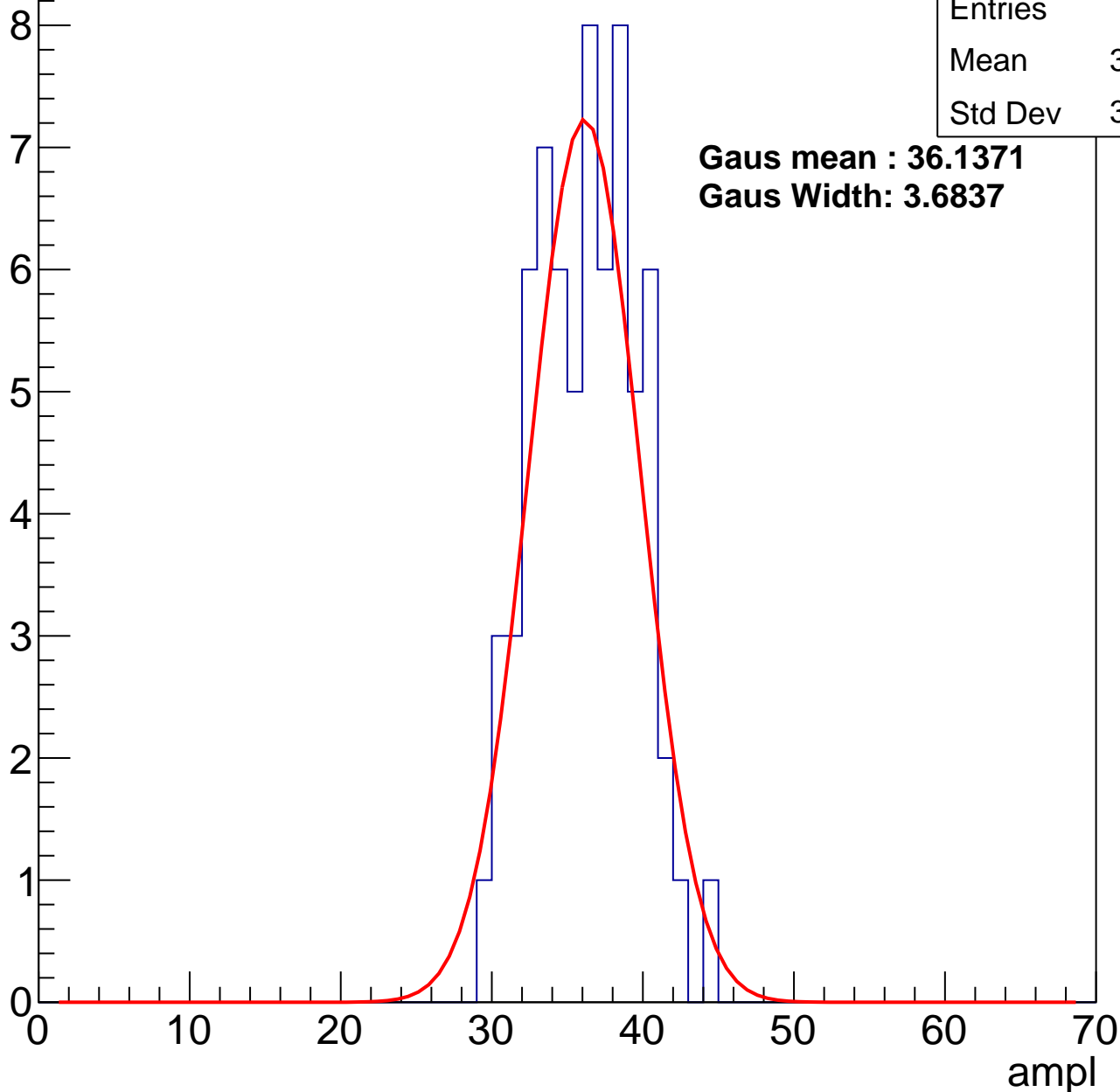
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 35.75 |
| Std Dev | 3.309 |

Gaus mean : 36.1371

Gaus Width: 3.6837



B1L100S, U9-ch81, adc2

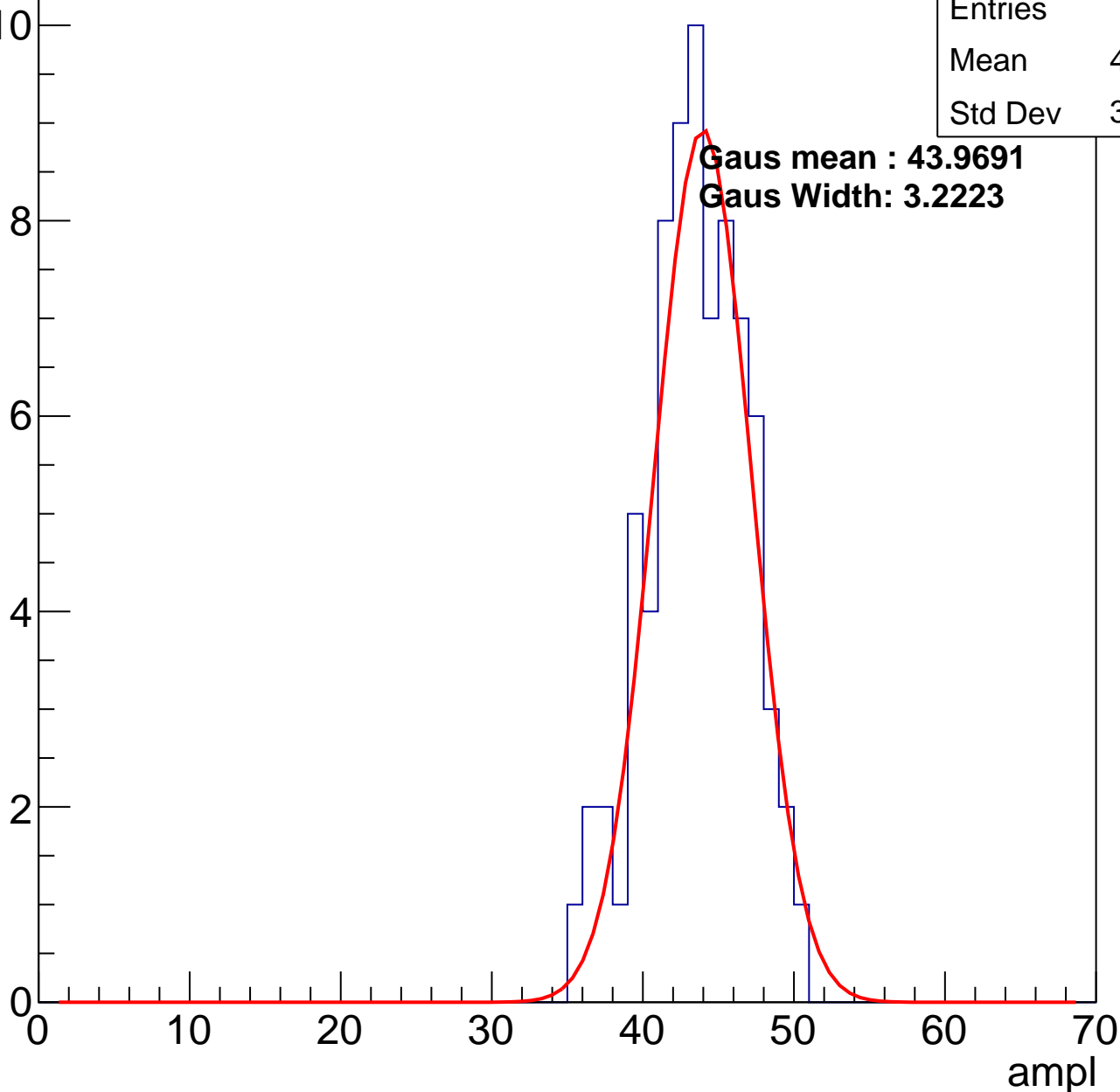
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 43.08 |
| Std Dev | 3.264 |

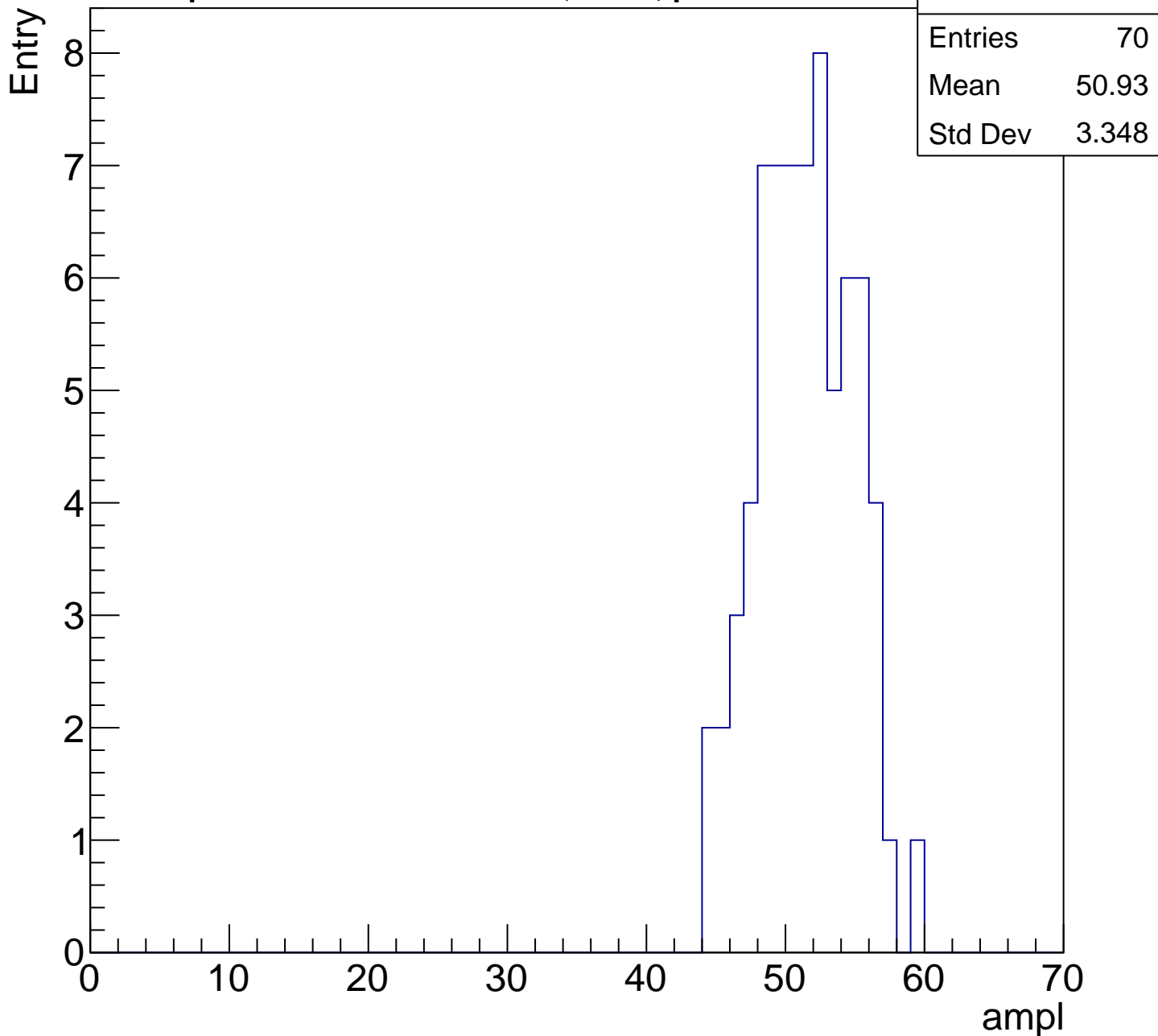
Gaus mean : 43.9691

Gaus Width: 3.2223



B1L100S, U9-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

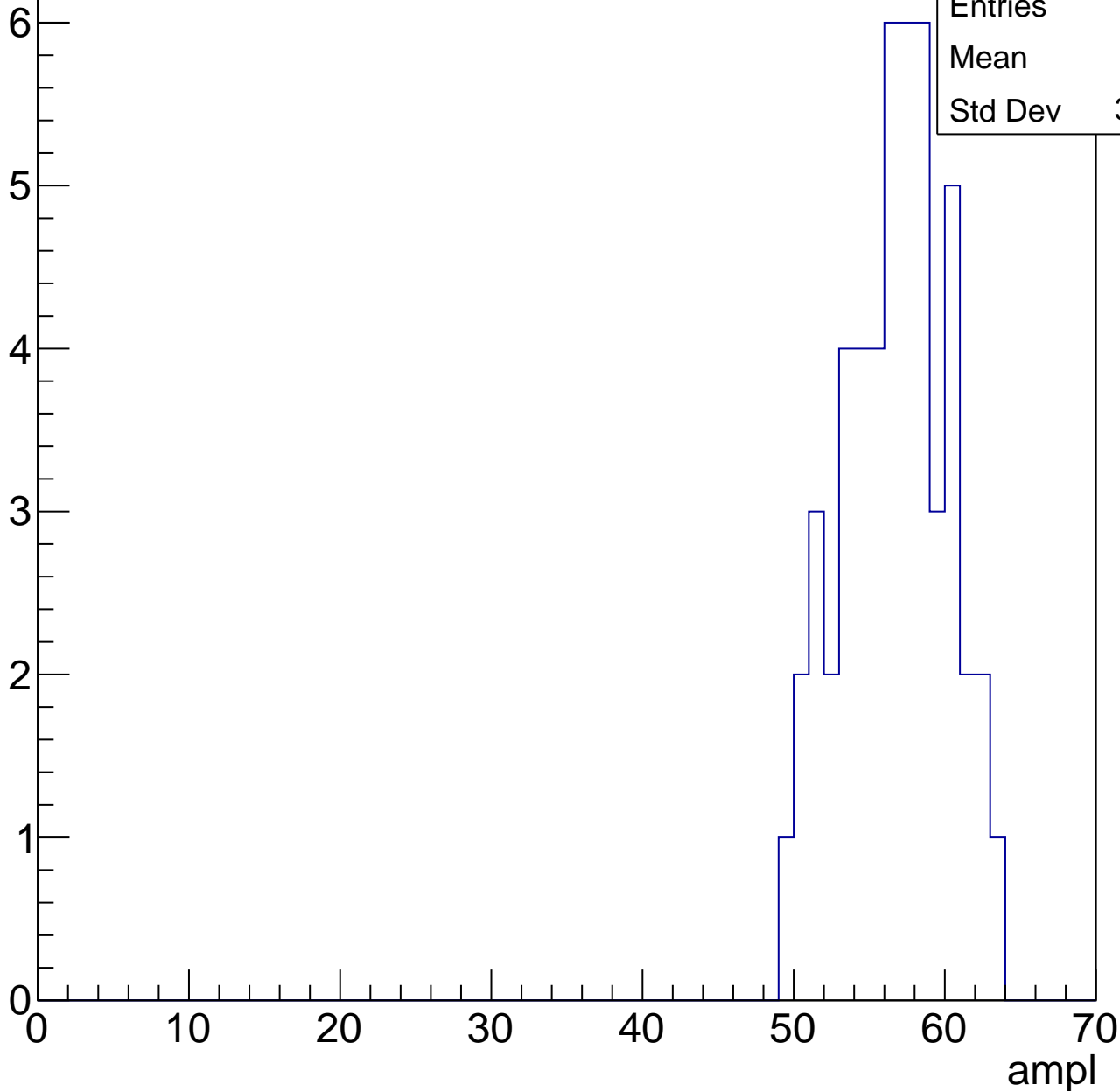


B1L100S, U9-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.2 |
| Std Dev | 3.401 |

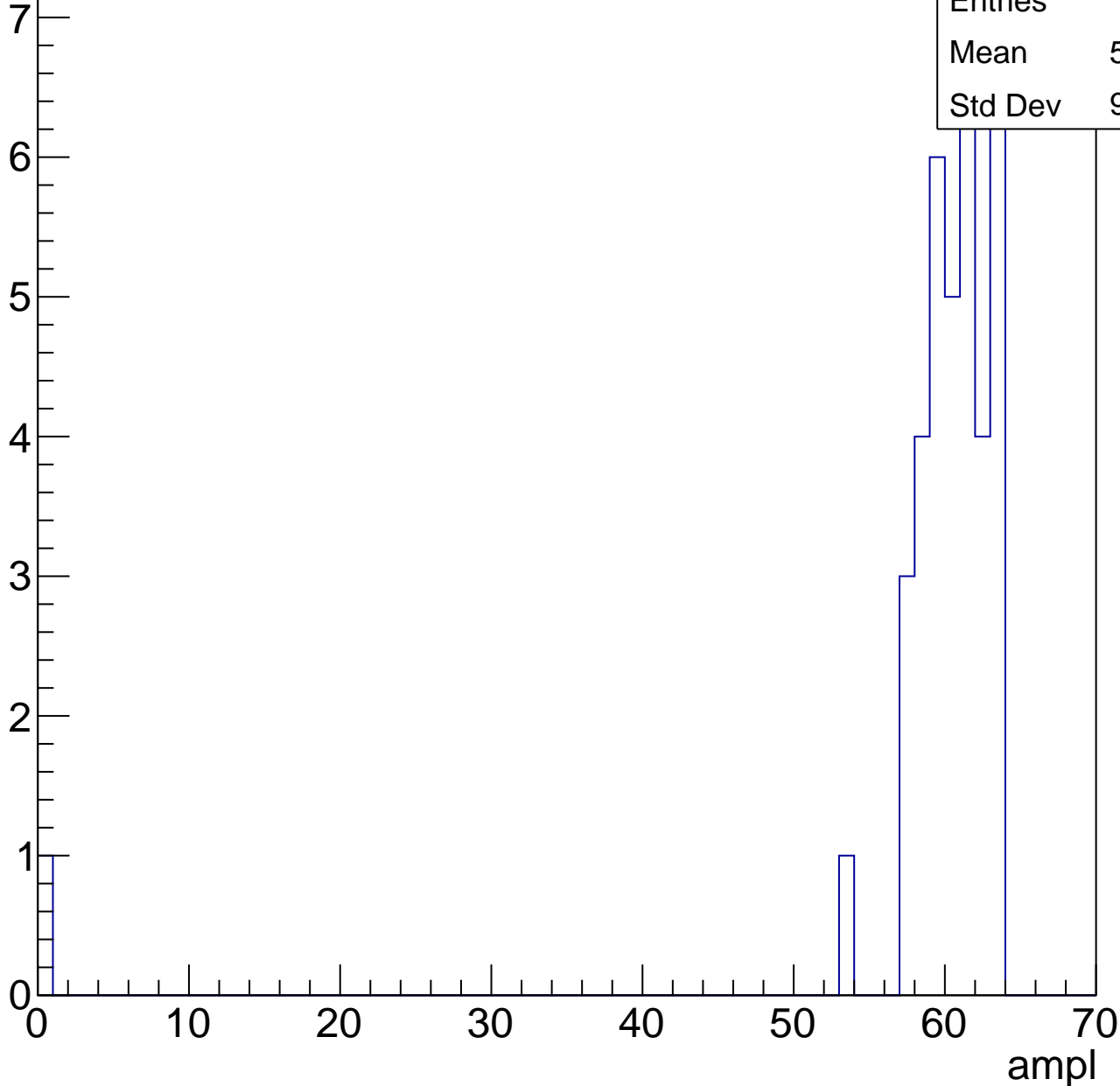


B1L100S, U9-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

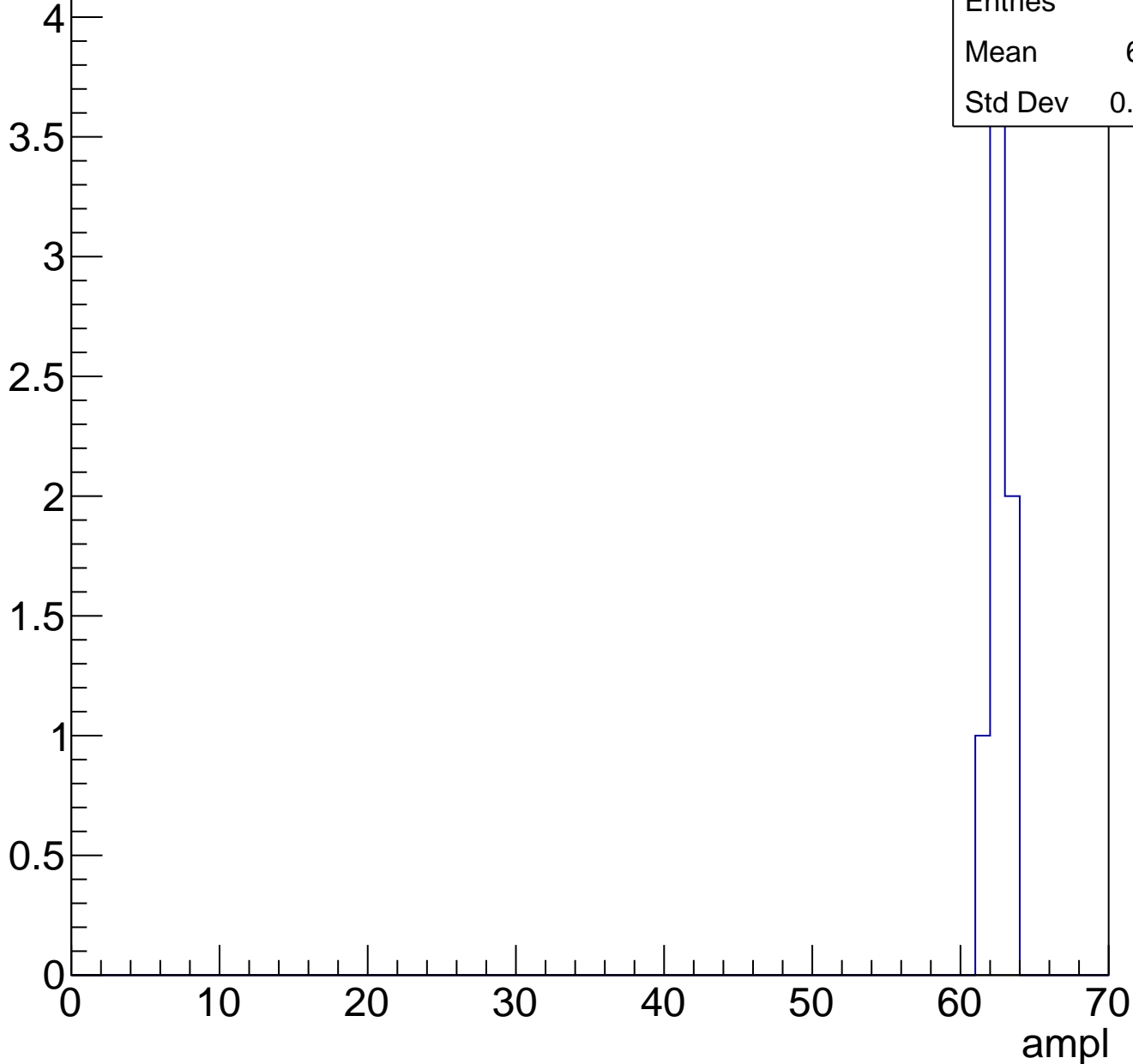
| | |
|---------|-------|
| Entries | 38 |
| Mean | 58.58 |
| Std Dev | 9.877 |



B1L100S, U9-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch82, adc0

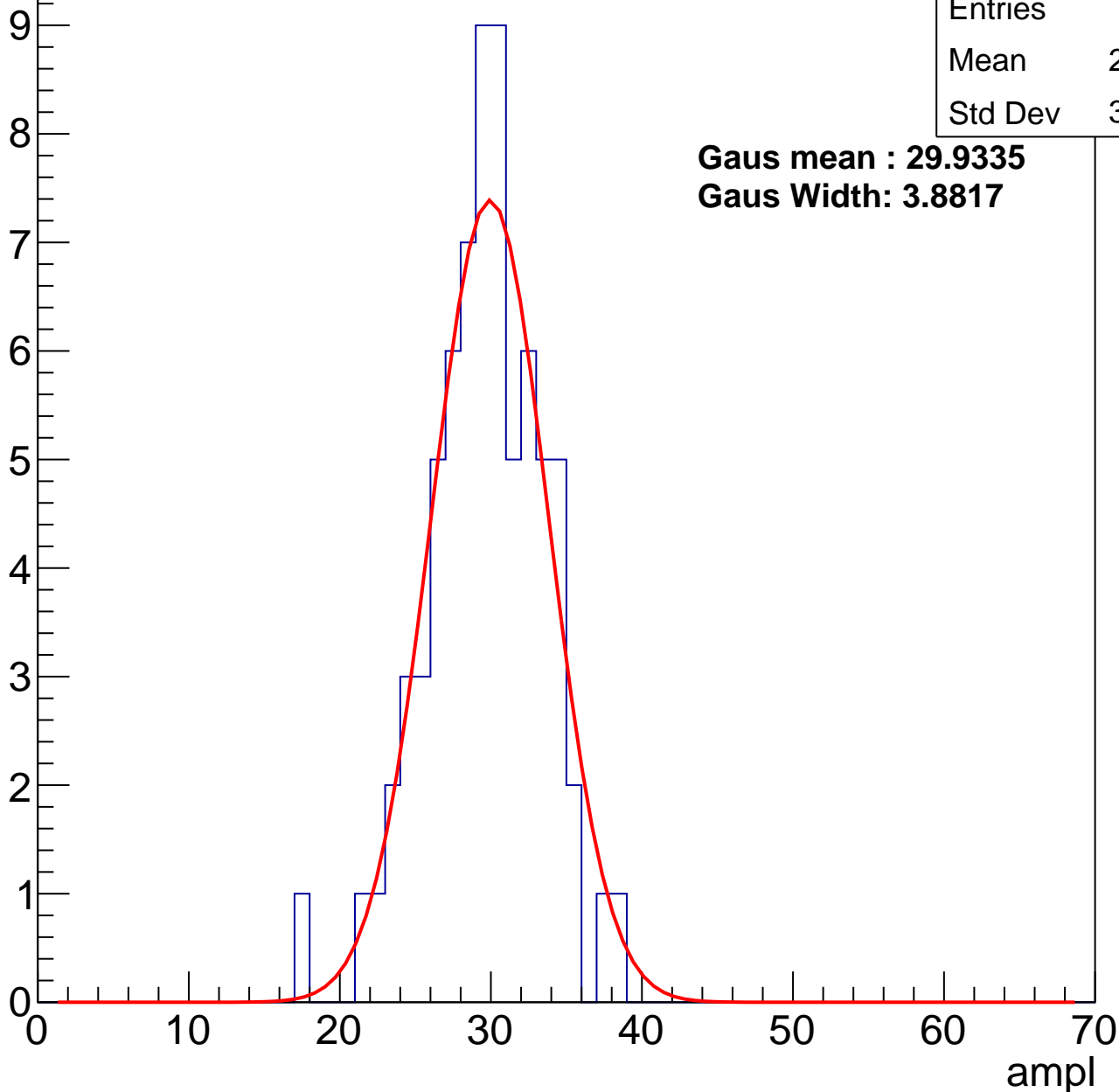
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 29.15 |
| Std Dev | 3.785 |

Gaus mean : 29.9335

Gaus Width: 3.8817



B1L100S, U9-ch82, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.21 |
| Std Dev | 3.505 |

Gaus mean : 36.0660

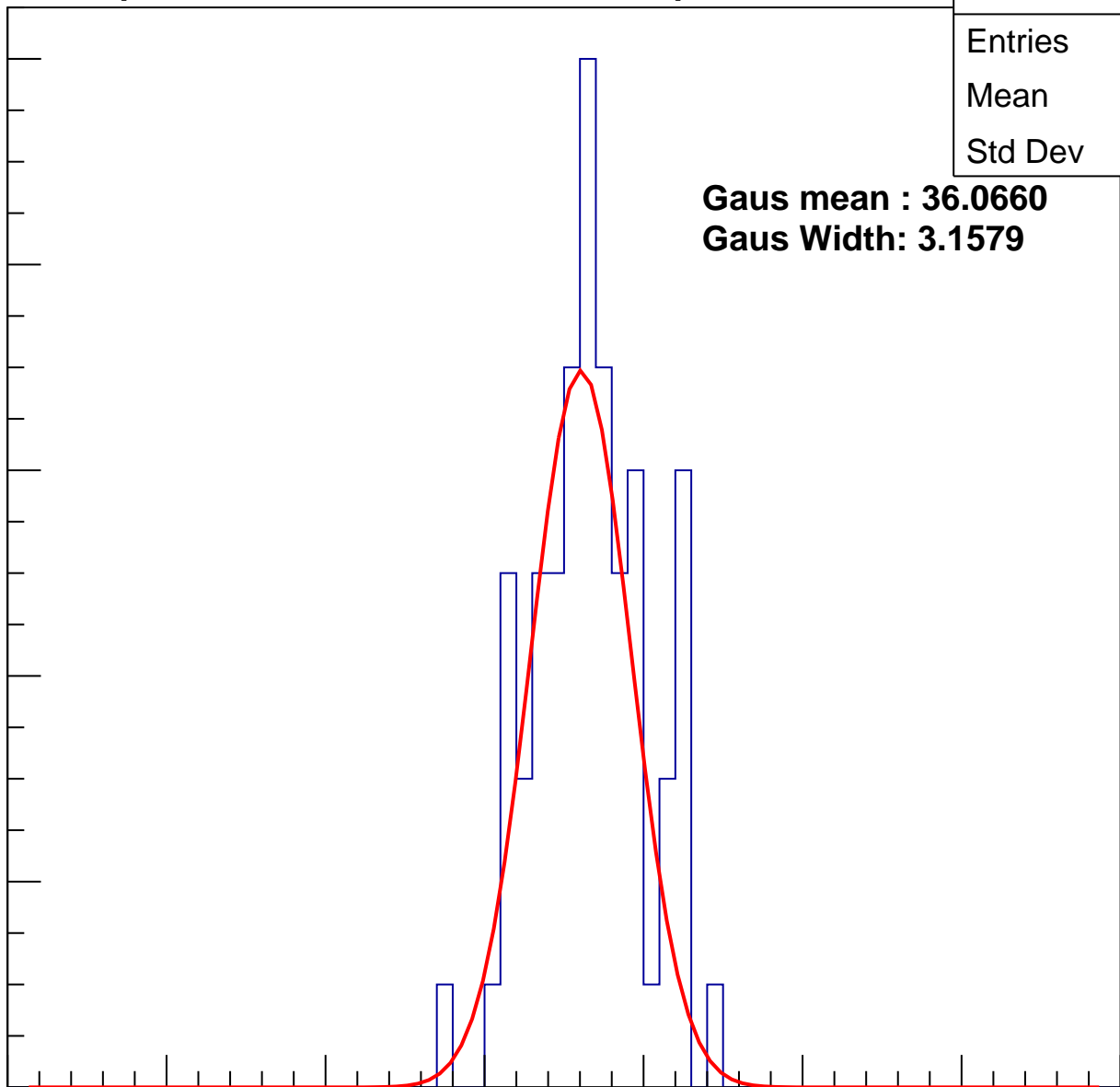
Gaus Width: 3.1579

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch82, adc2

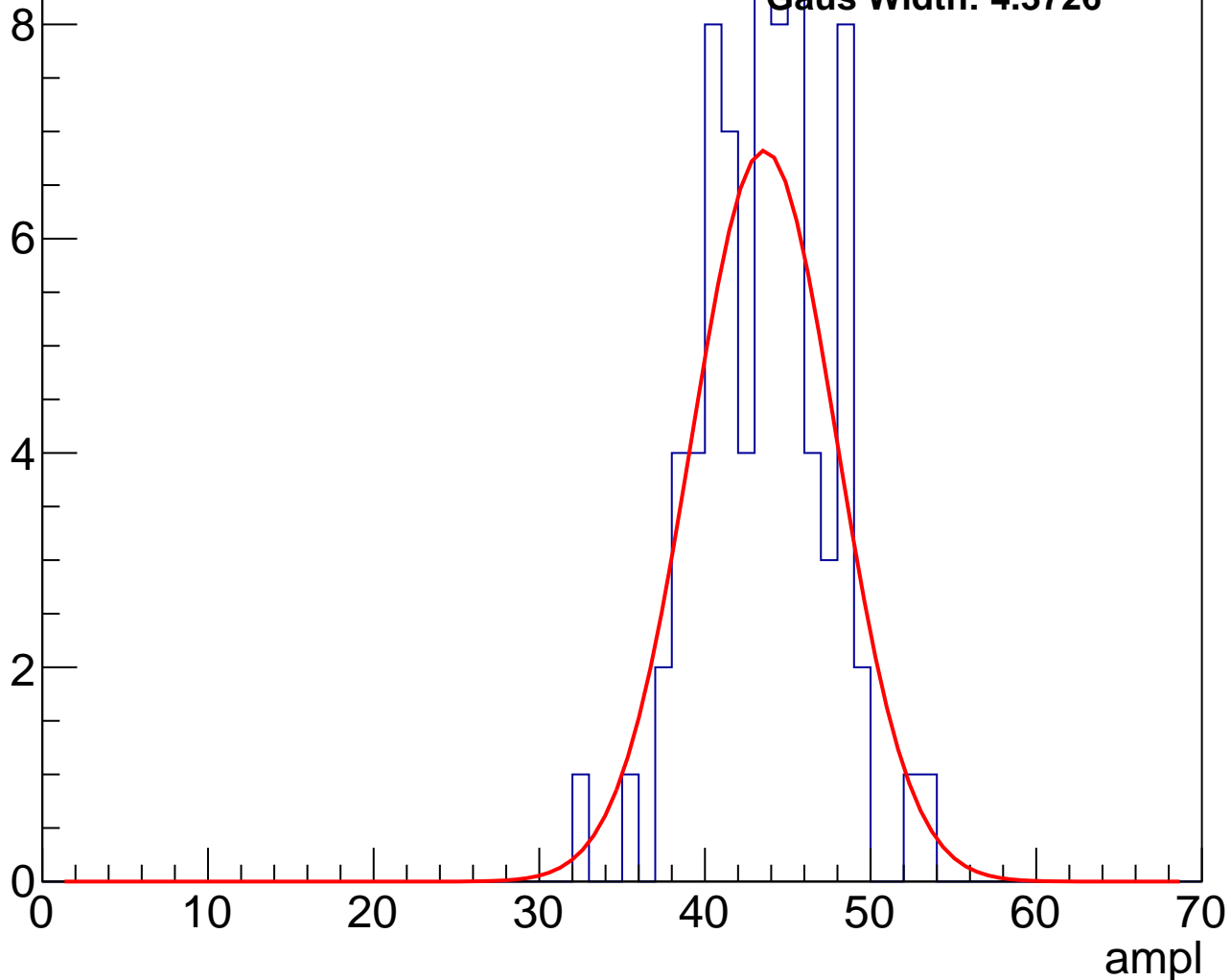
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 43.18 |
| Std Dev | 3.768 |

Gaus mean : 43.5726

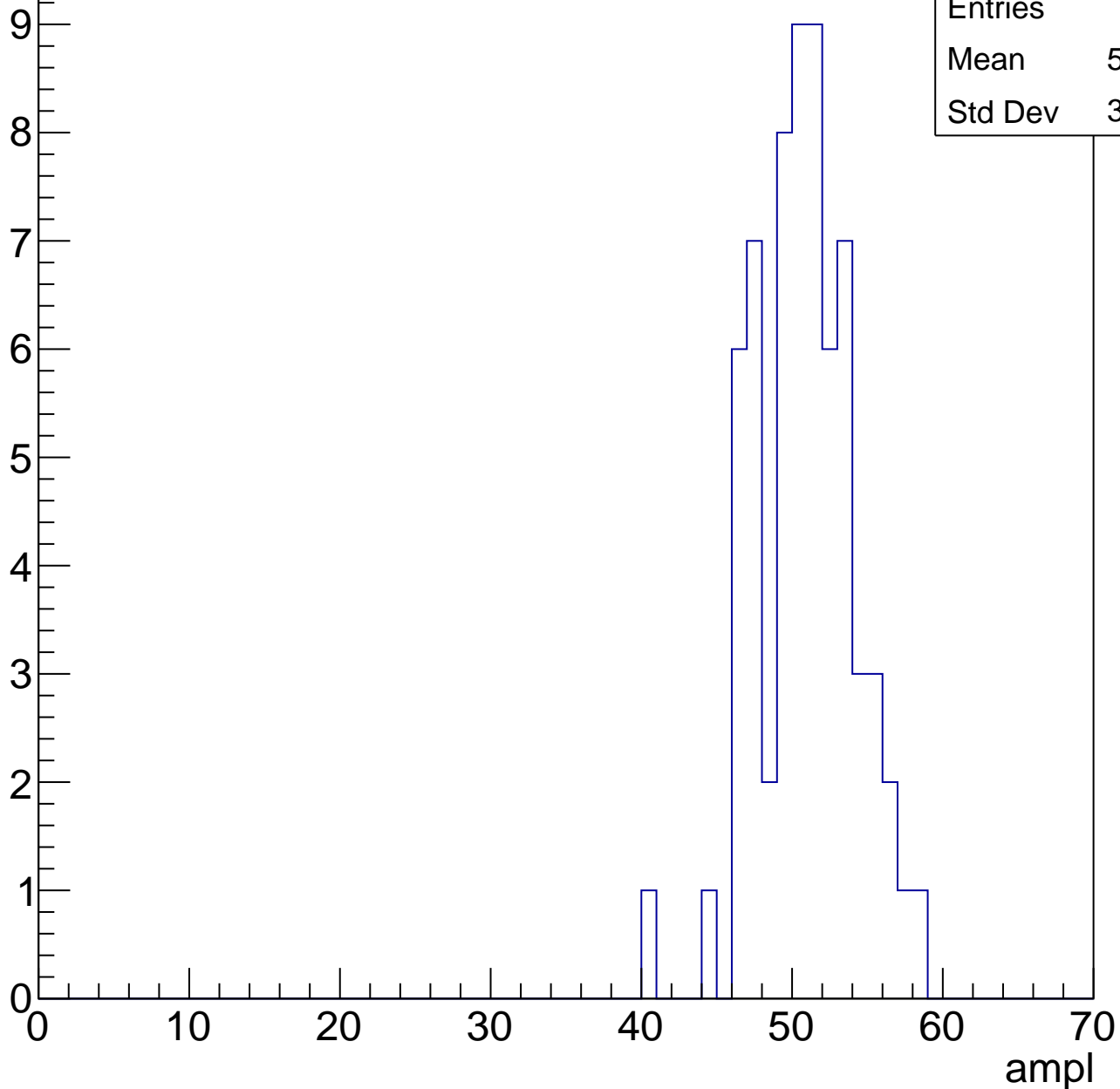
Gaus Width: 4.3726



B1L100S, U9-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

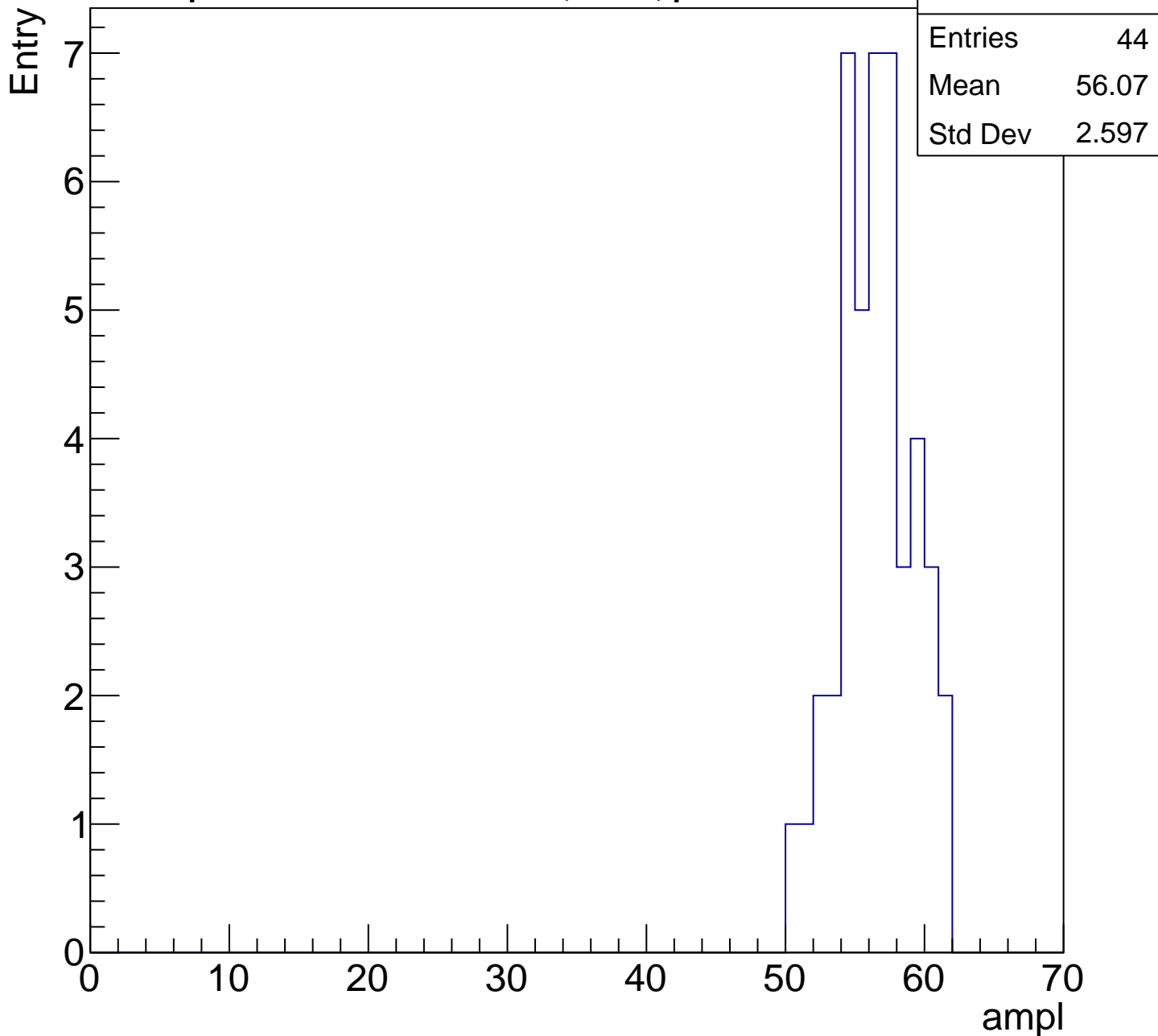
Entry



| | |
|---------|-------|
| Entries | 66 |
| Mean | 50.35 |
| Std Dev | 3.273 |

B1L100S, U9-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

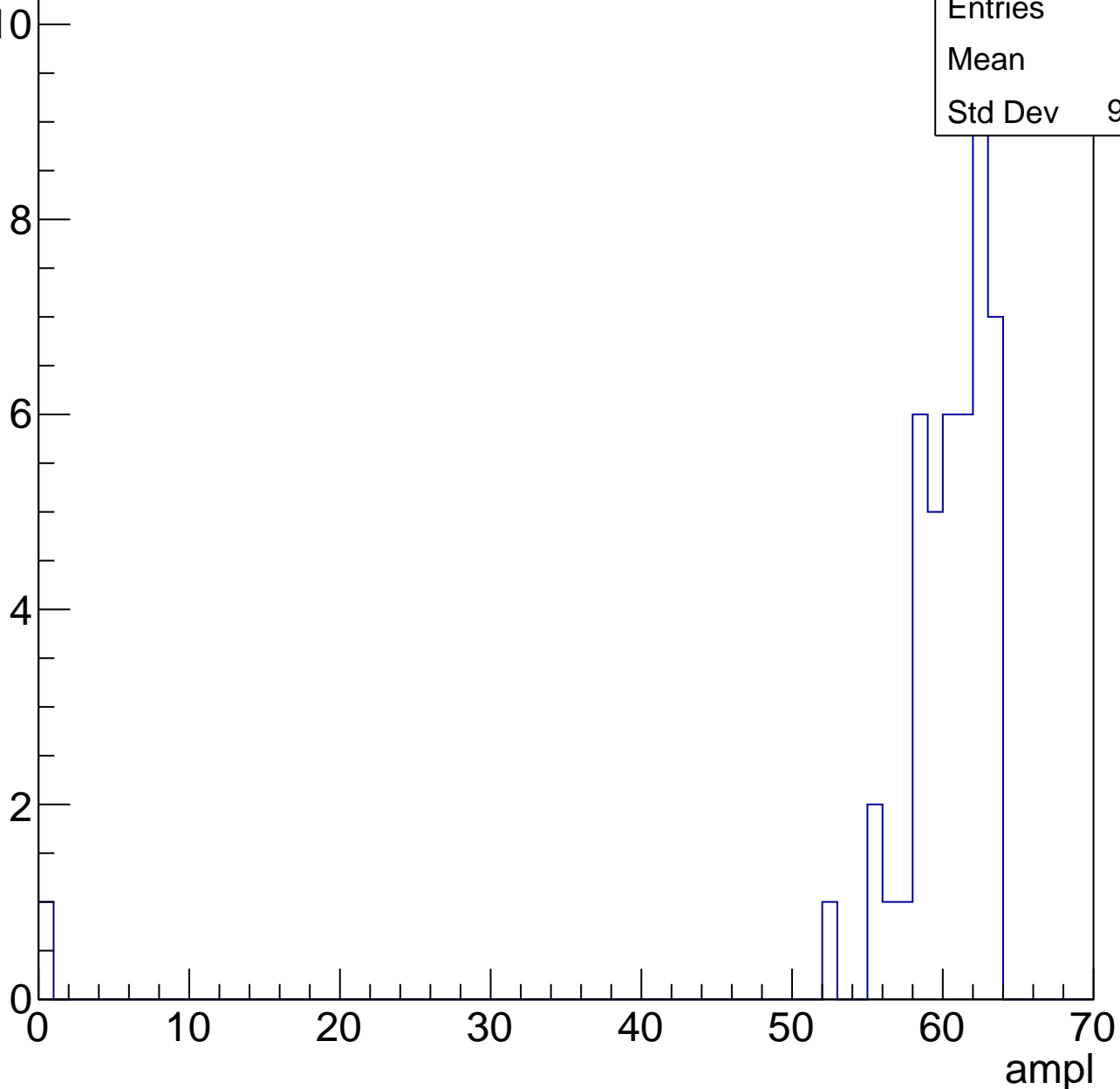


B1L100S, U9-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.8 |
| Std Dev | 9.102 |



B1L100S, U9-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

8

Mean

60.88

Std Dev

2.027

ampl

0 10 20 30 40 50 60 70

3

2.5

2

1.5

1

0.5

0

Entries

8

Mean

60.88

Std Dev

2.027

ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch83, adc0

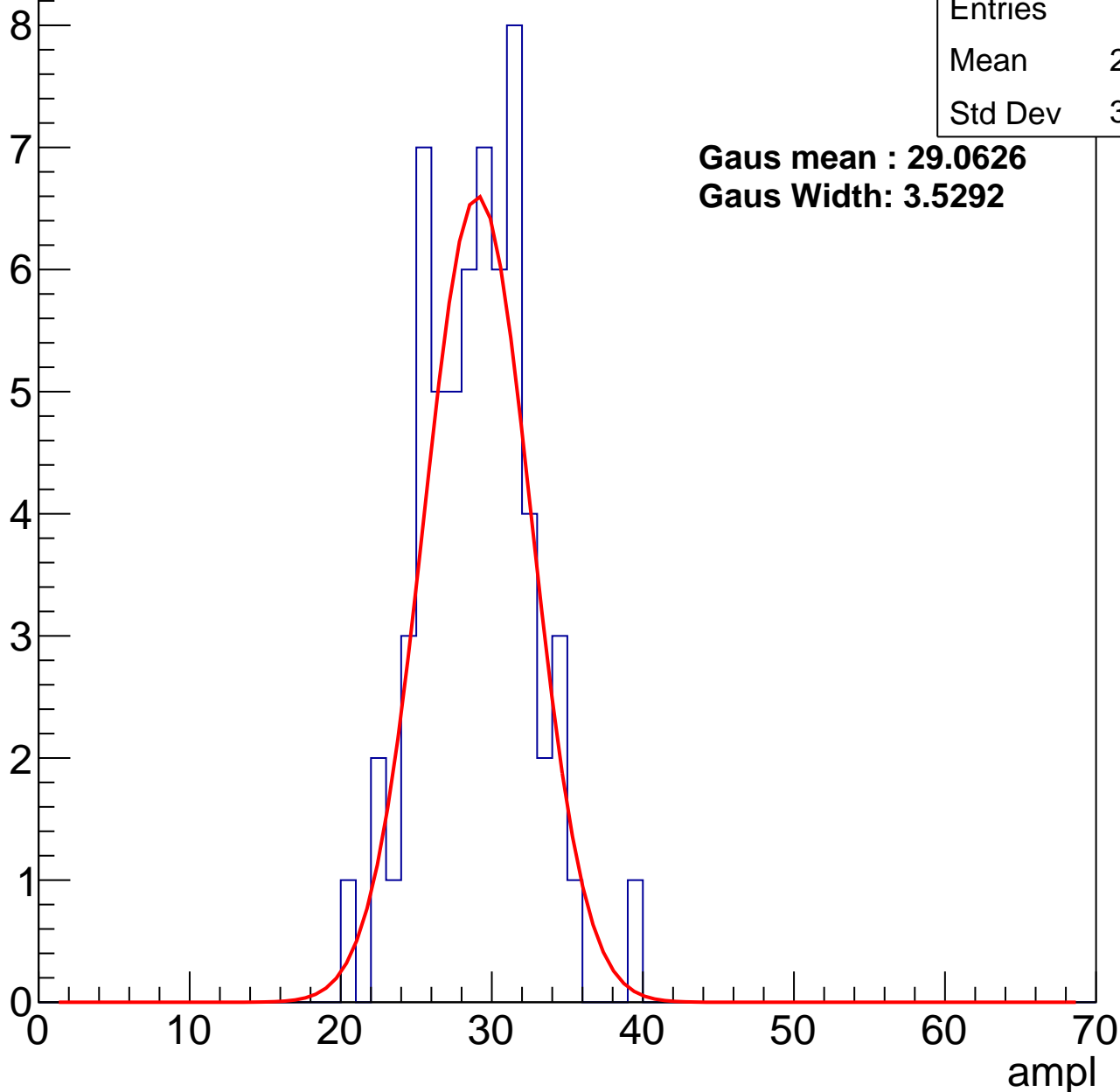
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.52 |
| Std Dev | 3.537 |

Gaus mean : 29.0626

Gaus Width: 3.5292



B1L100S, U9-ch83, adc1

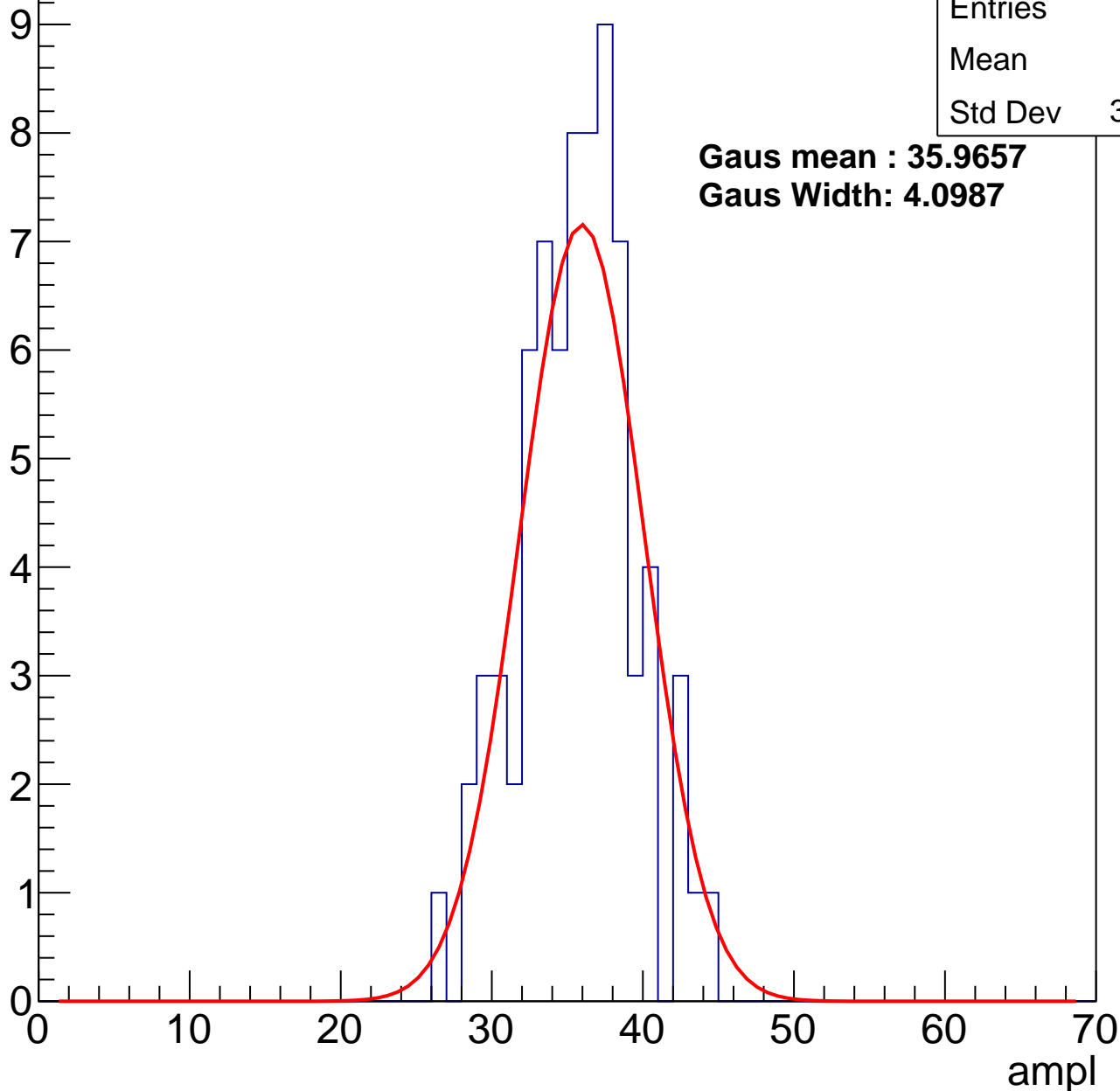
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.2 |
| Std Dev | 3.723 |

Gaus mean : 35.9657

Gaus Width: 4.0987



B1L100S, U9-ch83, adc2

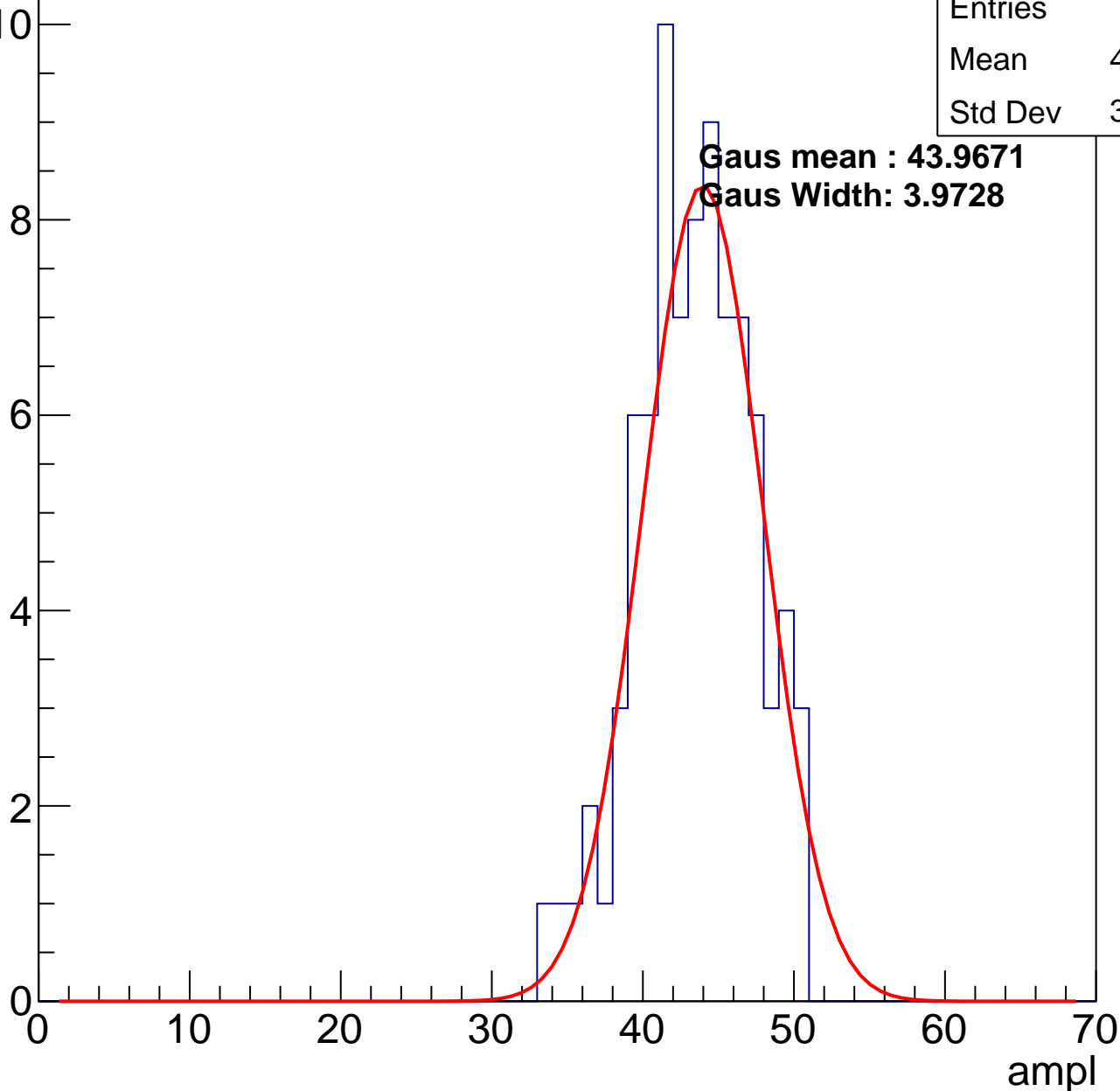
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 42.96 |
| Std Dev | 3.777 |

Gaus mean : 43.9671

Gaus Width: 3.9728

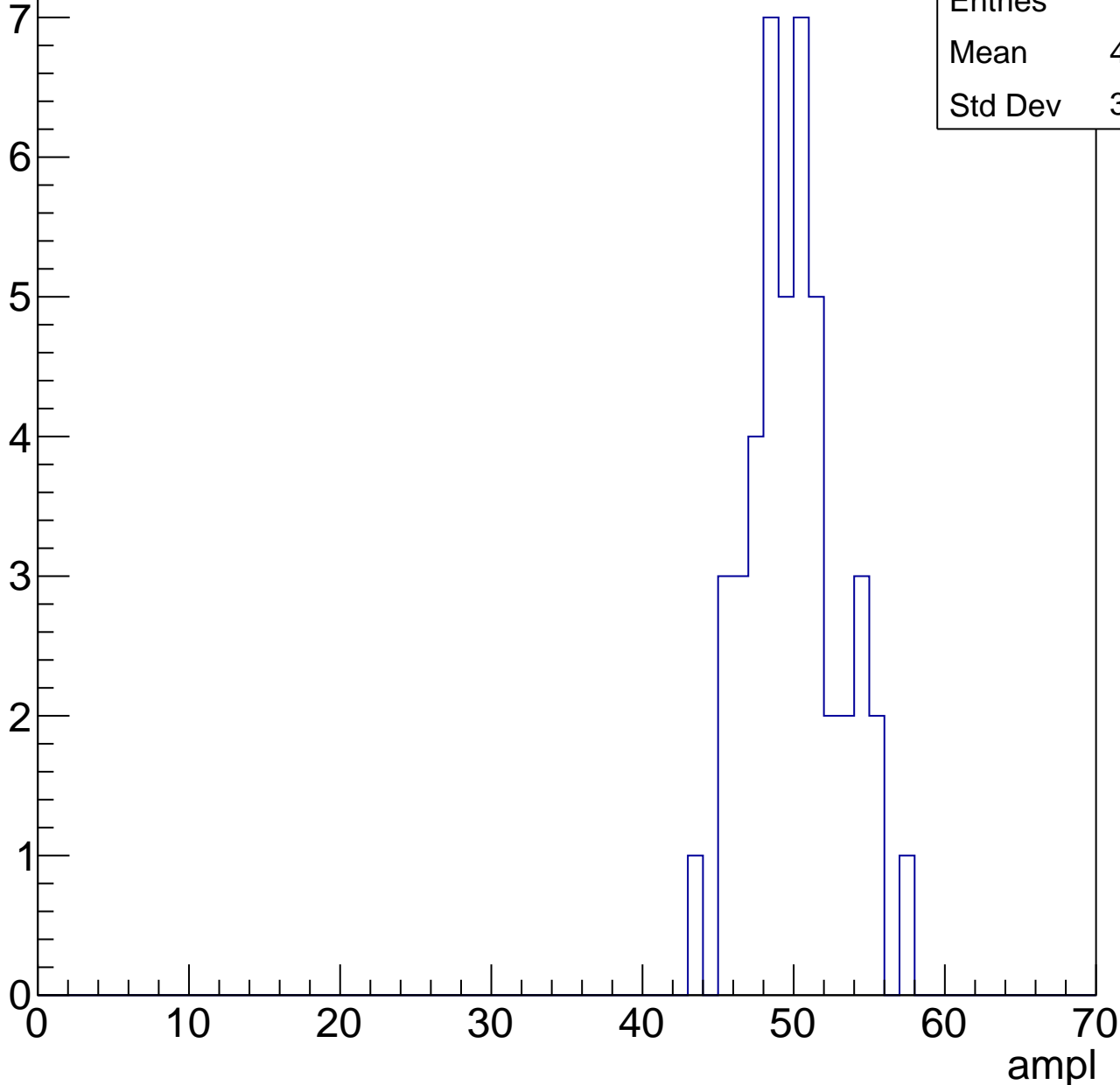


B1L100S, U9-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

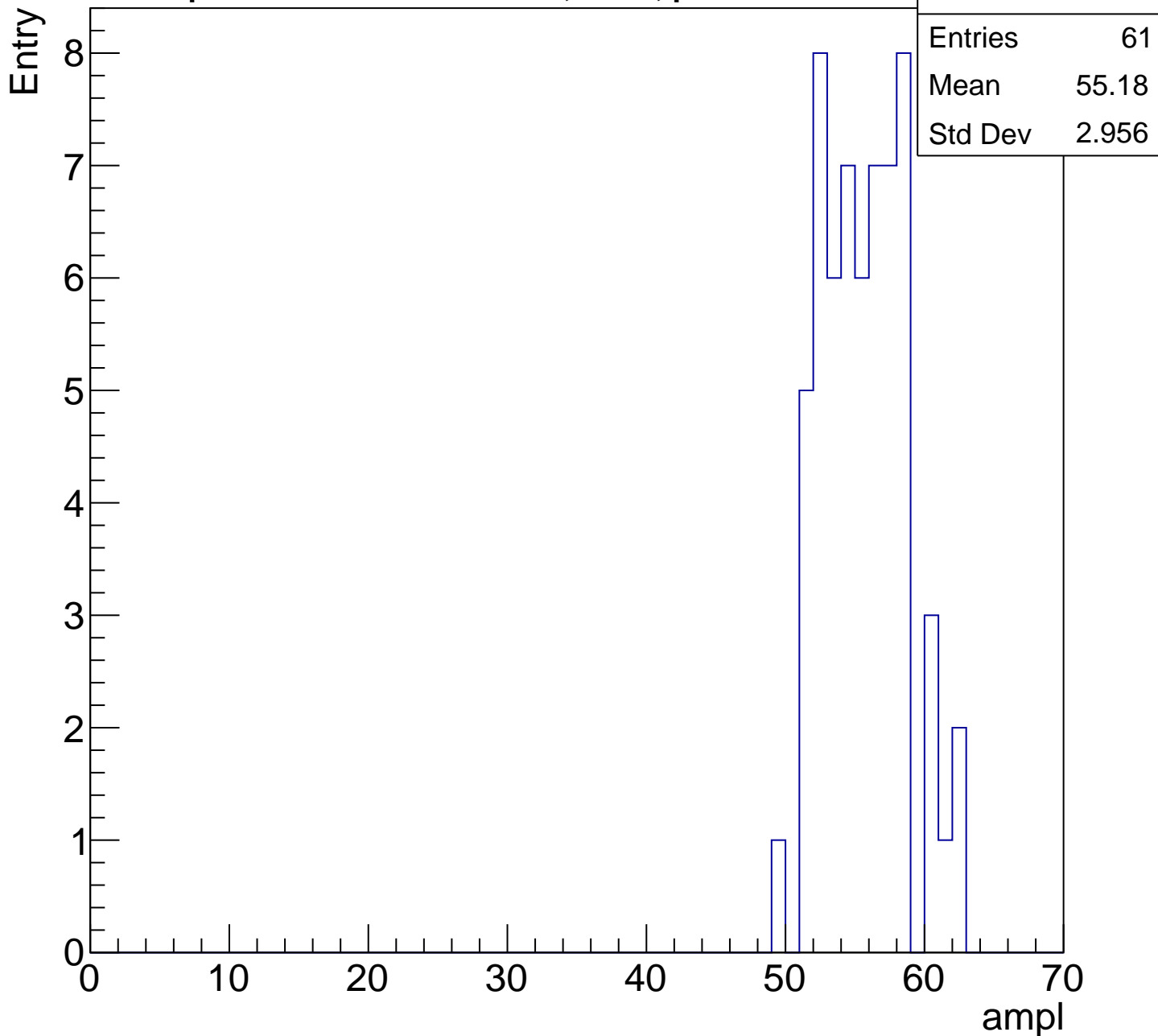
Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 49.53 |
| Std Dev | 3.016 |



B1L100S, U9-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

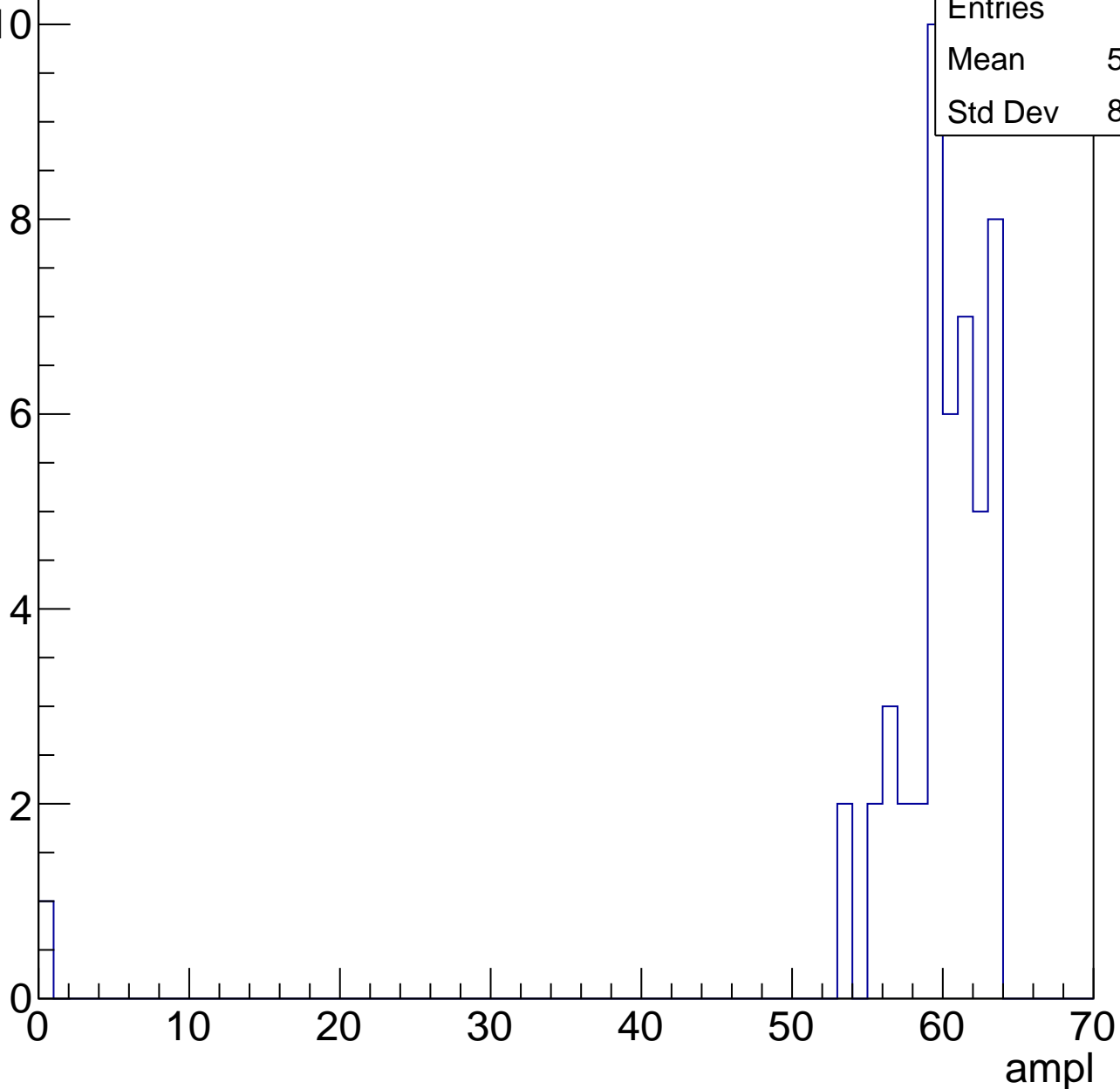


B1L100S, U9-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

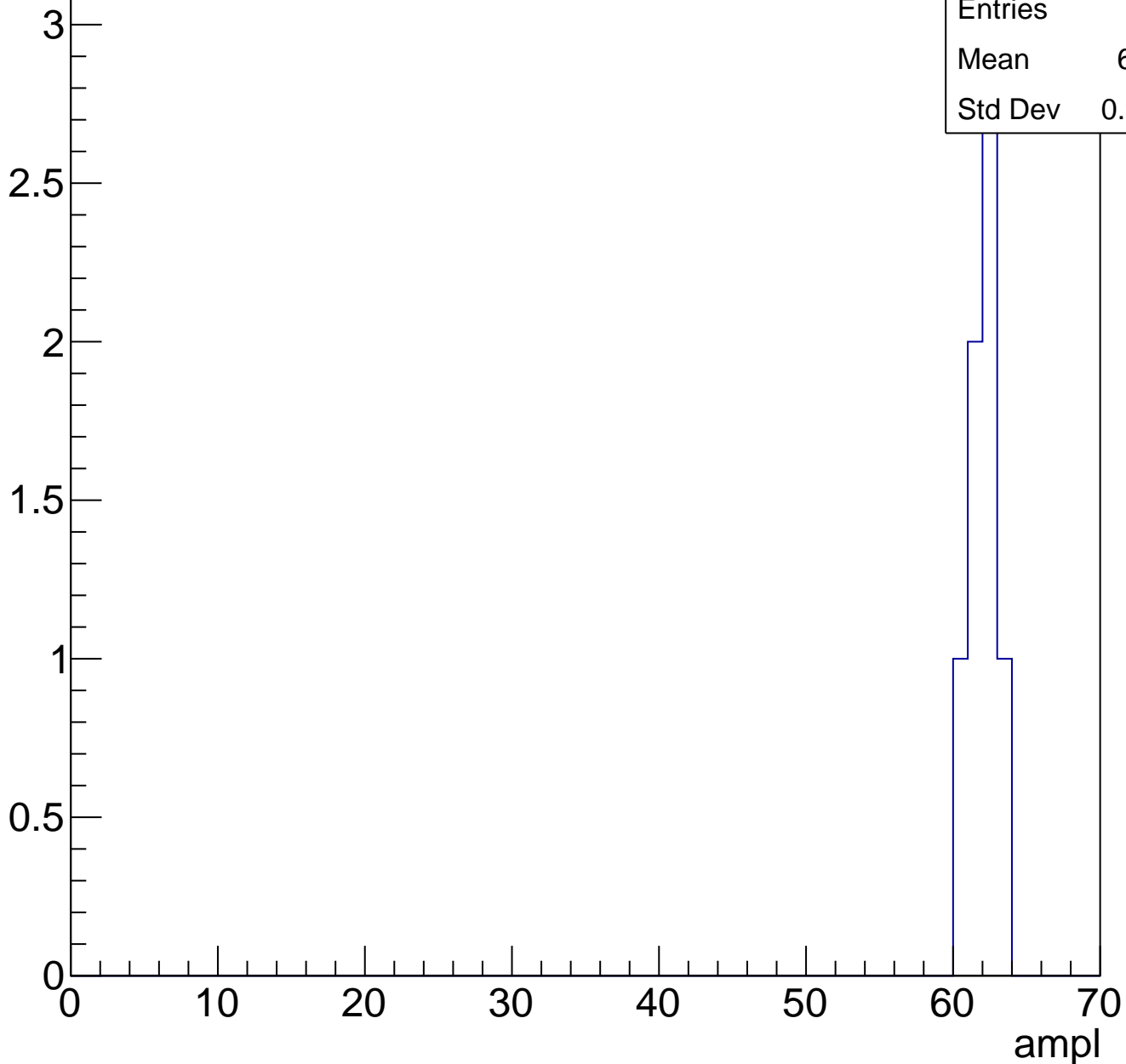
| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.44 |
| Std Dev | 8.914 |



B1L100S, U9-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

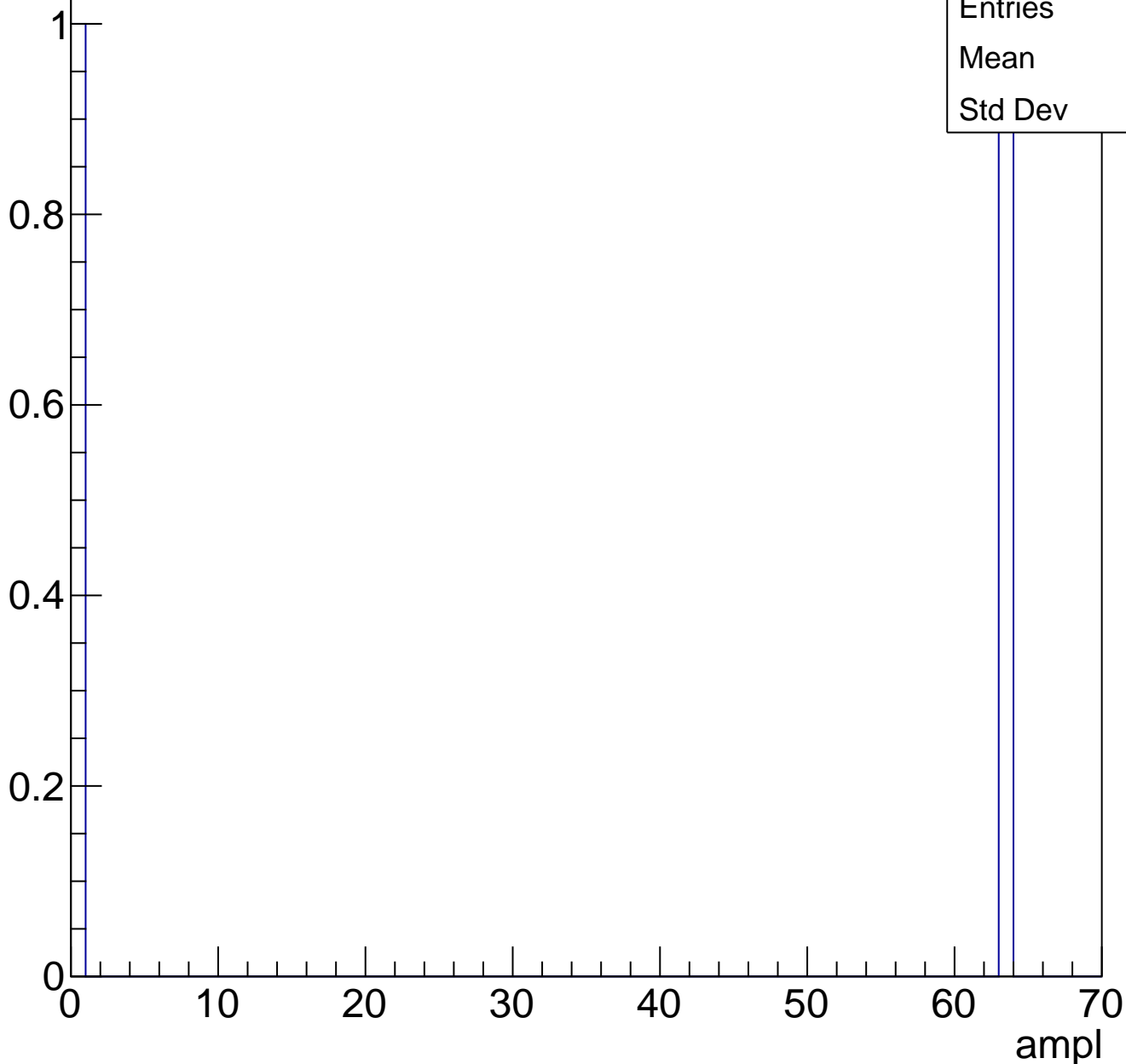
Entry



B1L100S, U9-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch84, adc0

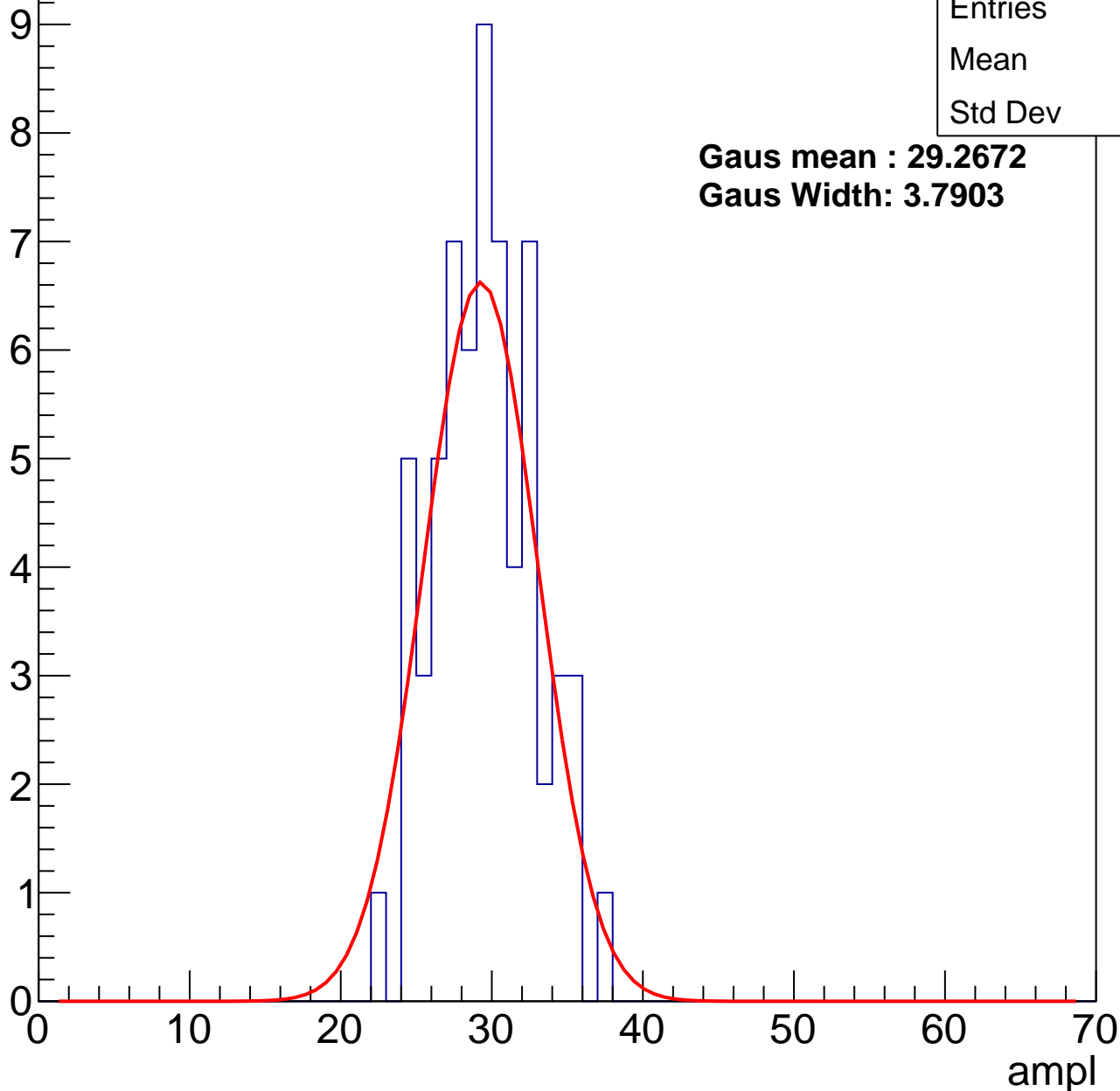
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 63 |
| Mean | 29.1 |
| Std Dev | 3.25 |

Gaus mean : 29.2672

Gaus Width: 3.7903



B1L100S, U9-ch84, adc1

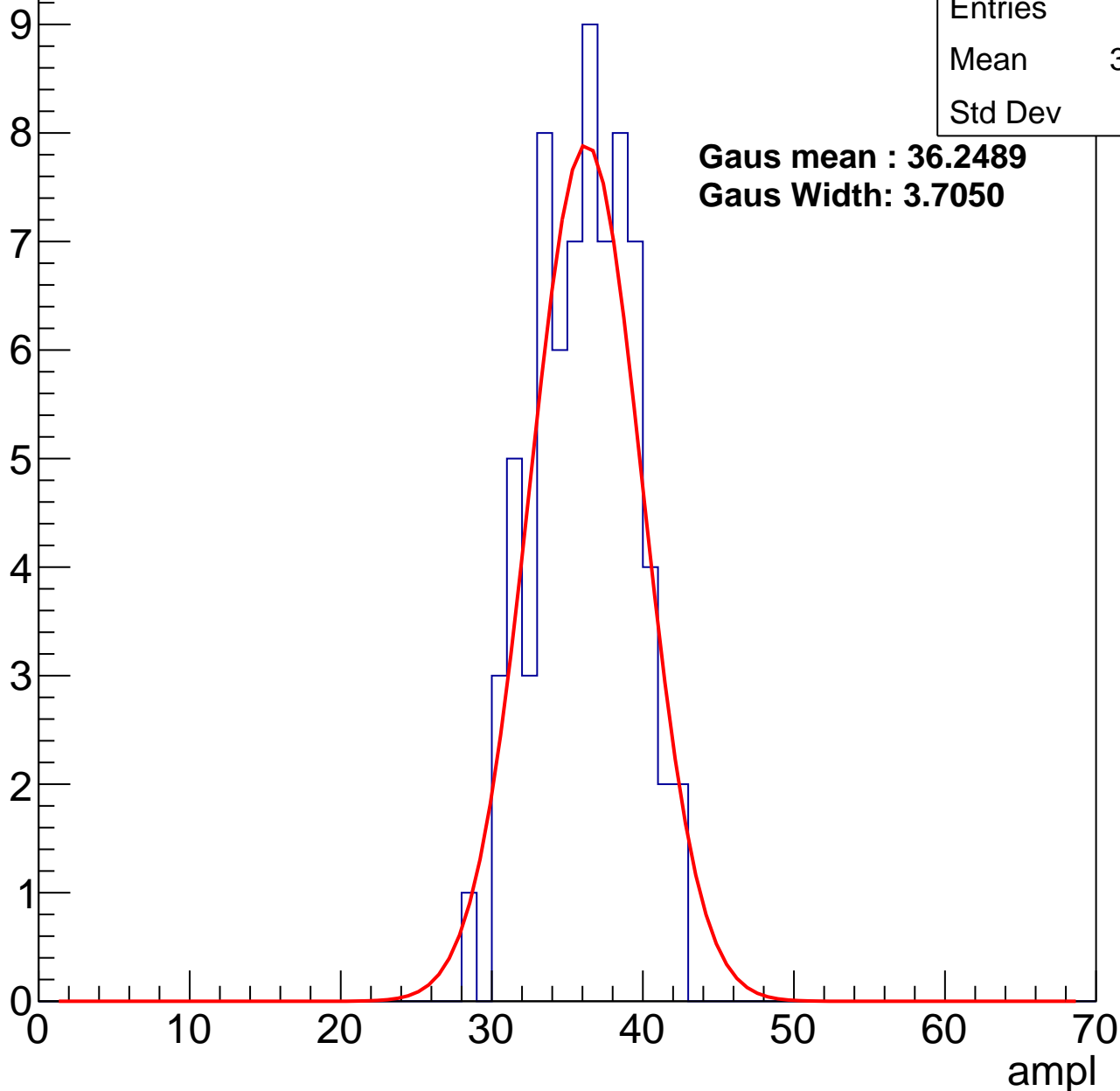
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.67 |
| Std Dev | 3.18 |

Gaus mean : 36.2489

Gaus Width: 3.7050



B1L100S, U9-ch84, adc2

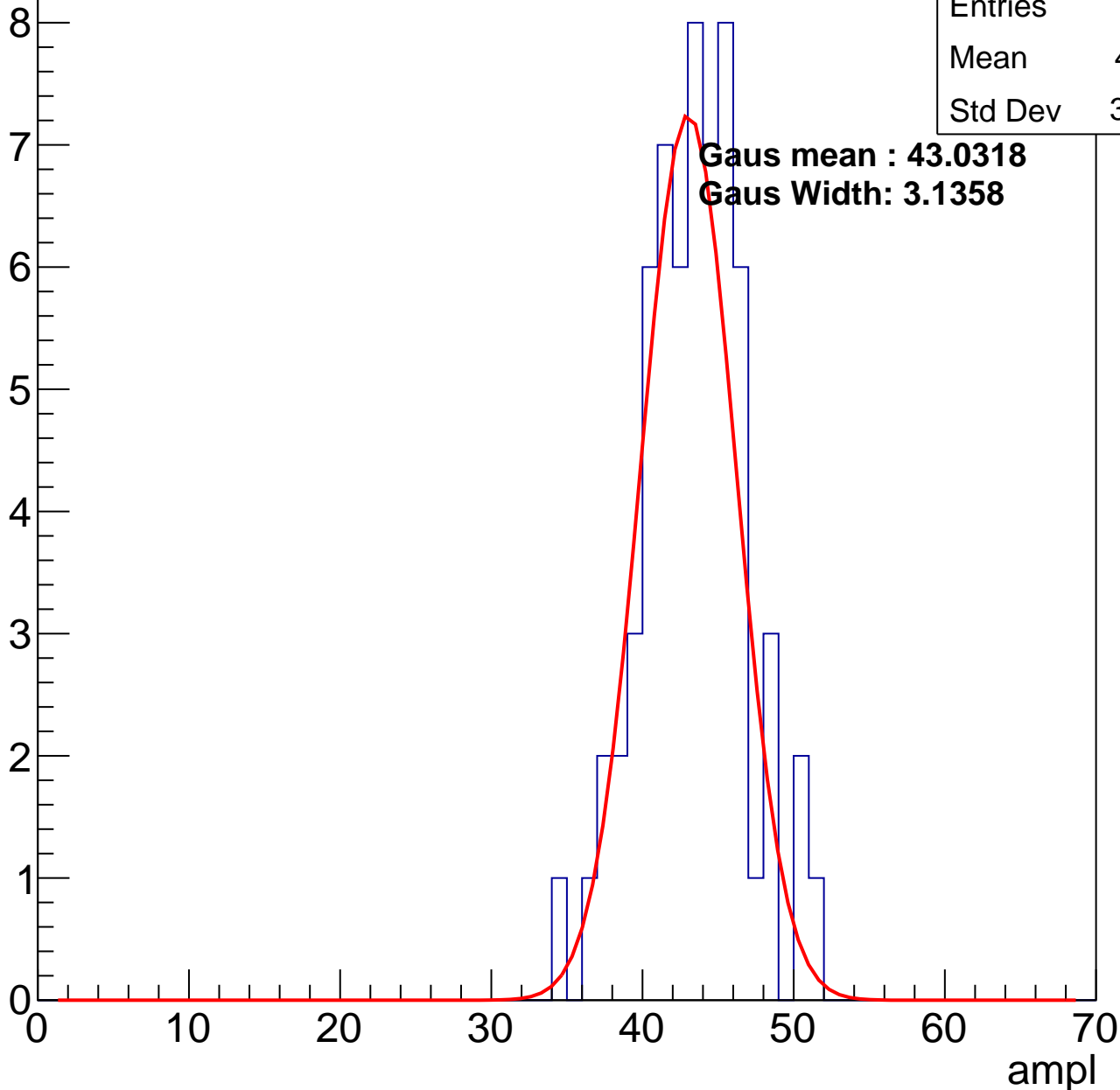
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.91 |
| Std Dev | 3.385 |

Gaus mean : 43.0318

Gaus Width: 3.1358

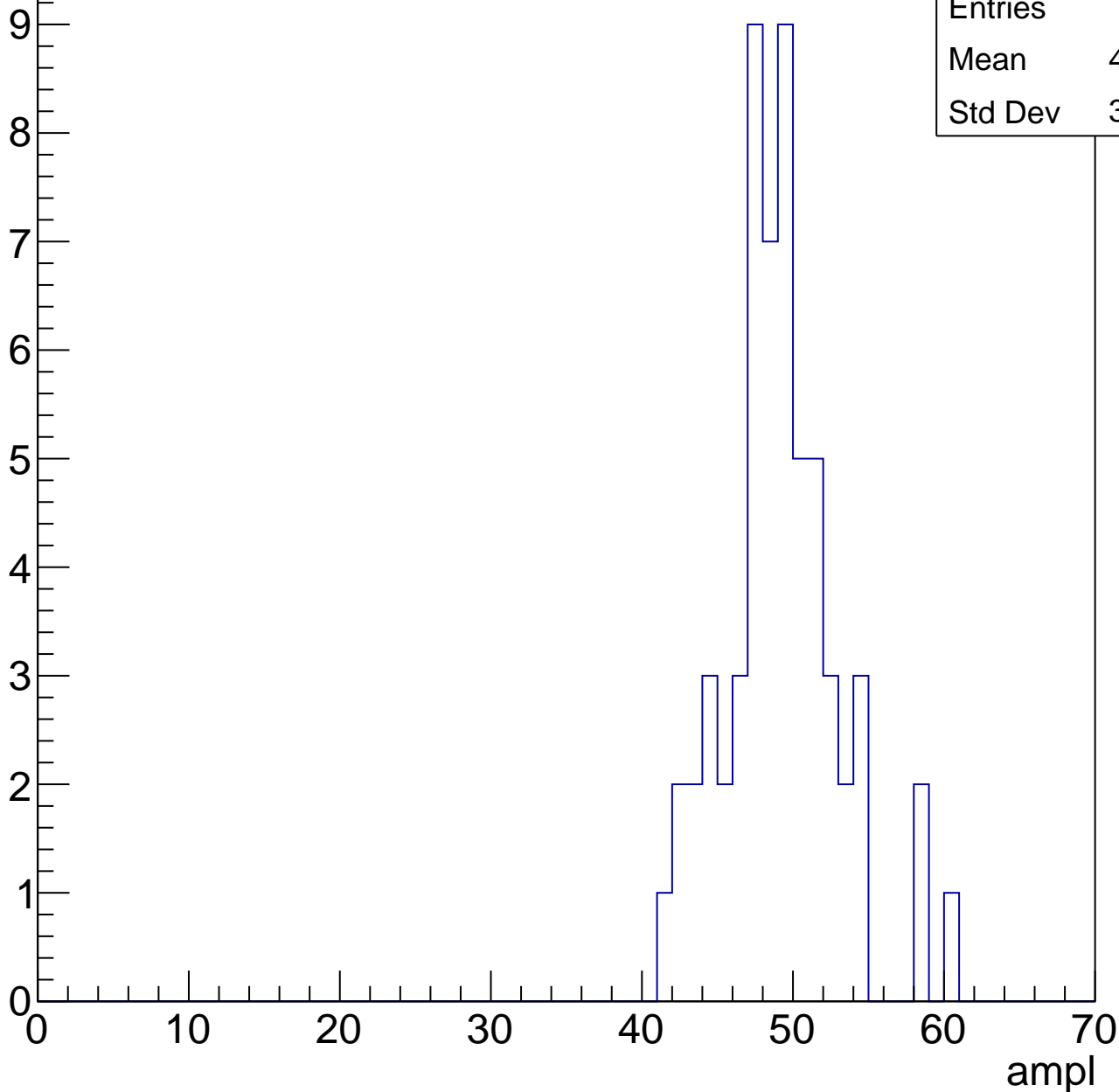


B1L100S, U9-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

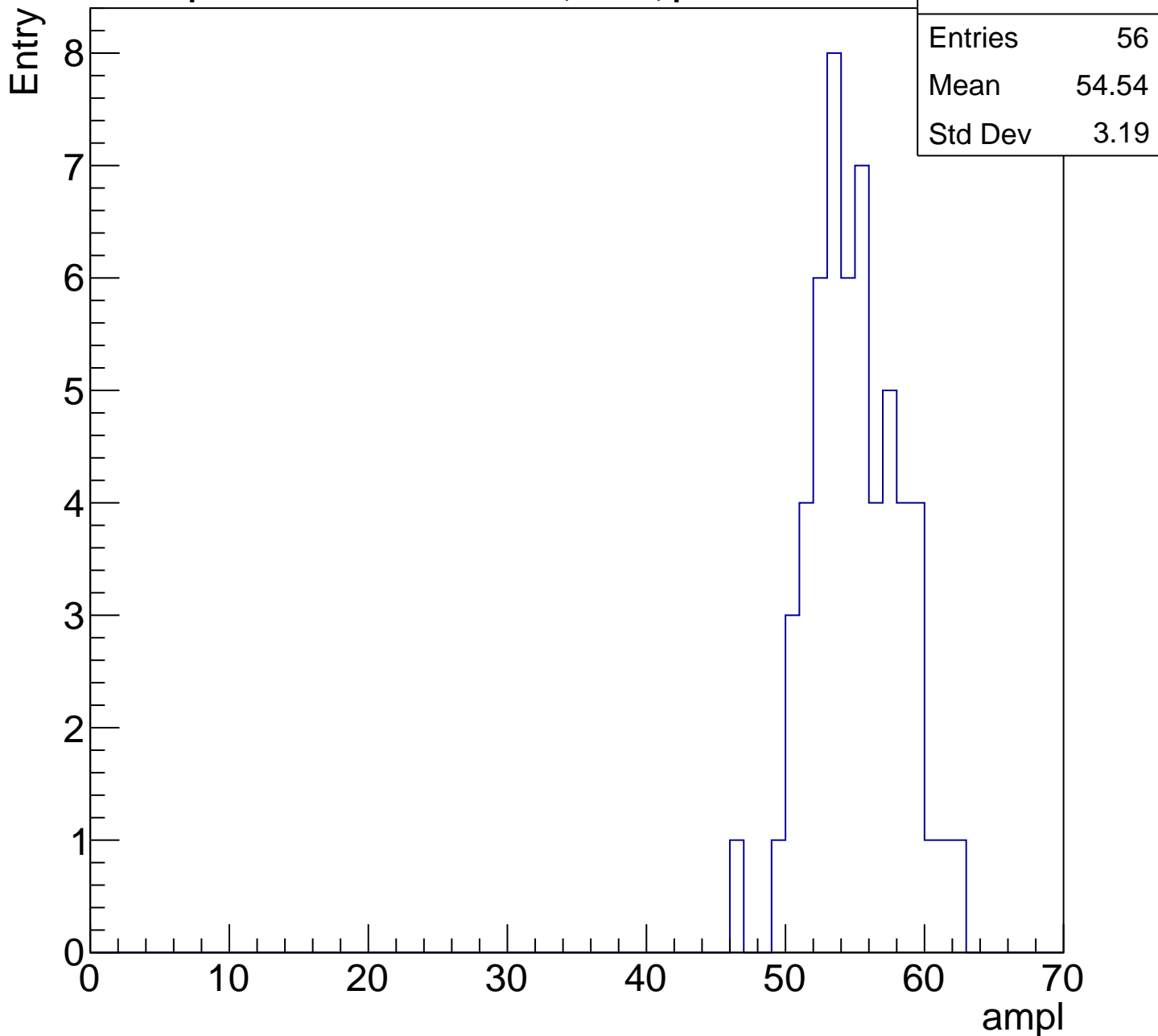
Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 48.75 |
| Std Dev | 3.794 |



B1L100S, U9-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

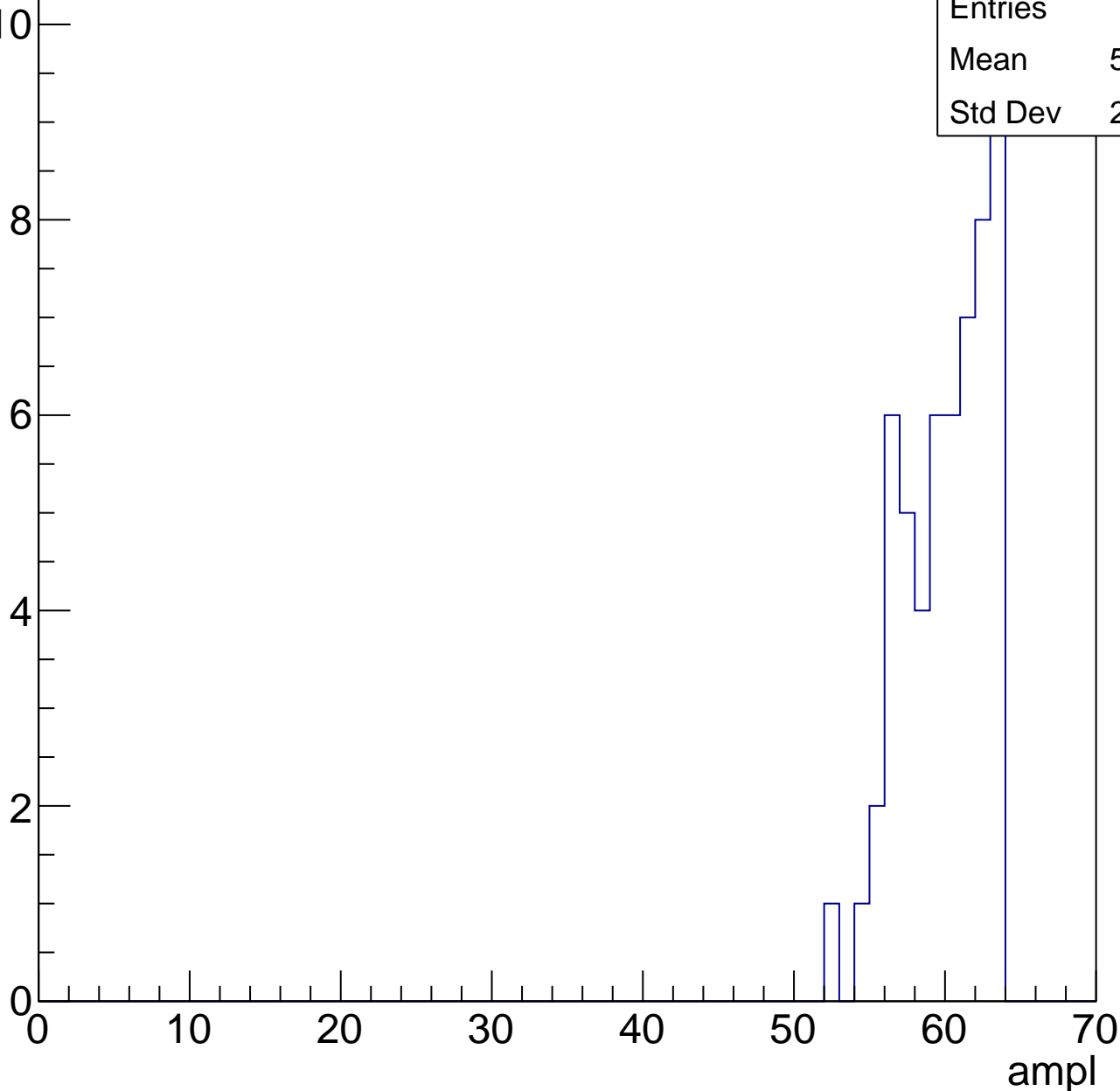


B1L100S, U9-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

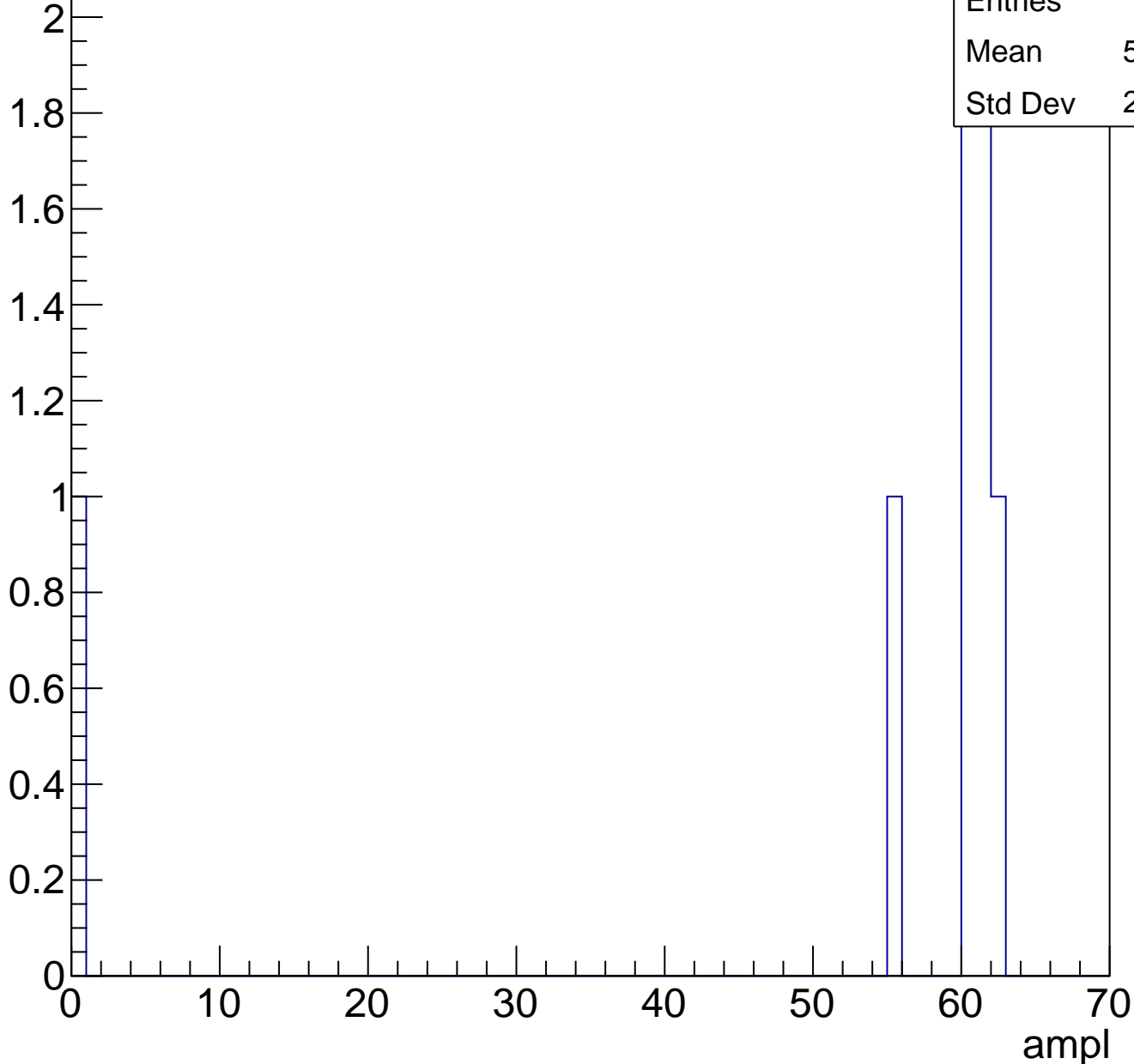
| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.57 |
| Std Dev | 2.777 |



B1L100S, U9-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch85, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 29.56 |
| Std Dev | 3.156 |

Gaus mean : 30.0787

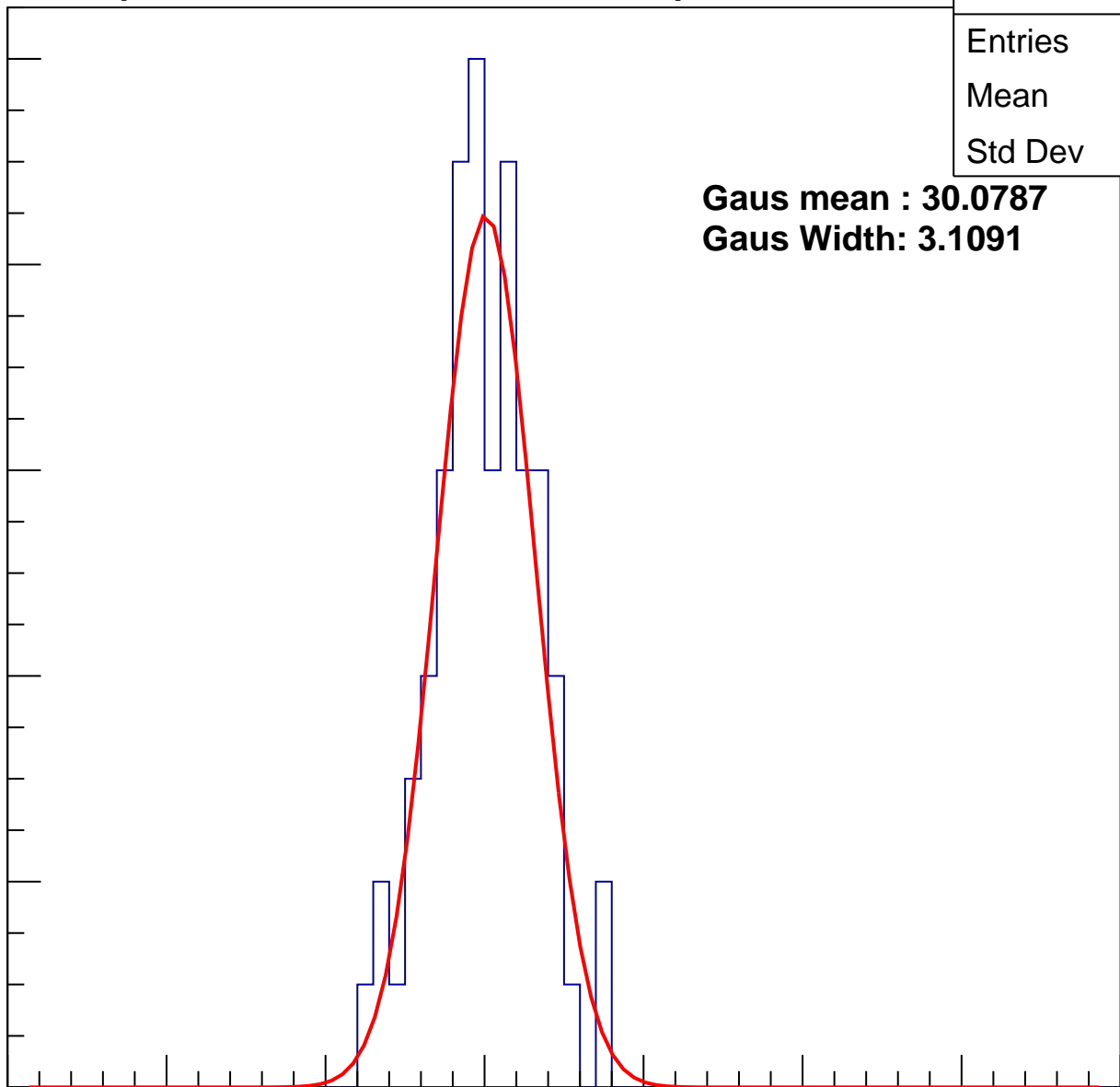
Gaus Width: 3.1091

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch85, adc1

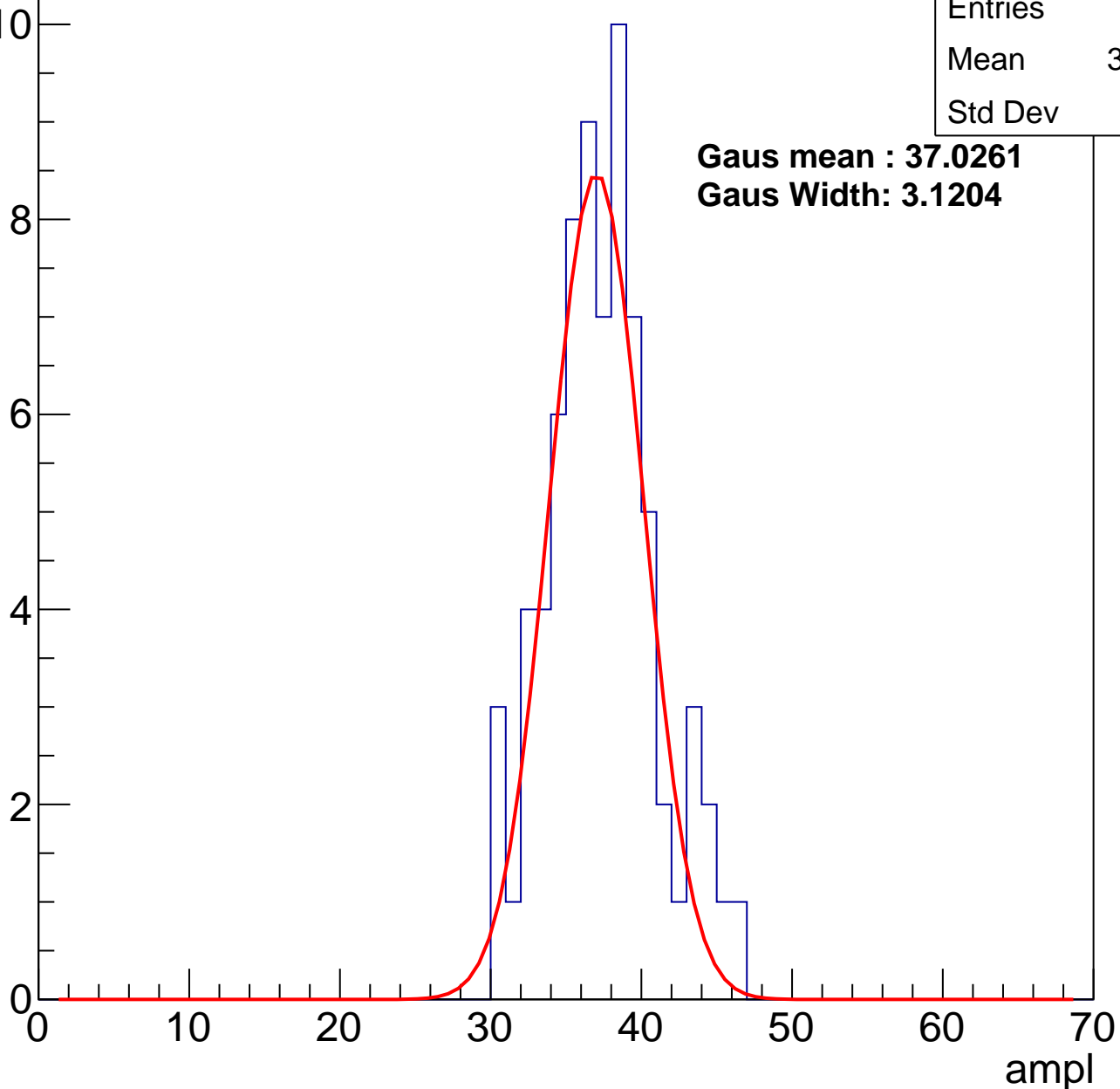
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.93 |
| Std Dev | 3.55 |

Gaus mean : 37.0261

Gaus Width: 3.1204



B1L100S, U9-ch85, adc2

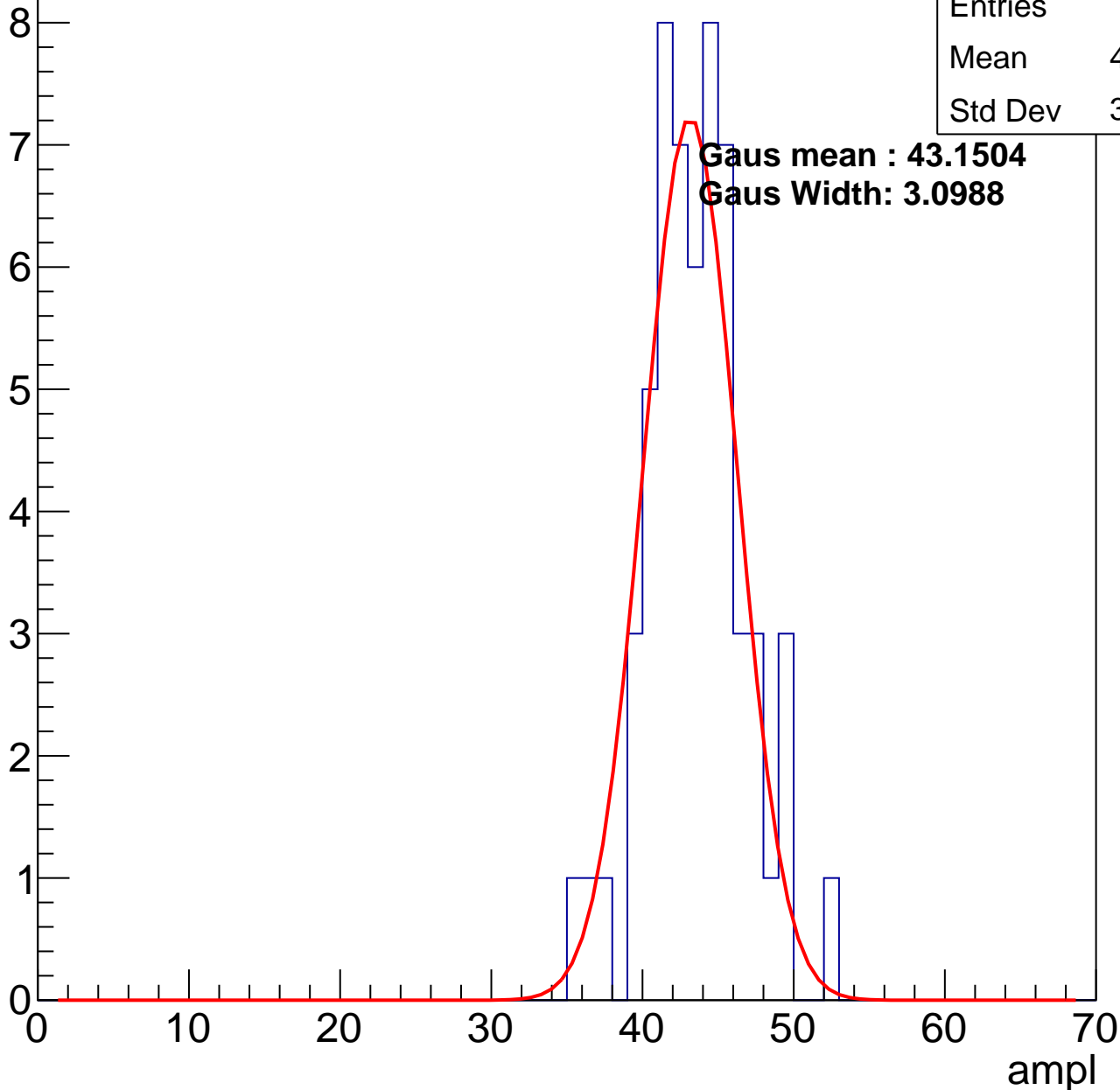
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 43.07 |
| Std Dev | 3.232 |

Gaus mean : 43.1504

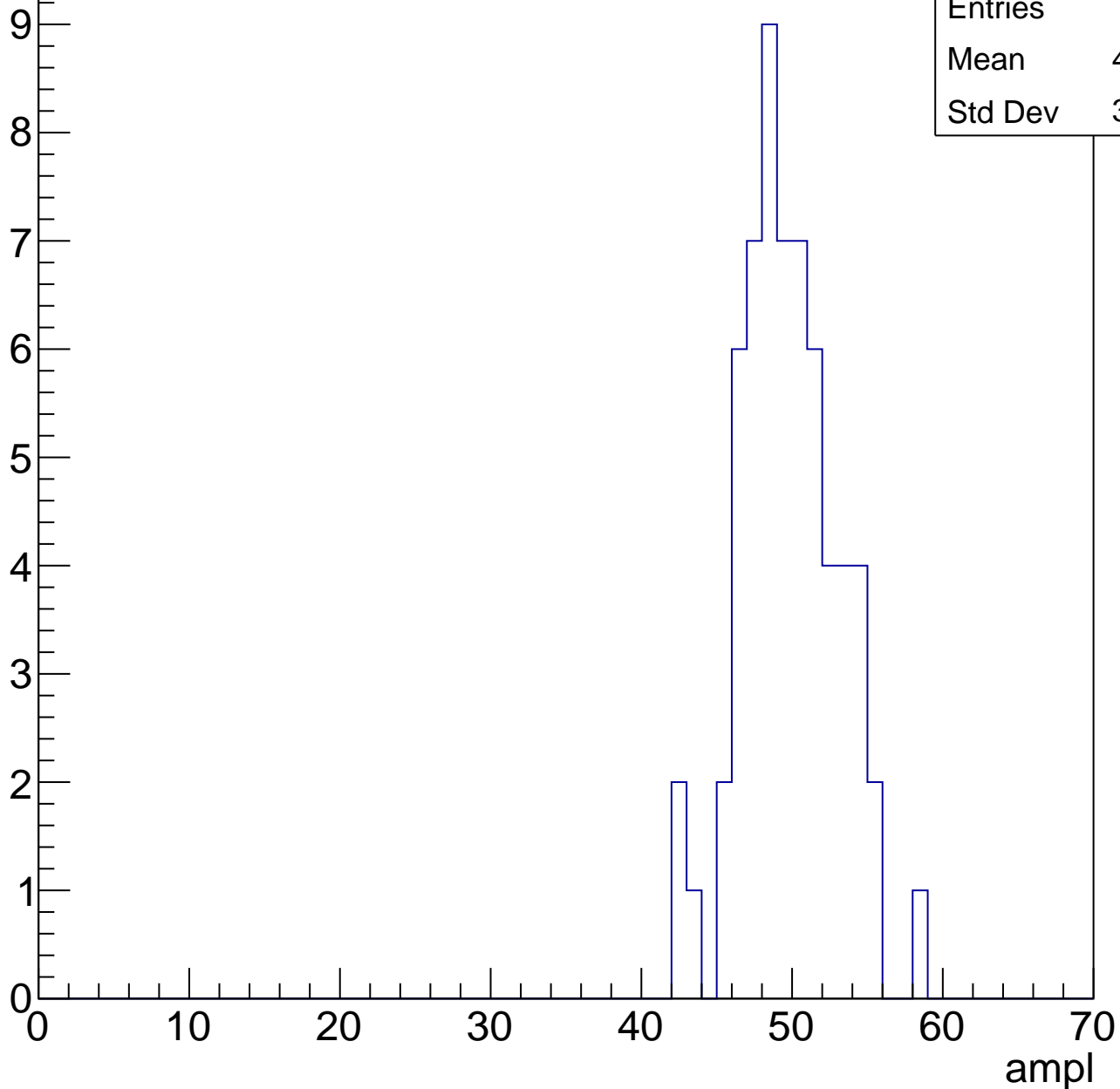
Gaus Width: 3.0988



B1L100S, U9-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

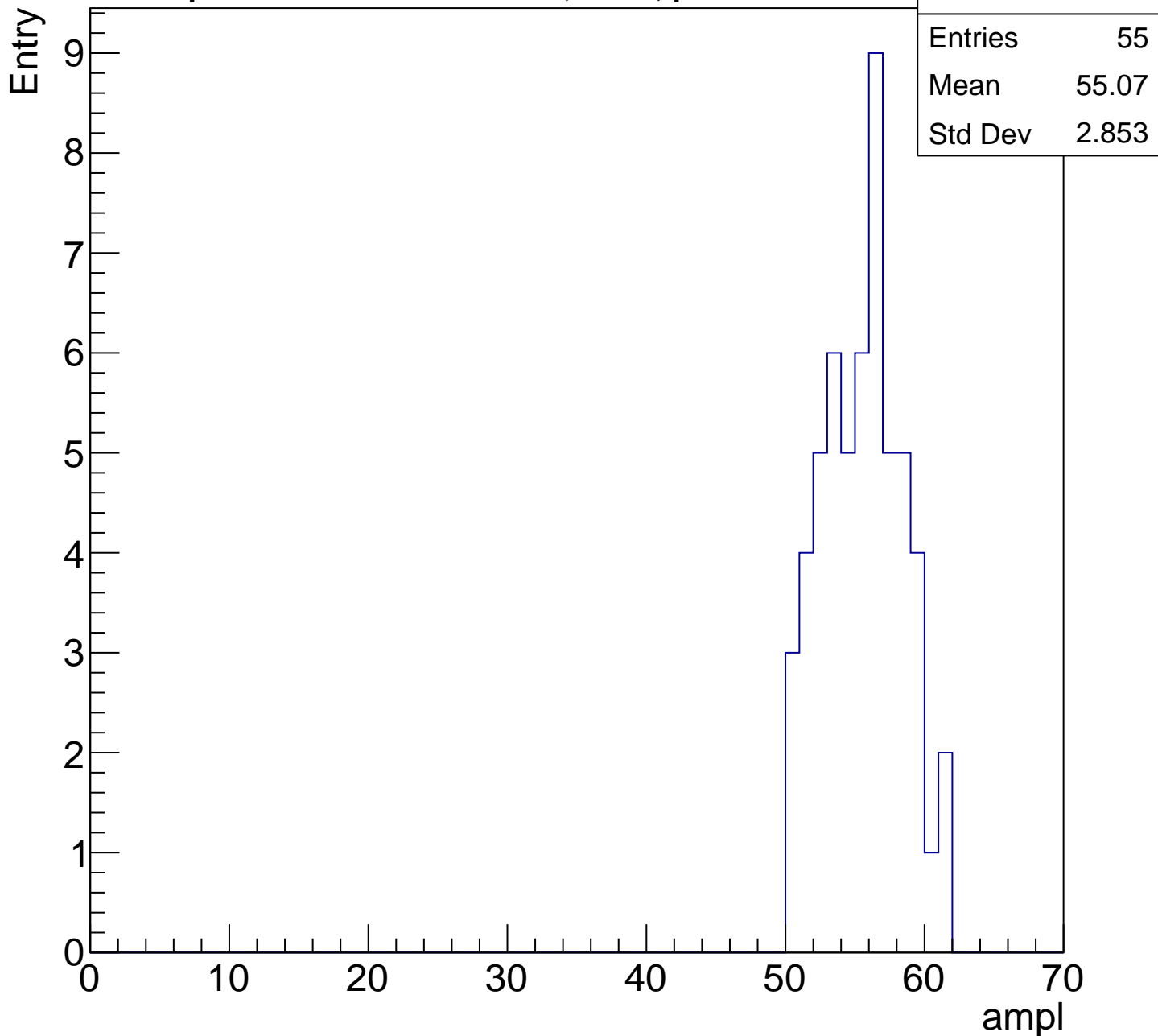
Entry



| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.31 |
| Std Dev | 3.201 |

B1L100S, U9-ch85, adc4

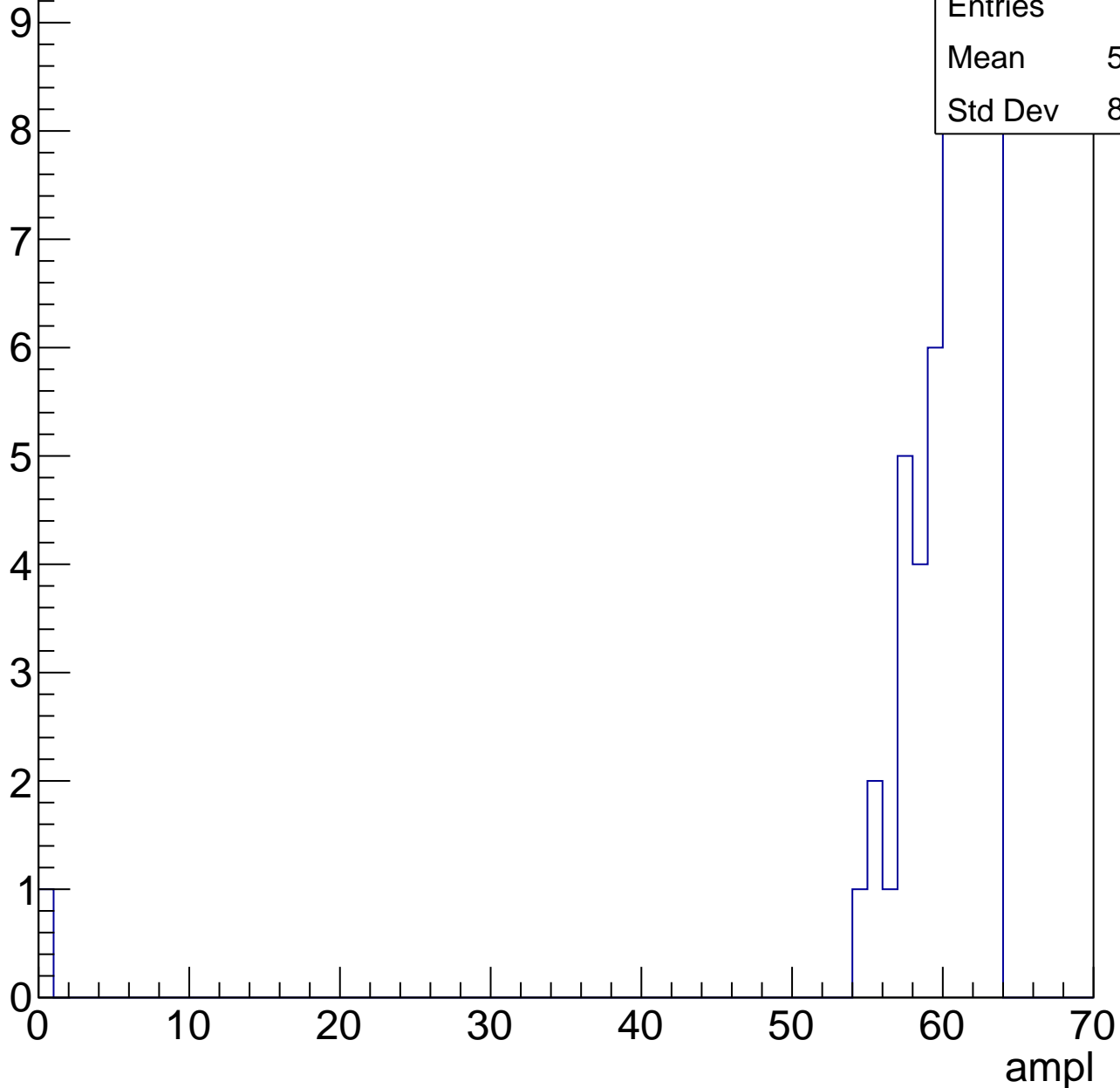
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

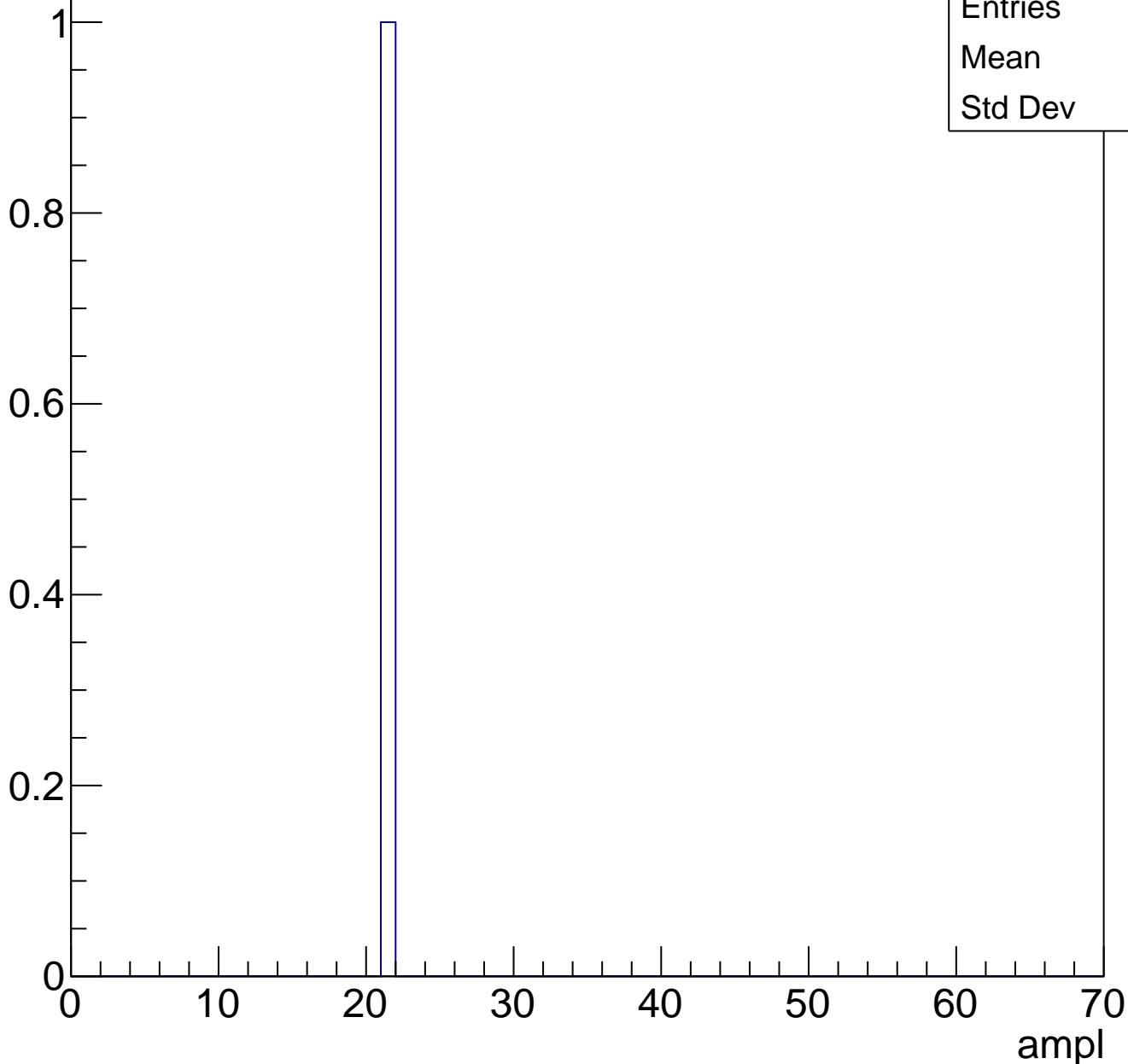
Entry



B1L100S, U9-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch86, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.97 |
| Std Dev | 2.794 |

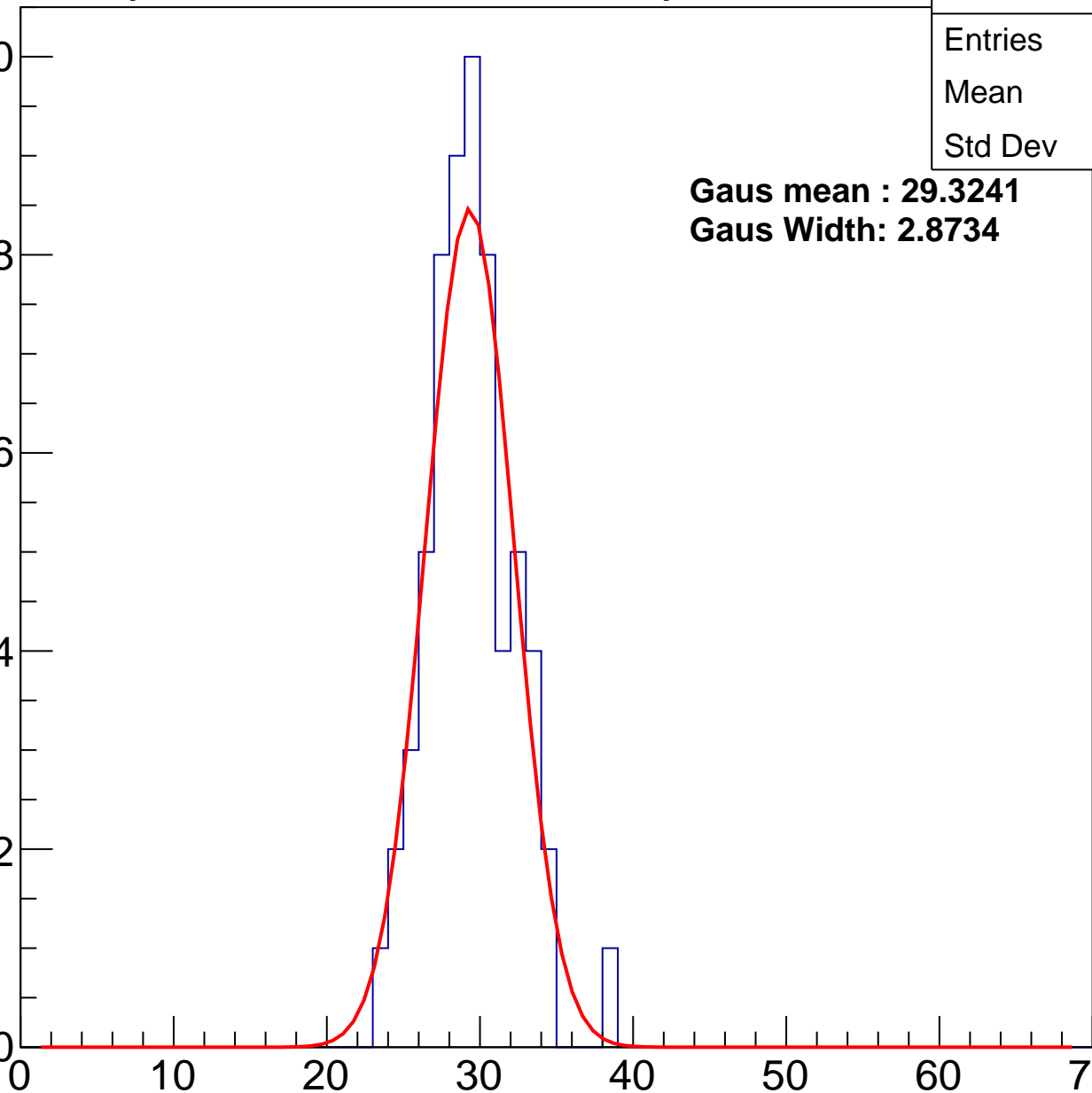
Gaus mean : 29.3241

Gaus Width: 2.8734

Entry

10
8
6
4
2
0

ampl



B1L100S, U9-ch86, adc1

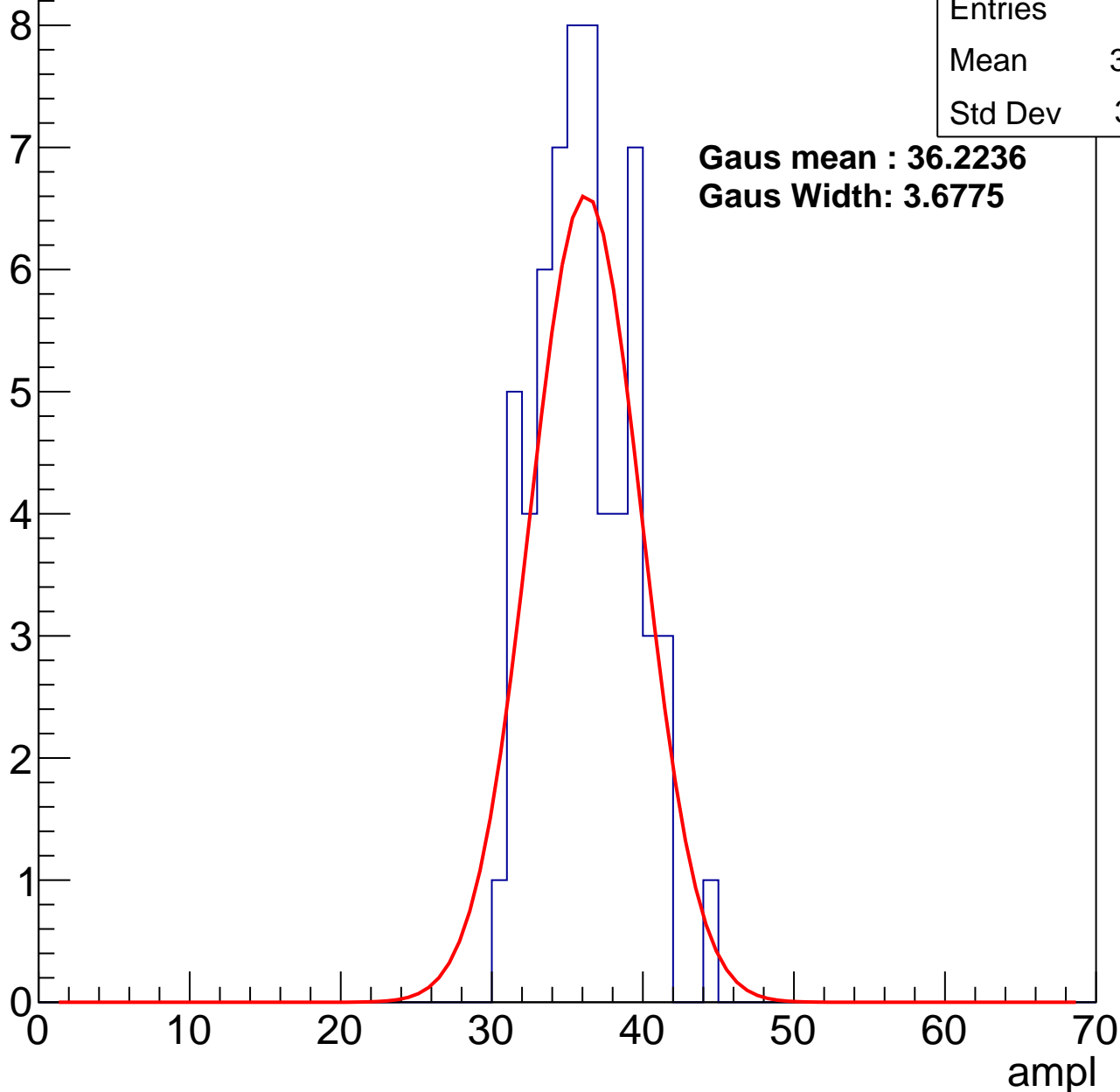
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.69 |
| Std Dev | 3.081 |

Gaus mean : 36.2236

Gaus Width: 3.6775



B1L100S, U9-ch86, adc2

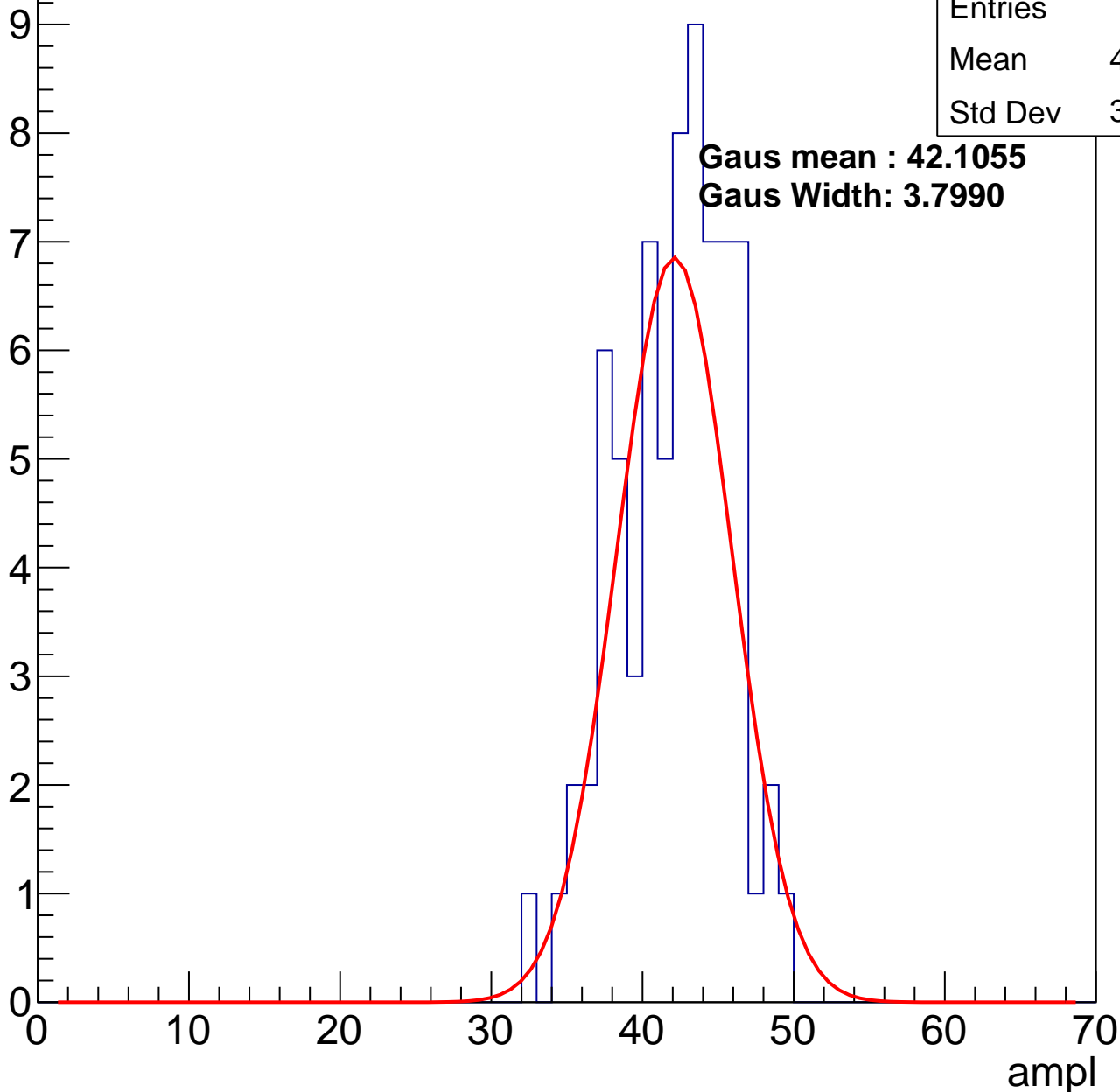
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 41.65 |
| Std Dev | 3.626 |

Gaus mean : 42.1055

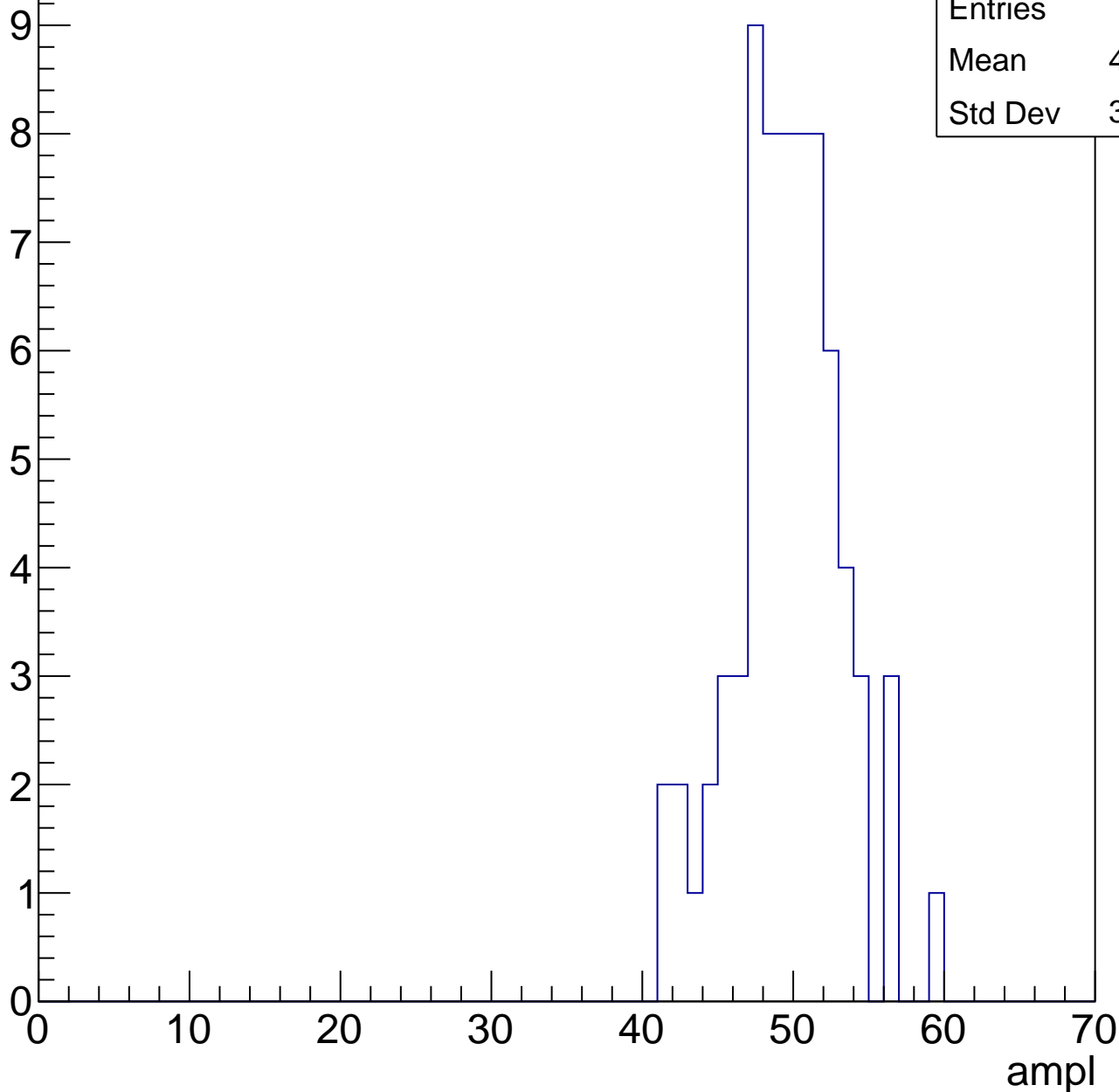
Gaus Width: 3.7990



B1L100S, U9-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

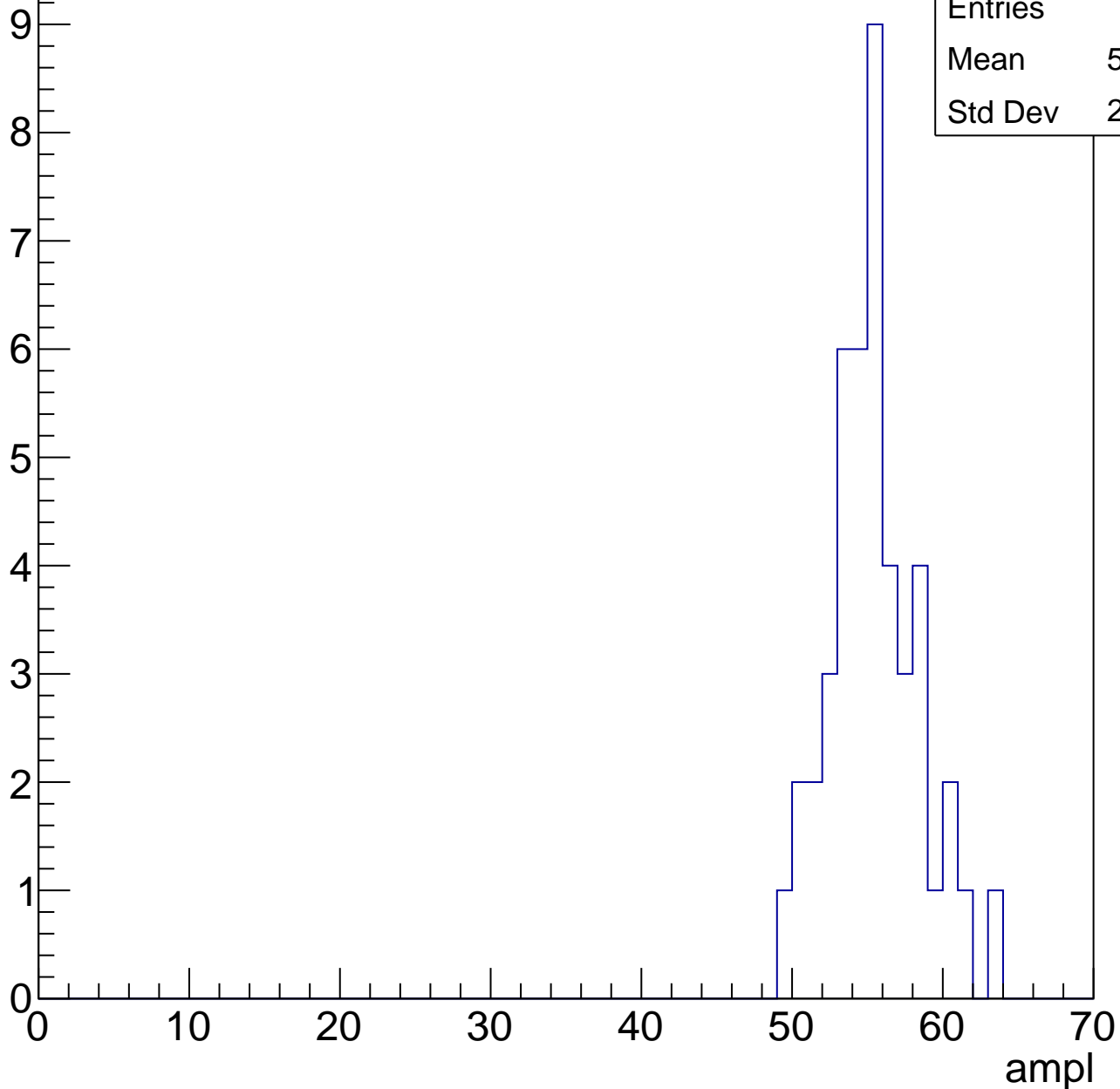
Entry



B1L100S, U9-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch86, adc5

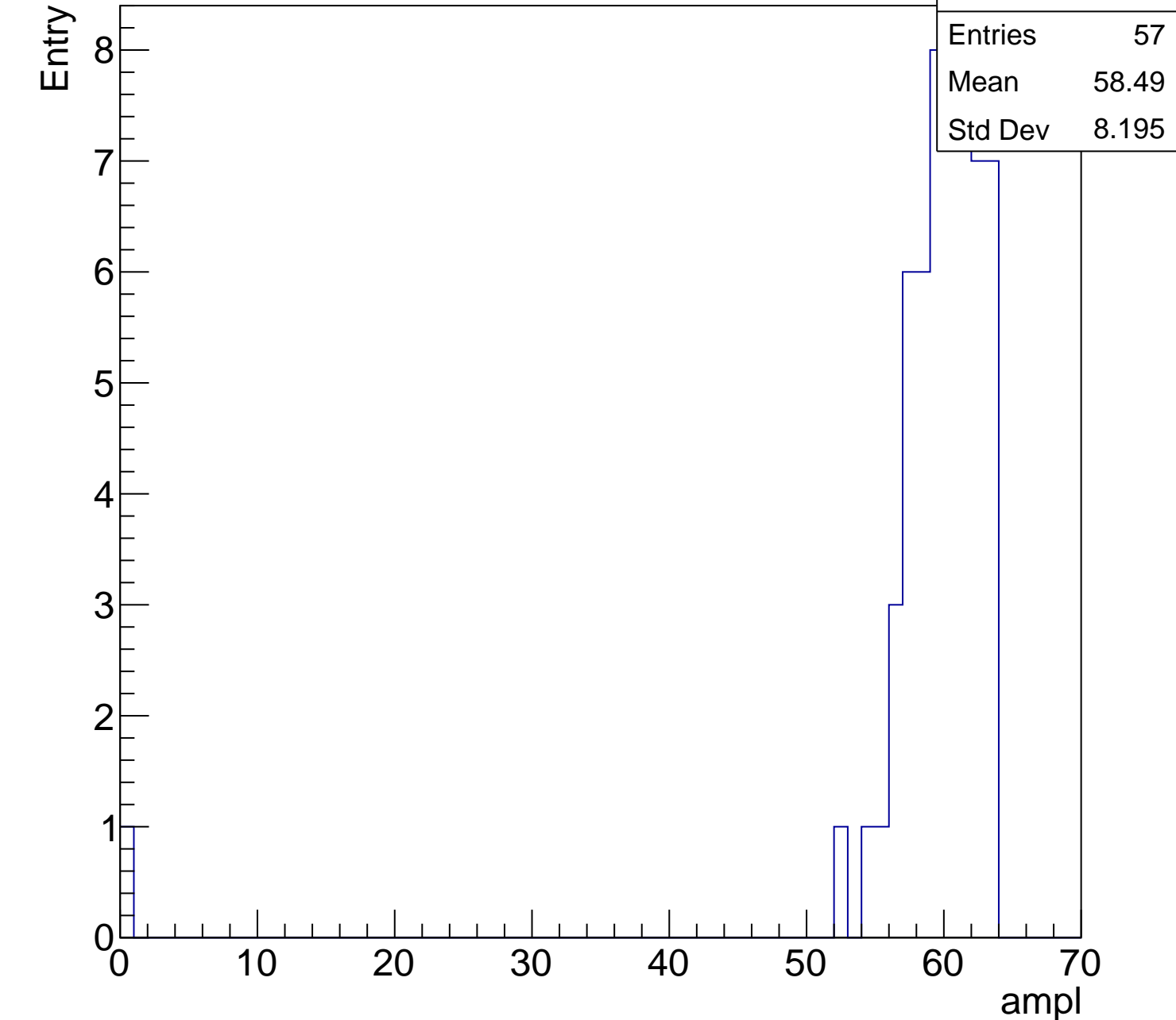
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 57 |
| Mean | 58.49 |
| Std Dev | 8.195 |

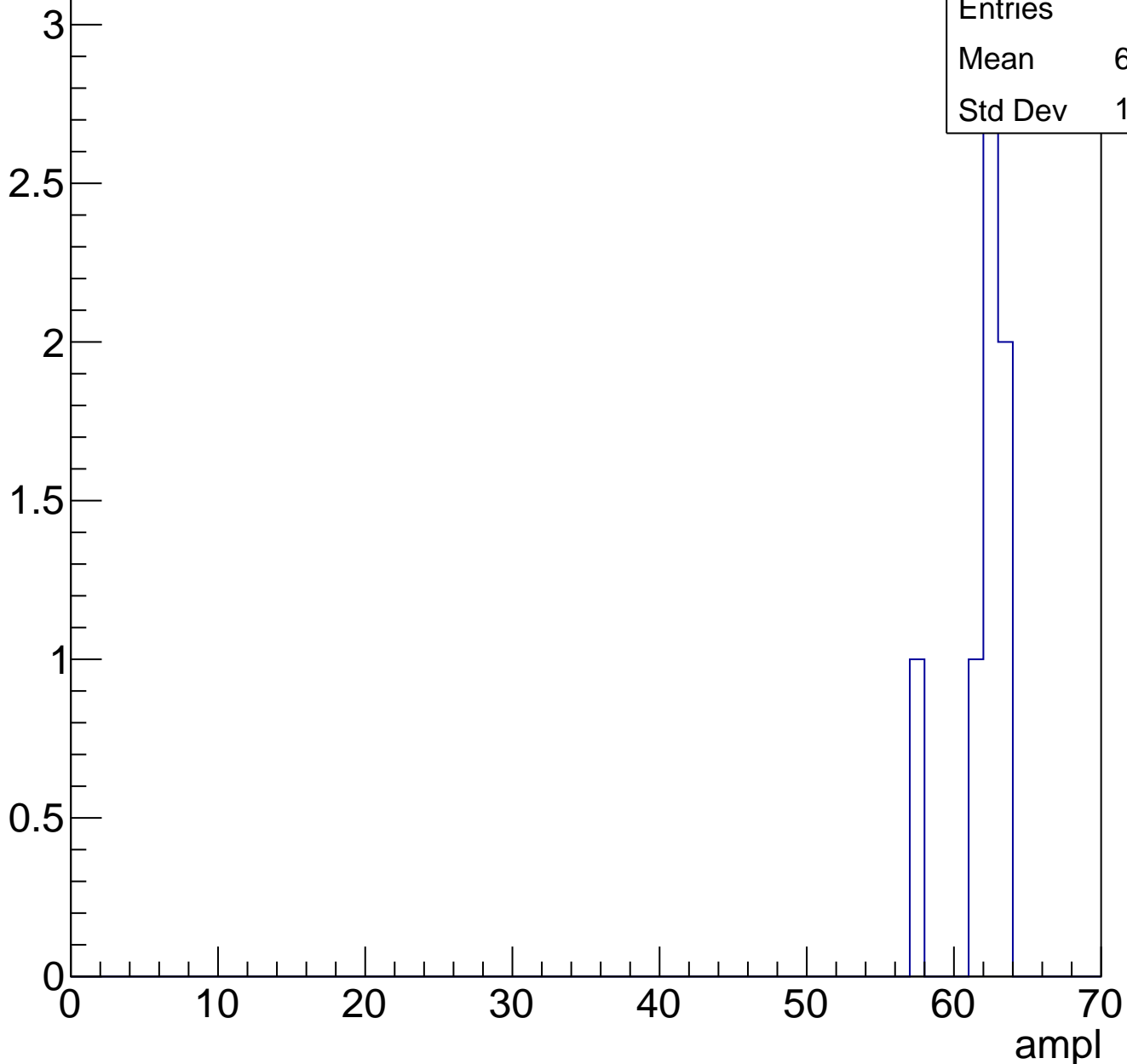
ampl



B1L100S, U9-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch87, adc0

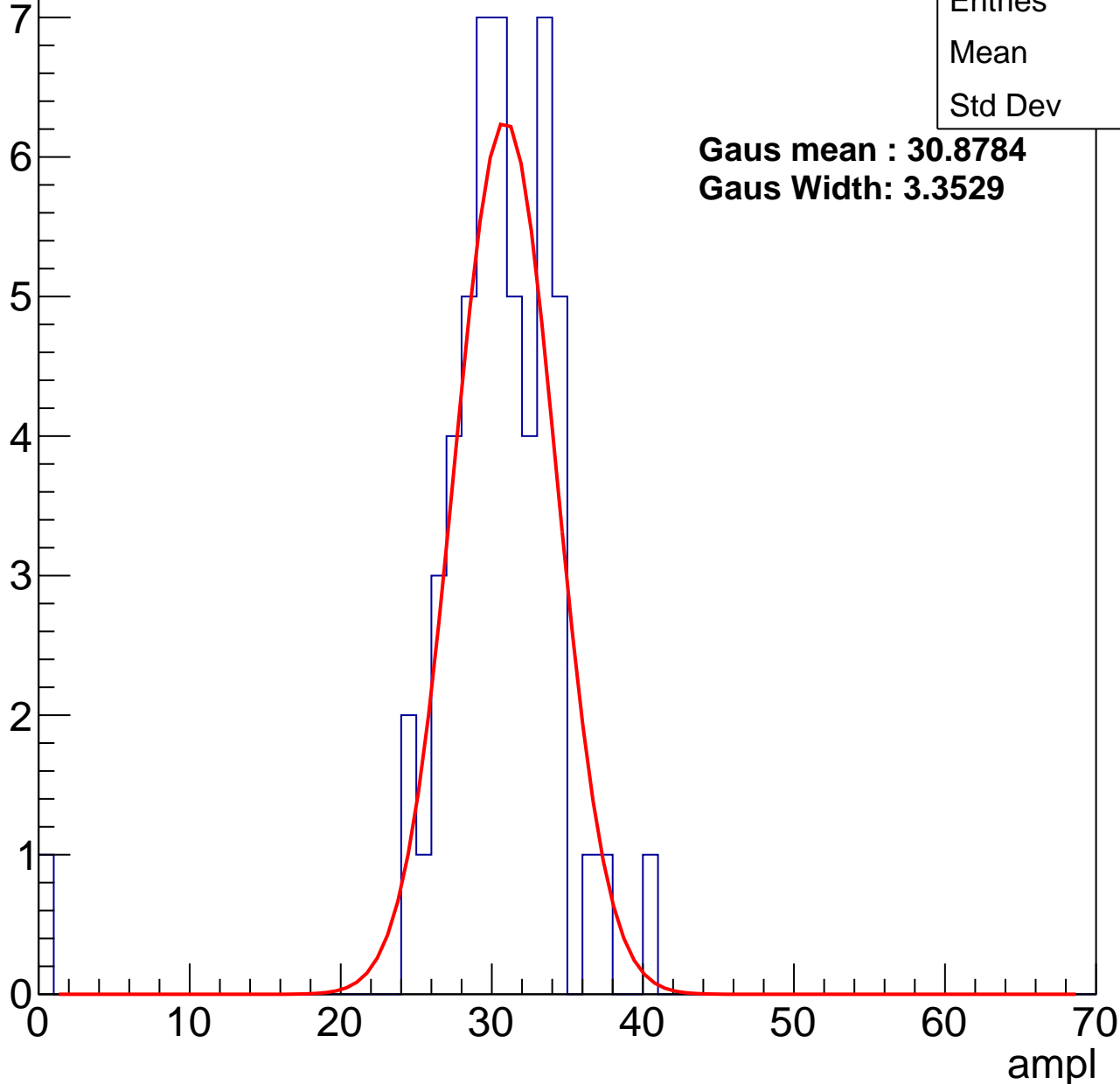
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 54 |
| Mean | 29.8 |
| Std Dev | 5.19 |

Gaus mean : 30.8784

Gaus Width: 3.3529



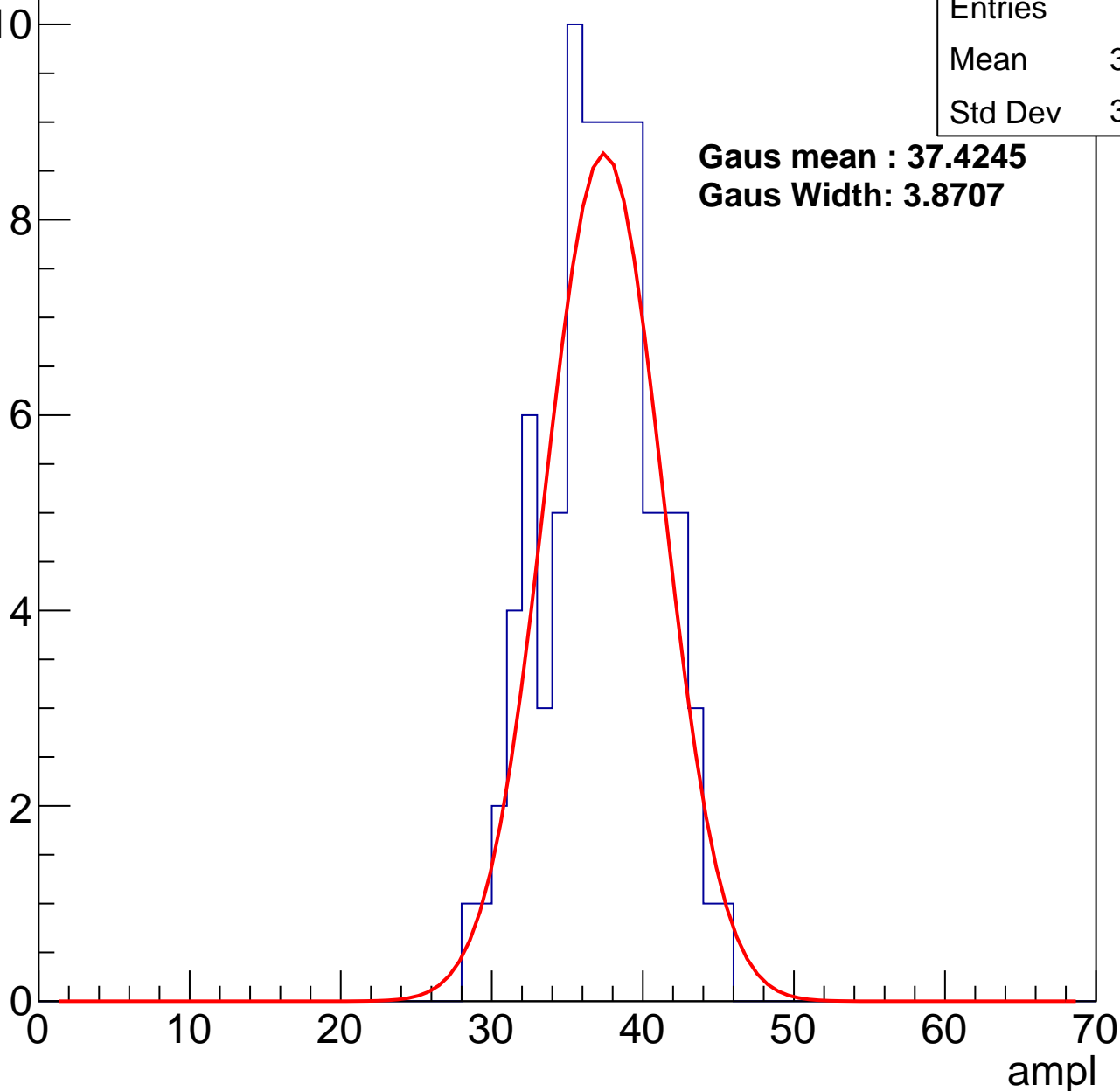
B1L100S, U9-ch87, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 36.76 |
| Std Dev | 3.677 |

Gaus mean : 37.4245
Gaus Width: 3.8707



B1L100S, U9-ch87, adc2

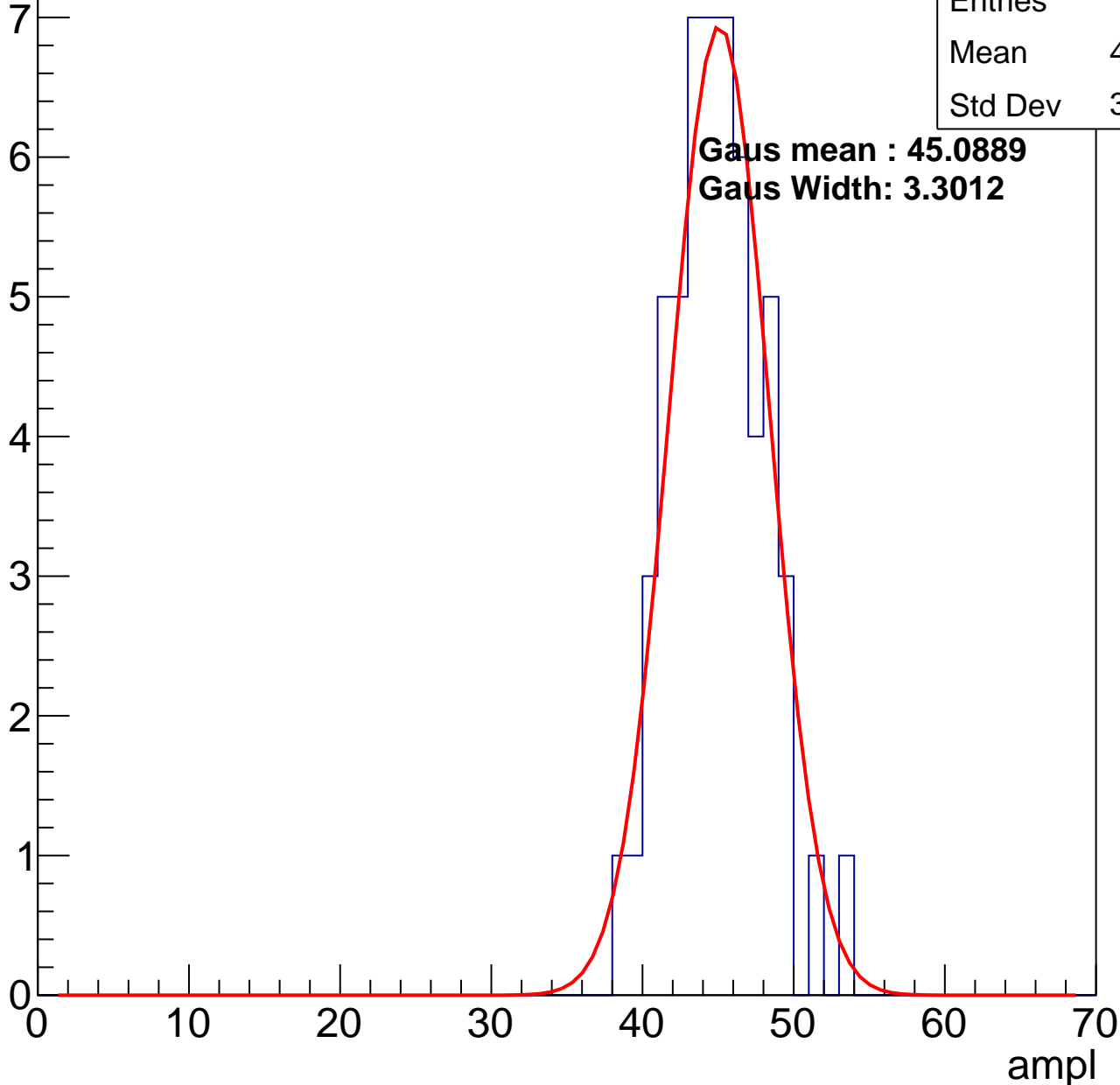
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 44.48 |
| Std Dev | 3.047 |

Gaus mean : 45.0889

Gaus Width: 3.3012

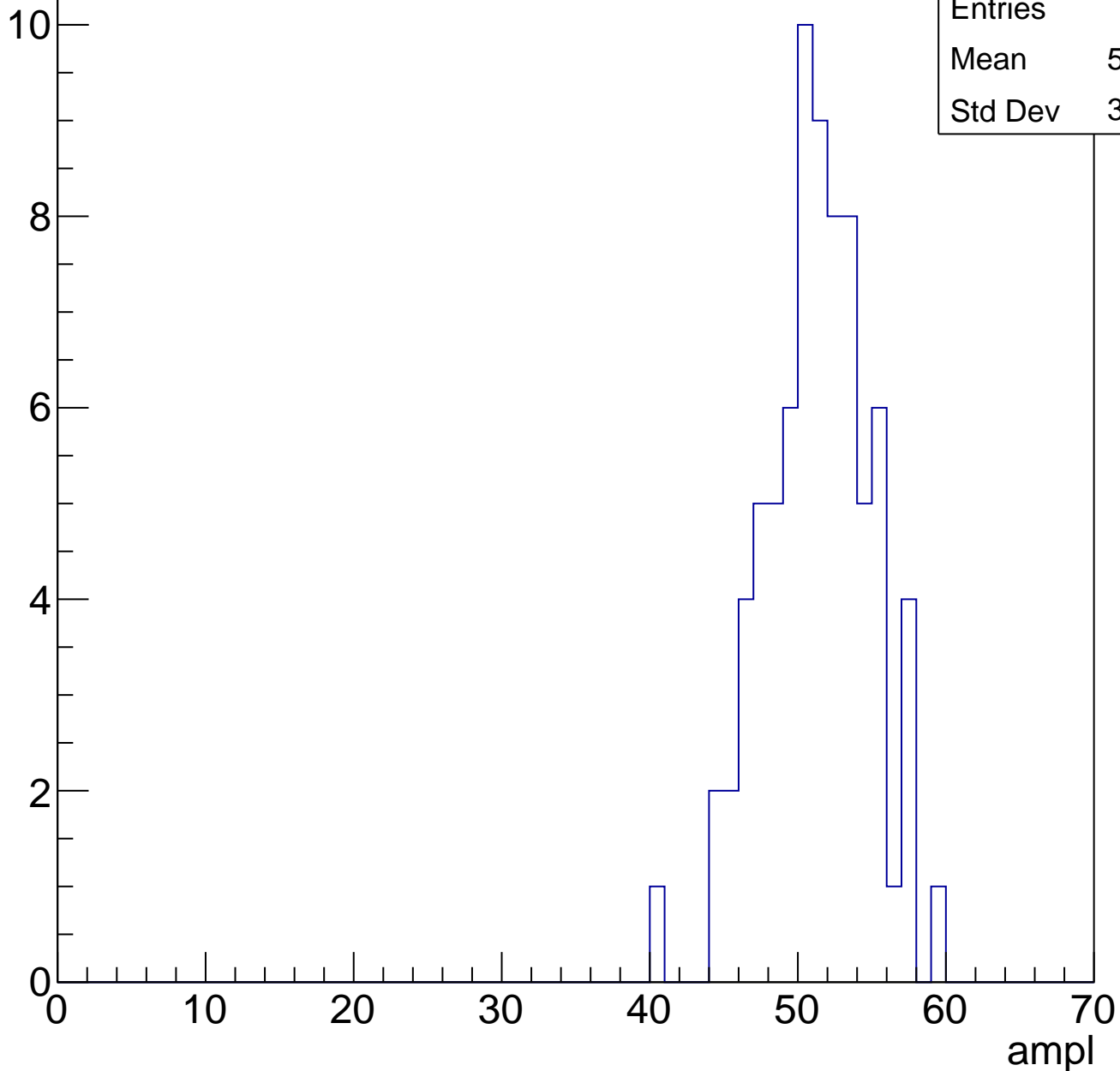


B1L100S, U9-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.82 |
| Std Dev | 3.534 |

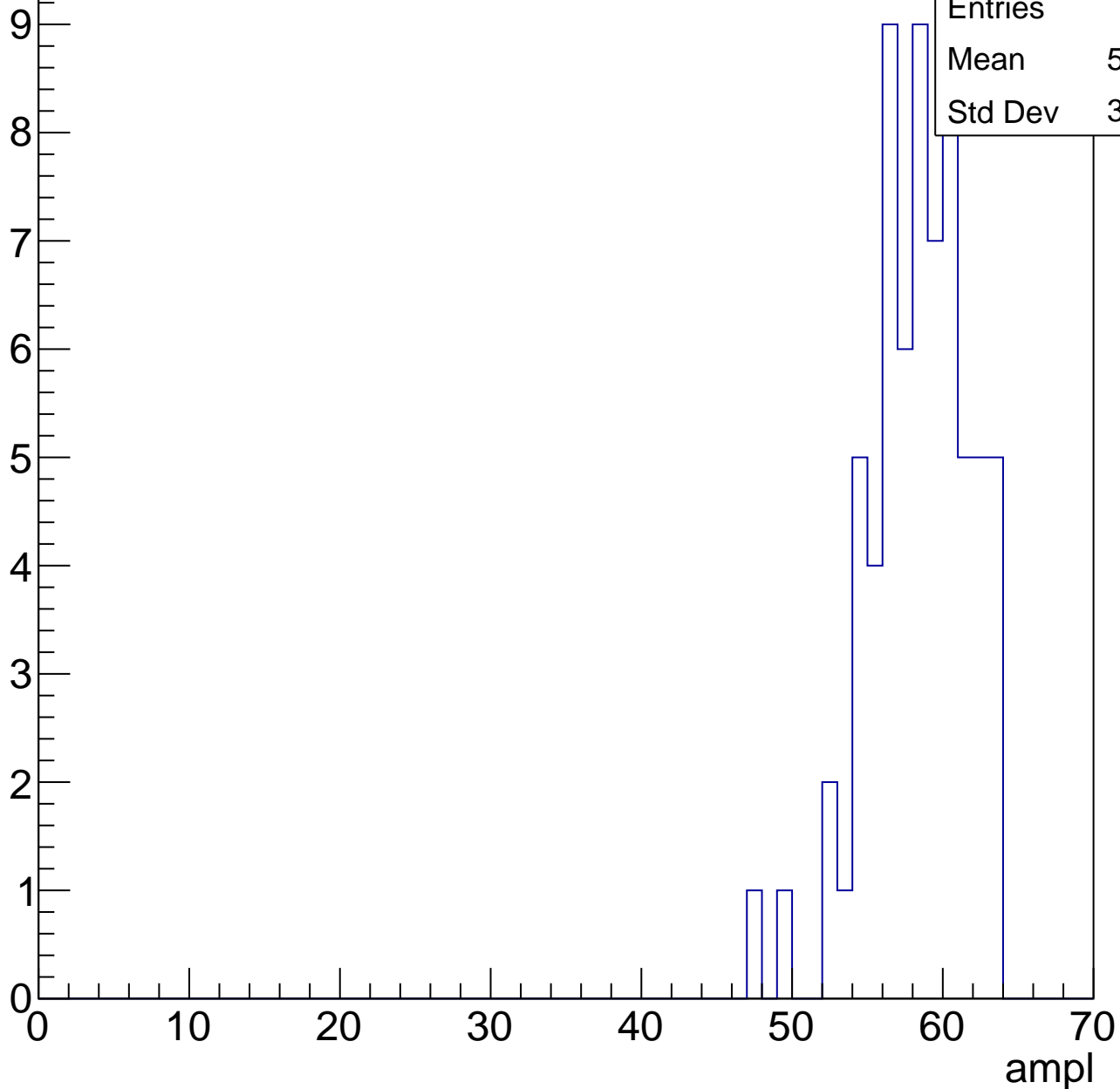
Entry



B1L100S, U9-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



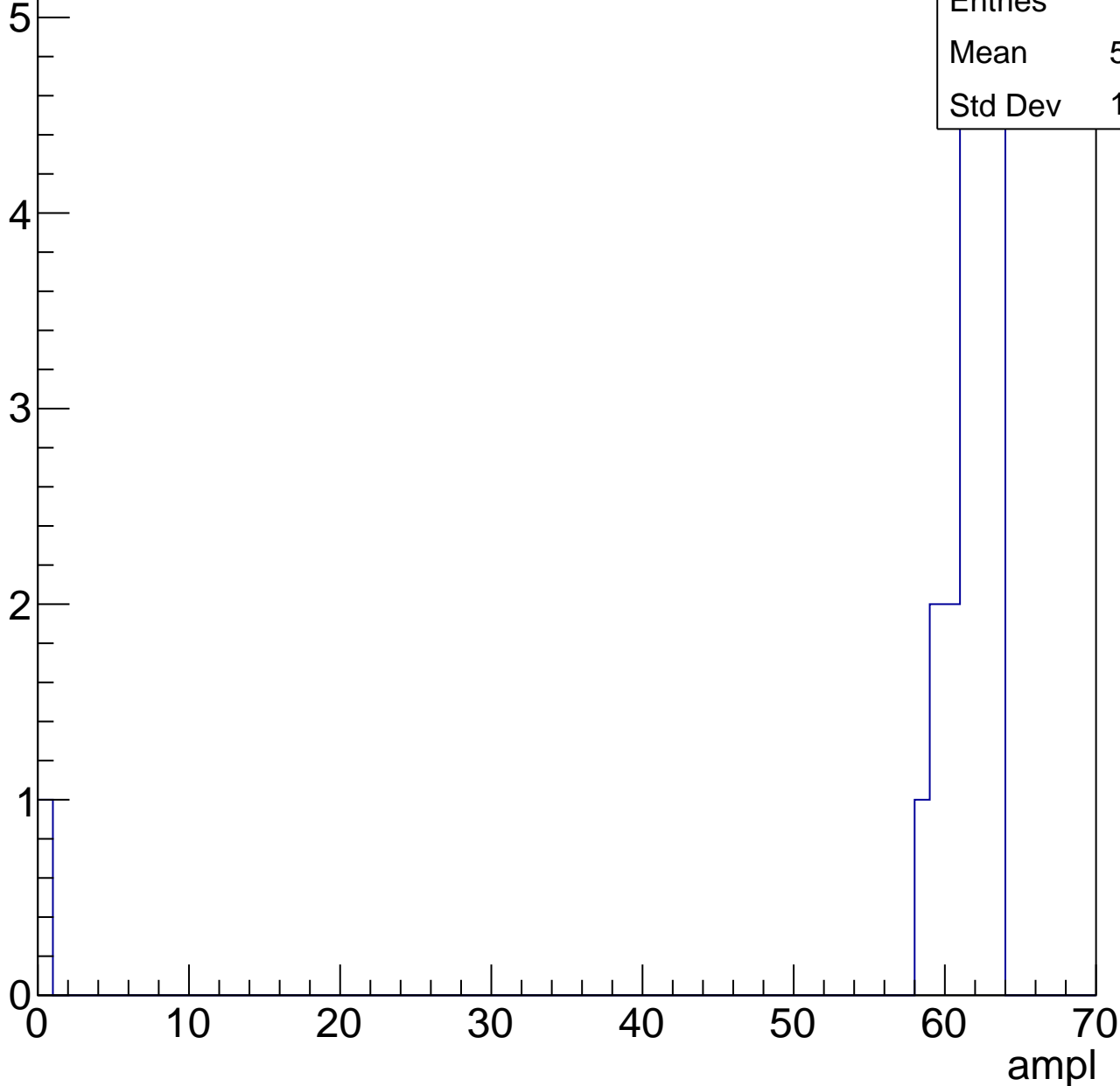
| | |
|---------|-------|
| Entries | 68 |
| Mean | 57.85 |
| Std Dev | 3.309 |

B1L100S, U9-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 21 |
| Mean | 58.38 |
| Std Dev | 13.13 |



B1L100S, U9-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch88, adc0

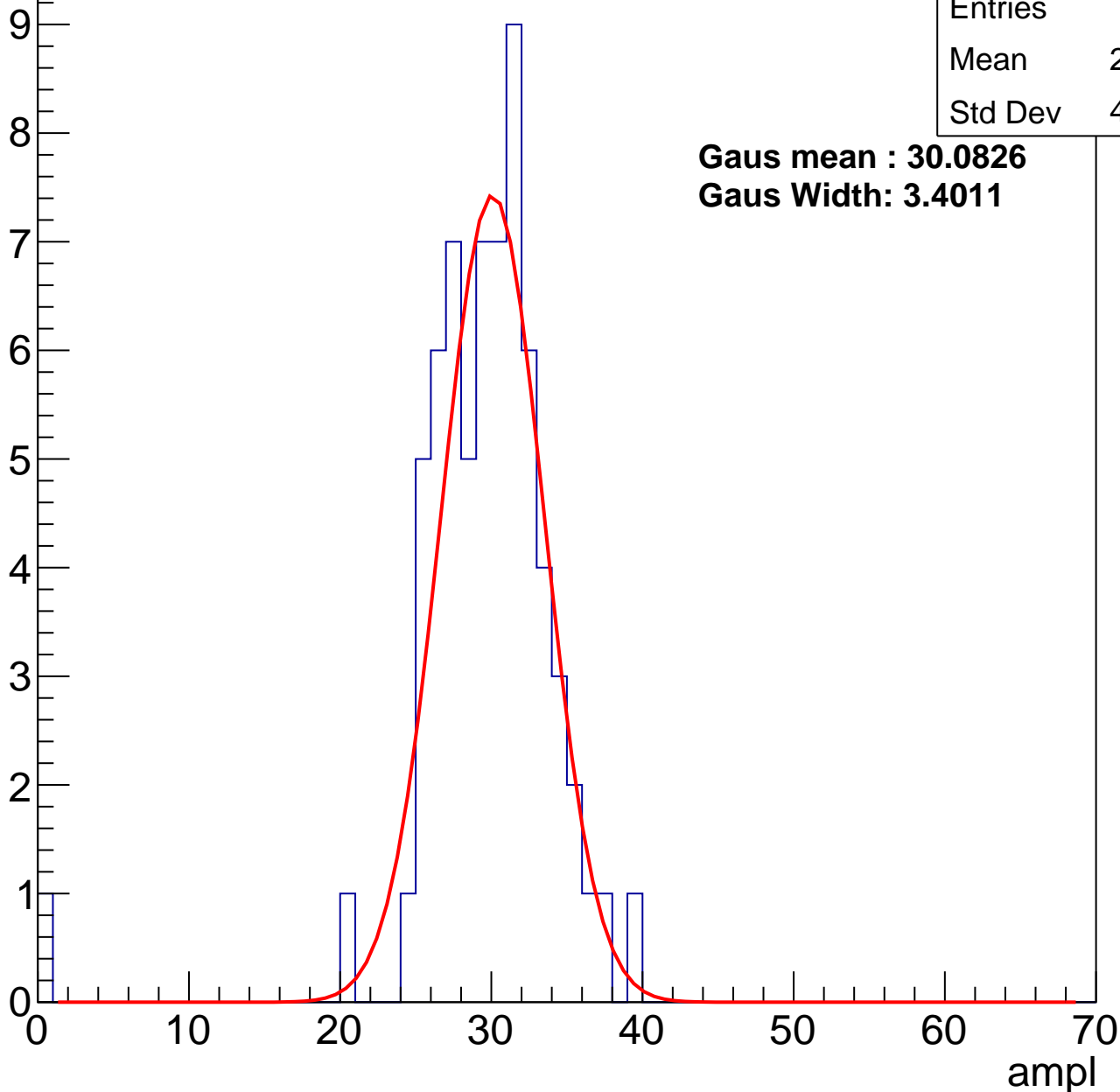
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 29.16 |
| Std Dev | 4.937 |

Gaus mean : 30.0826

Gaus Width: 3.4011



B1L100S, U9-ch88, adc1

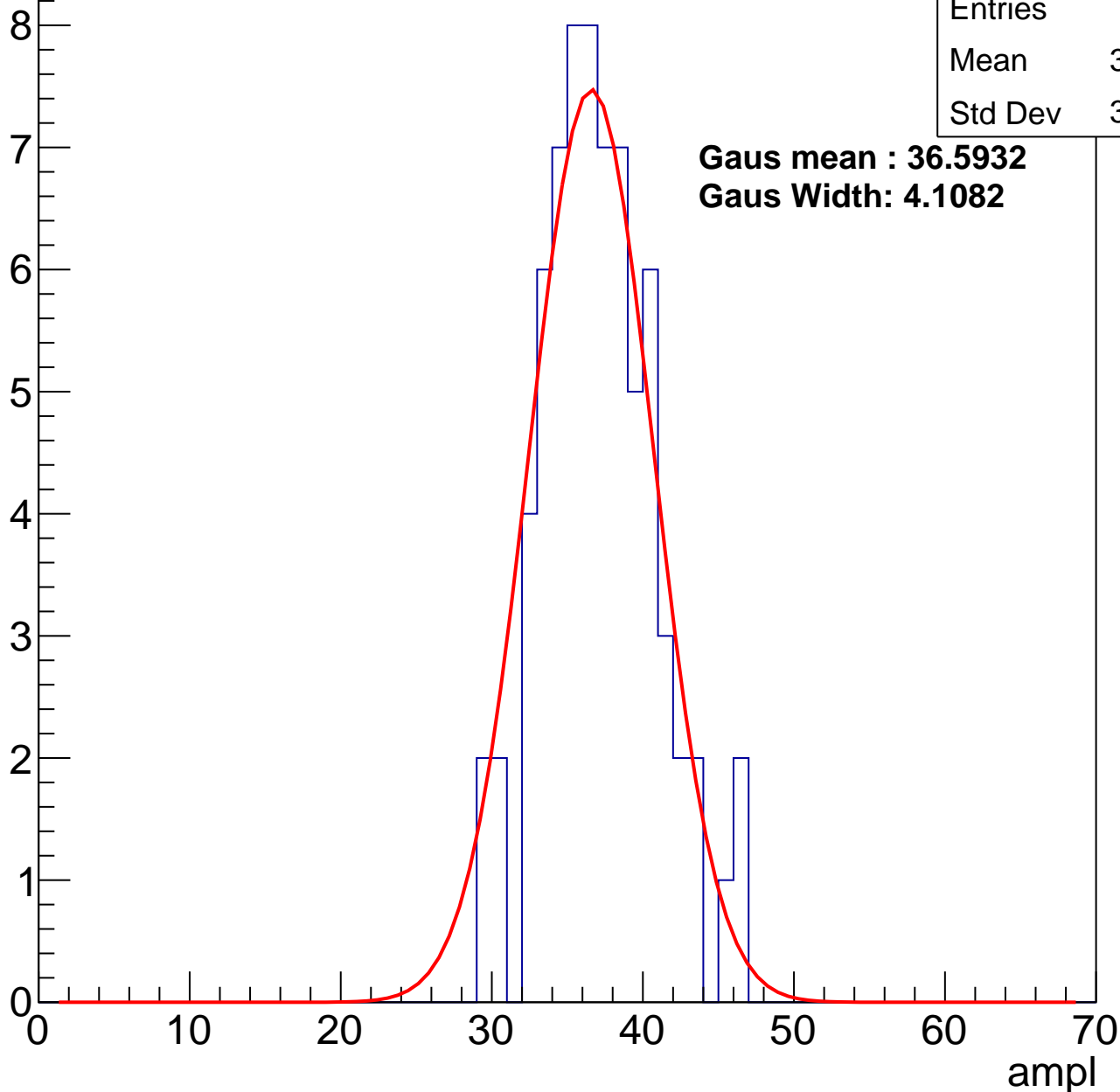
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.67 |
| Std Dev | 3.716 |

Gaus mean : 36.5932

Gaus Width: 4.1082



B1L100S, U9-ch88, adc2

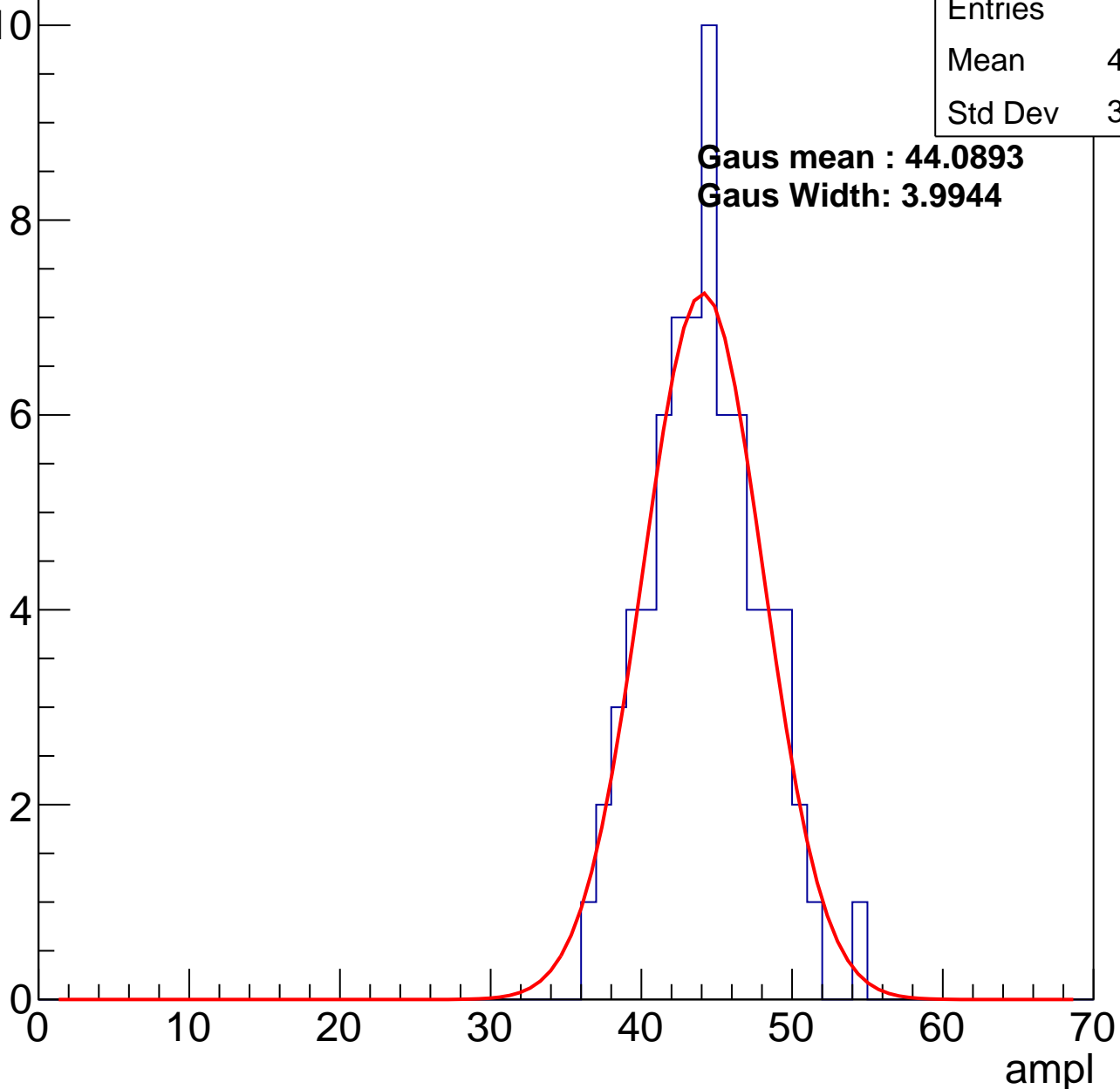
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 43.72 |
| Std Dev | 3.668 |

Gaus mean : 44.0893

Gaus Width: 3.9944

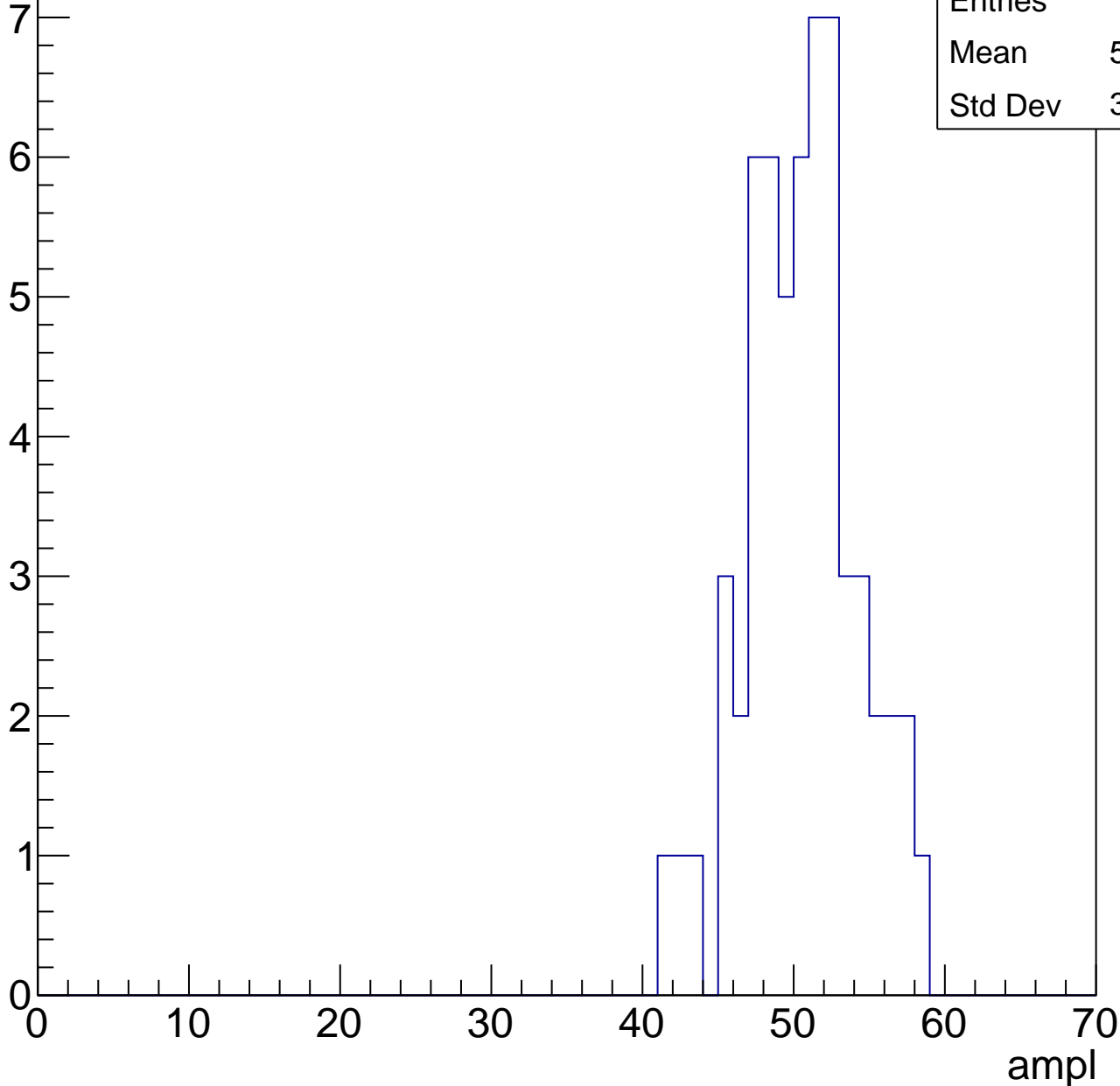


B1L100S, U9-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

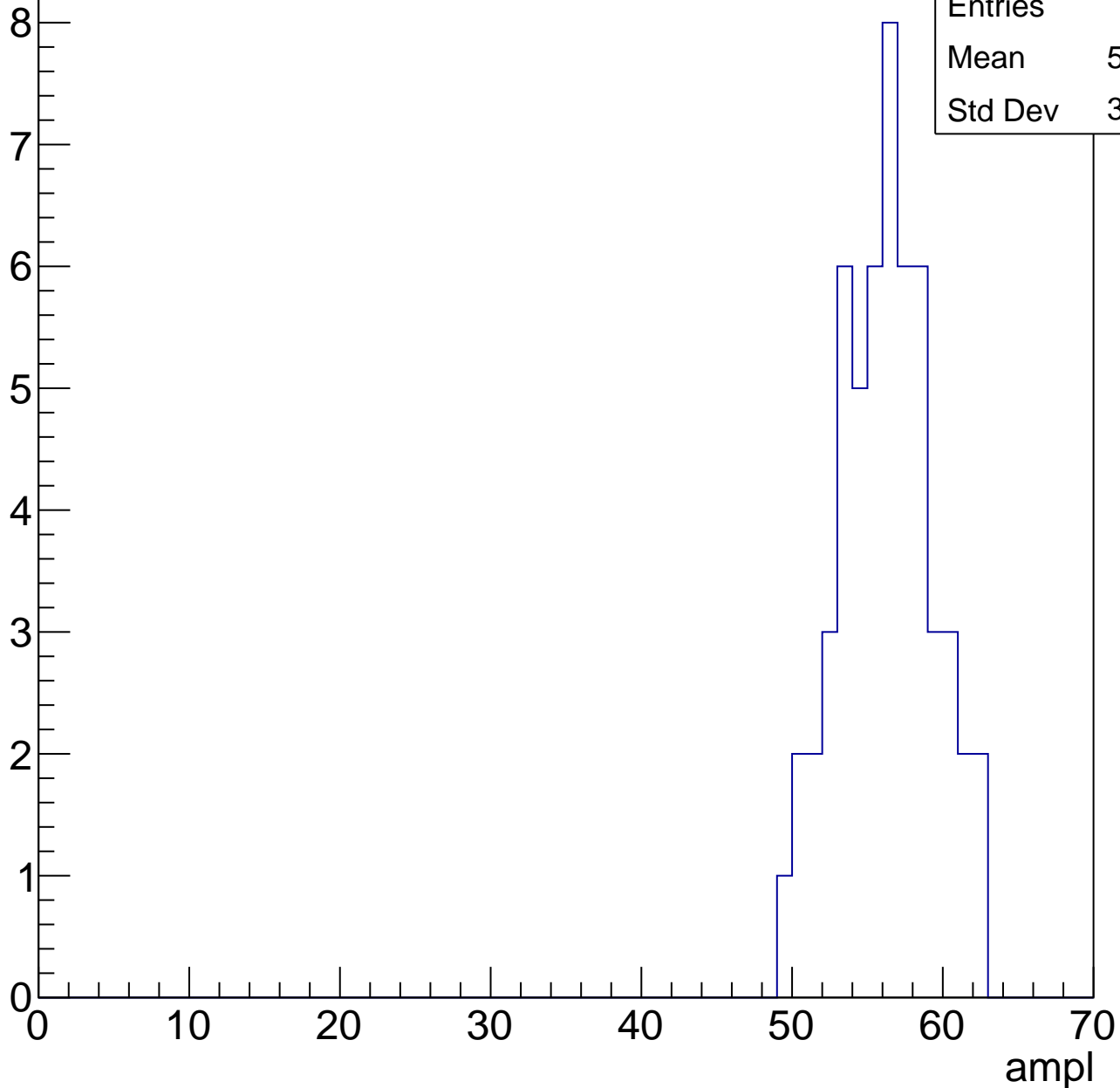
| | |
|---------|-------|
| Entries | 58 |
| Mean | 50.07 |
| Std Dev | 3.662 |



B1L100S, U9-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

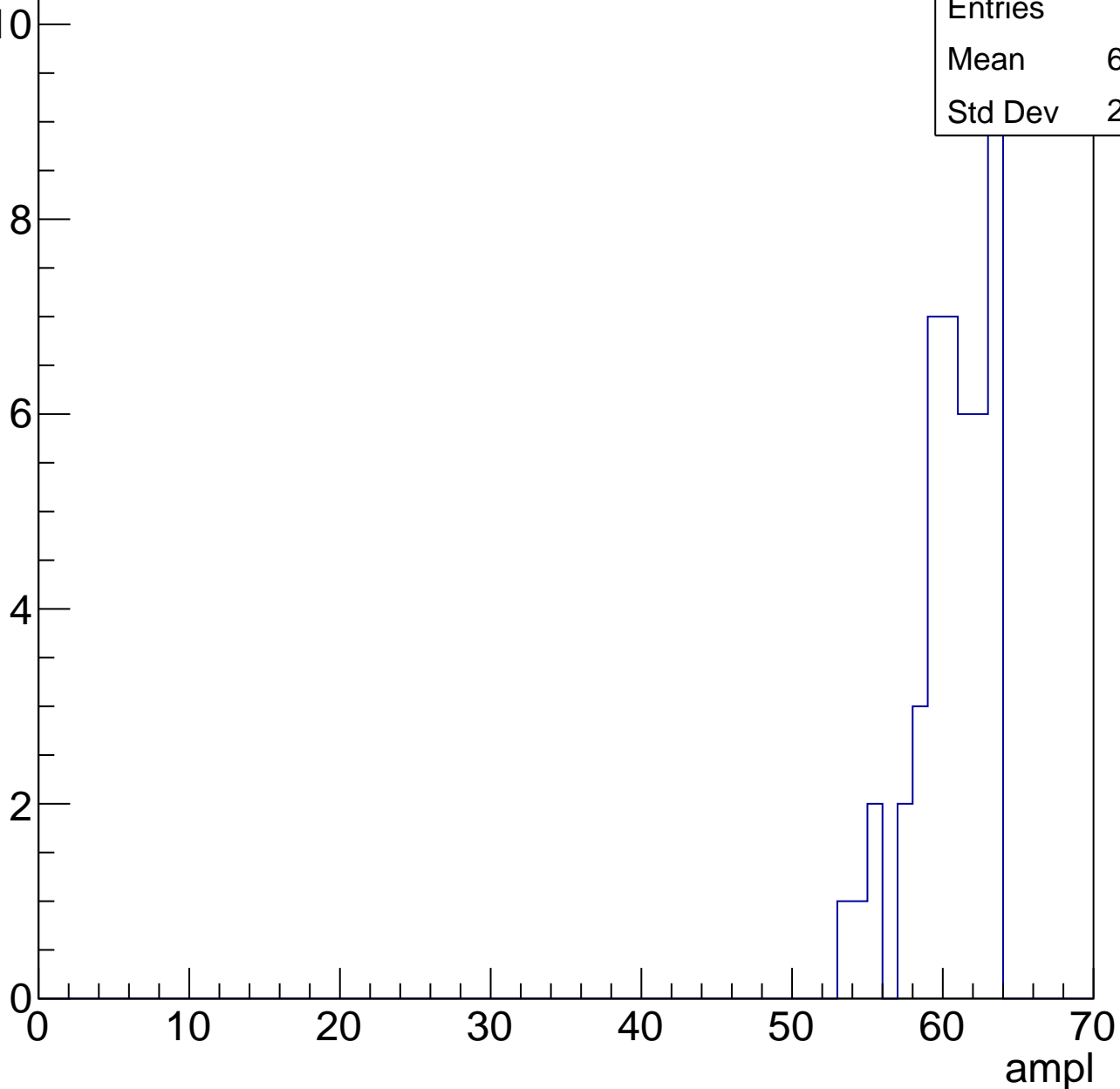


B1L100S, U9-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

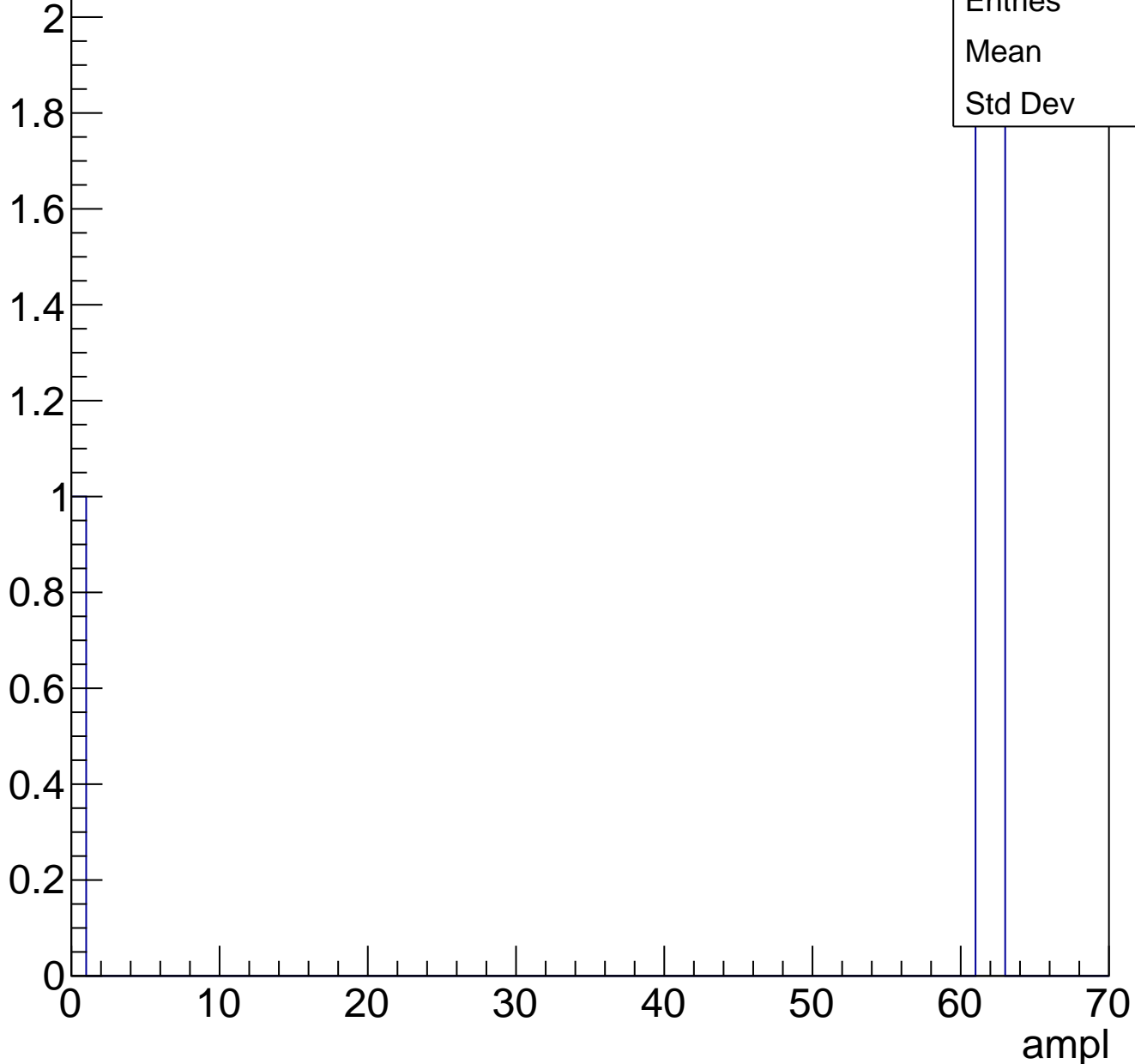
| | |
|---------|-------|
| Entries | 45 |
| Mean | 60.13 |
| Std Dev | 2.544 |



B1L100S, U9-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch89, adc0

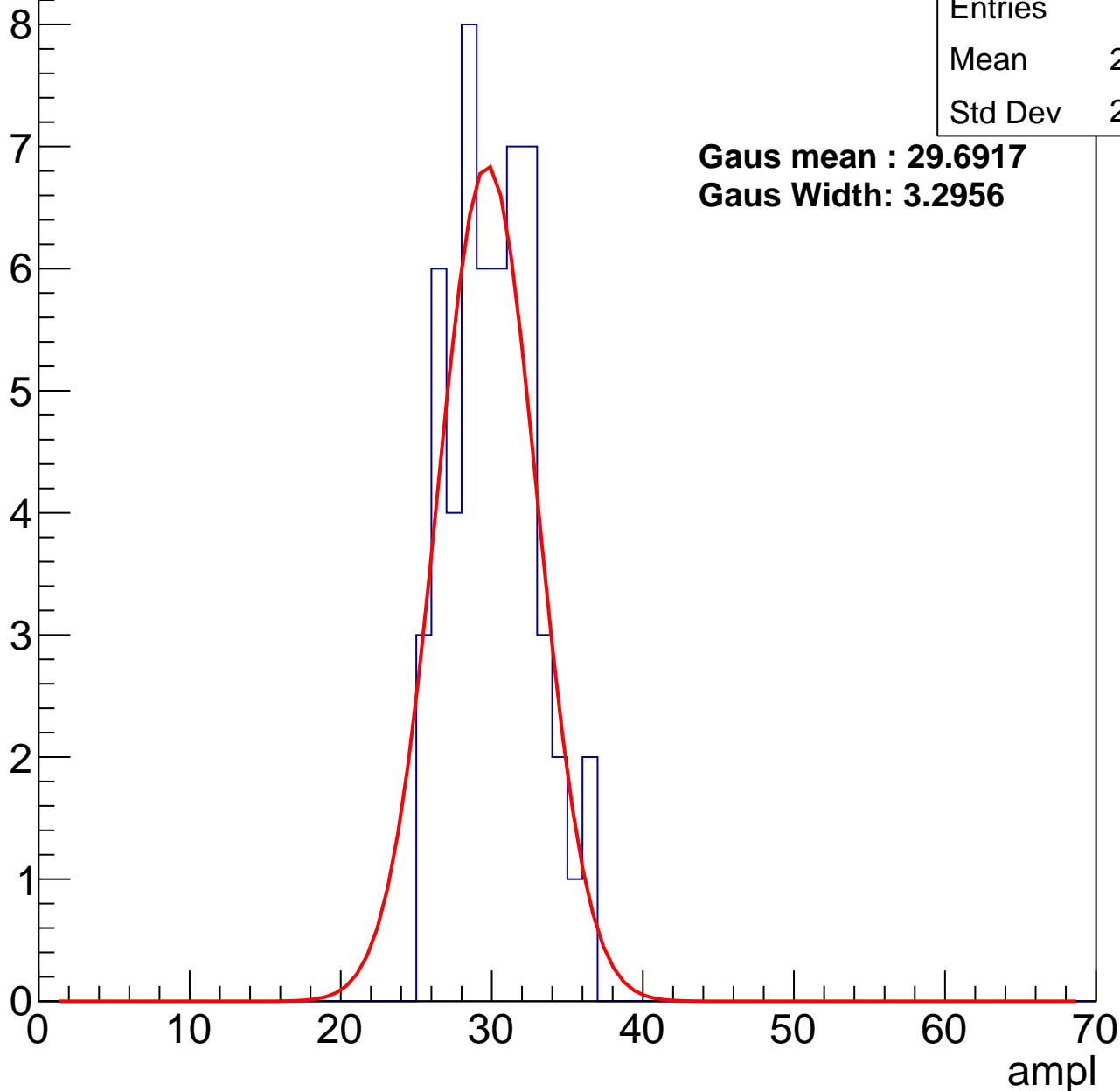
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 29.67 |
| Std Dev | 2.796 |

Gaus mean : 29.6917

Gaus Width: 3.2956



B1L100S, U9-ch89, adc1

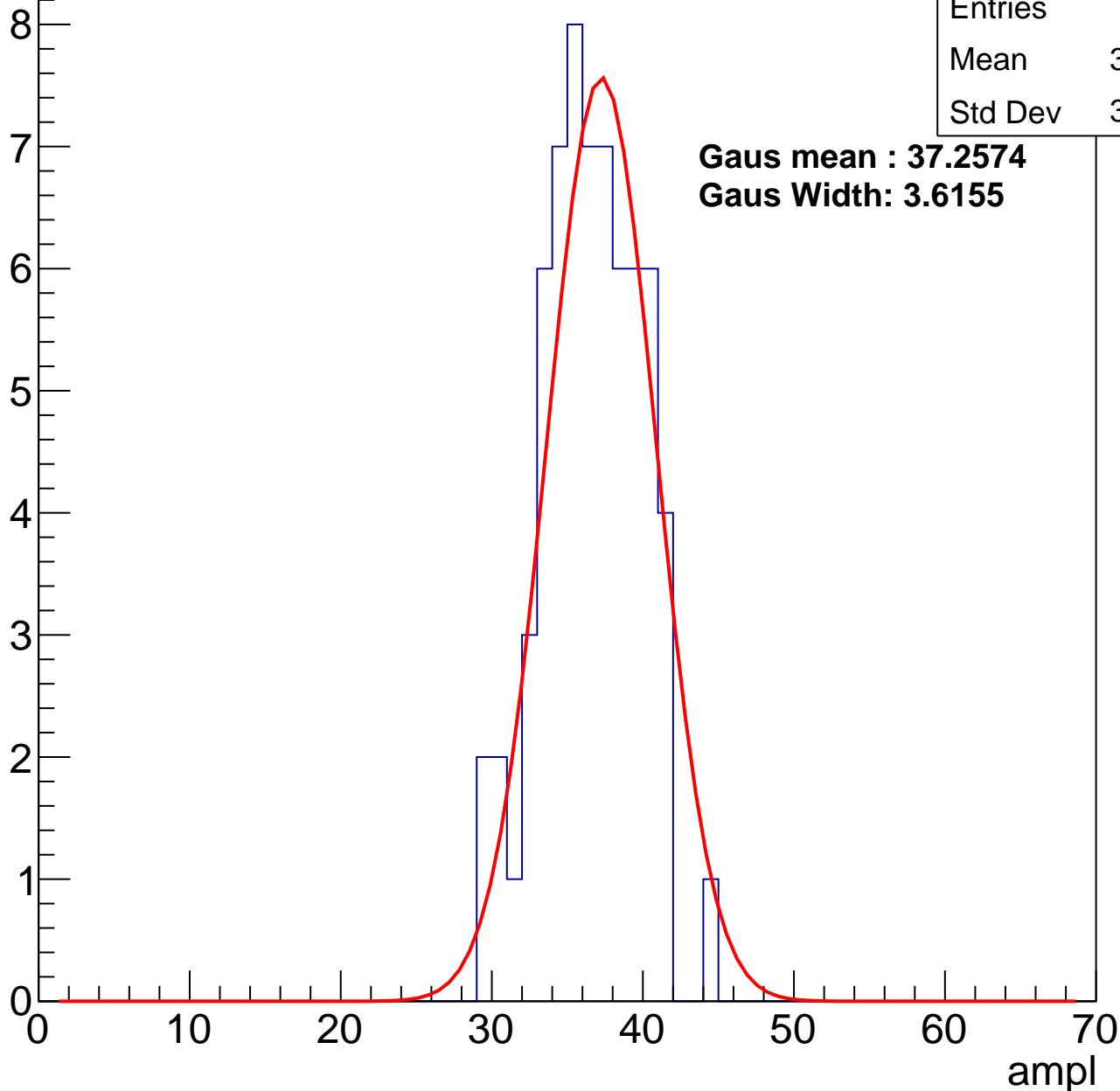
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.09 |
| Std Dev | 3.204 |

Gaus mean : 37.2574

Gaus Width: 3.6155



B1L100S, U9-ch89, adc2

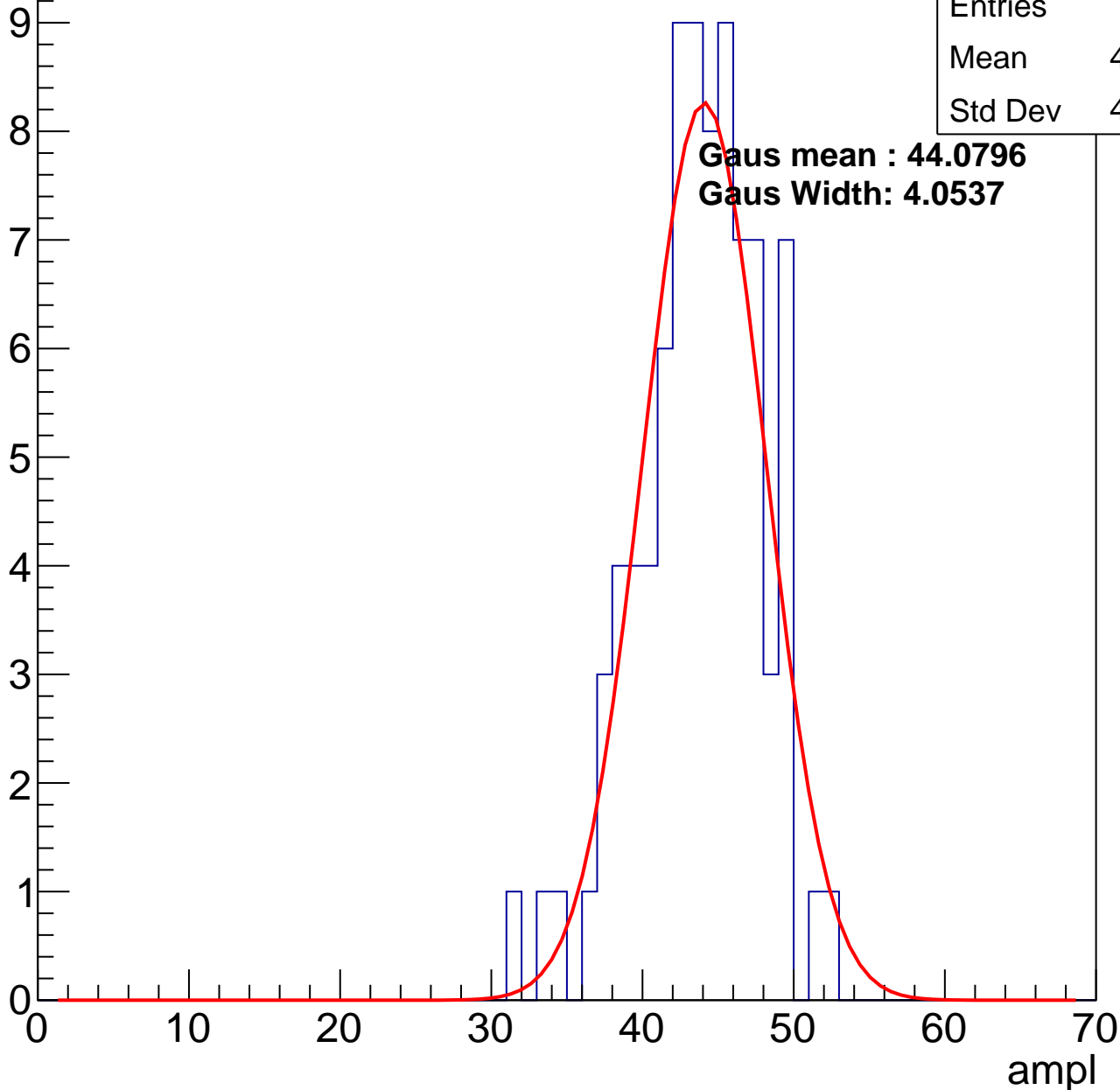
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 43.28 |
| Std Dev | 4.034 |

Gaus mean : 44.0796

Gaus Width: 4.0537

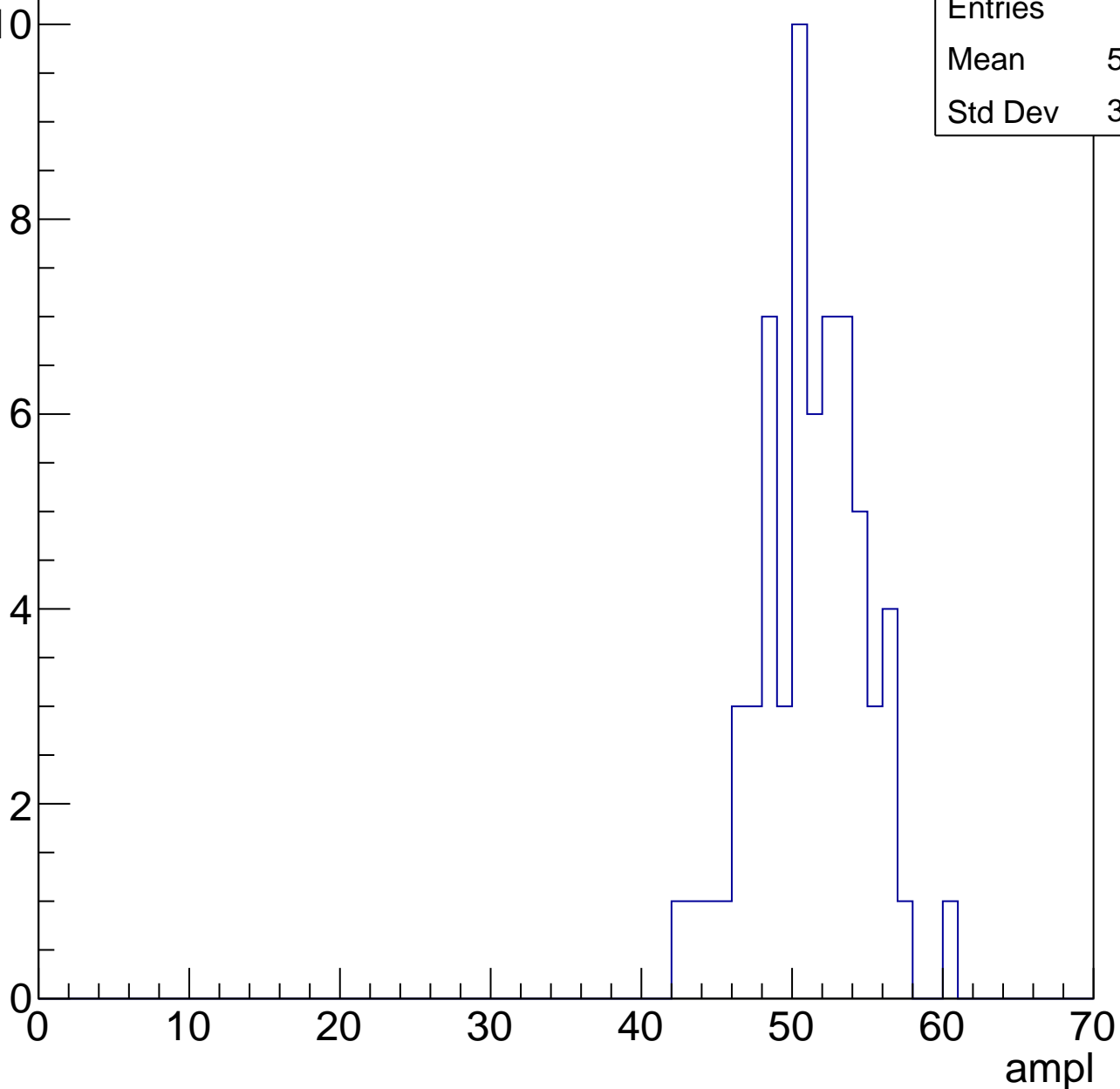


B1L100S, U9-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

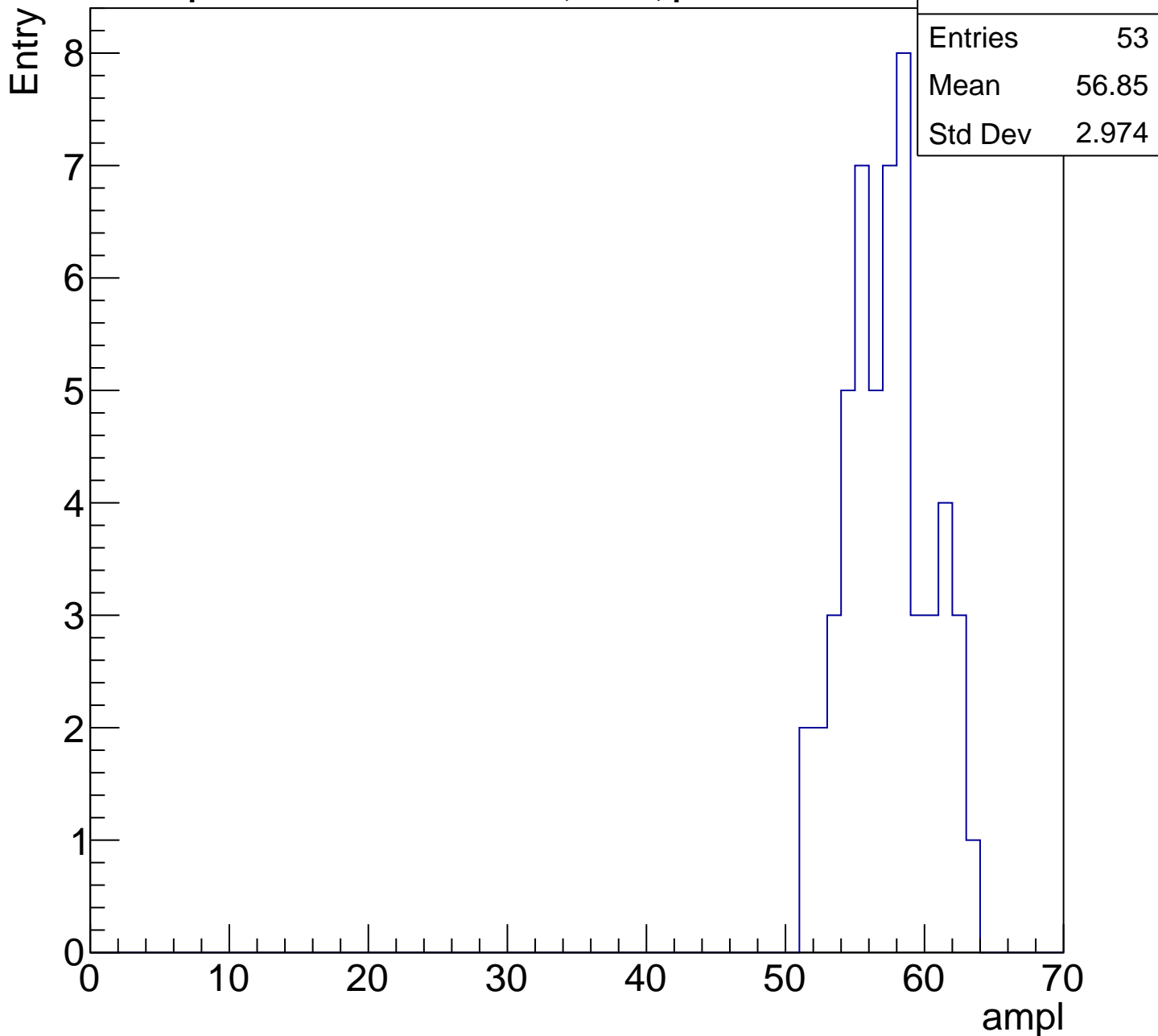
Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.83 |
| Std Dev | 3.494 |



B1L100S, U9-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

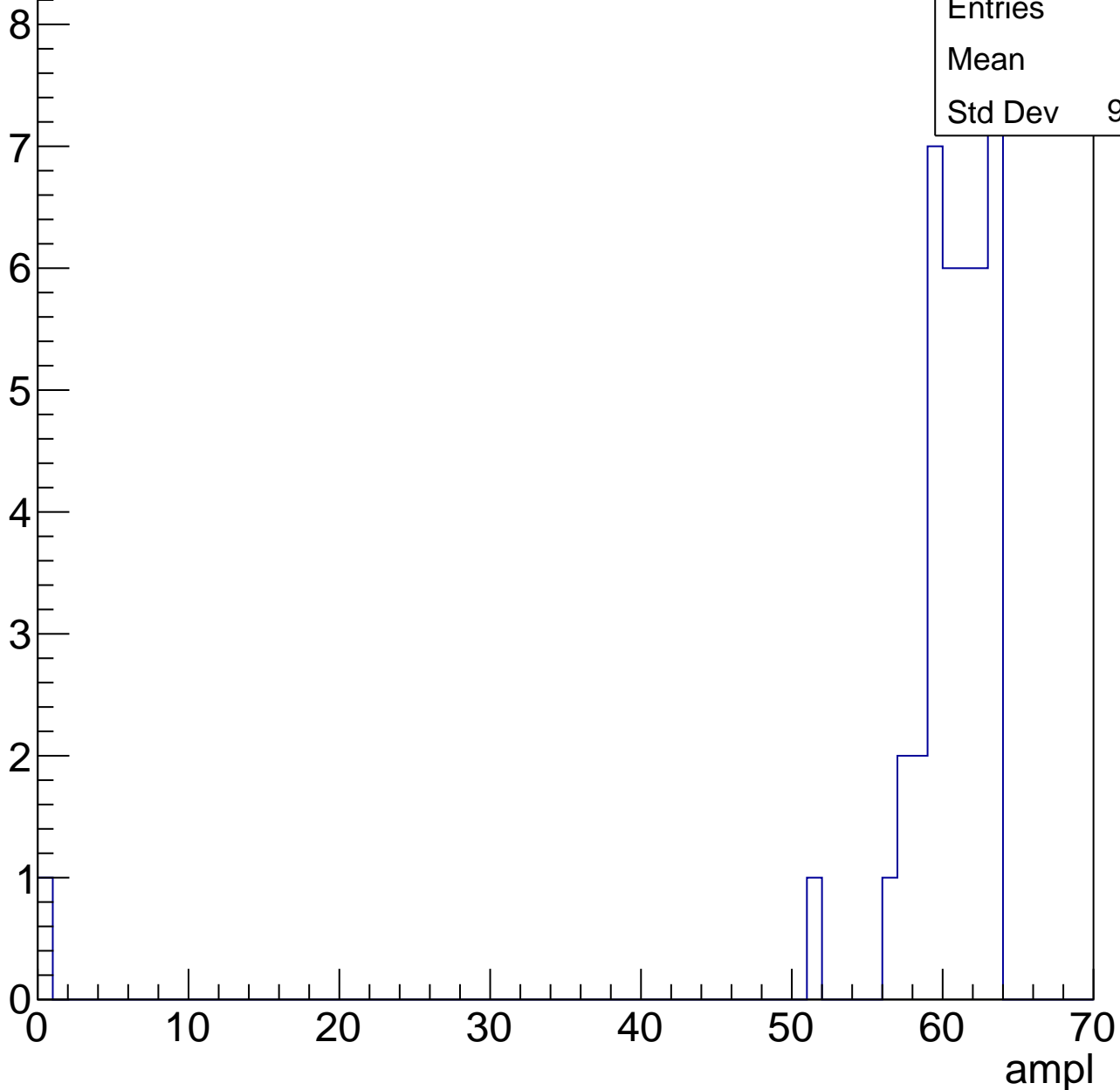


B1L100S, U9-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.8 |
| Std Dev | 9.714 |



B1L100S, U9-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

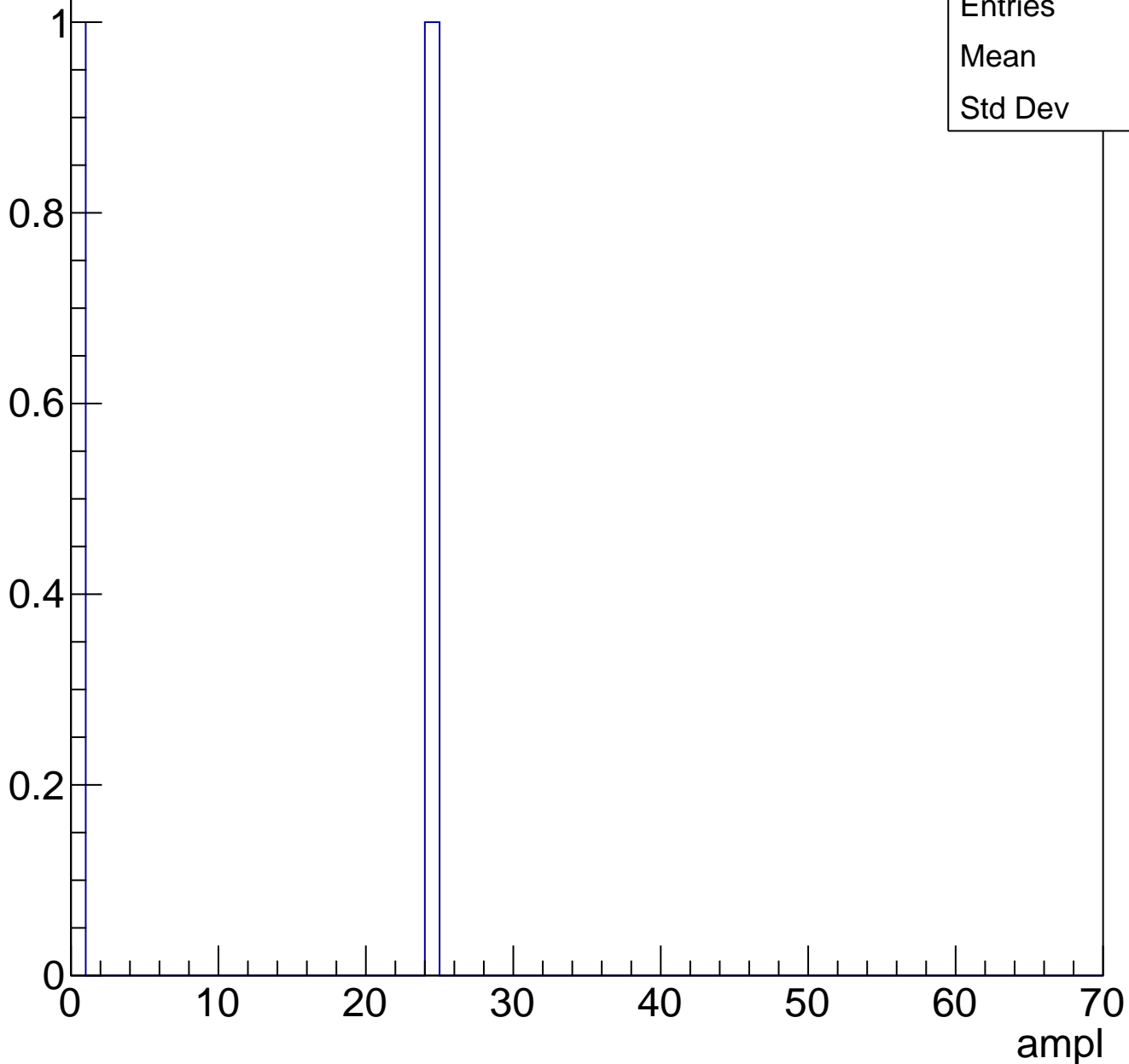
Entry



B1L100S, U9-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch90, adc0

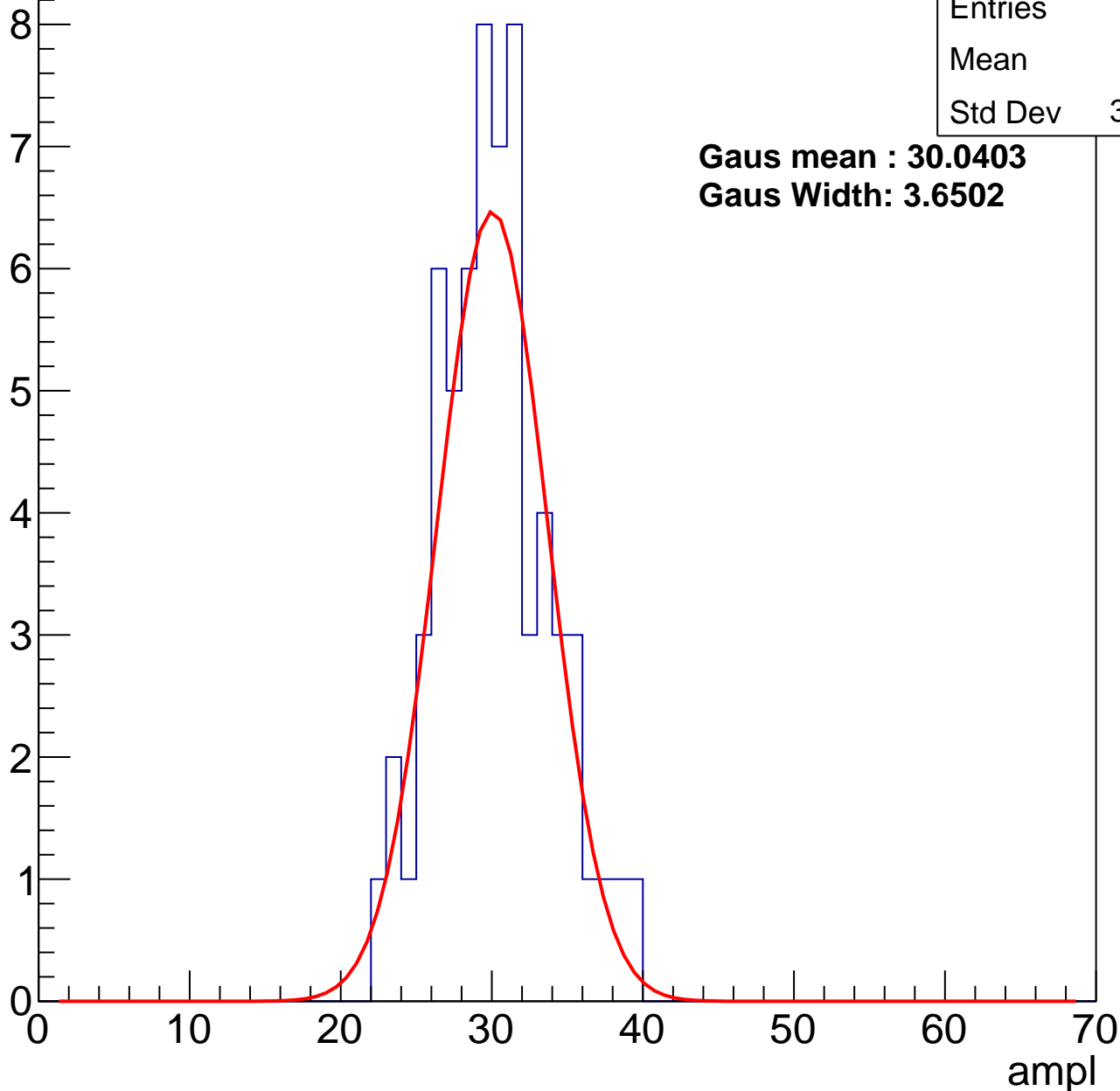
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.7 |
| Std Dev | 3.643 |

Gaus mean : 30.0403

Gaus Width: 3.6502



B1L100S, U9-ch90, adc1

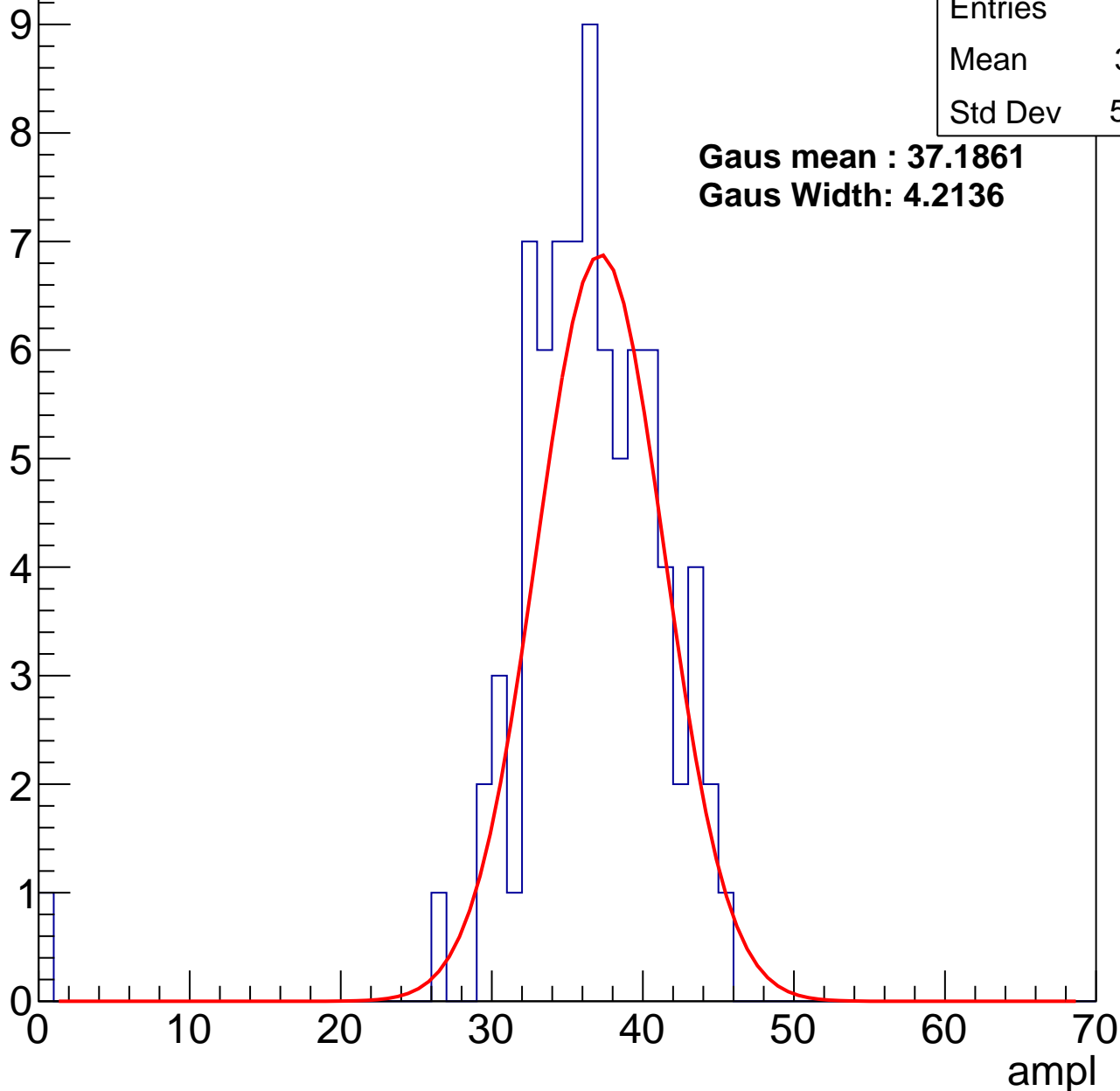
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 35.91 |
| Std Dev | 5.679 |

Gaus mean : 37.1861

Gaus Width: 4.2136



B1L100S, U9-ch90, adc2

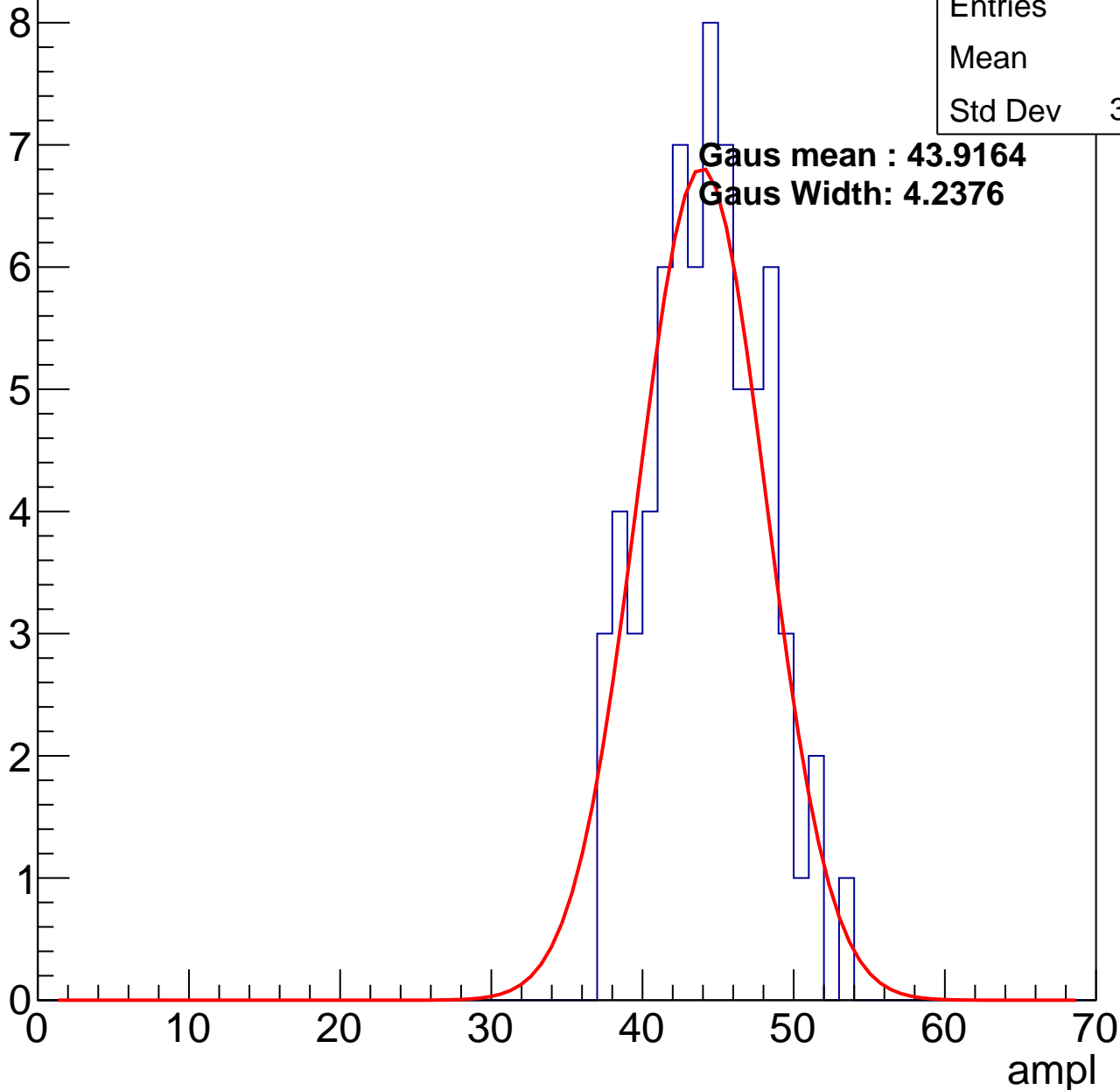
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 43.8 |
| Std Dev | 3.702 |

Gaus mean : 43.9164

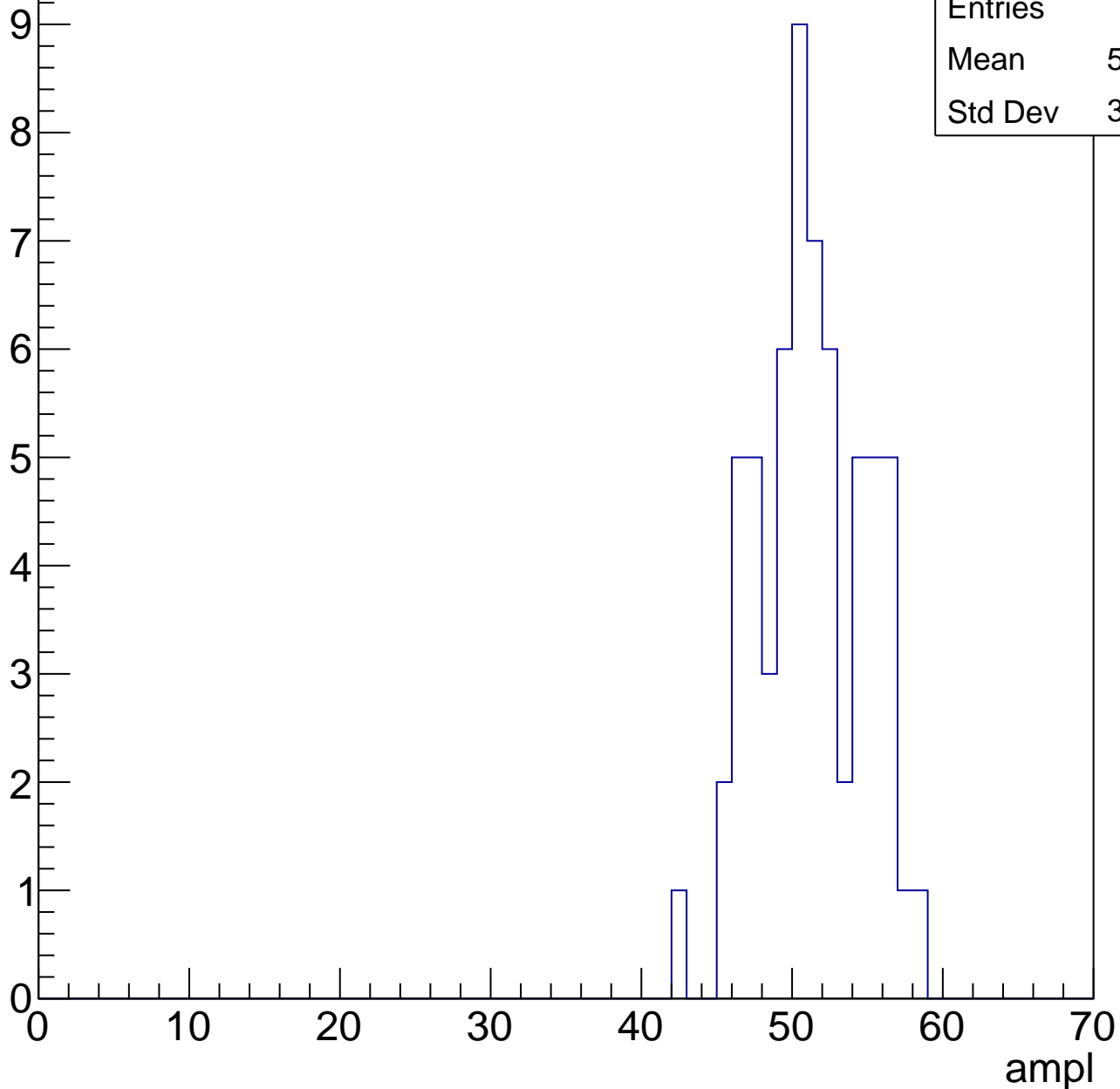
Gaus Width: 4.2376



B1L100S, U9-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

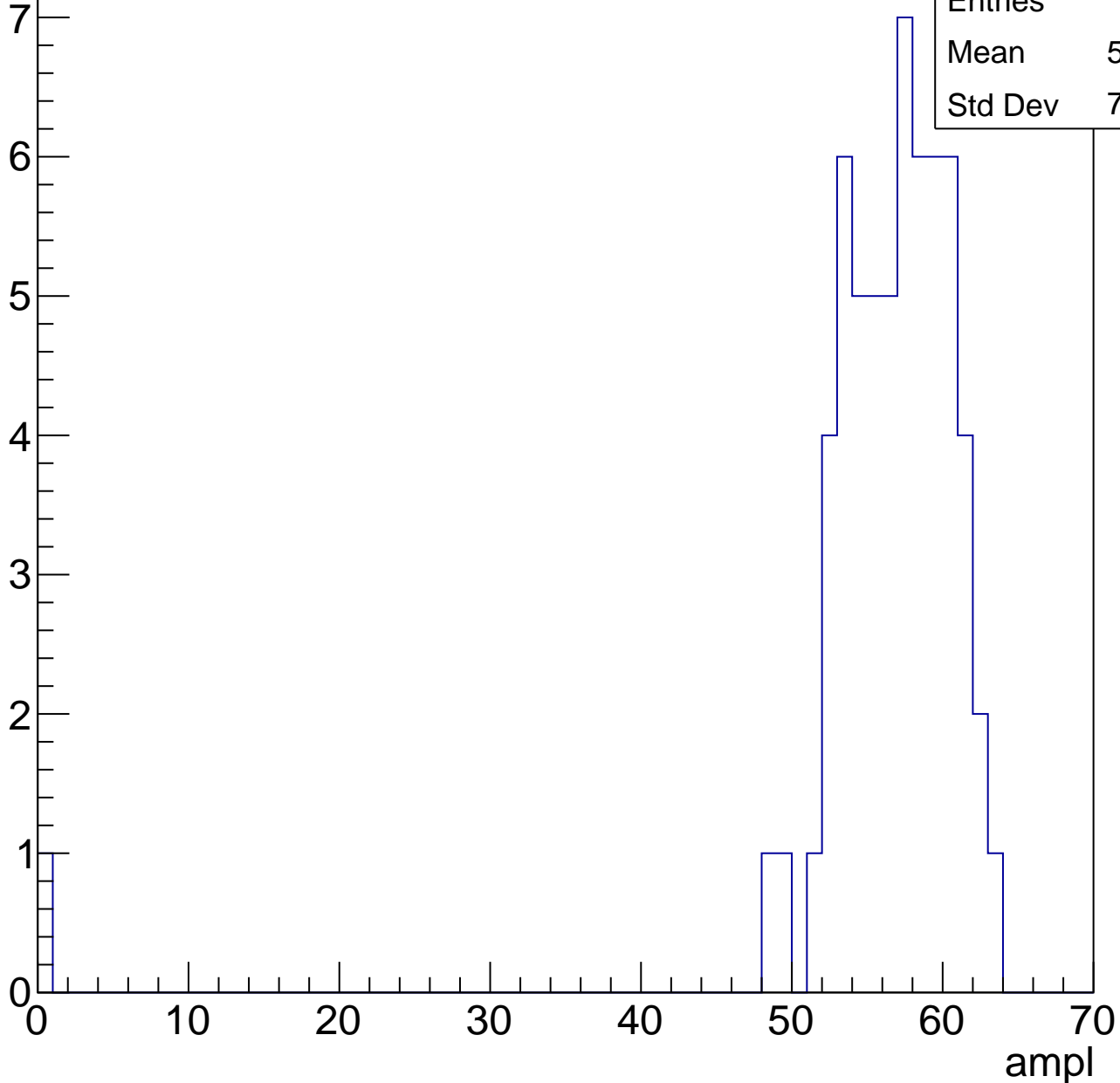


B1L100S, U9-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.59 |
| Std Dev | 7.902 |

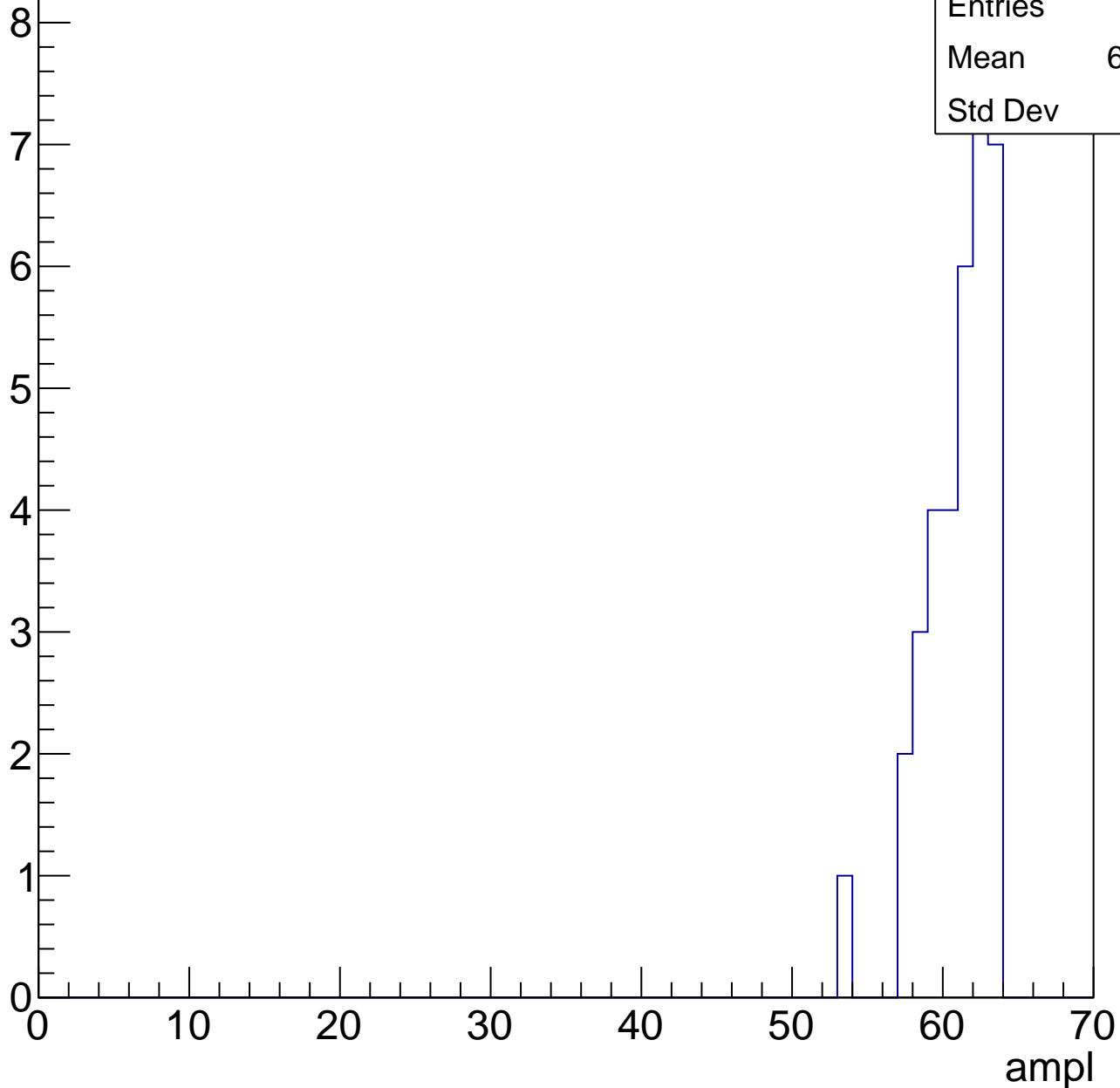


B1L100S, U9-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.57 |
| Std Dev | 2.22 |



B1L100S, U9-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch91, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 29.49 |
| Std Dev | 3.511 |

Gaus mean : 30.1459

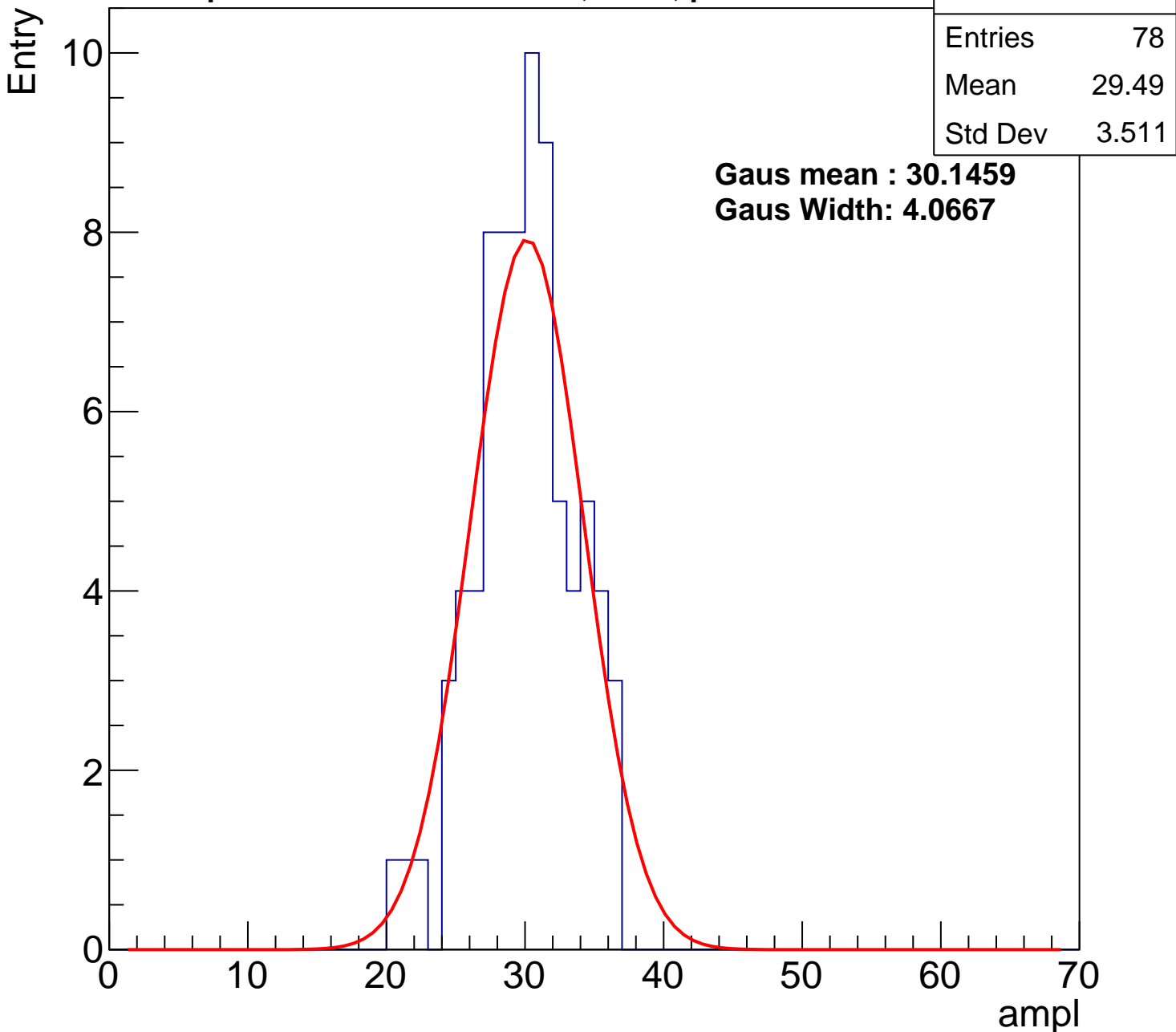
Gaus Width: 4.0667

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch91, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 37.52 |
| Std Dev | 3.545 |

Gaus mean : 38.1970

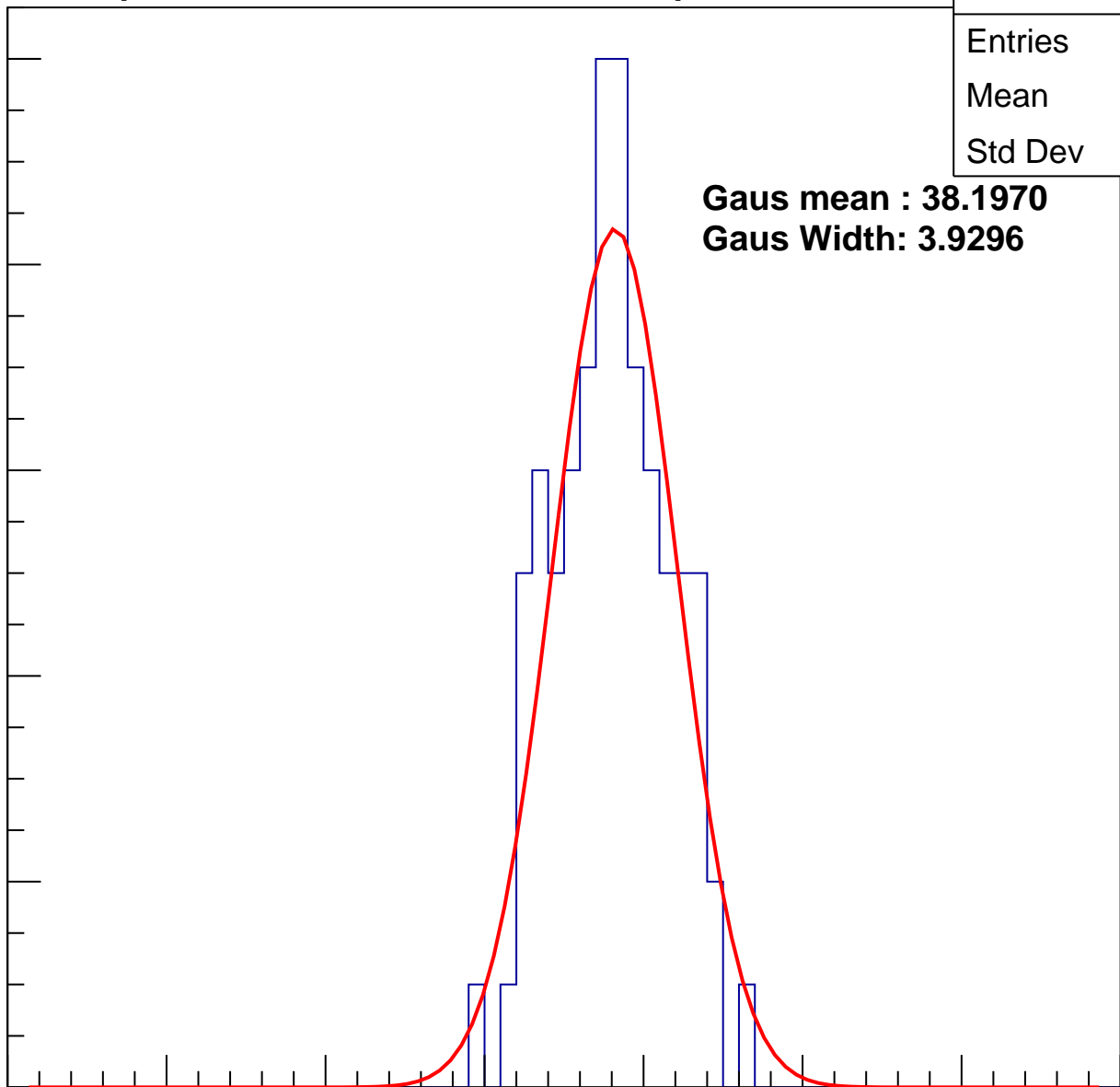
Gaus Width: 3.9296

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch91, adc2

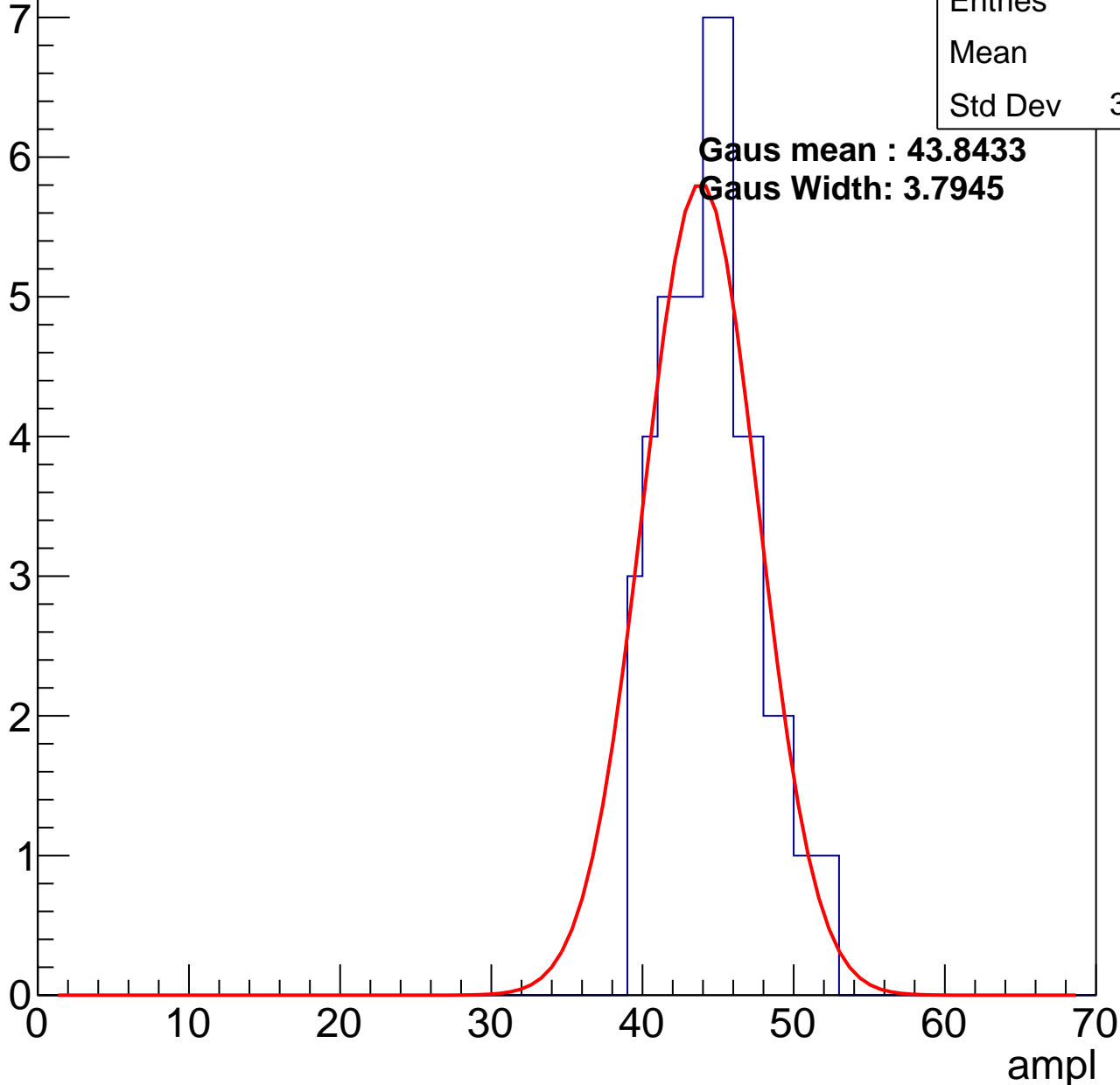
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 44.1 |
| Std Dev | 3.126 |

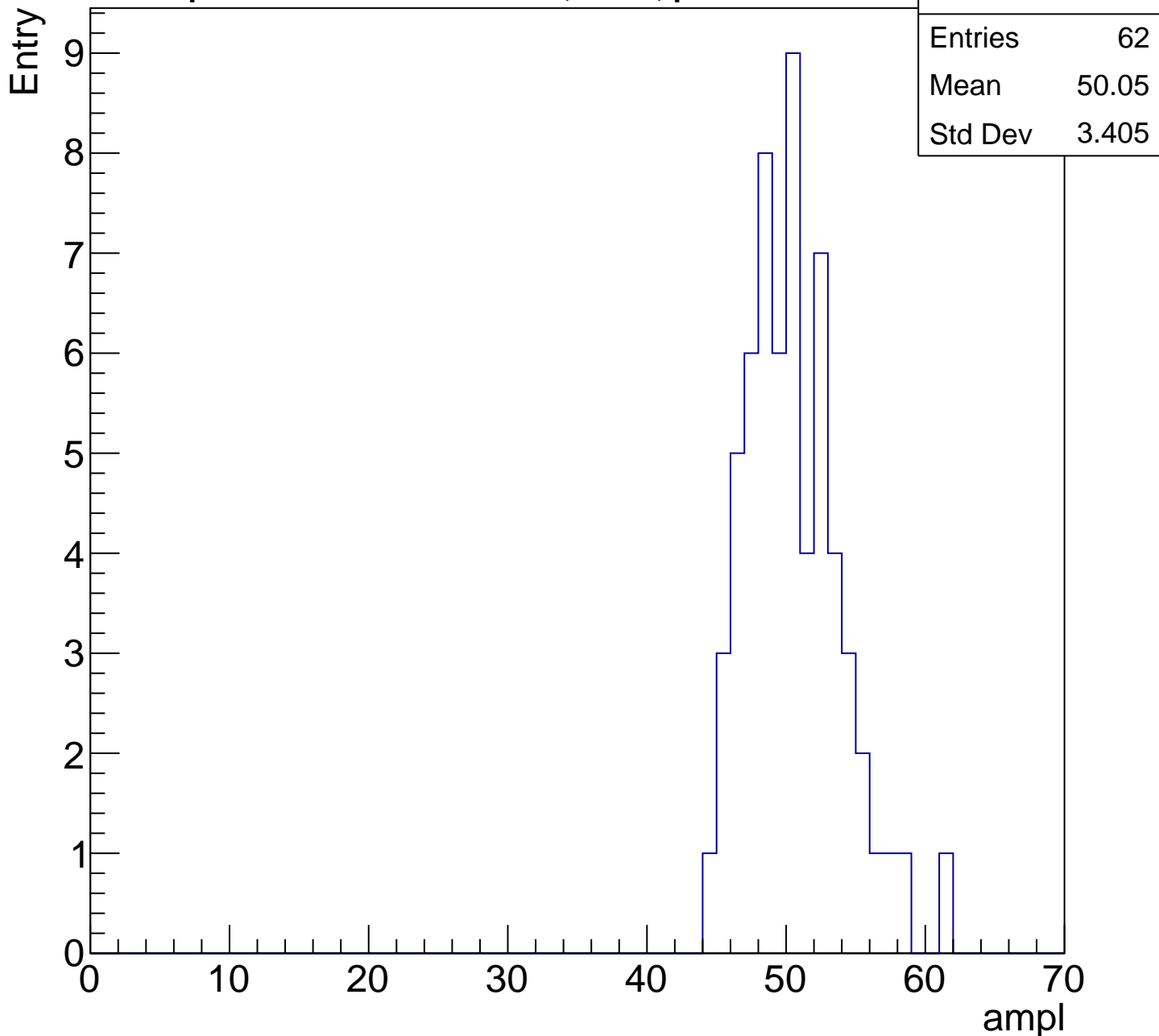
Gaus mean : 43.8433

Gaus Width: 3.7945



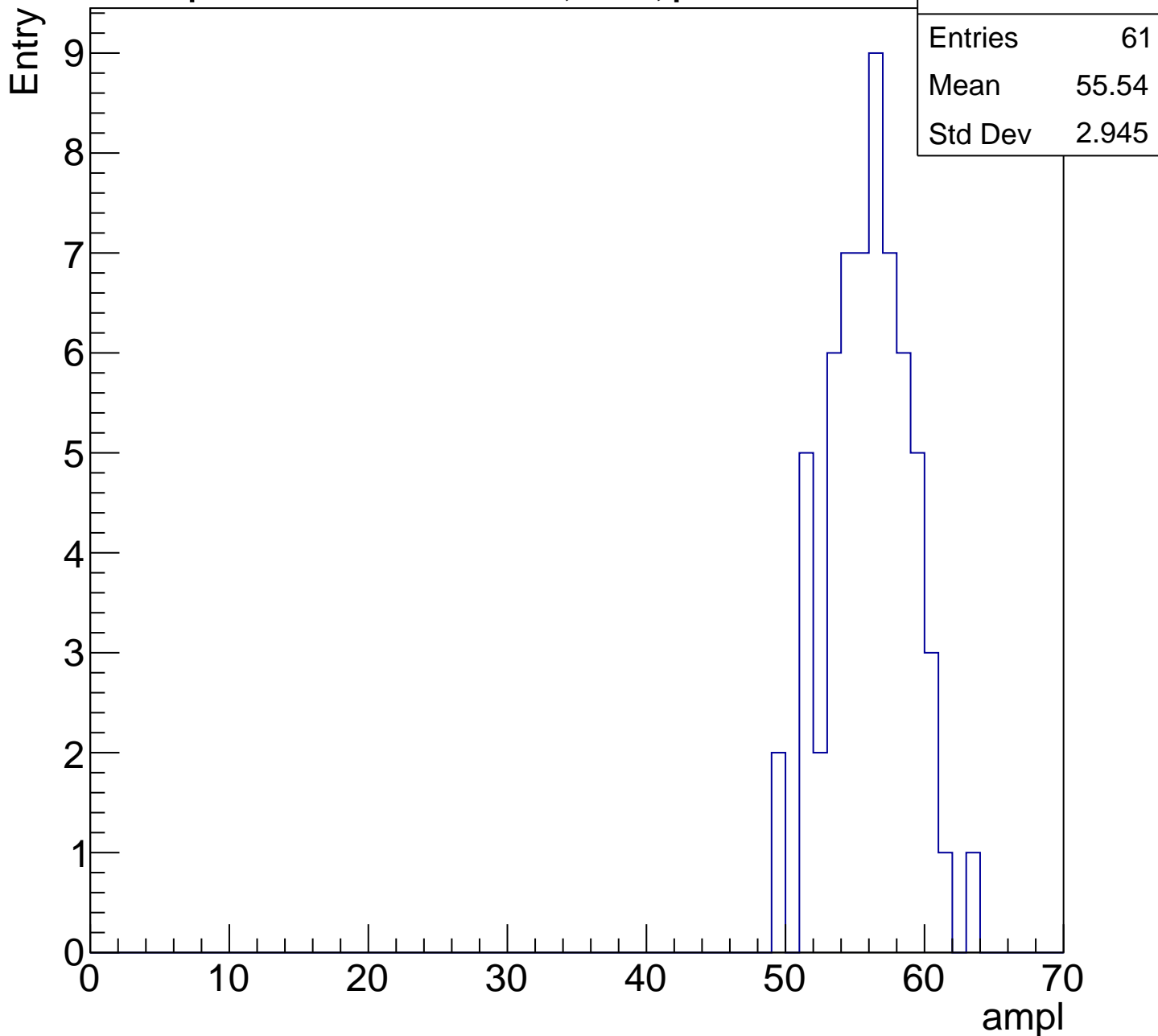
B1L100S, U9-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

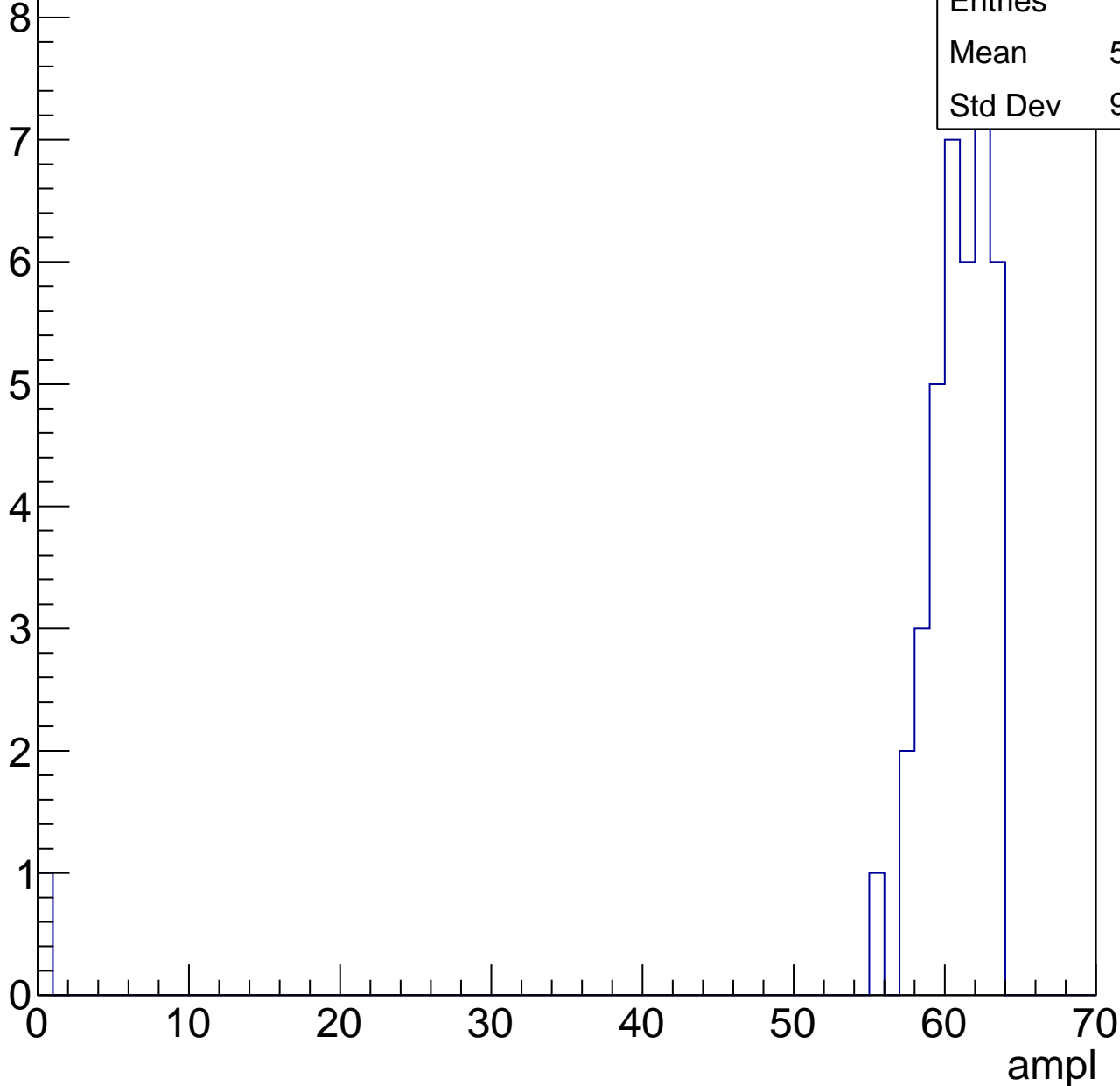


B1L100S, U9-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

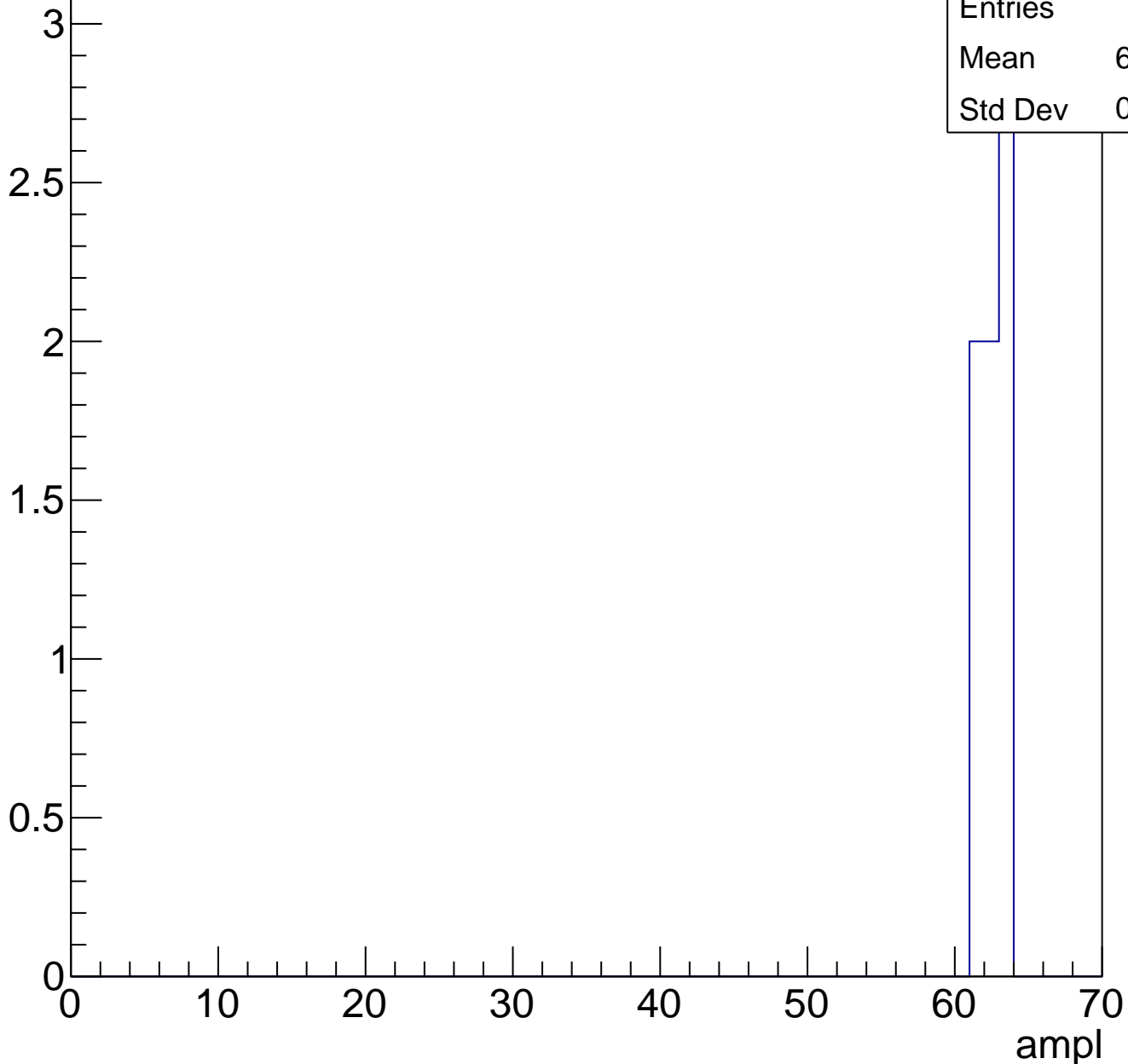
| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.92 |
| Std Dev | 9.749 |



B1L100S, U9-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

B1L100S, U9-ch92, adc0

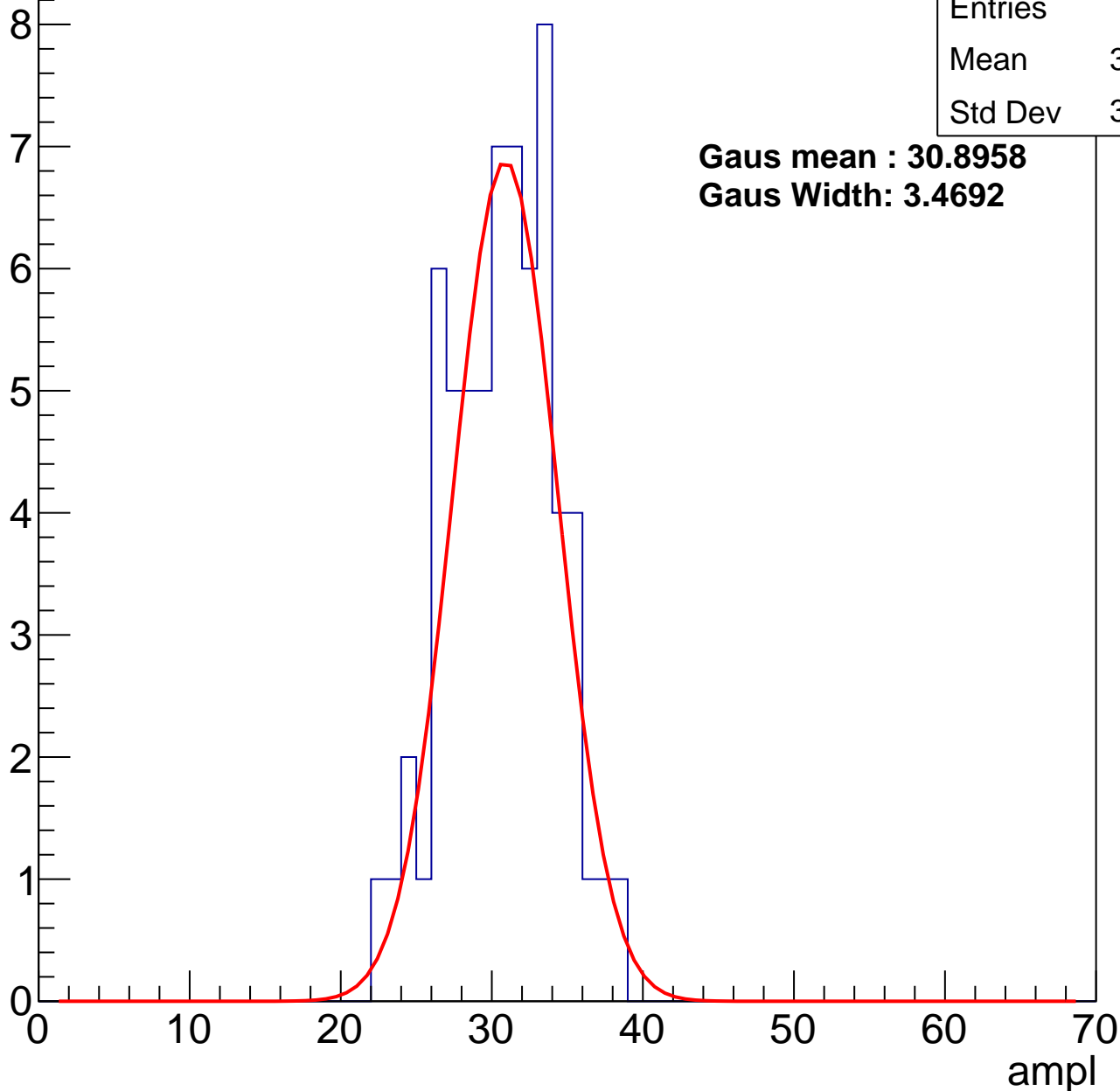
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 30.22 |
| Std Dev | 3.484 |

Gaus mean : 30.8958

Gaus Width: 3.4692



B1L100S, U9-ch92, adc1

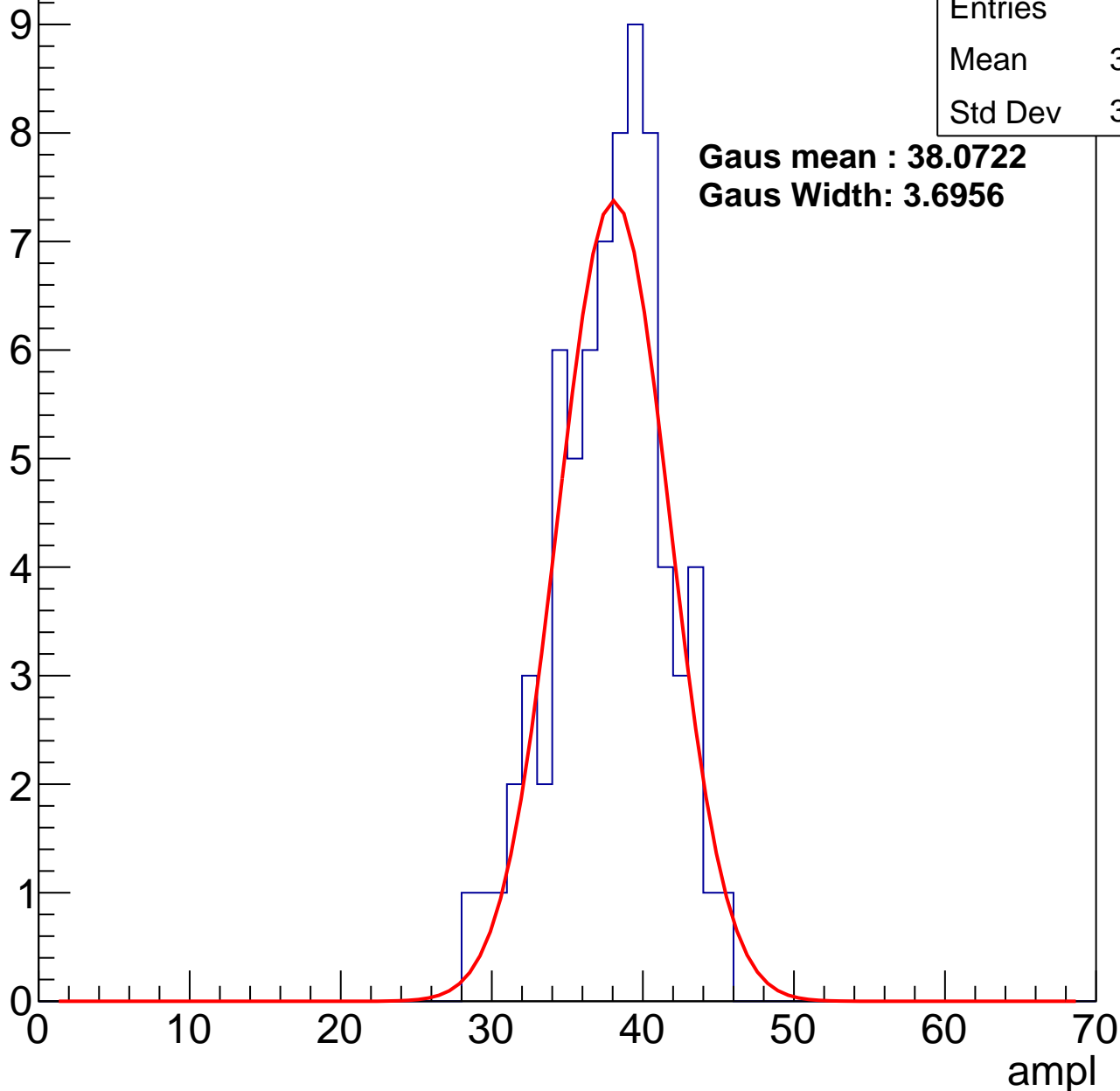
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.38 |
| Std Dev | 3.642 |

Gaus mean : 38.0722

Gaus Width: 3.6956



B1L100S, U9-ch92, adc2

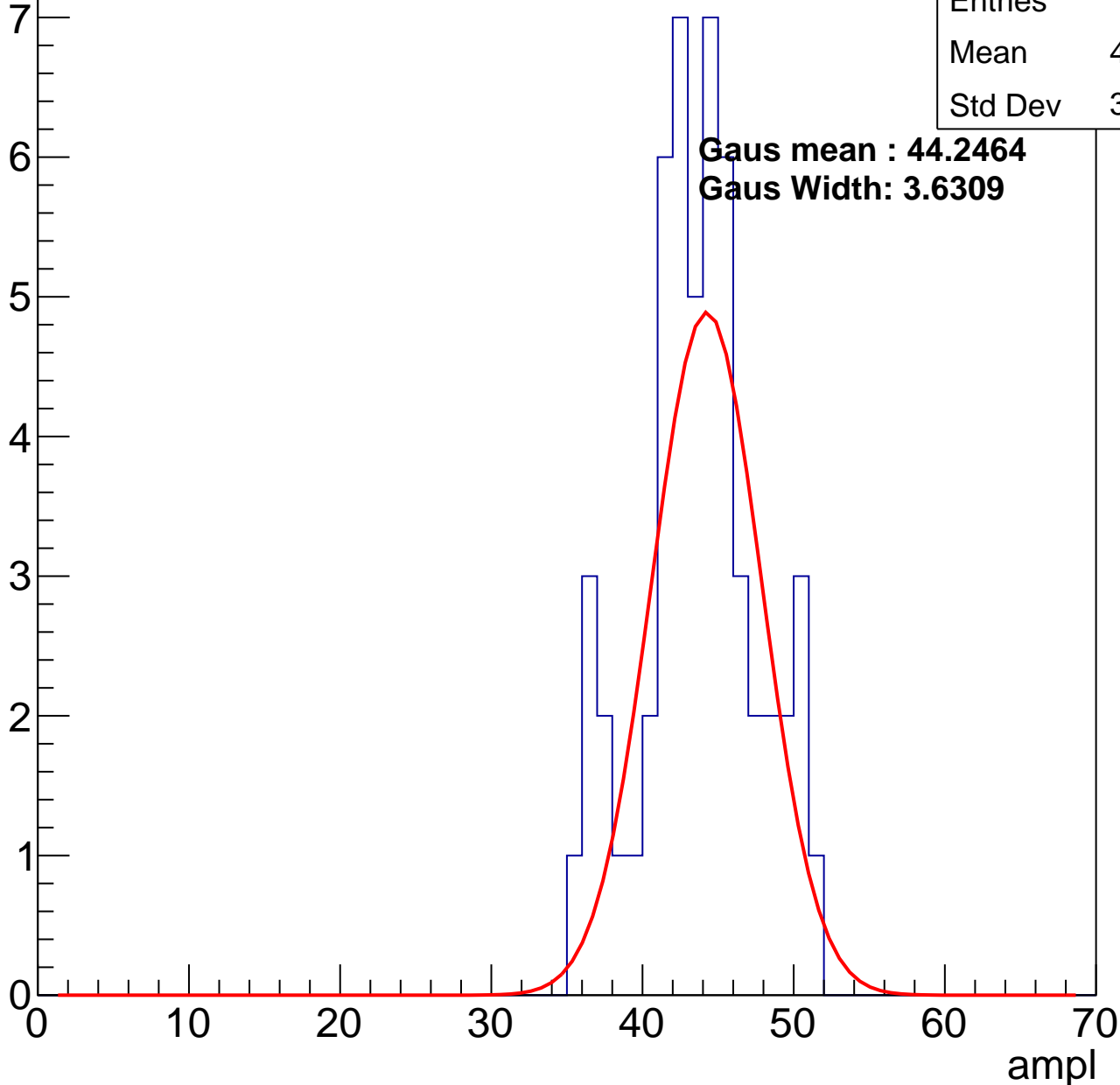
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.22 |
| Std Dev | 3.833 |

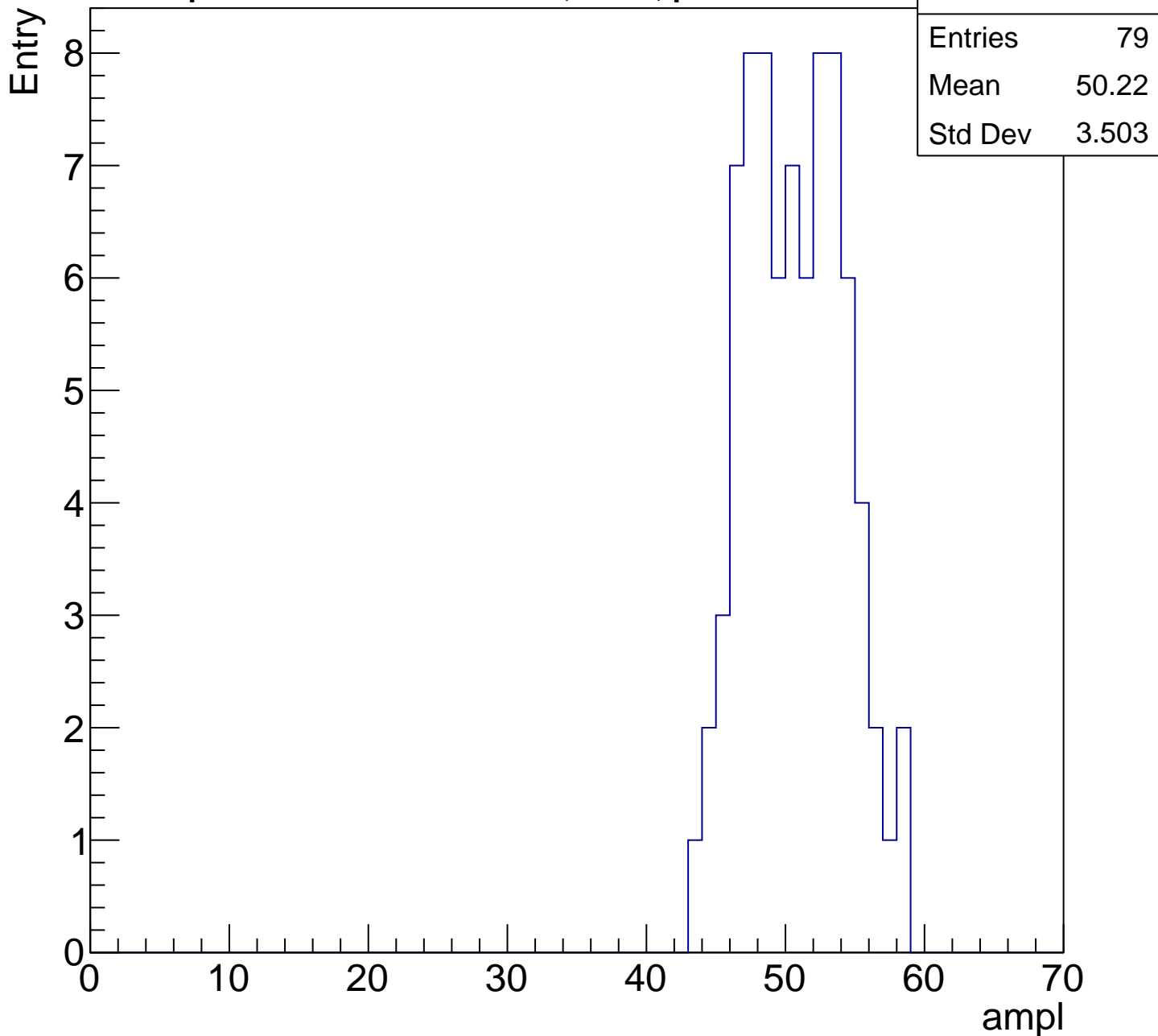
Gaus mean : 44.2464

Gaus Width: 3.6309



B1L100S, U9-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

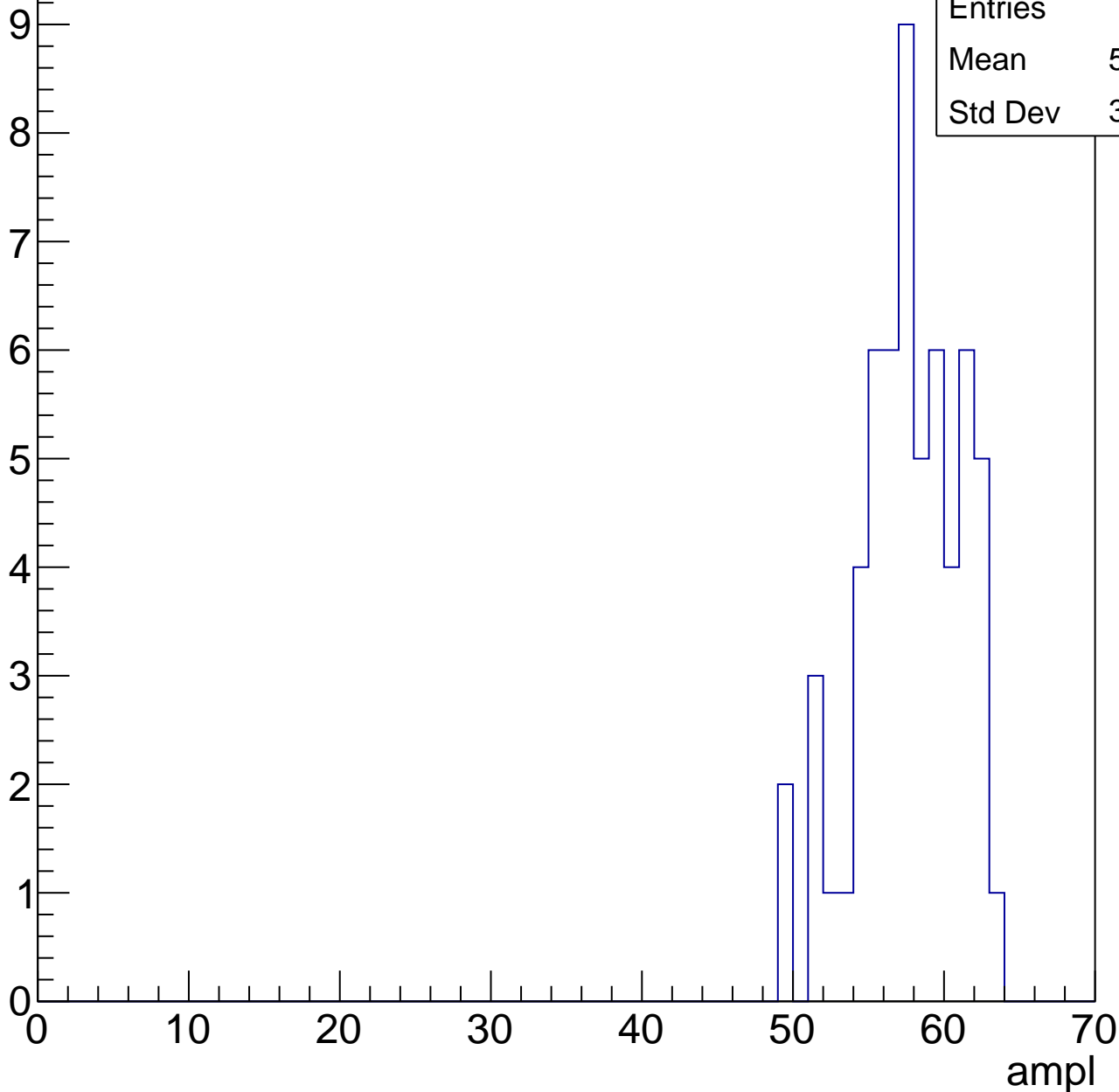


B1L100S, U9-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 57.19 |
| Std Dev | 3.352 |

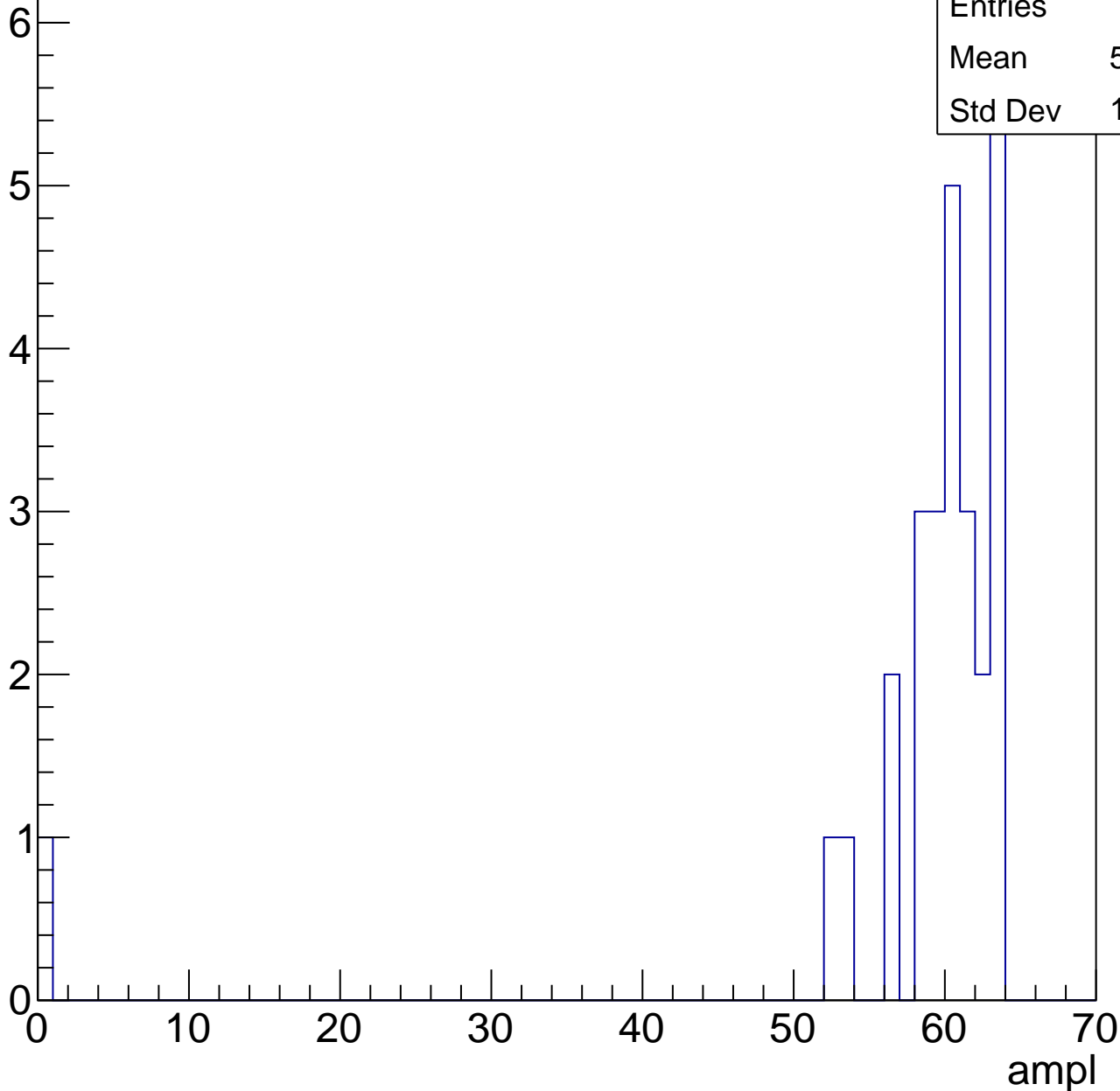


B1L100S, U9-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

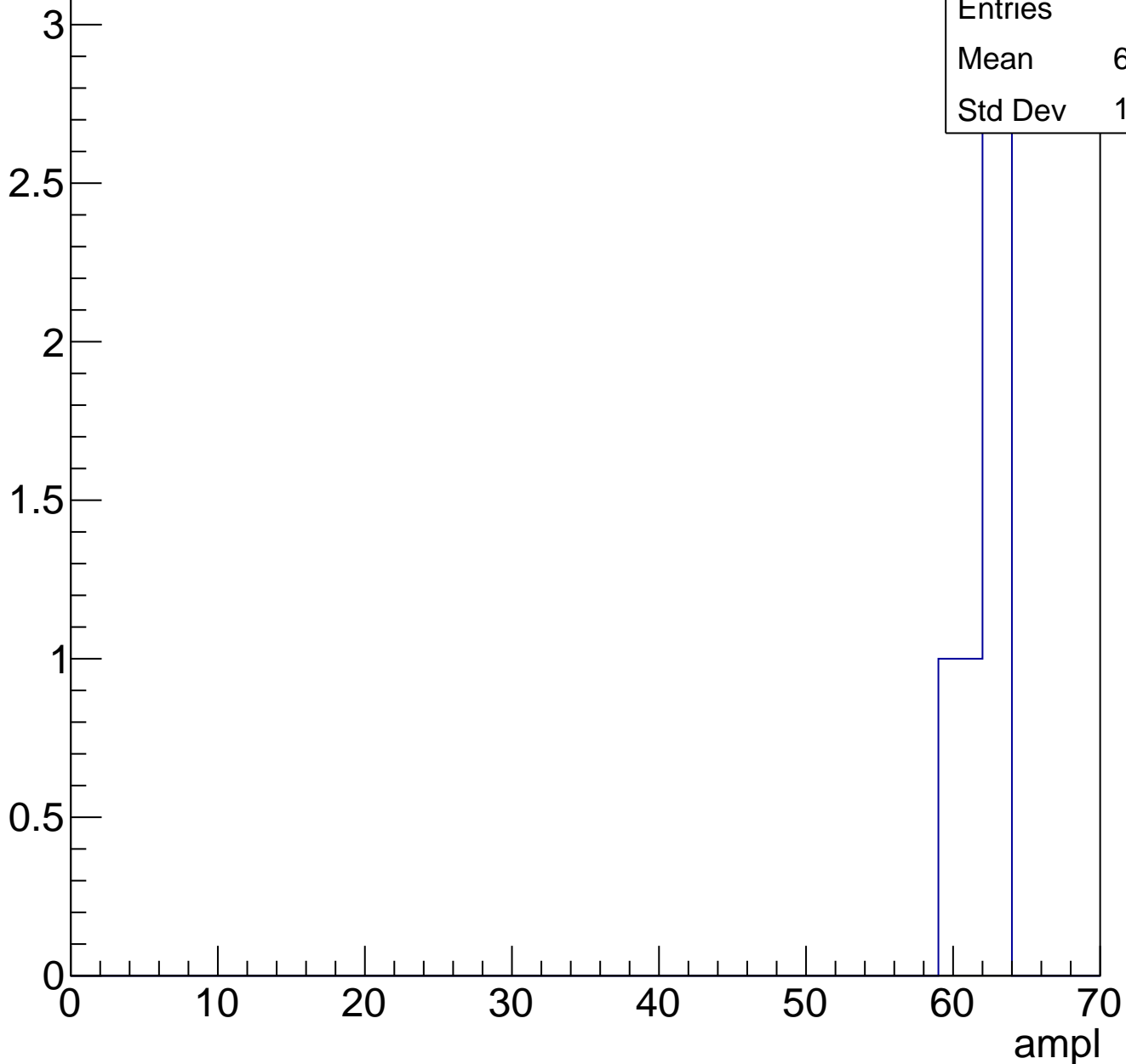
| | |
|---------|-------|
| Entries | 27 |
| Mean | 57.52 |
| Std Dev | 11.64 |



B1L100S, U9-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch93, adc0

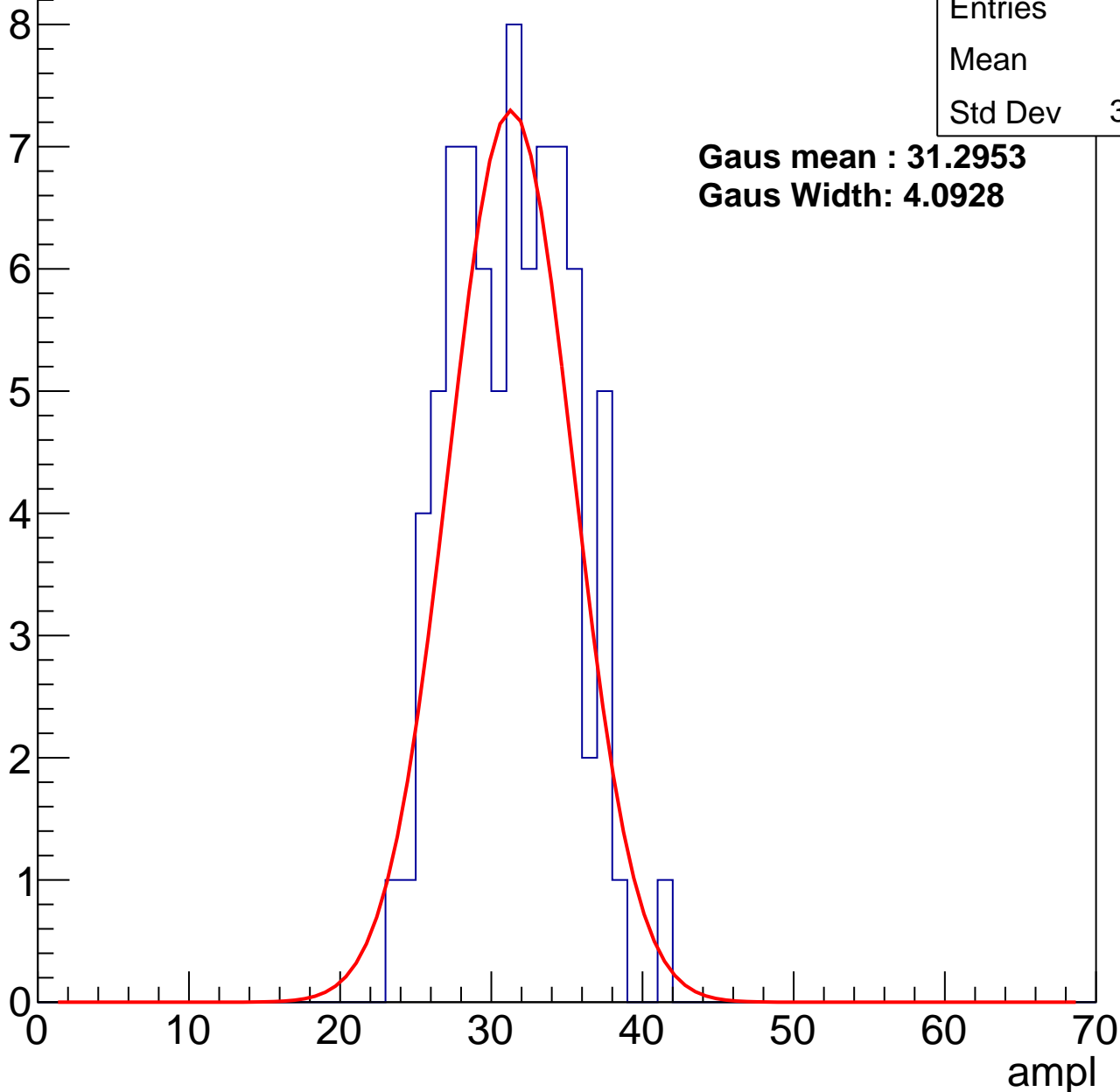
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 30.9 |
| Std Dev | 3.827 |

Gaus mean : 31.2953

Gaus Width: 4.0928



B1L100S, U9-ch93, adc1

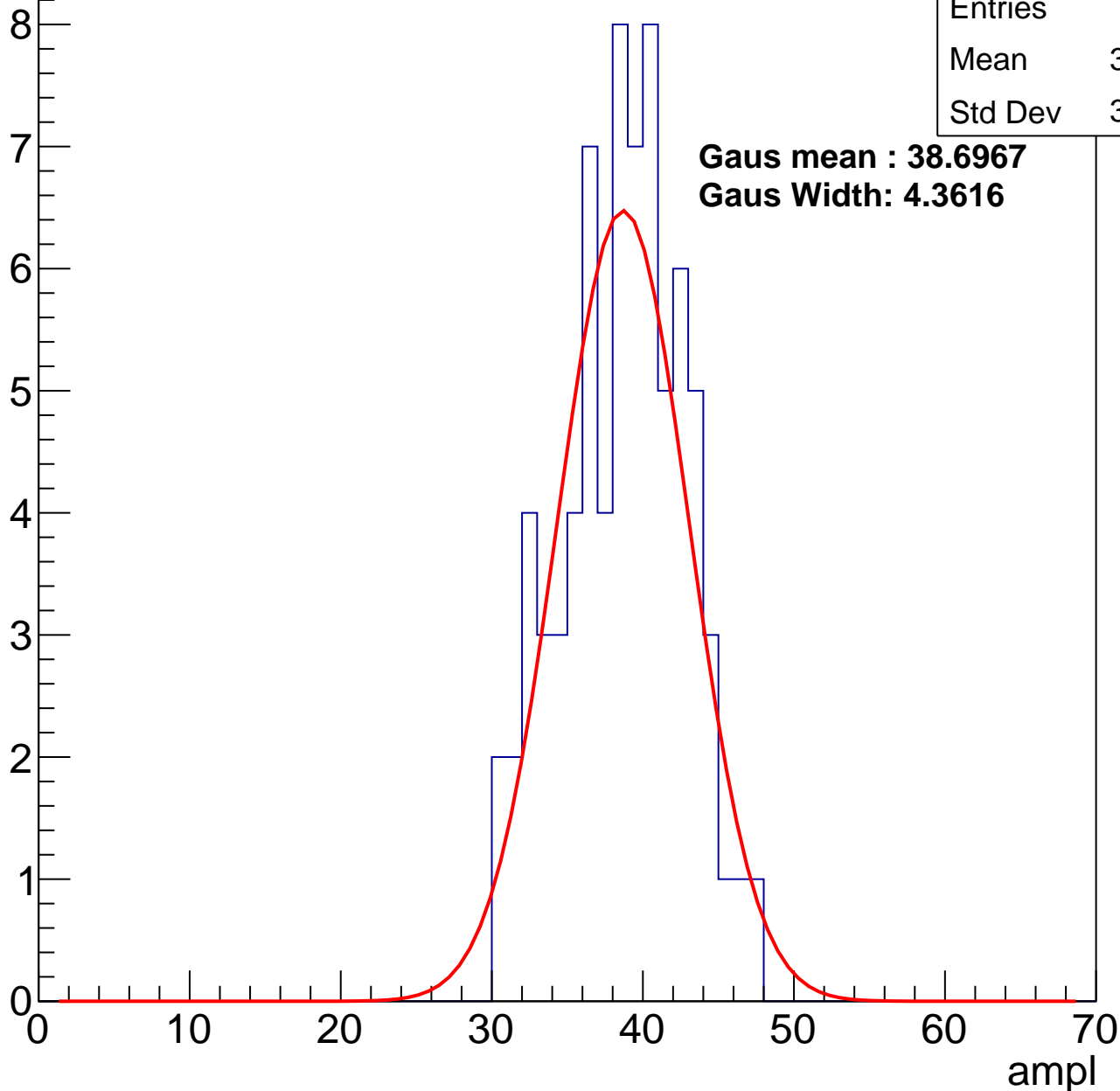
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 38.24 |
| Std Dev | 3.965 |

Gaus mean : 38.6967

Gaus Width: 4.3616



B1L100S, U9-ch93, adc2

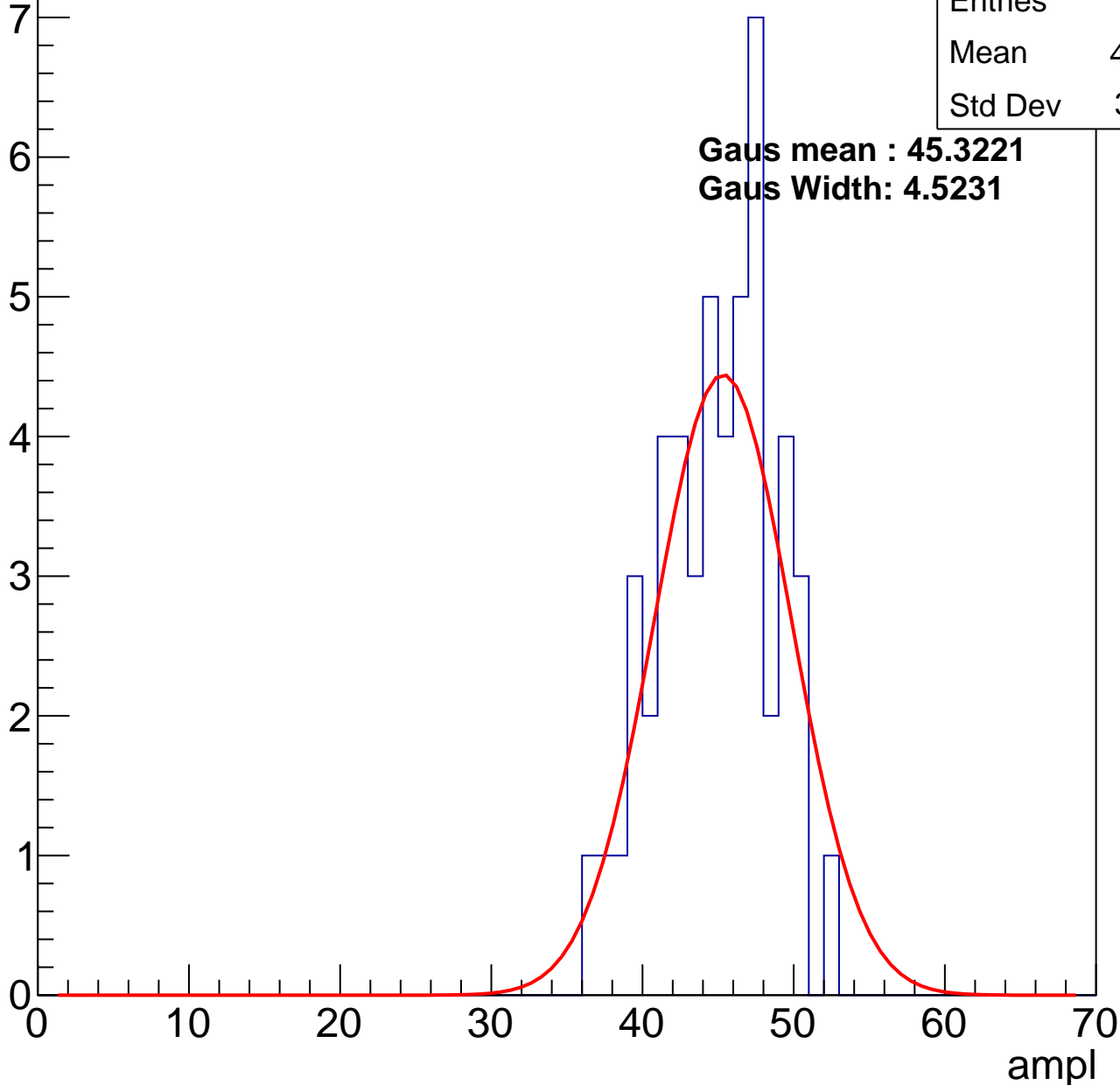
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 44.44 |
| Std Dev | 3.721 |

Gaus mean : 45.3221

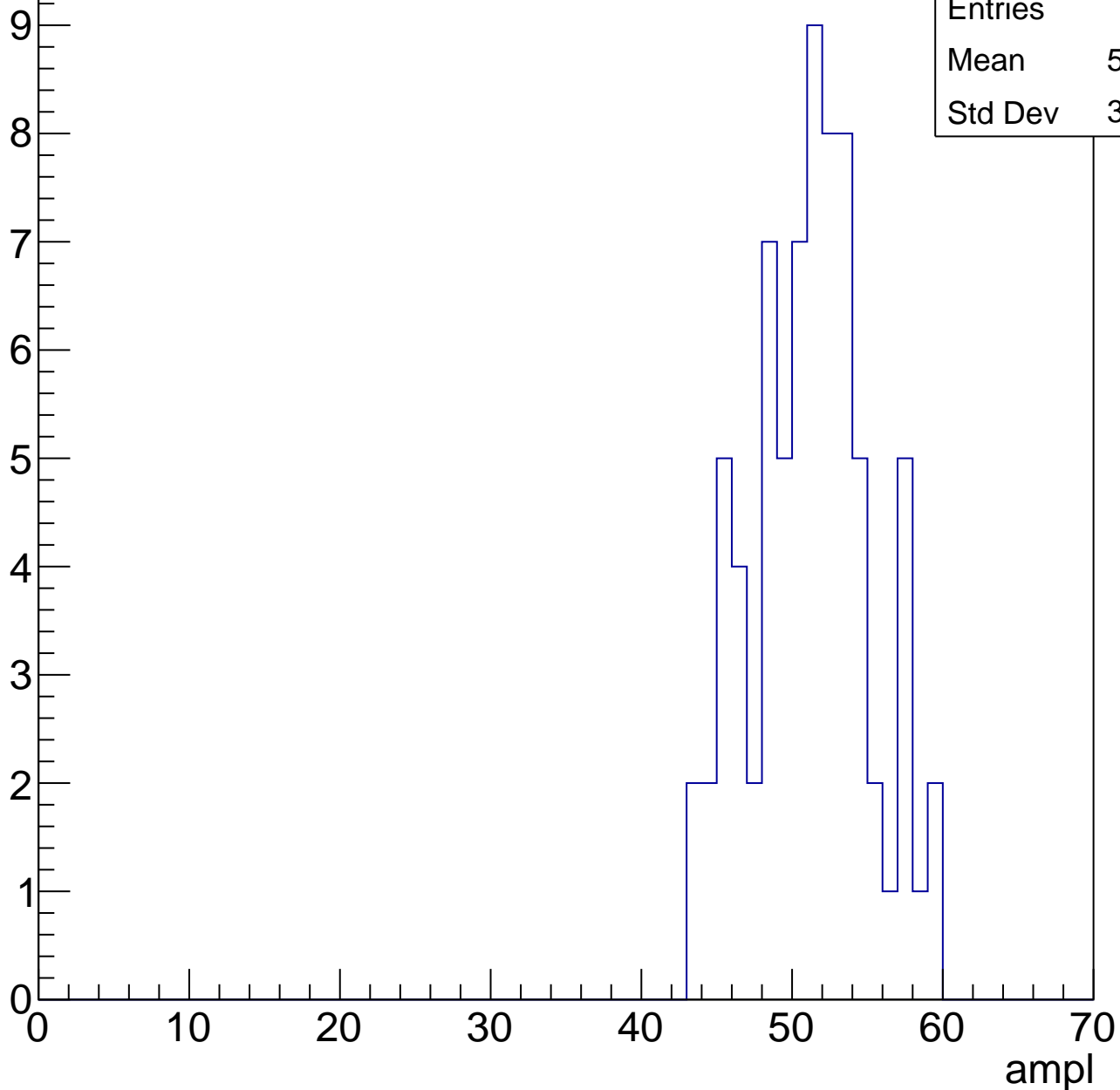
Gaus Width: 4.5231



B1L100S, U9-ch93, adc3

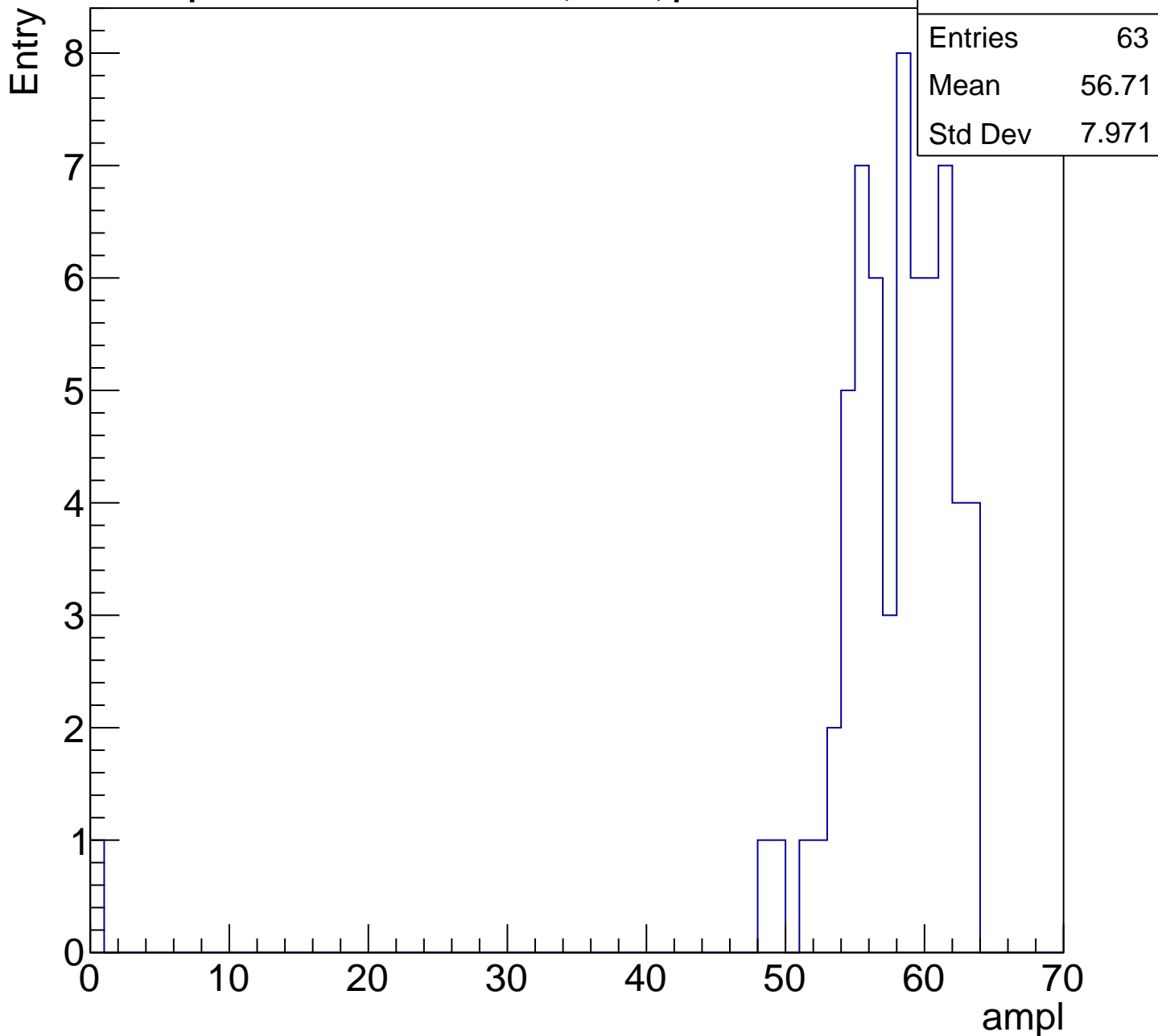
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

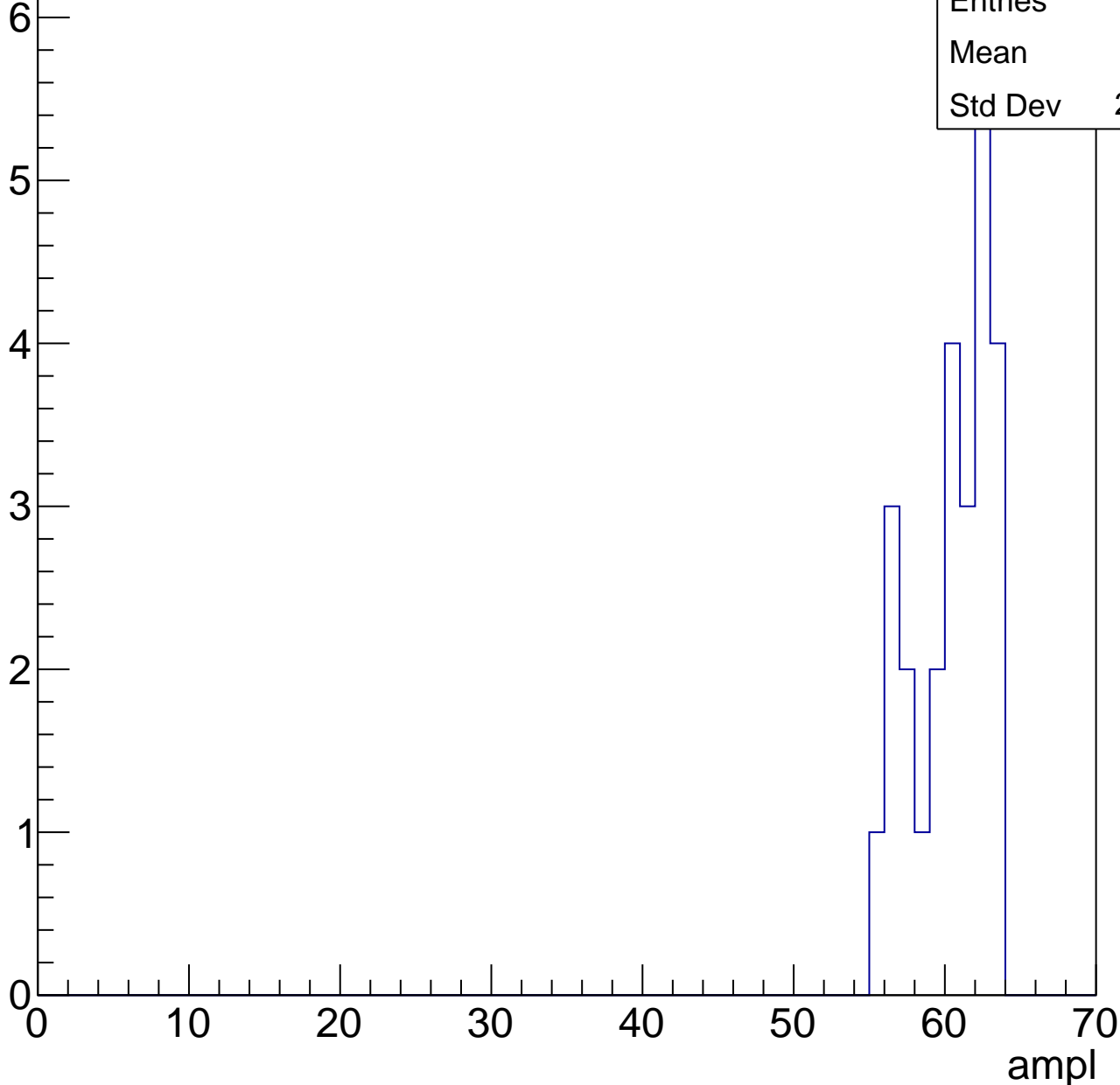


B1L100S, U9-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 60 |
| Std Dev | 2.481 |



B1L100S, U9-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B1L100S, U9-ch94, adc0

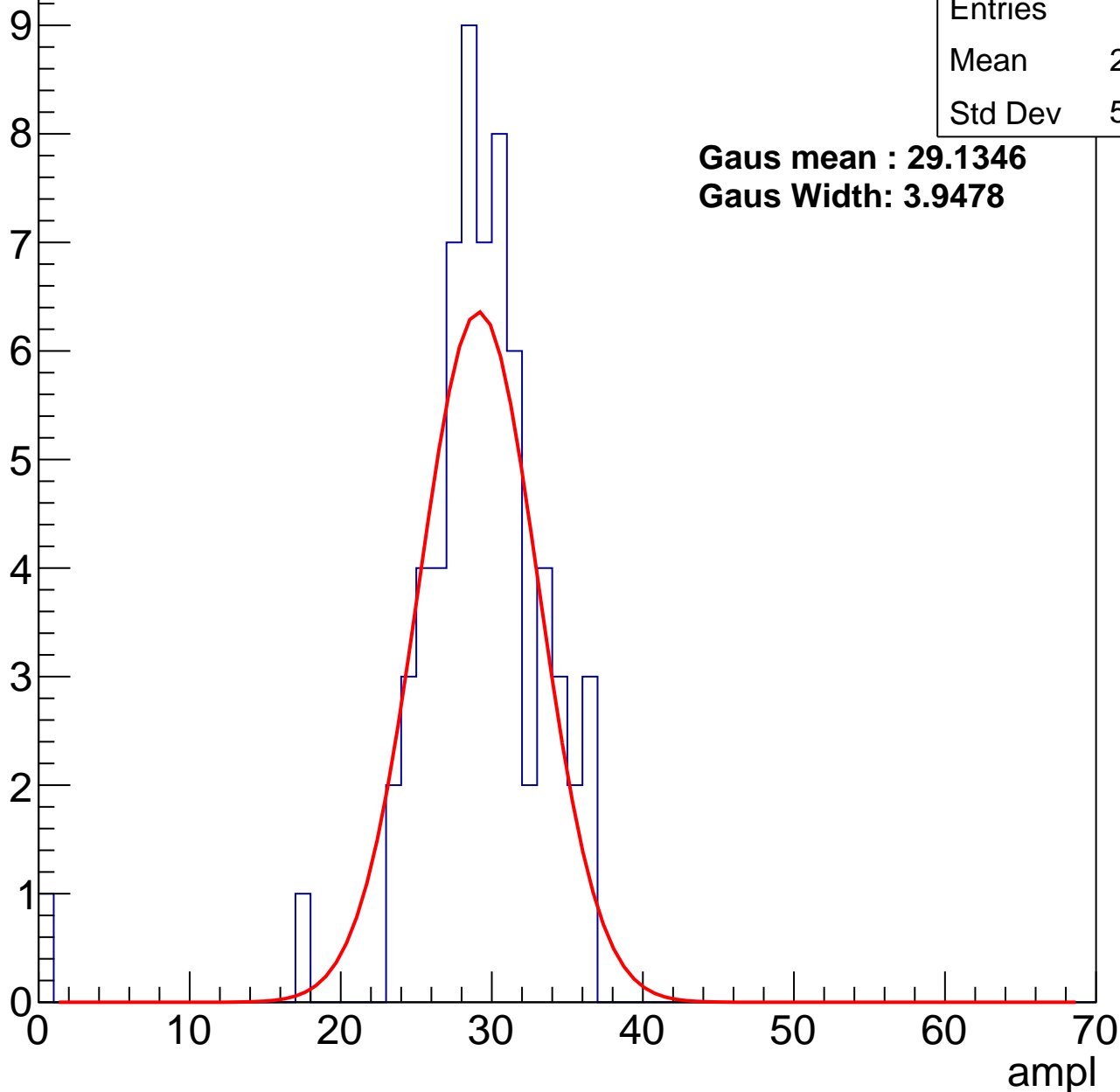
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.56 |
| Std Dev | 5.022 |

Gaus mean : 29.1346

Gaus Width: 3.9478



B1L100S, U9-ch94, adc1

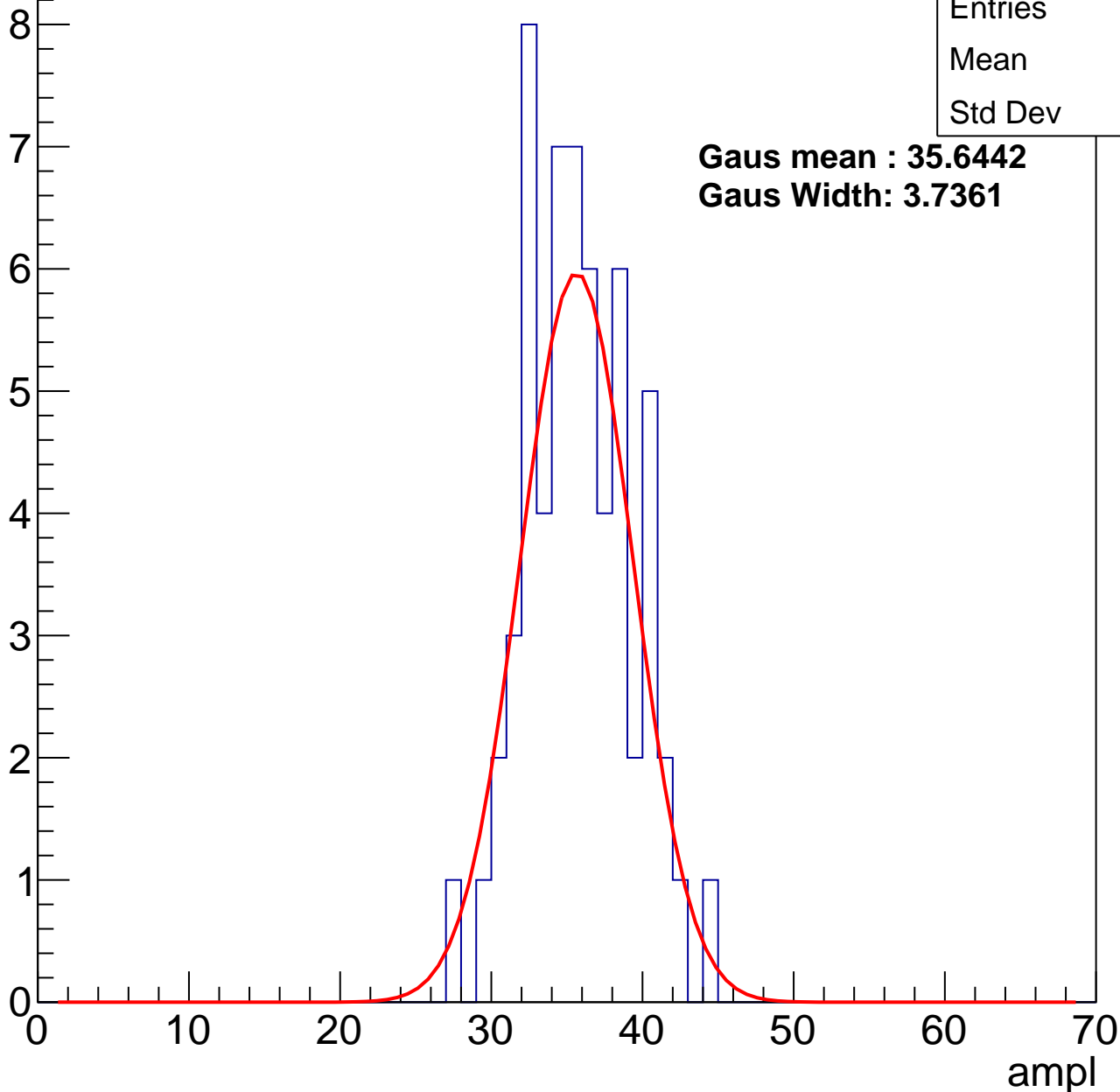
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 60 |
| Mean | 35.3 |
| Std Dev | 3.47 |

Gaus mean : 35.6442

Gaus Width: 3.7361



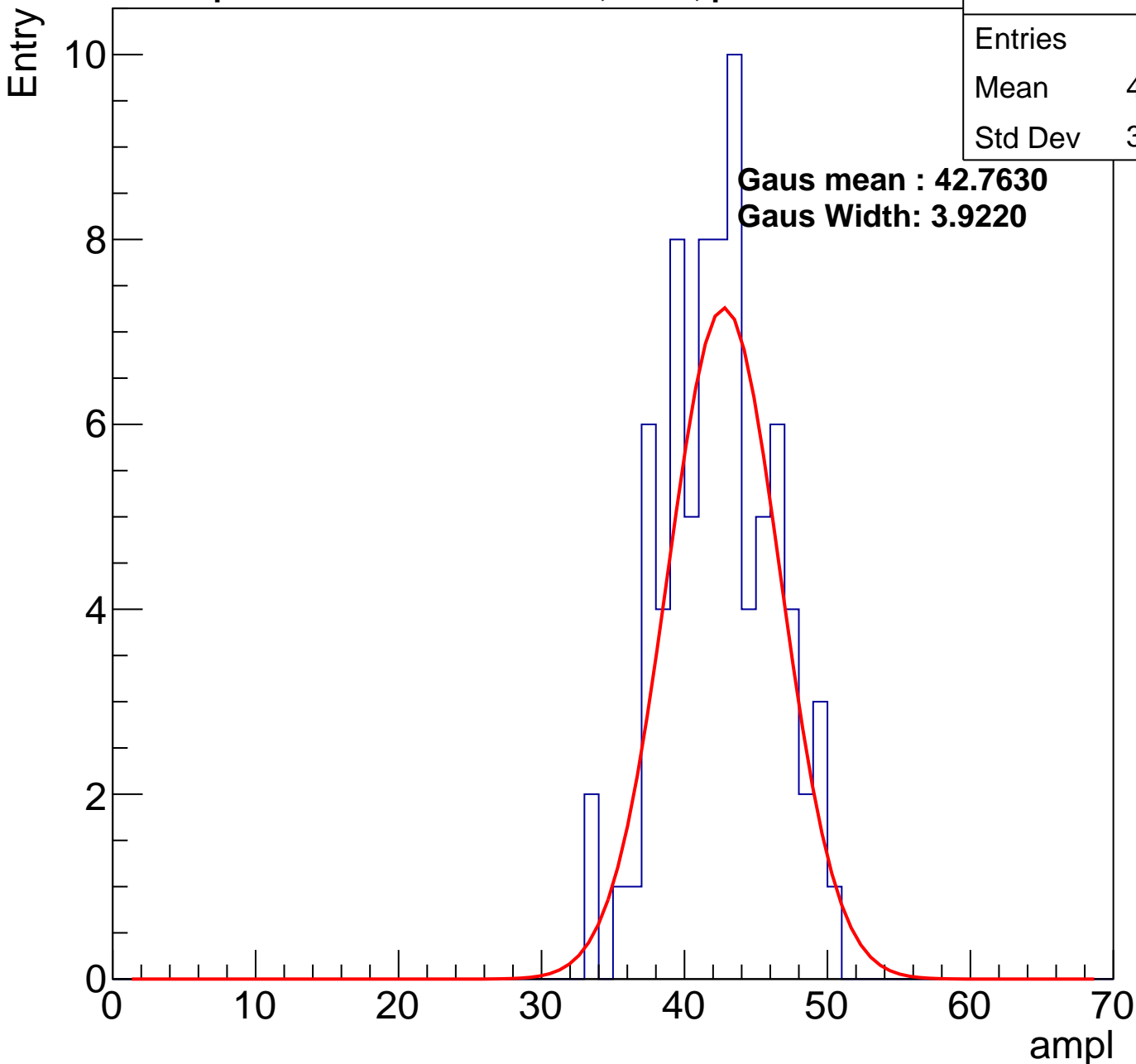
B1L100S, U9-ch94, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 41.99 |
| Std Dev | 3.774 |

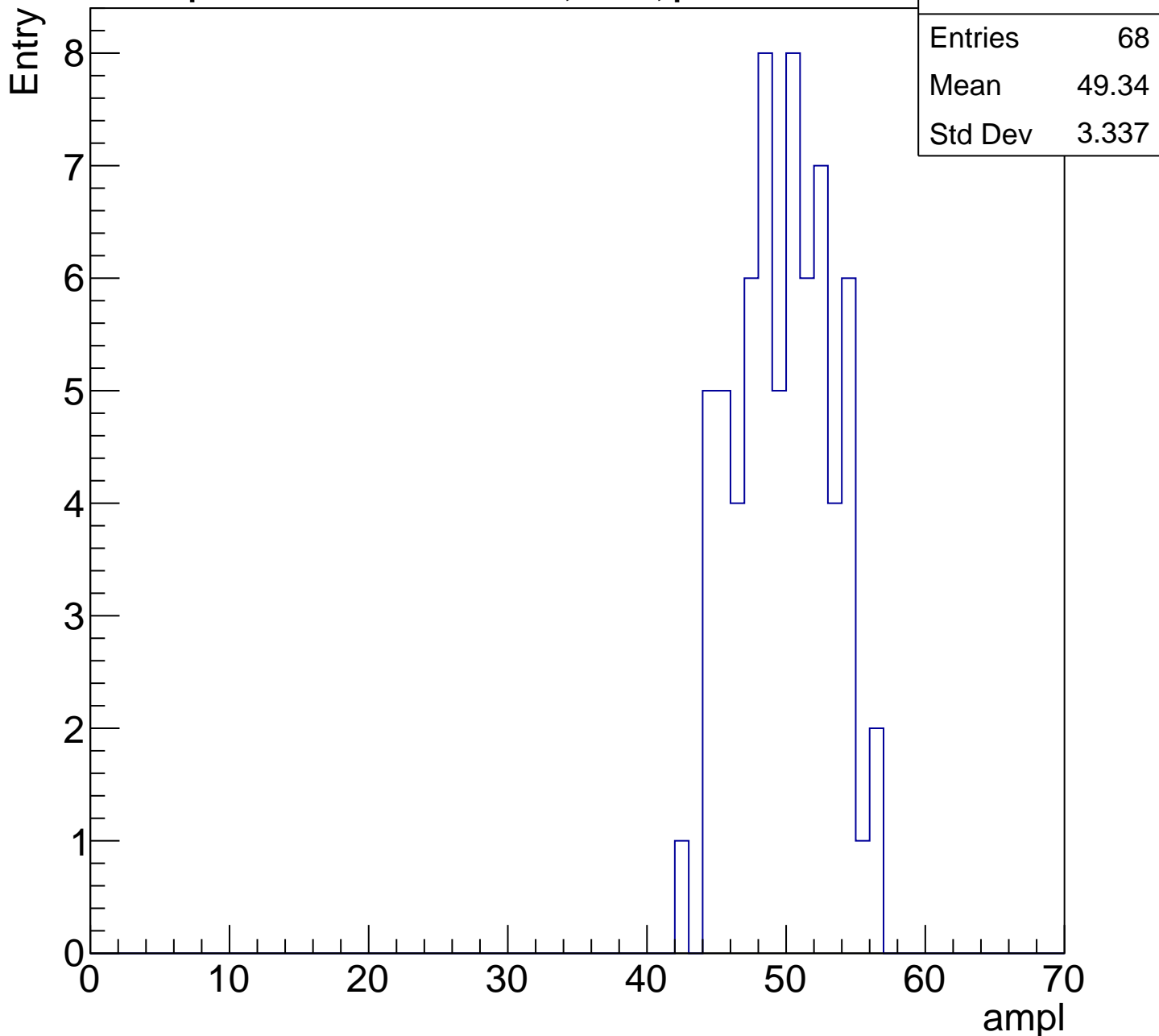
Gaus mean : 42.7630

Gaus Width: 3.9220



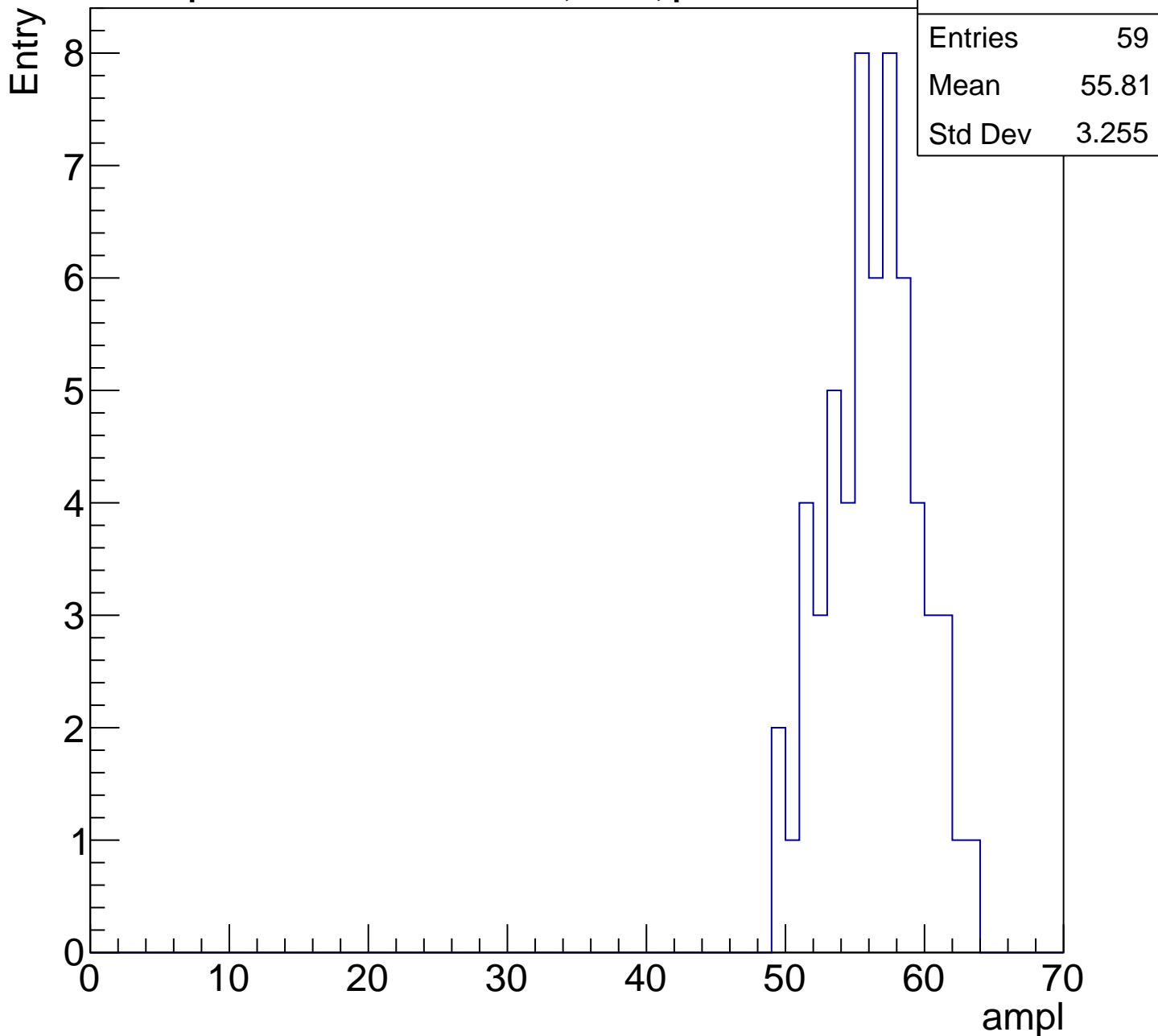
B1L100S, U9-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch94, adc5

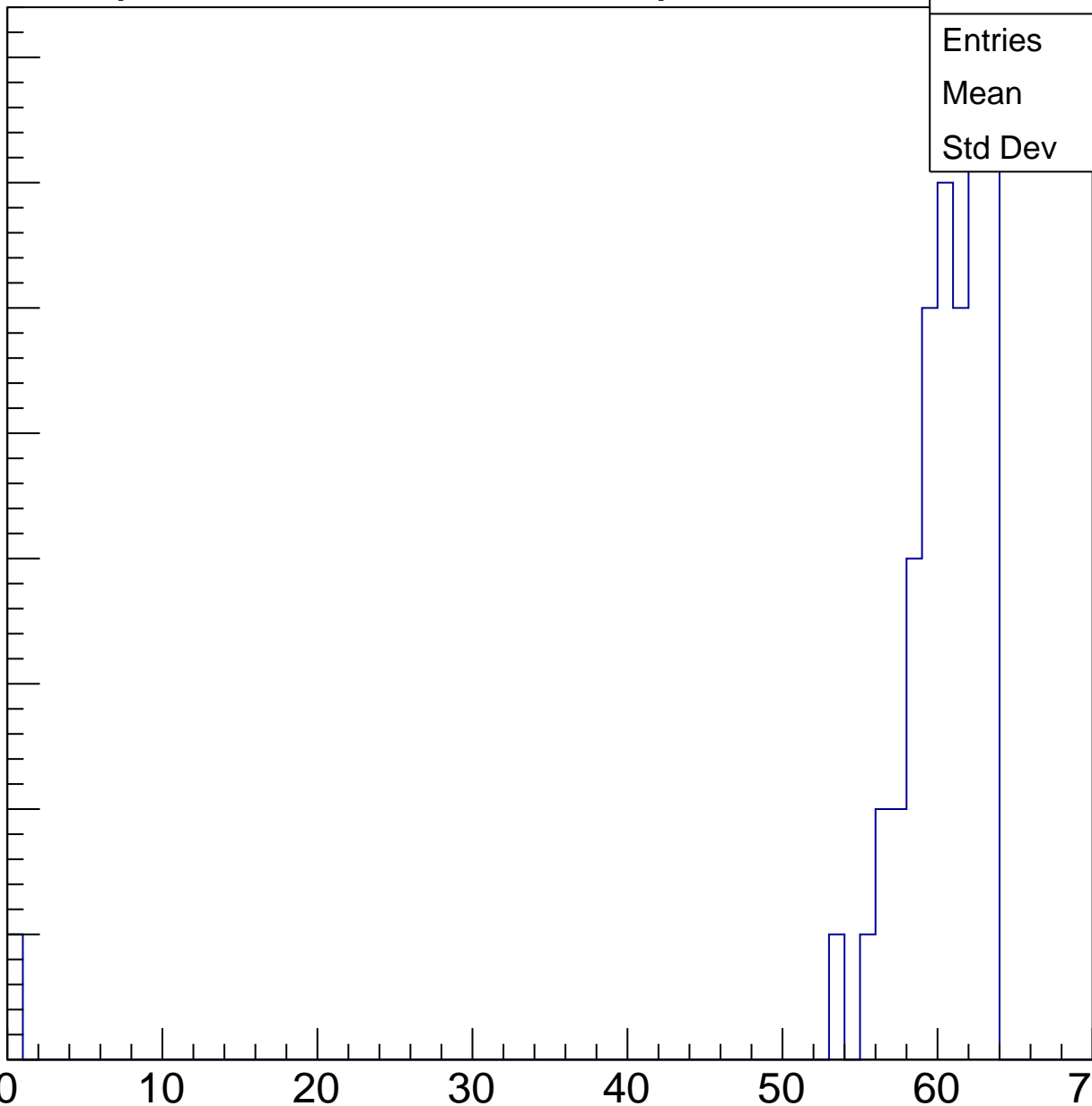
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.83 |
| Std Dev | 9.08 |

ampl



B1L100S, U9-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch95, adc0

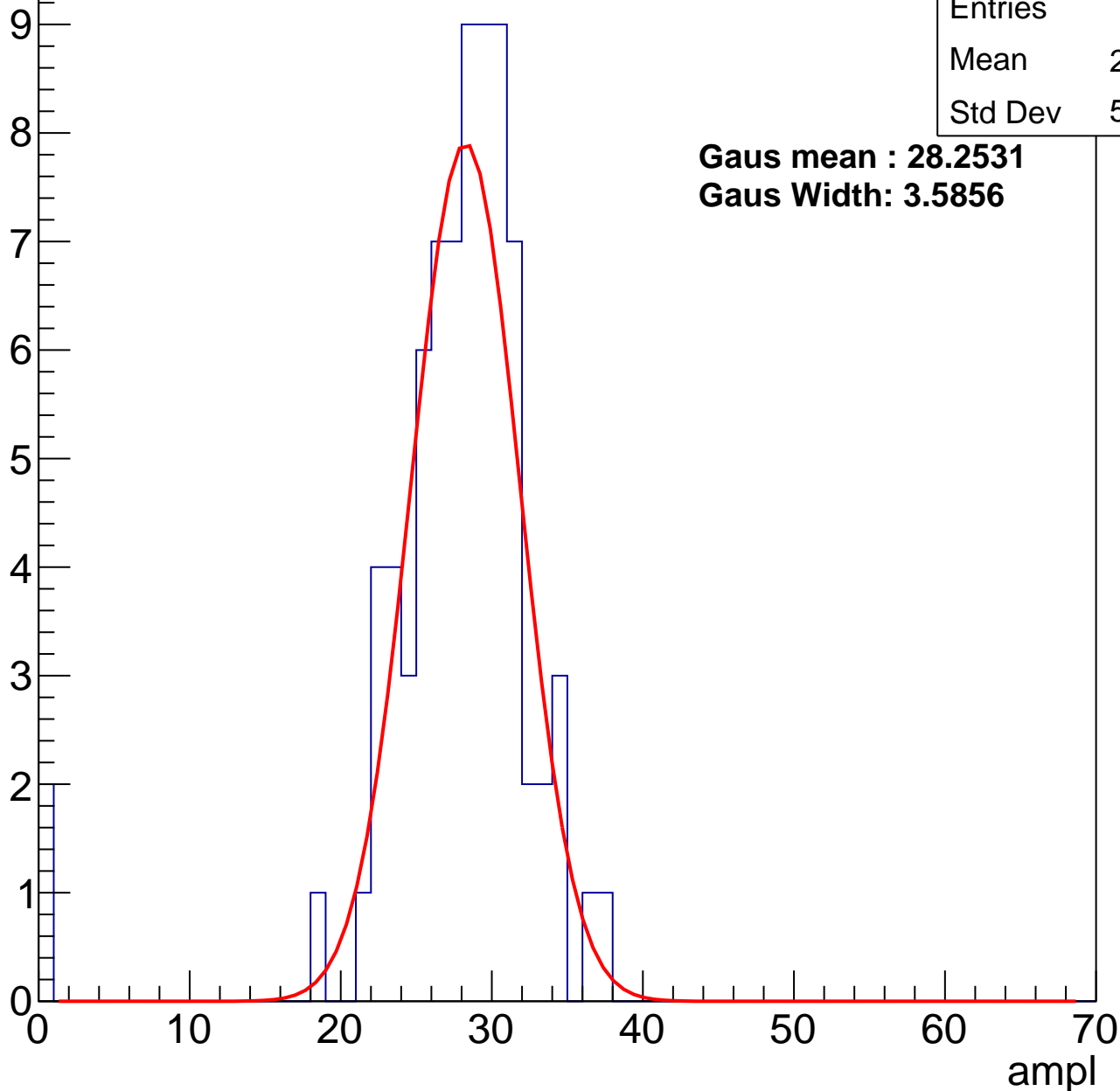
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 27.14 |
| Std Dev | 5.643 |

Gaus mean : 28.2531

Gaus Width: 3.5856



B1L100S, U9-ch95, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 35.97 |
| Std Dev | 3.39 |

Gaus mean : 35.9890

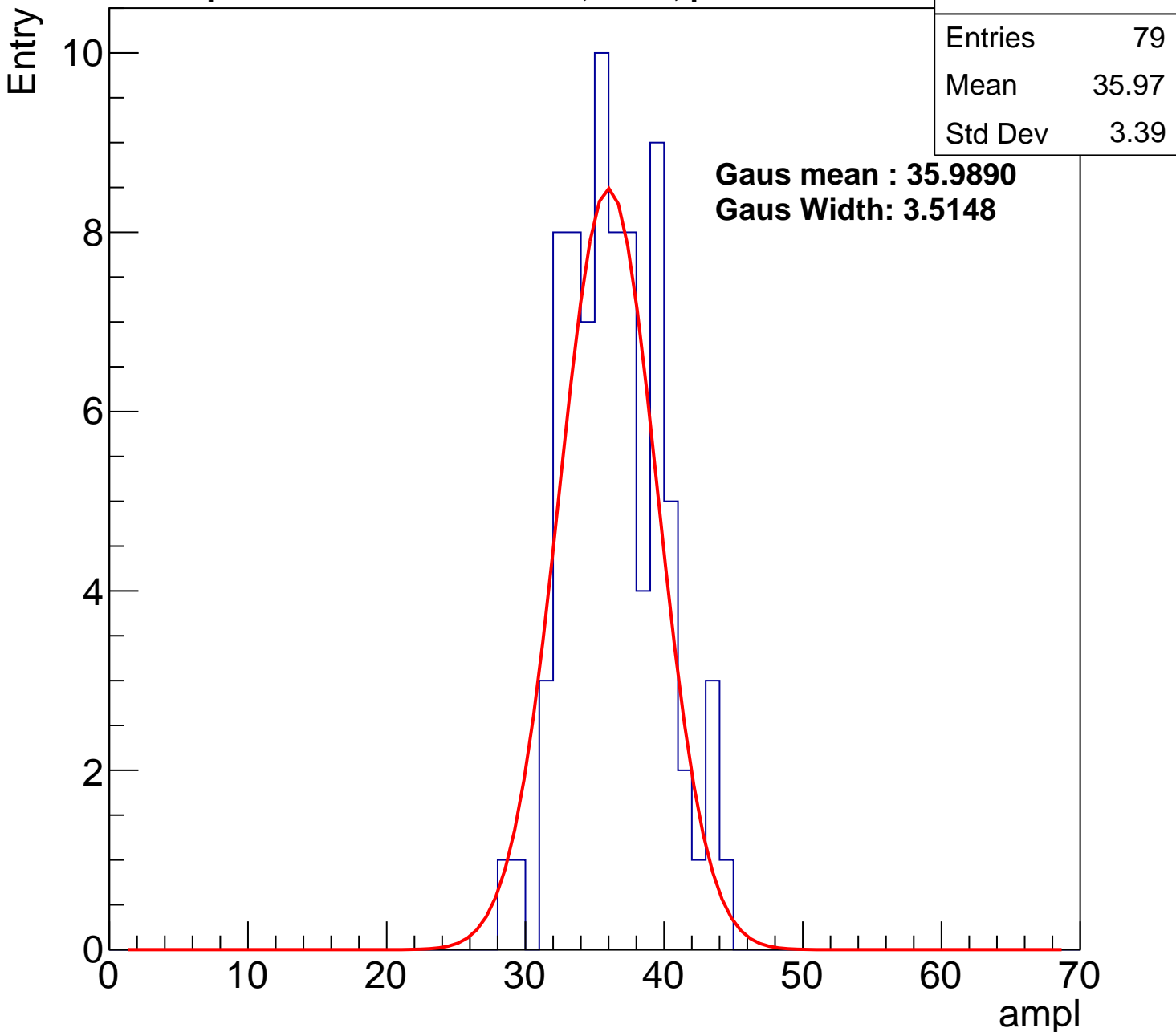
Gaus Width: 3.5148

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch95, adc2

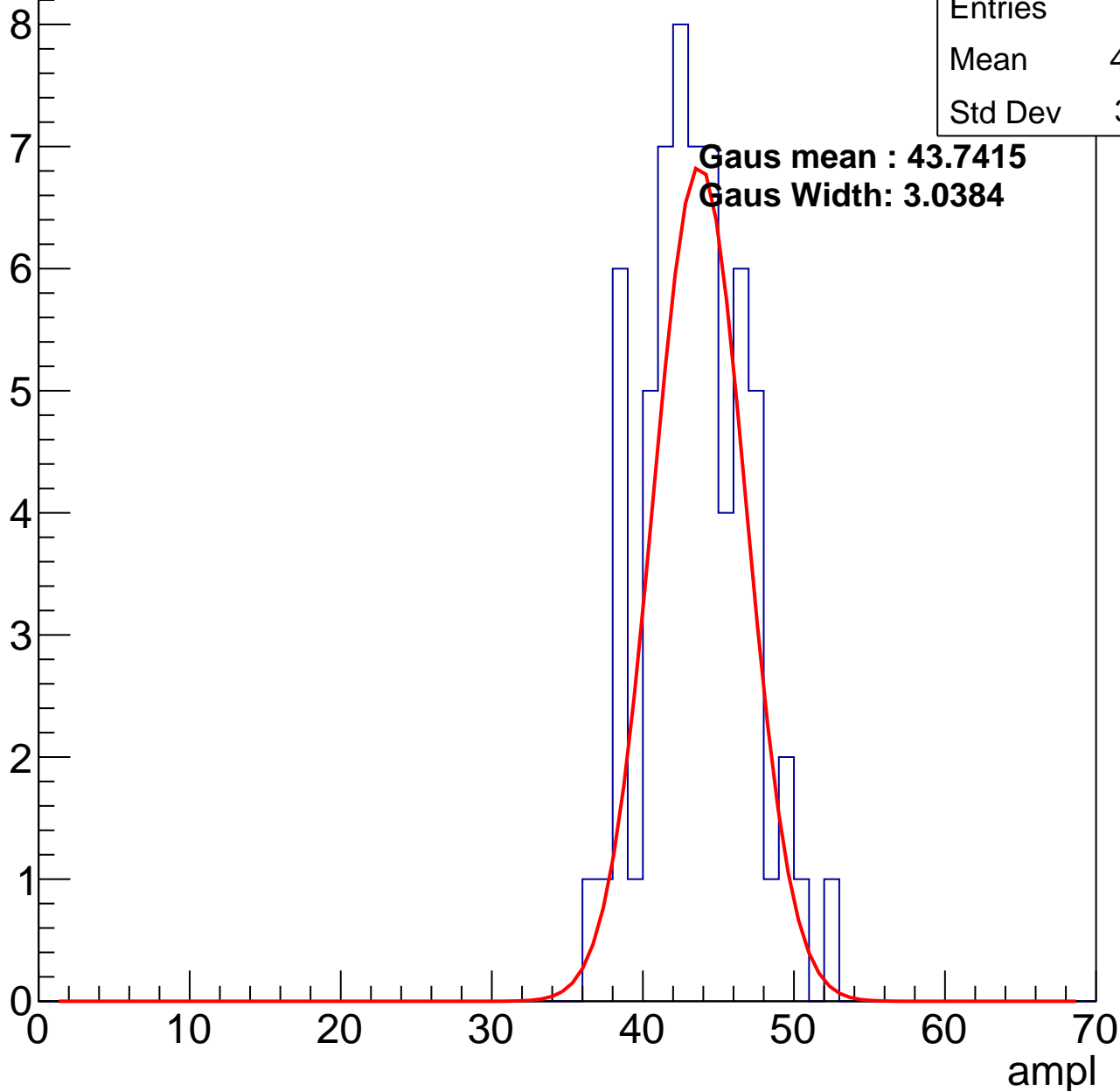
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.03 |
| Std Dev | 3.371 |

Gaus mean : 43.7415

Gaus Width: 3.0384

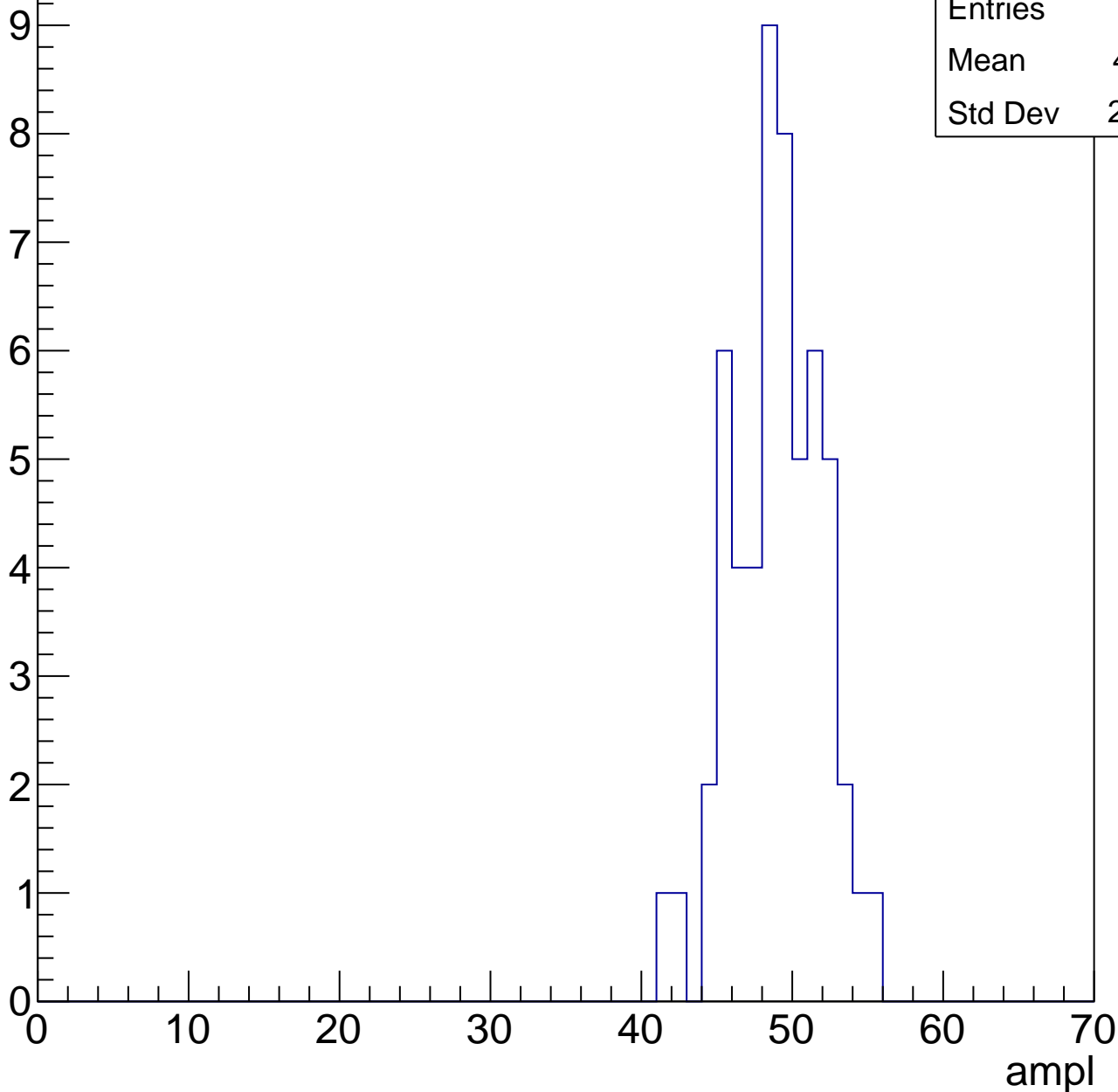


B1L100S, U9-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

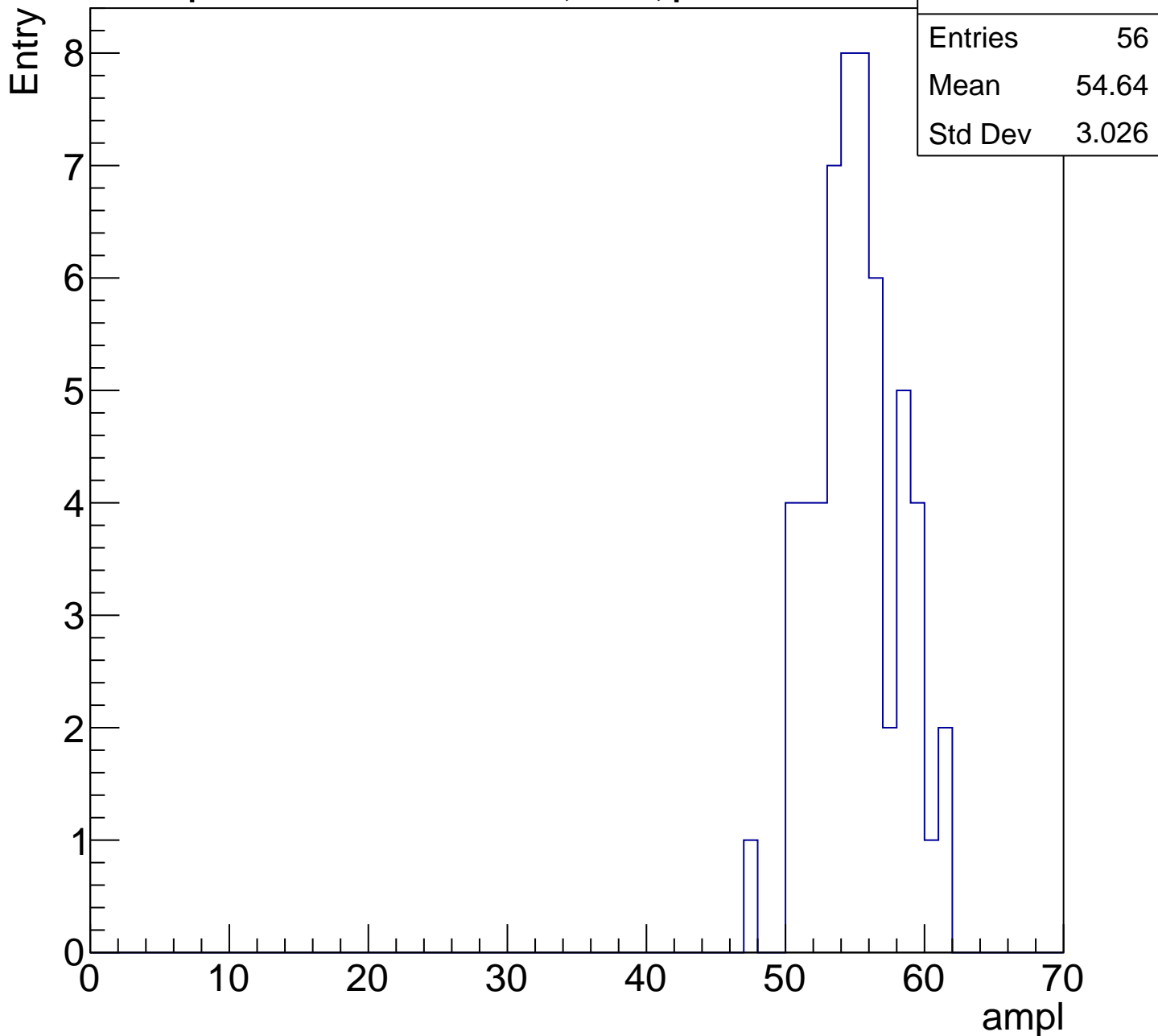
Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 48.51 |
| Std Dev | 2.935 |



B1L100S, U9-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

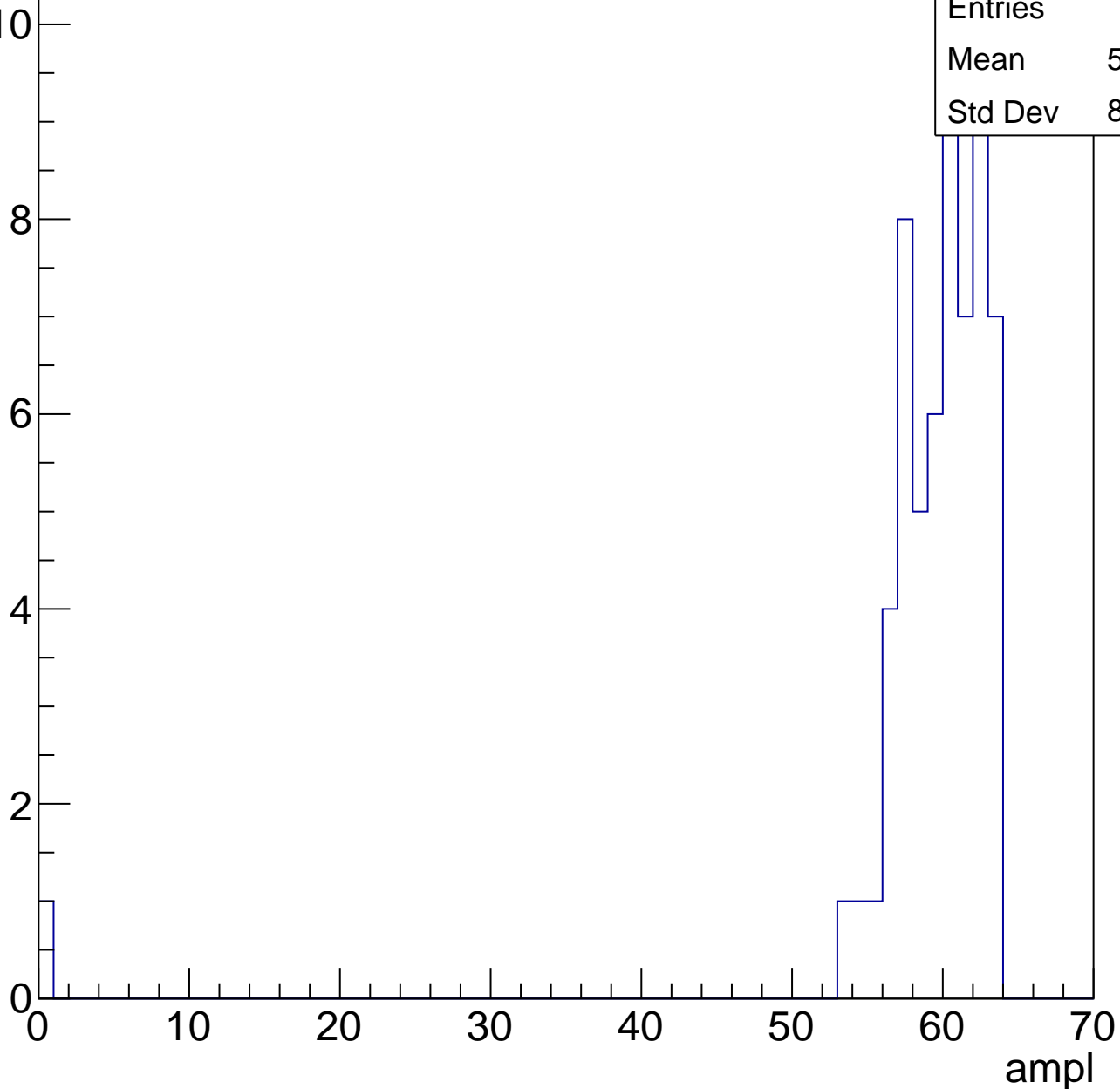


B1L100S, U9-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 58.57 |
| Std Dev | 8.019 |



B1L100S, U9-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch96, adc0

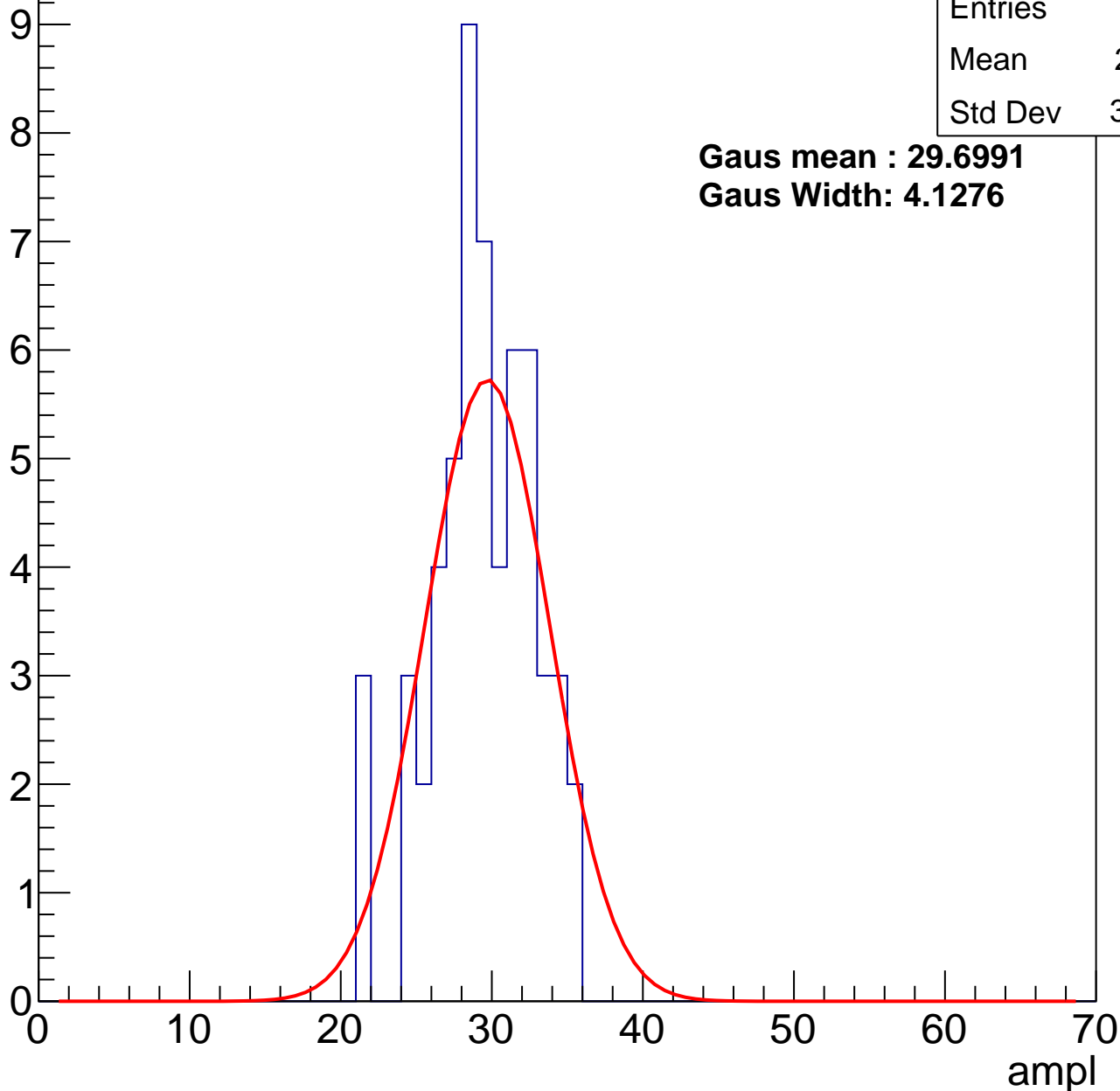
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 28.91 |
| Std Dev | 3.352 |

Gaus mean : 29.6991

Gaus Width: 4.1276



B1L100S, U9-ch96, adc1

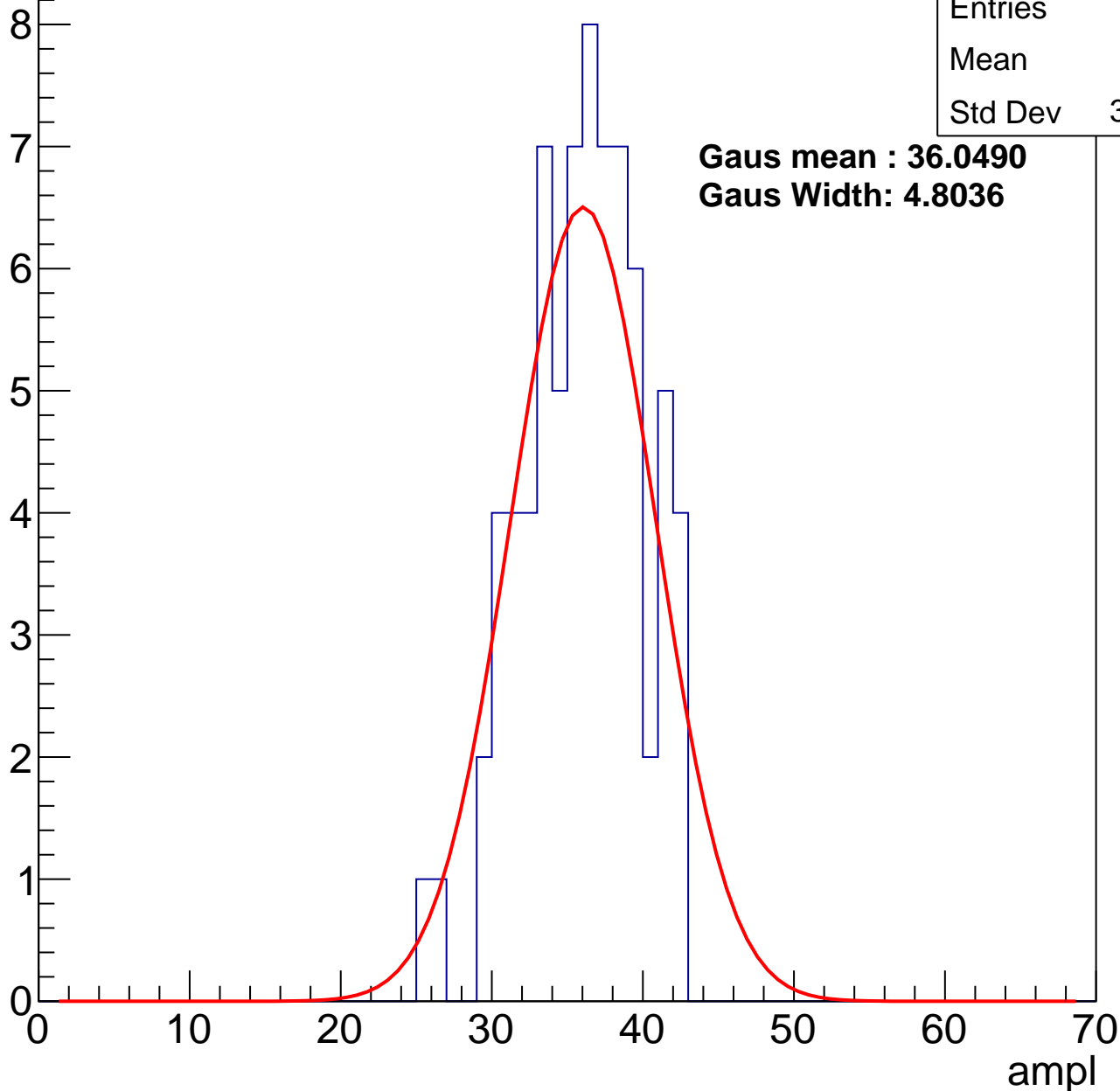
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.5 |
| Std Dev | 3.835 |

Gaus mean : 36.0490

Gaus Width: 4.8036



B1L100S, U9-ch96, adc2

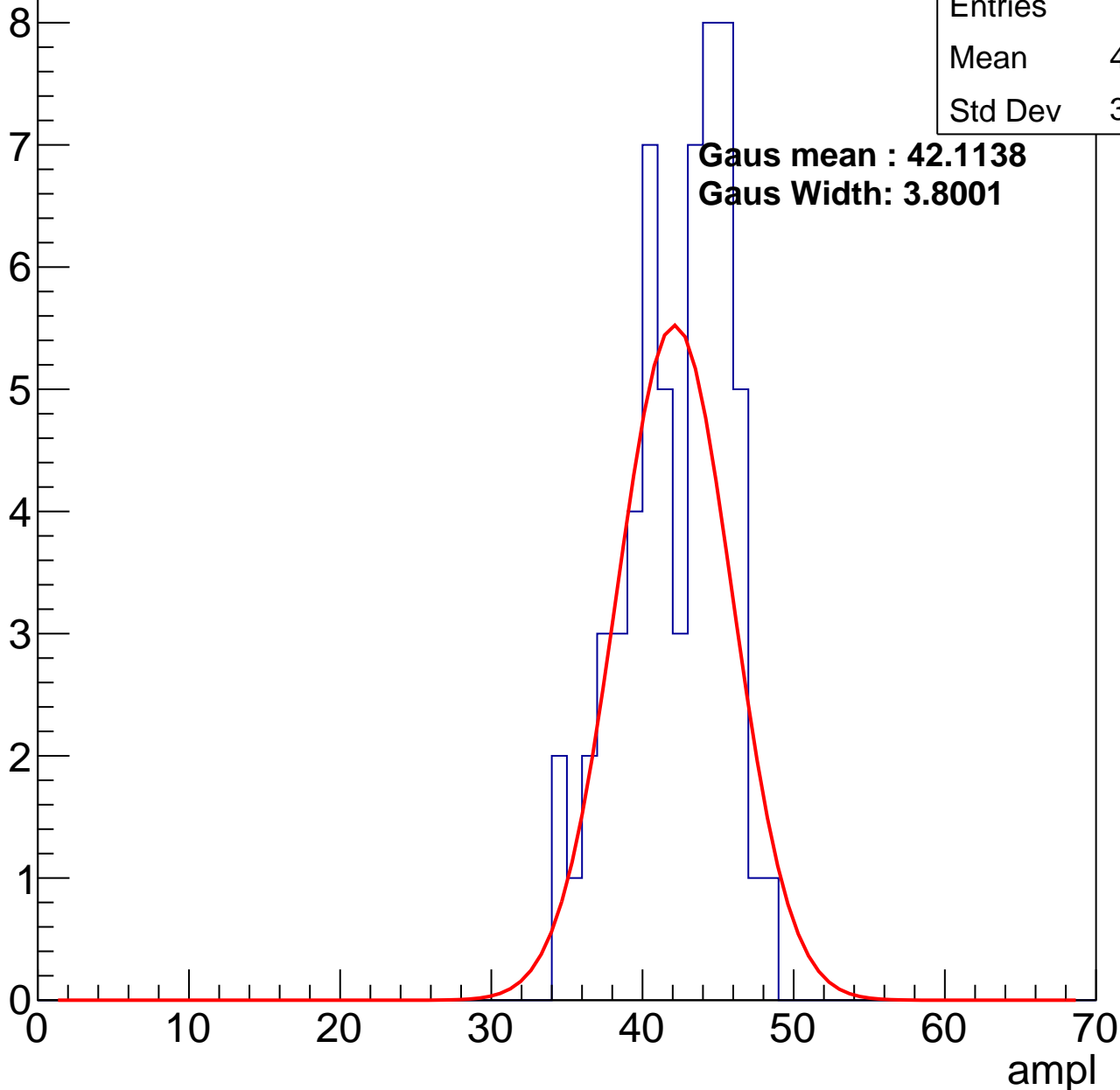
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 41.75 |
| Std Dev | 3.389 |

Gaus mean : 42.1138

Gaus Width: 3.8001

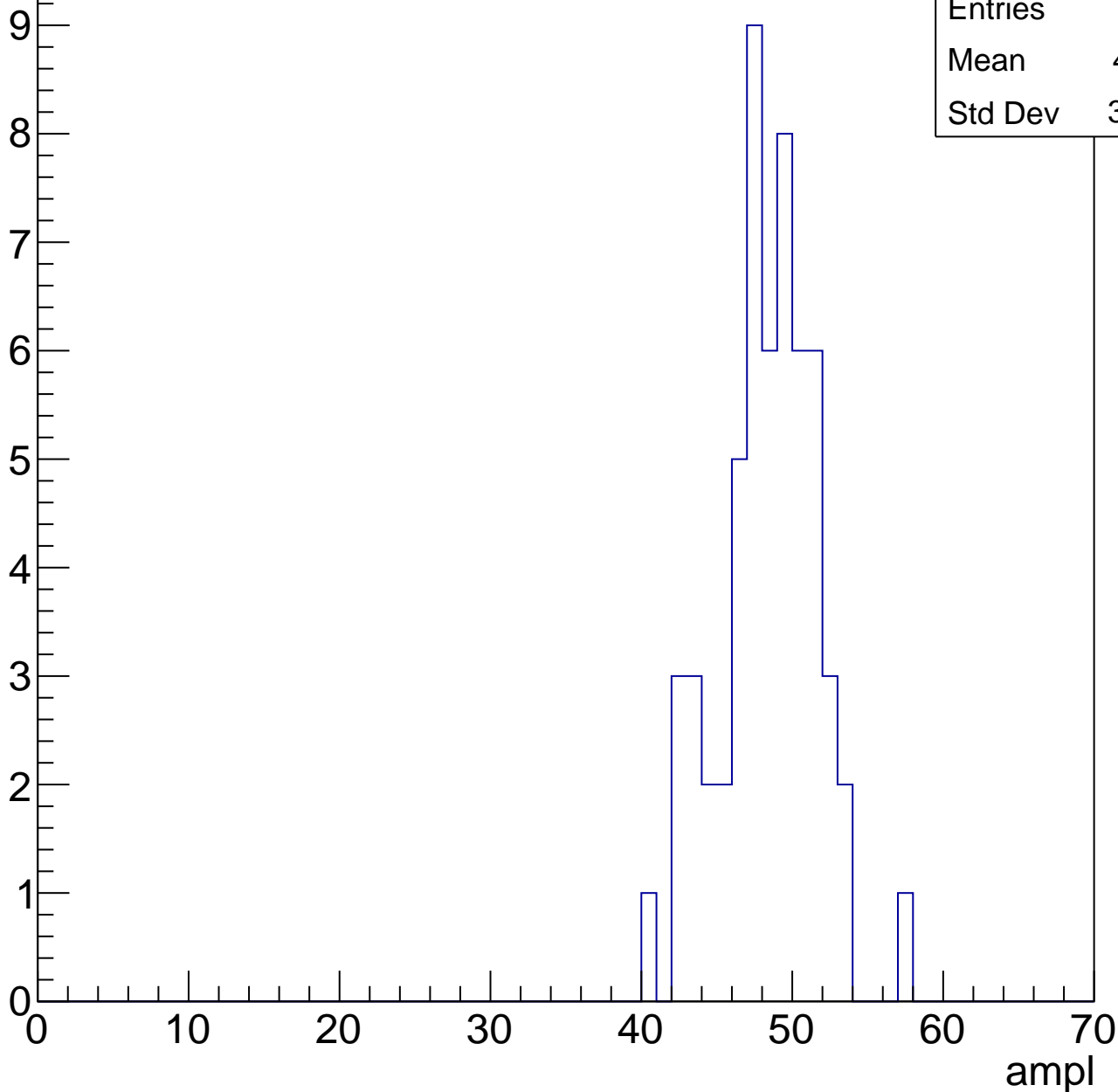


B1L100S, U9-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

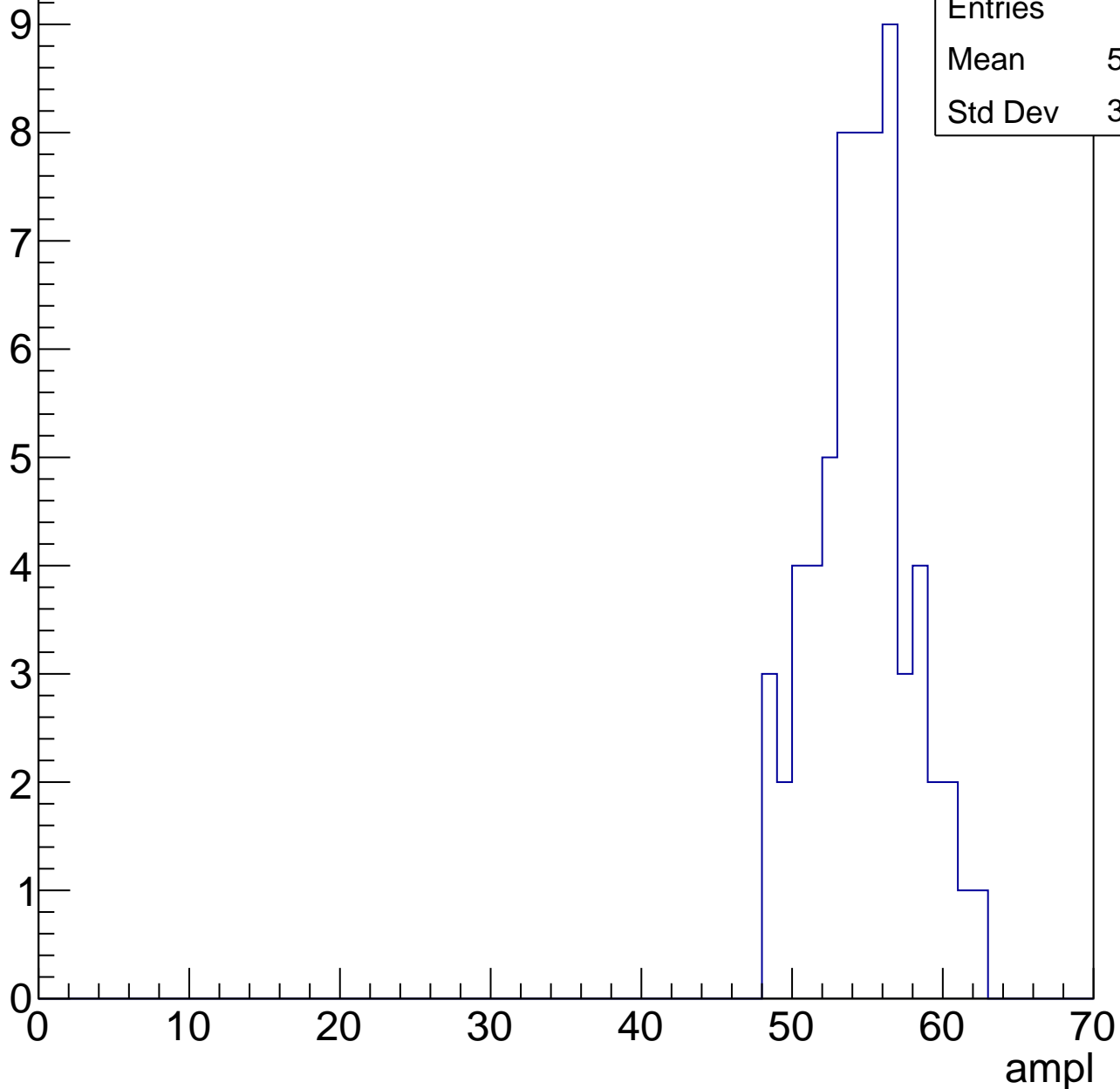
| | |
|---------|-------|
| Entries | 57 |
| Mean | 47.91 |
| Std Dev | 3.219 |



B1L100S, U9-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 64 |
| Mean | 54.22 |
| Std Dev | 3.194 |

B1L100S, U9-ch96, adc5

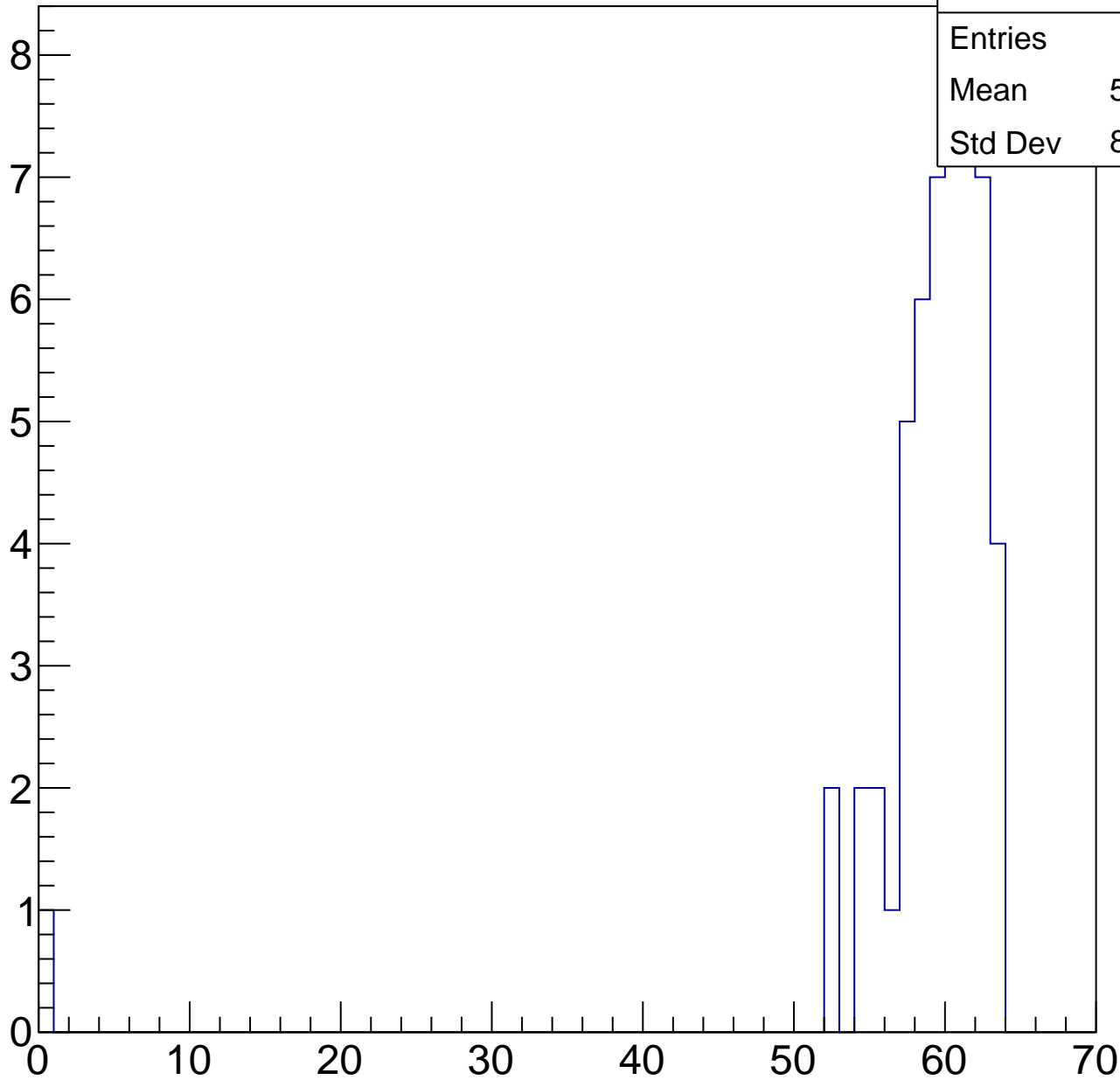
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.08 |
| Std Dev | 8.487 |

ampl

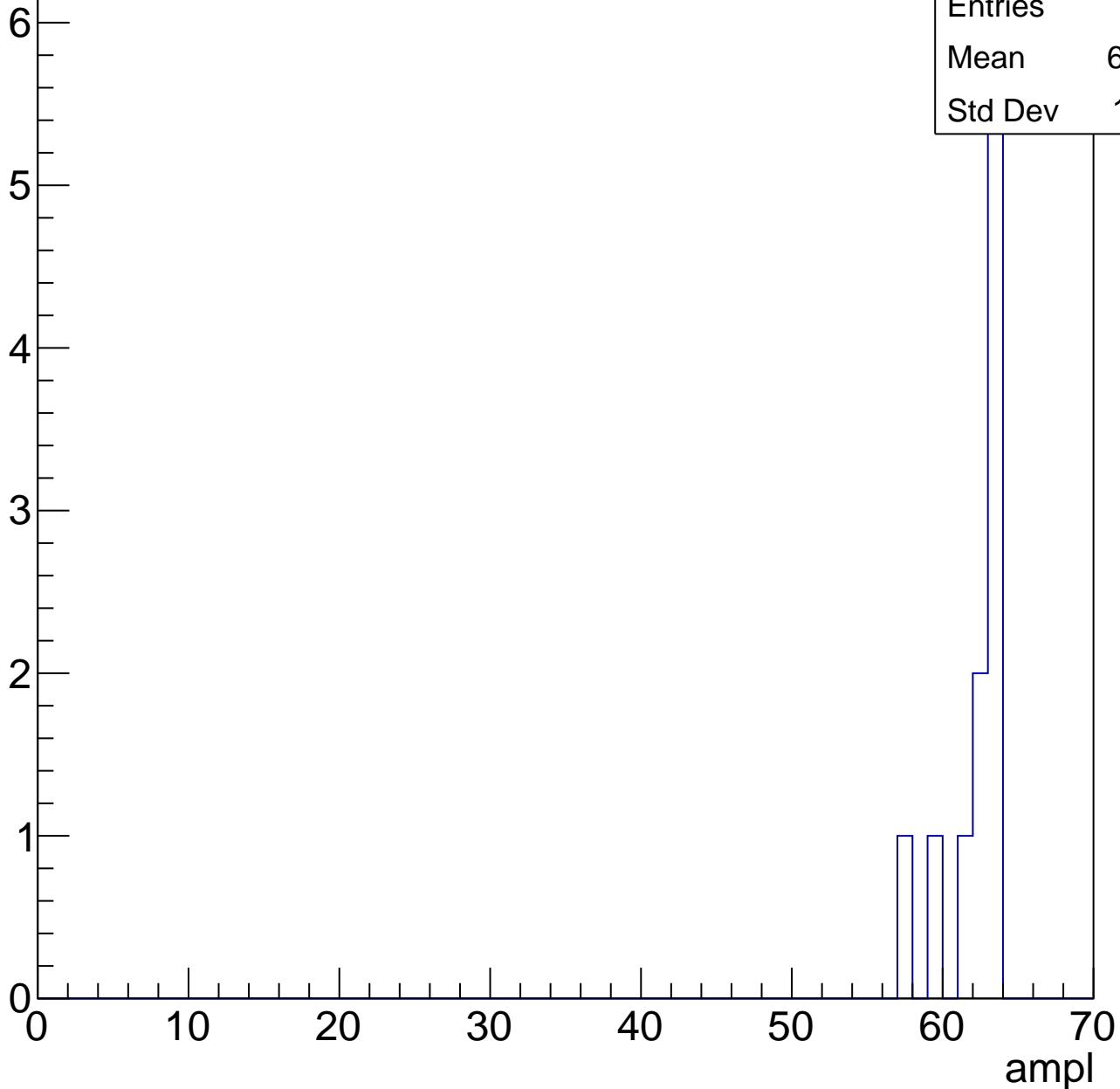


B1L100S, U9-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 61.73 |
| Std Dev | 1.911 |



B1L100S, U9-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch97, adc0

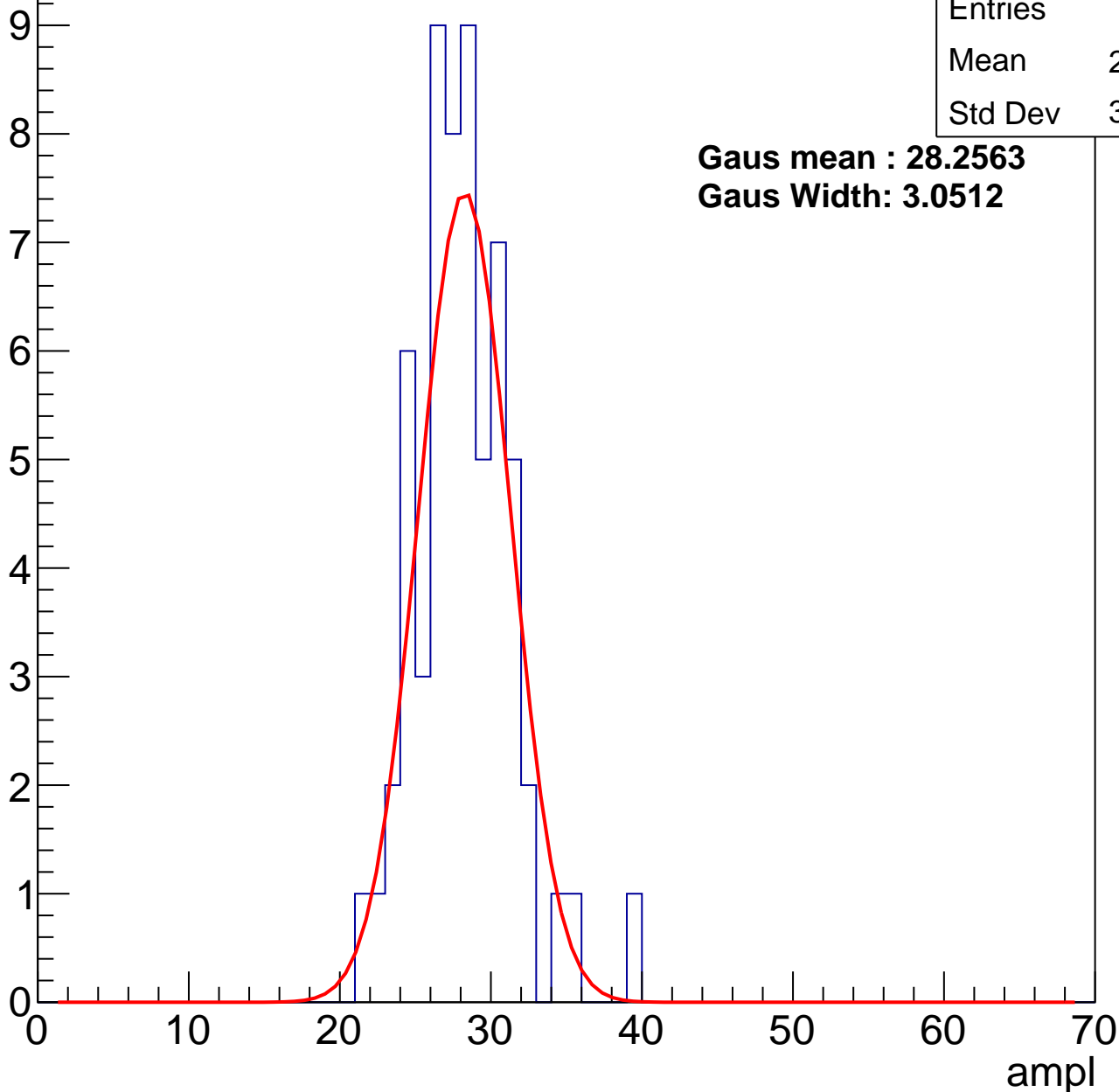
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 27.74 |
| Std Dev | 3.172 |

Gaus mean : 28.2563

Gaus Width: 3.0512



B1L100S, U9-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 87 |
| Mean | 35.23 |
| Std Dev | 3.947 |

Gaus mean : 35.4972

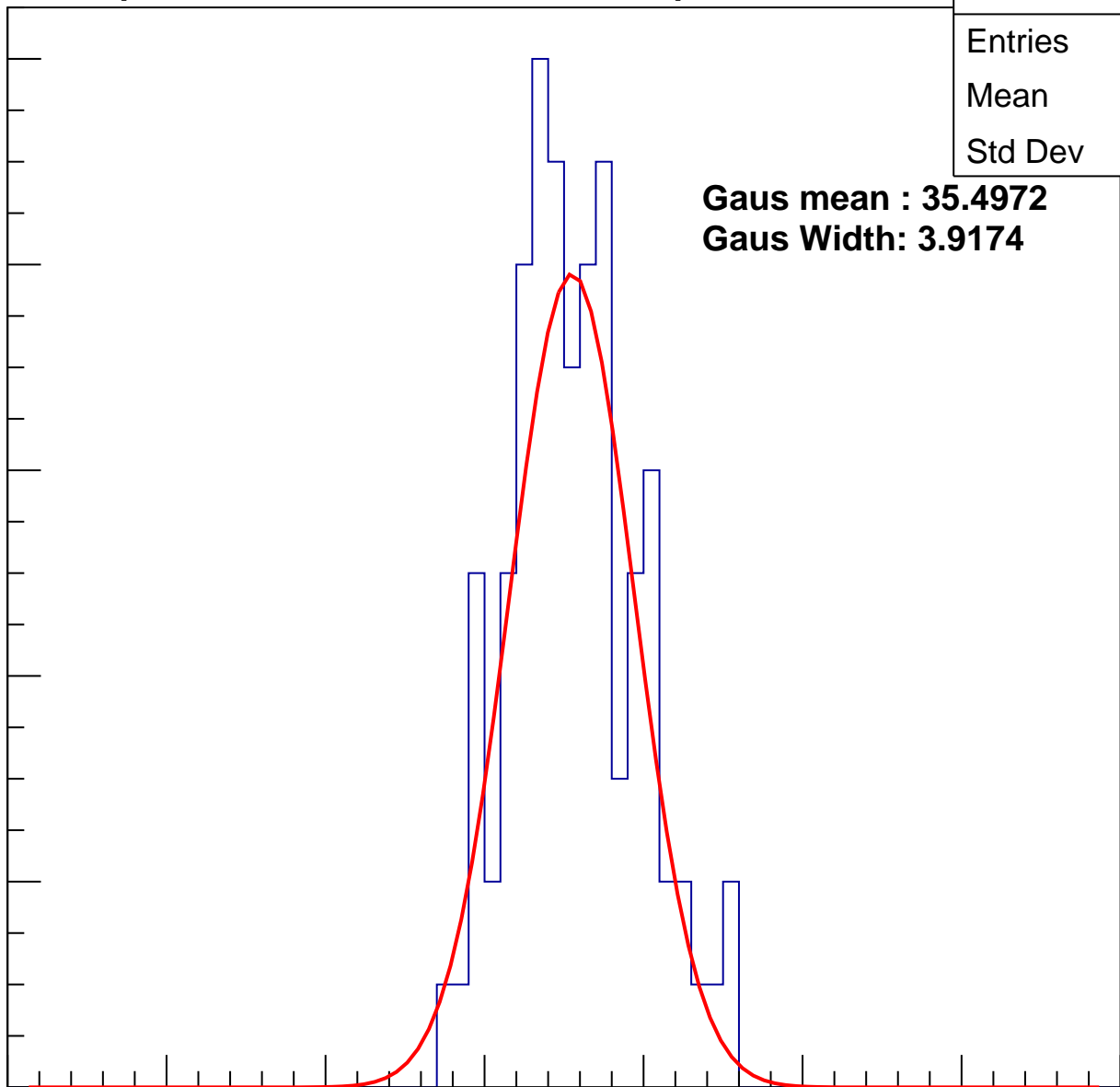
Gaus Width: 3.9174

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch97, adc2

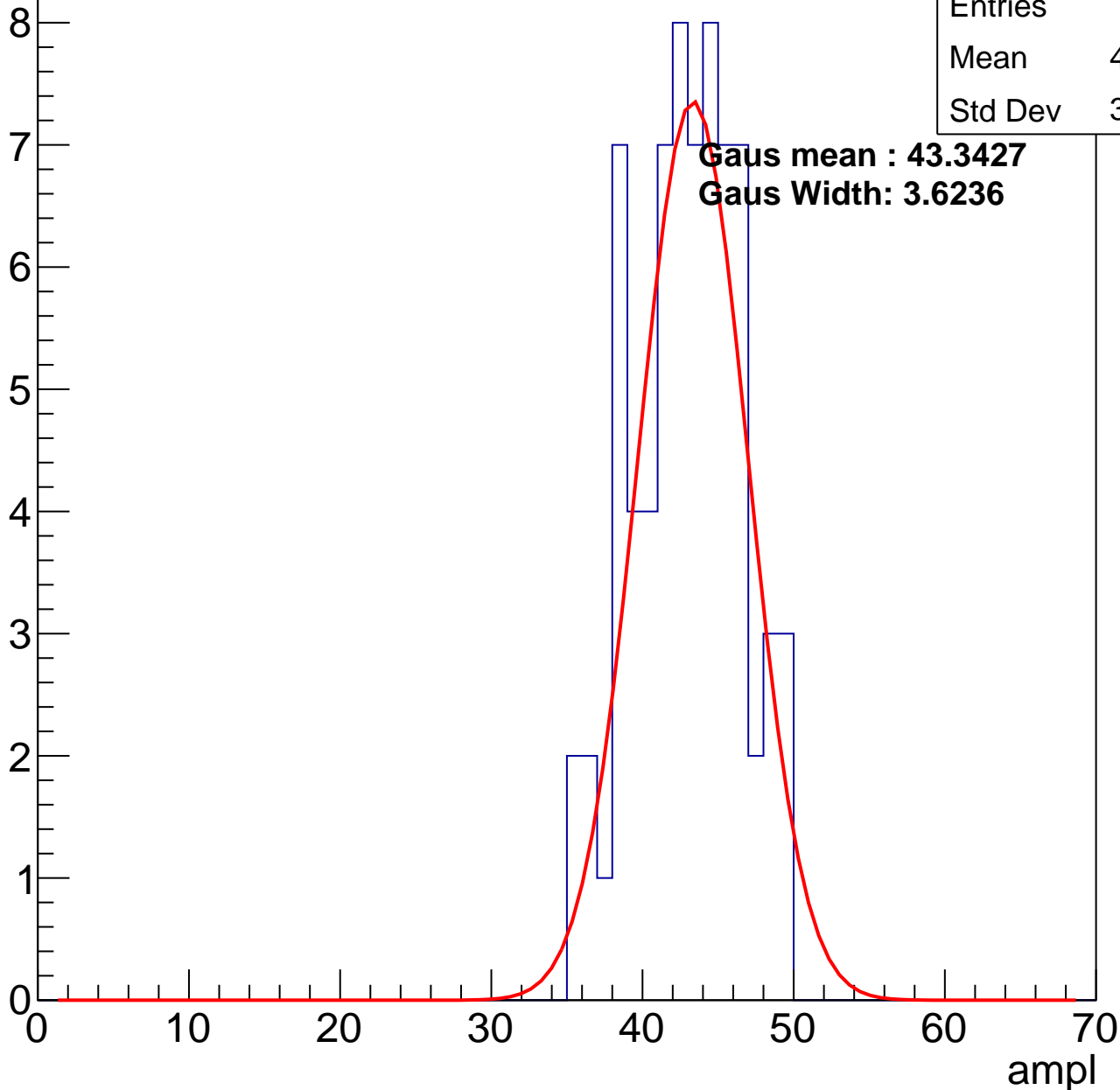
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 42.49 |
| Std Dev | 3.472 |

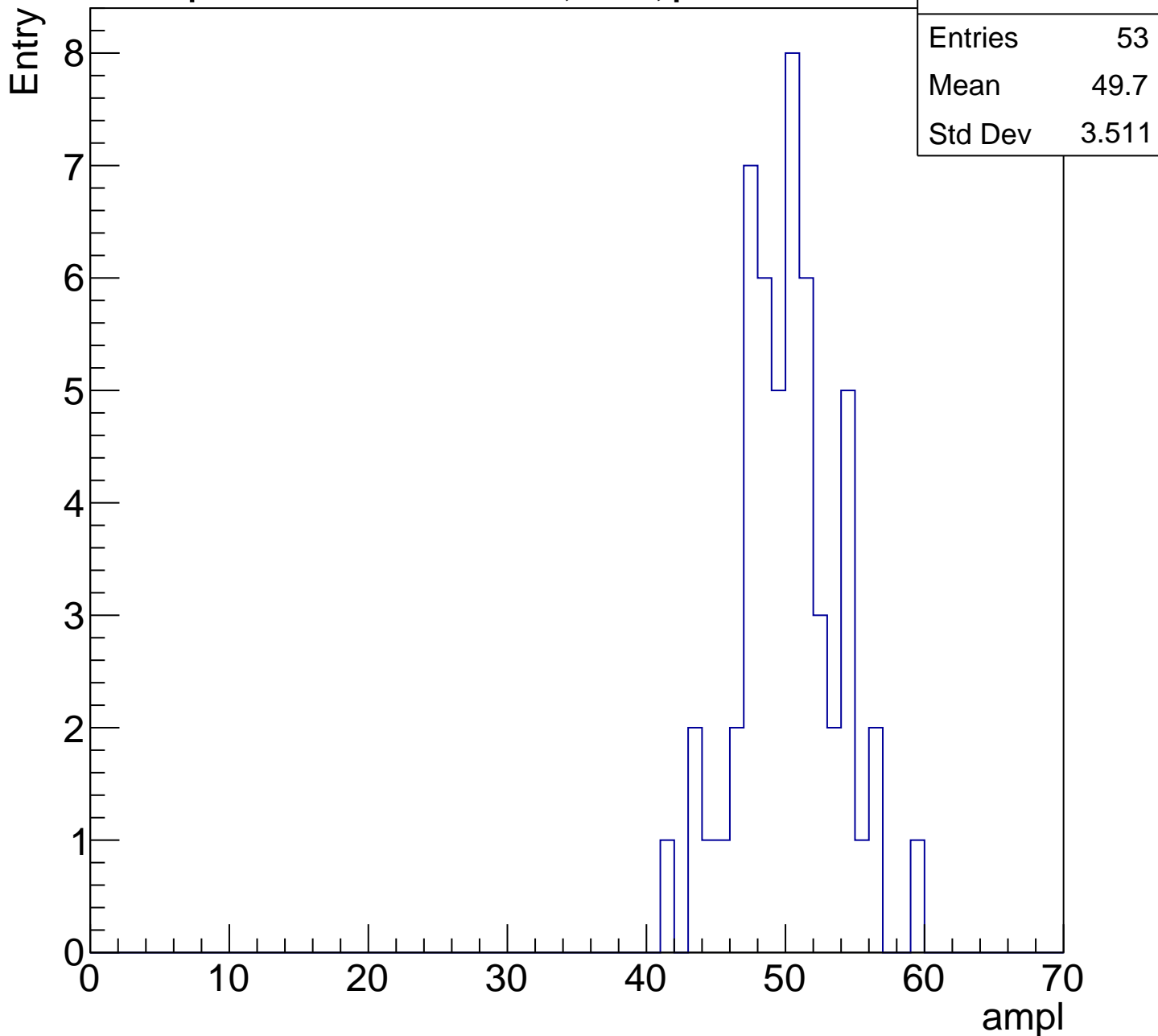
Gaus mean : 43.3427

Gaus Width: 3.6236



B1L100S, U9-ch97, adc3

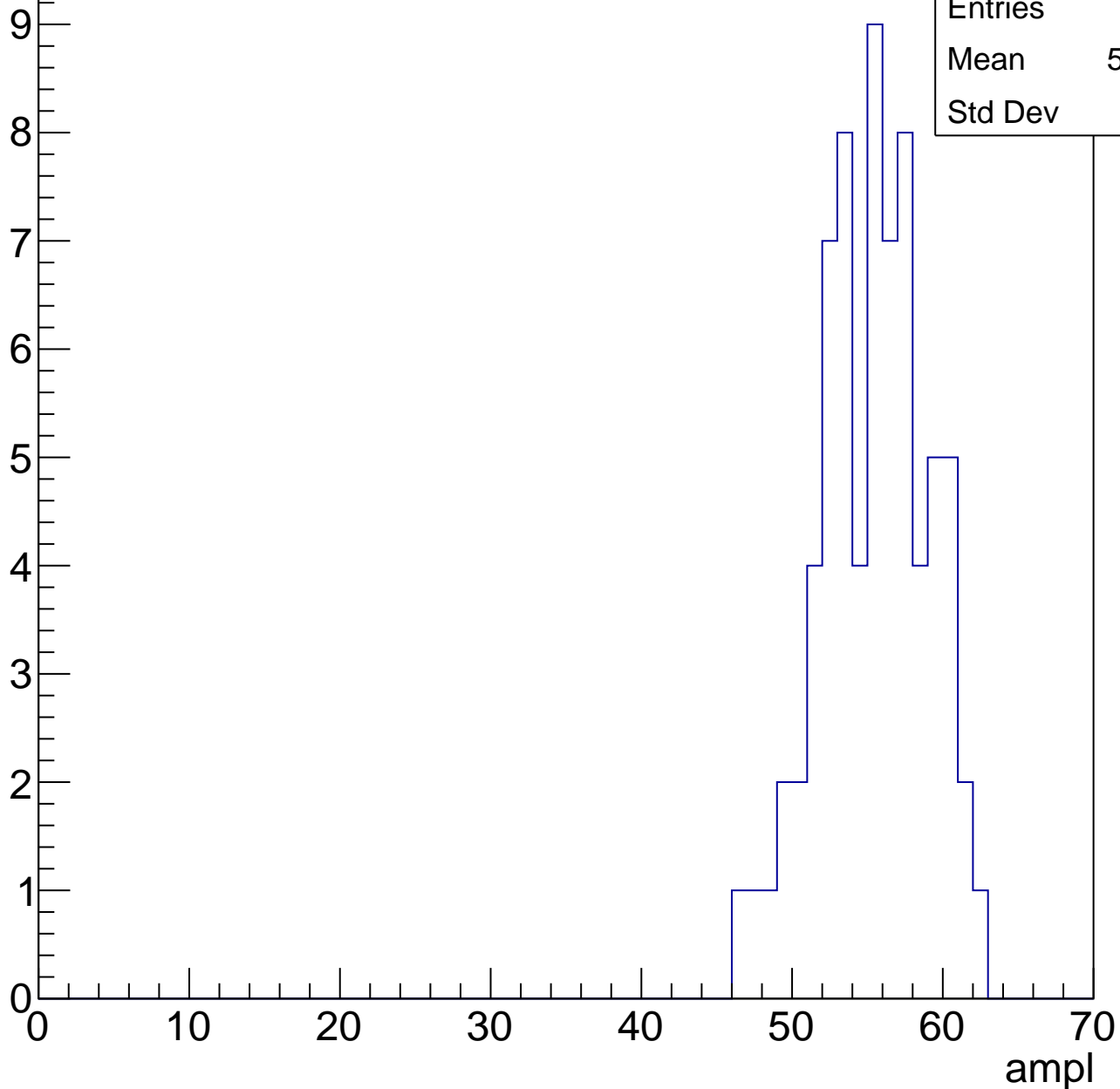
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

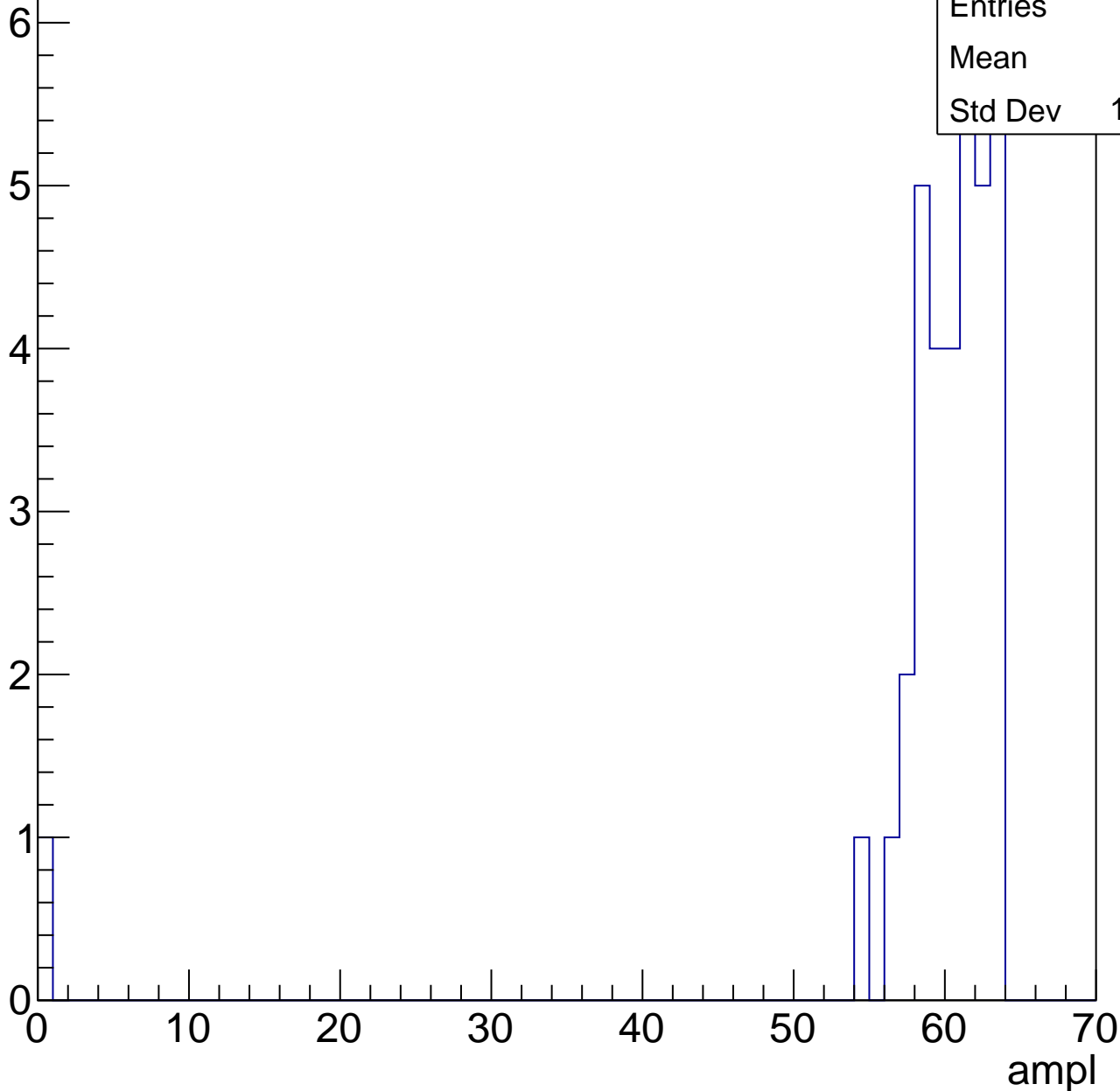


B1L100S, U9-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

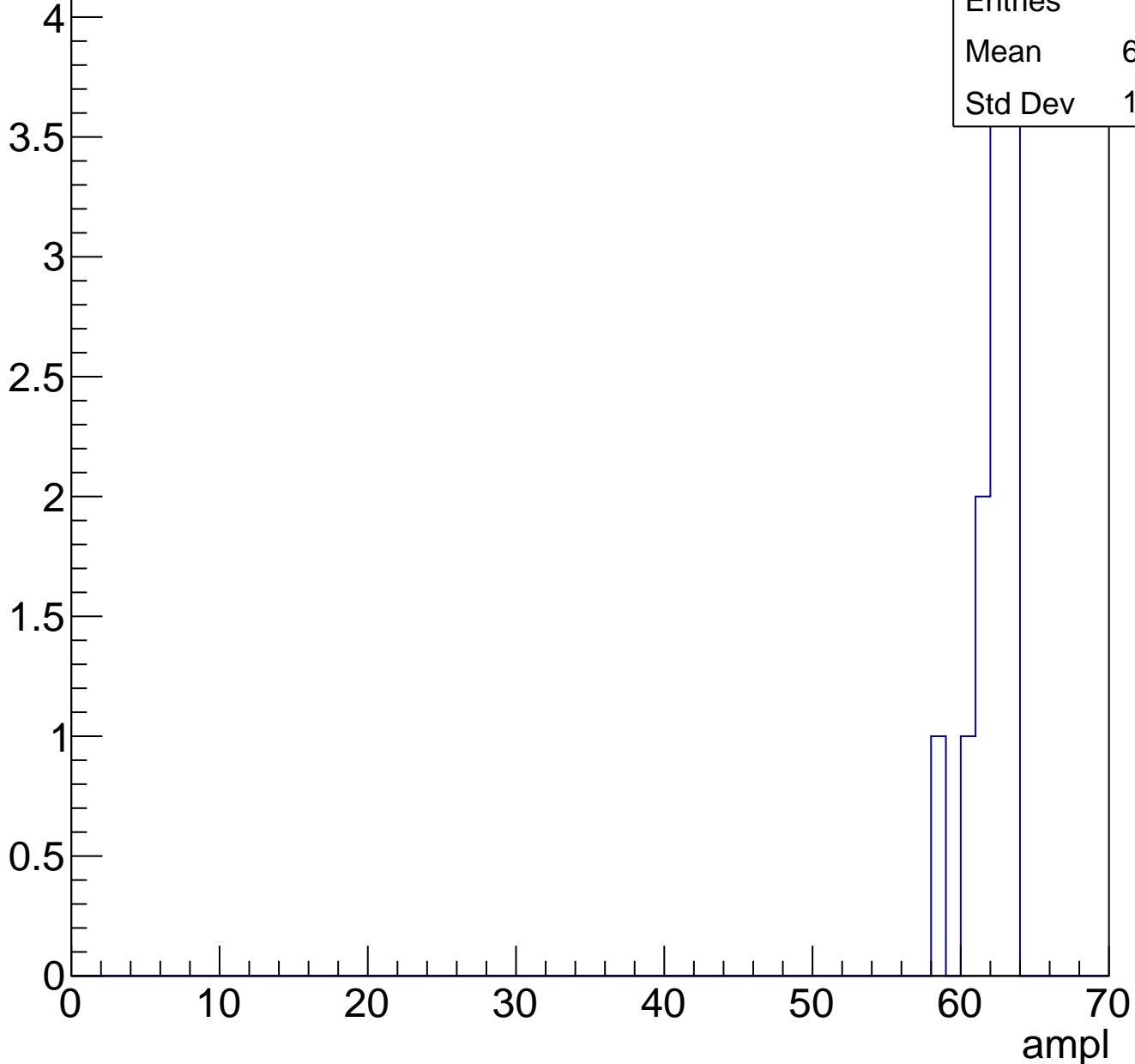
| | |
|---------|-------|
| Entries | 35 |
| Mean | 58.4 |
| Std Dev | 10.26 |



B1L100S, U9-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 10 |
| Std Dev | 10 |

B1L100S, U9-ch98, adc0

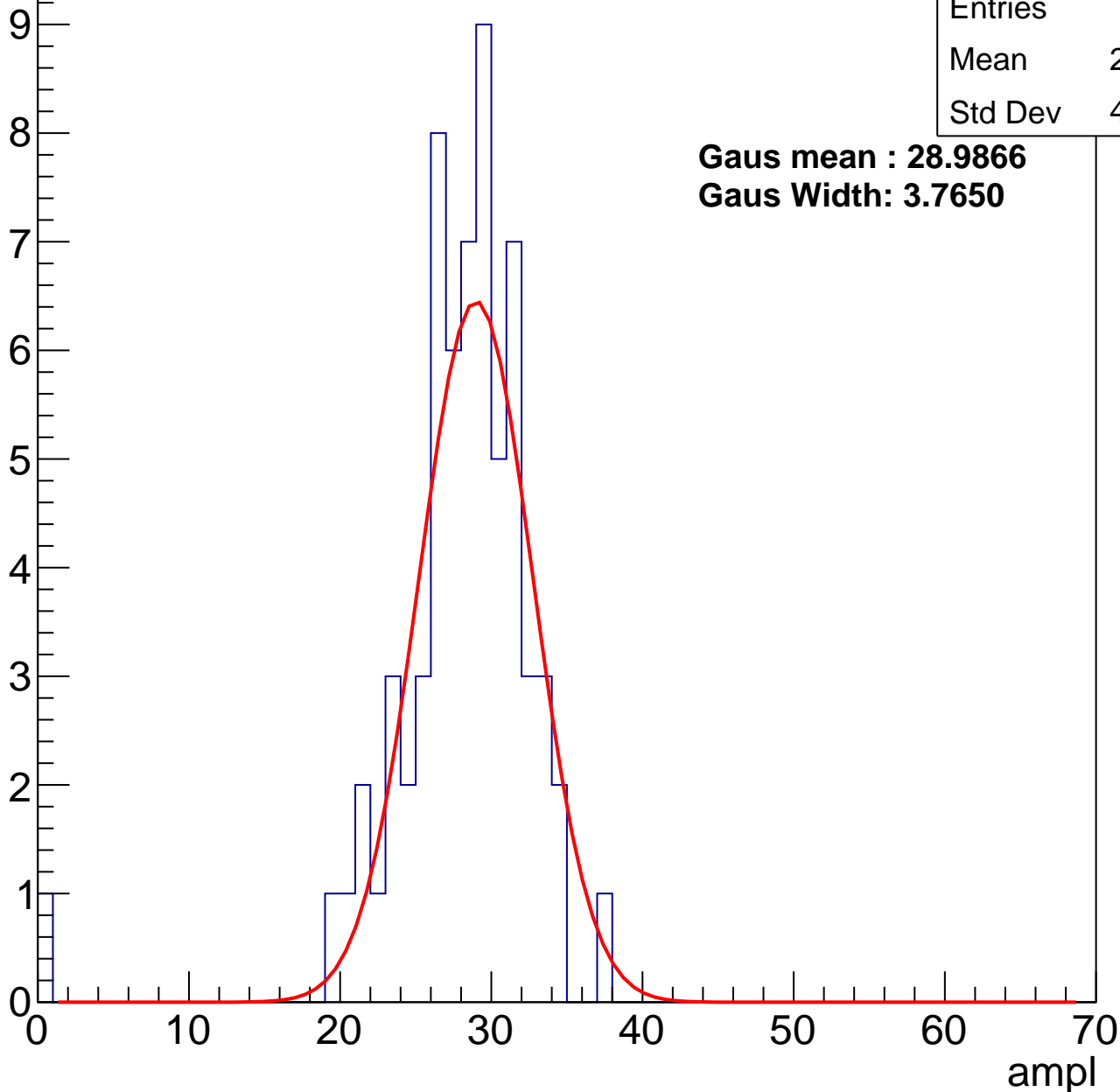
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 27.52 |
| Std Dev | 4.943 |

Gaus mean : 28.9866

Gaus Width: 3.7650



B1L100S, U9-ch98, adc1

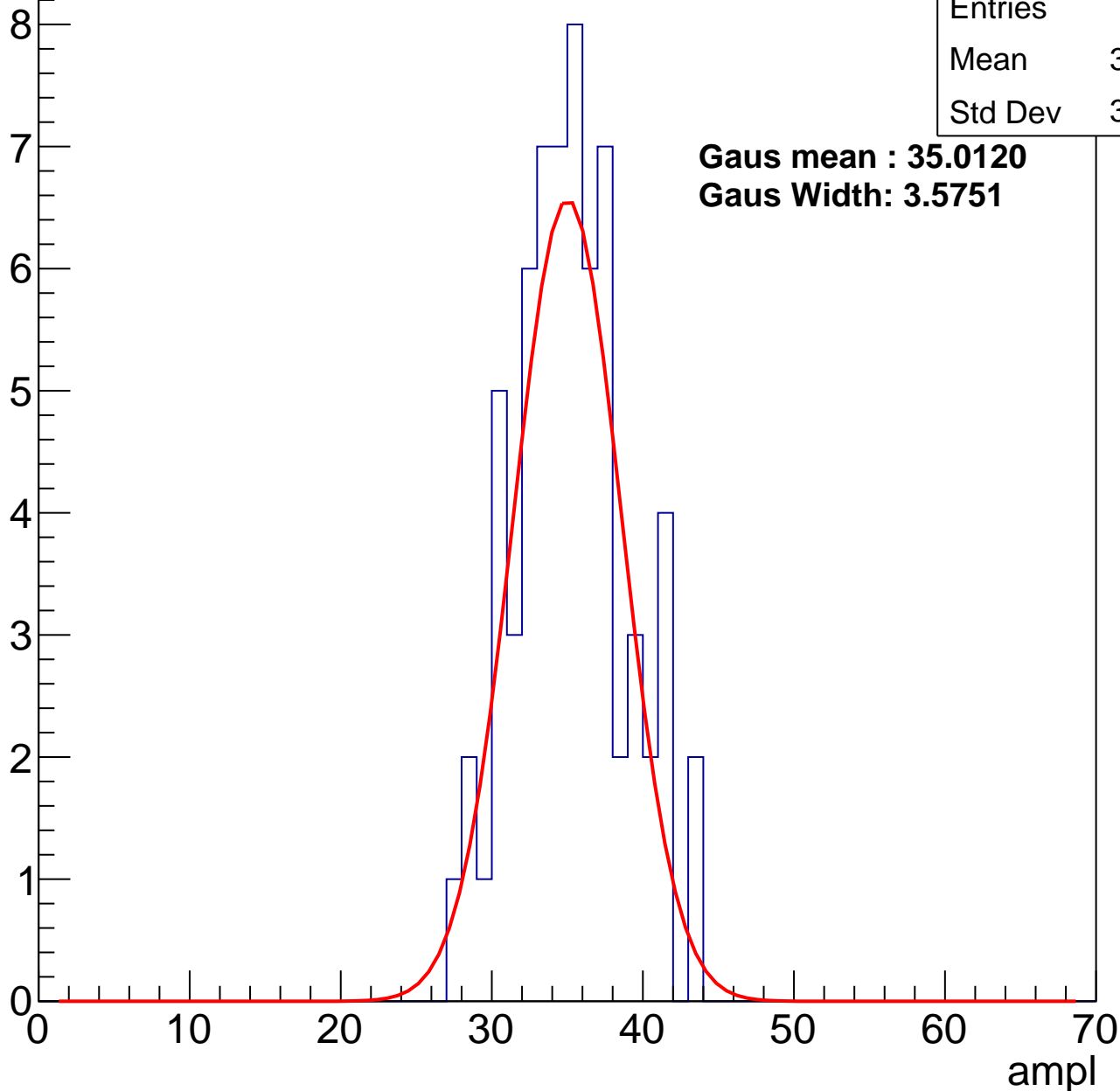
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 34.76 |
| Std Dev | 3.648 |

Gaus mean : 35.0120

Gaus Width: 3.5751



B1L100S, U9-ch98, adc2

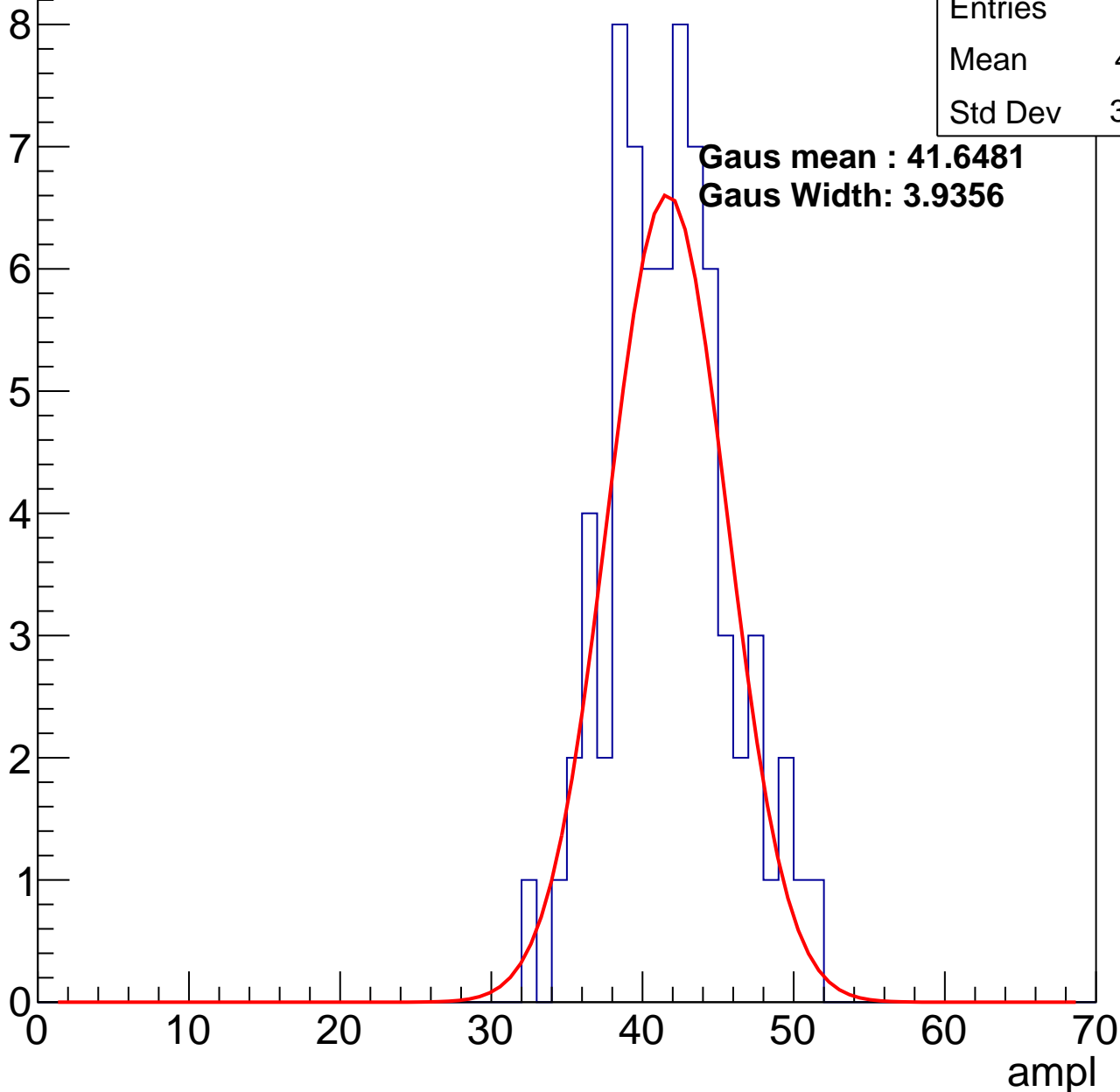
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 41.31 |
| Std Dev | 3.913 |

Gaus mean : 41.6481

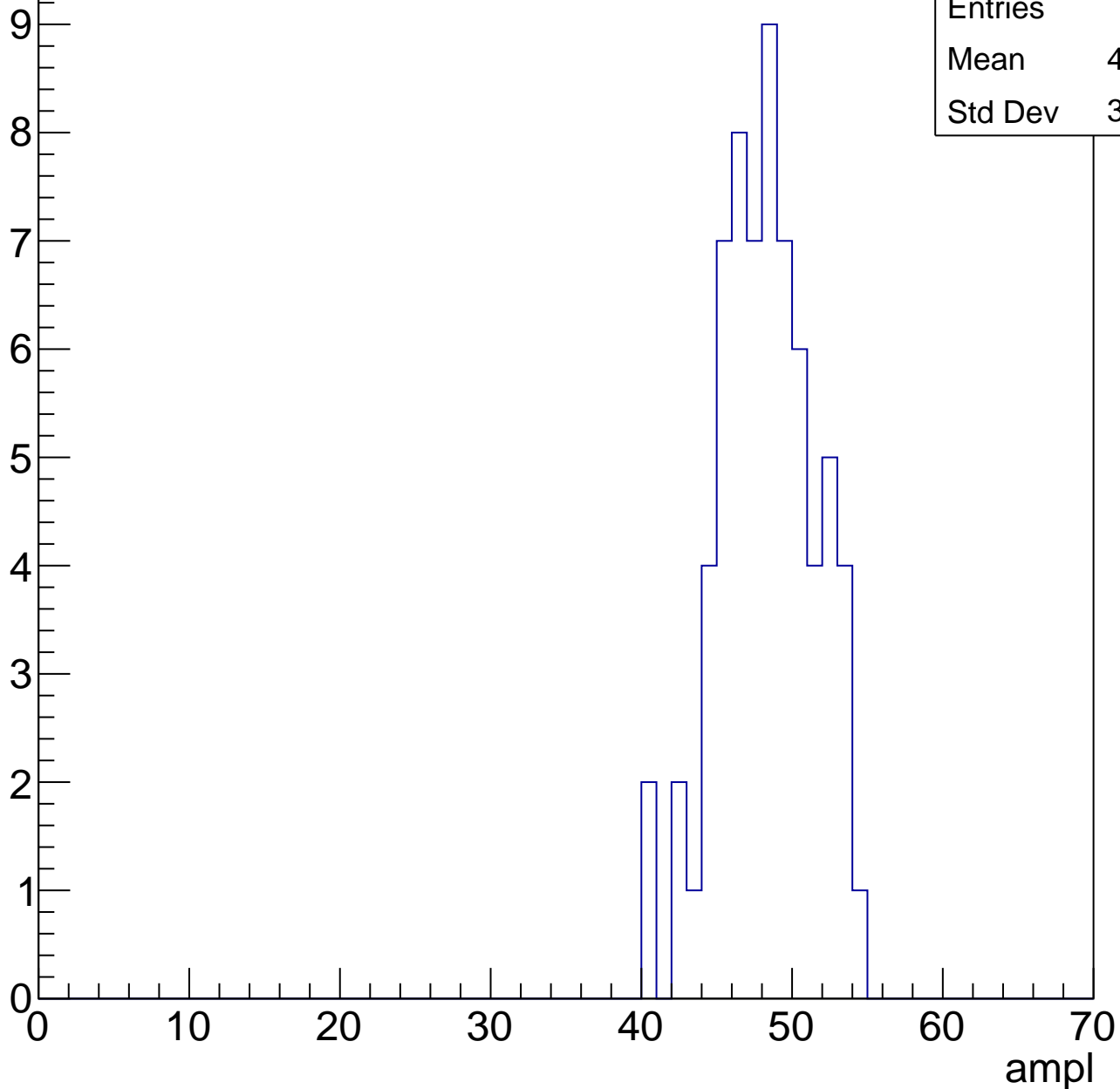
Gaus Width: 3.9356



B1L100S, U9-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

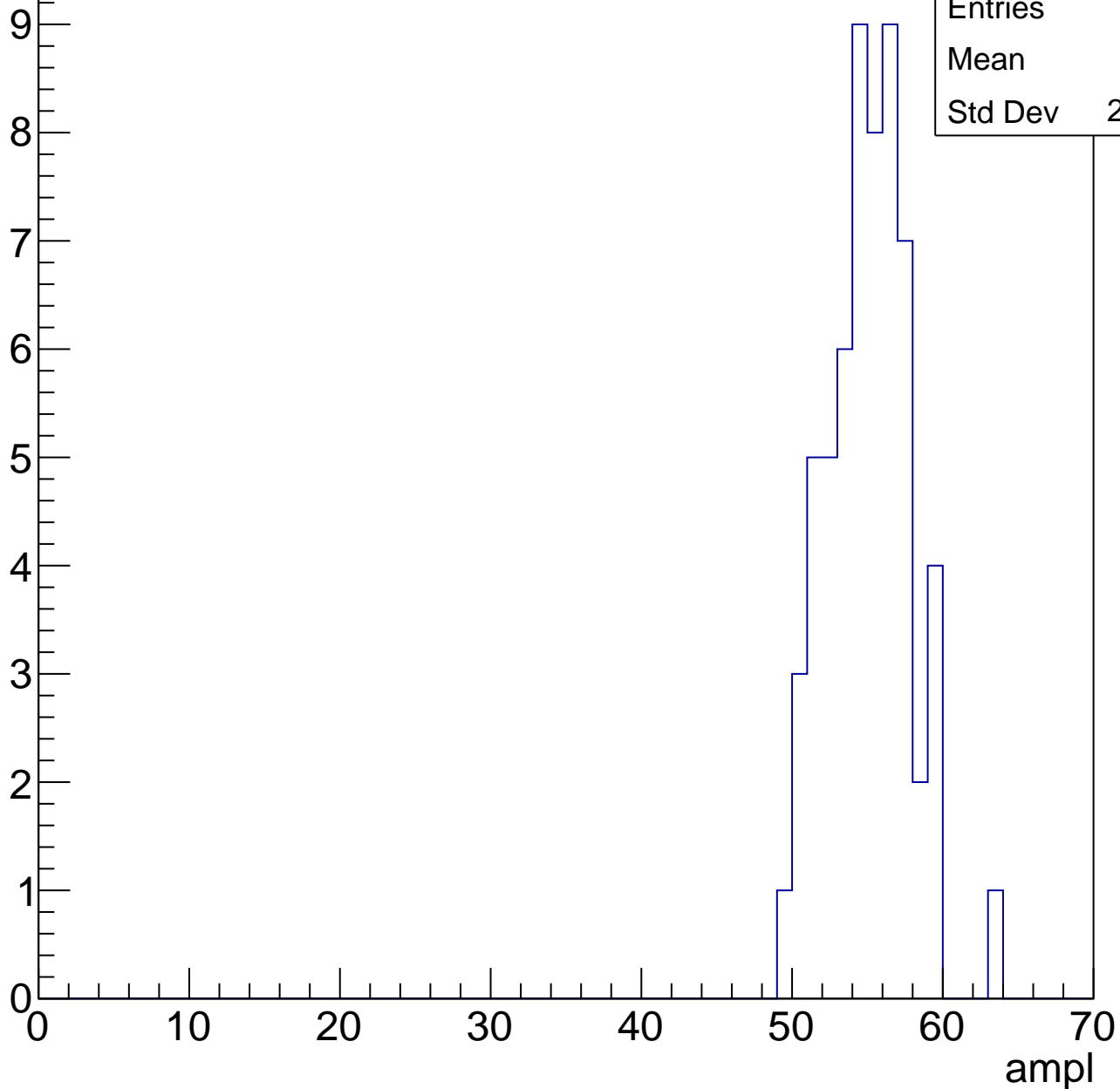
Entry



B1L100S, U9-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

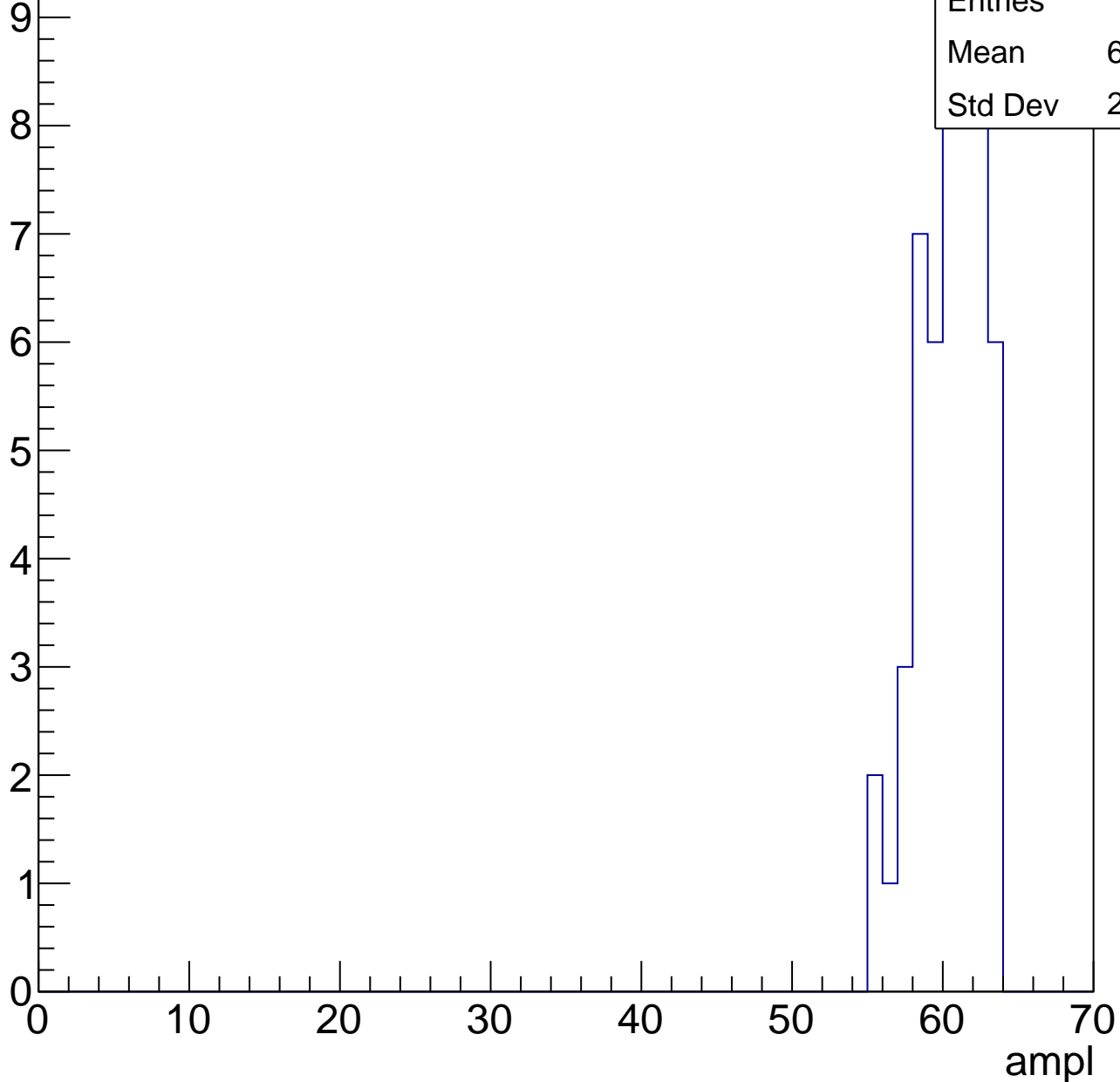


| | |
|---------|-------|
| Entries | 60 |
| Mean | 54.6 |
| Std Dev | 2.715 |

B1L100S, U9-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

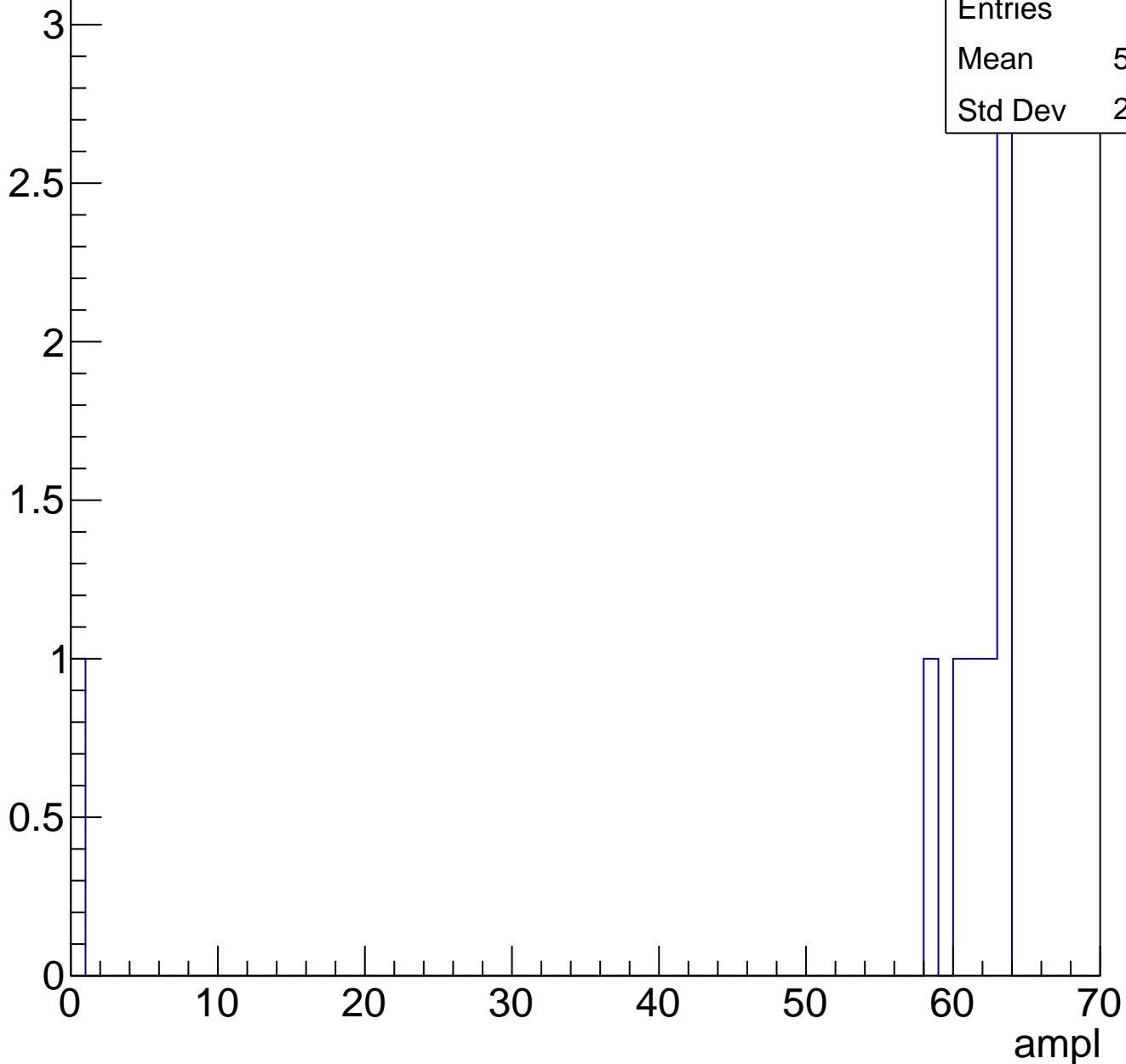
Entry



B1L100S, U9-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch99, adc0

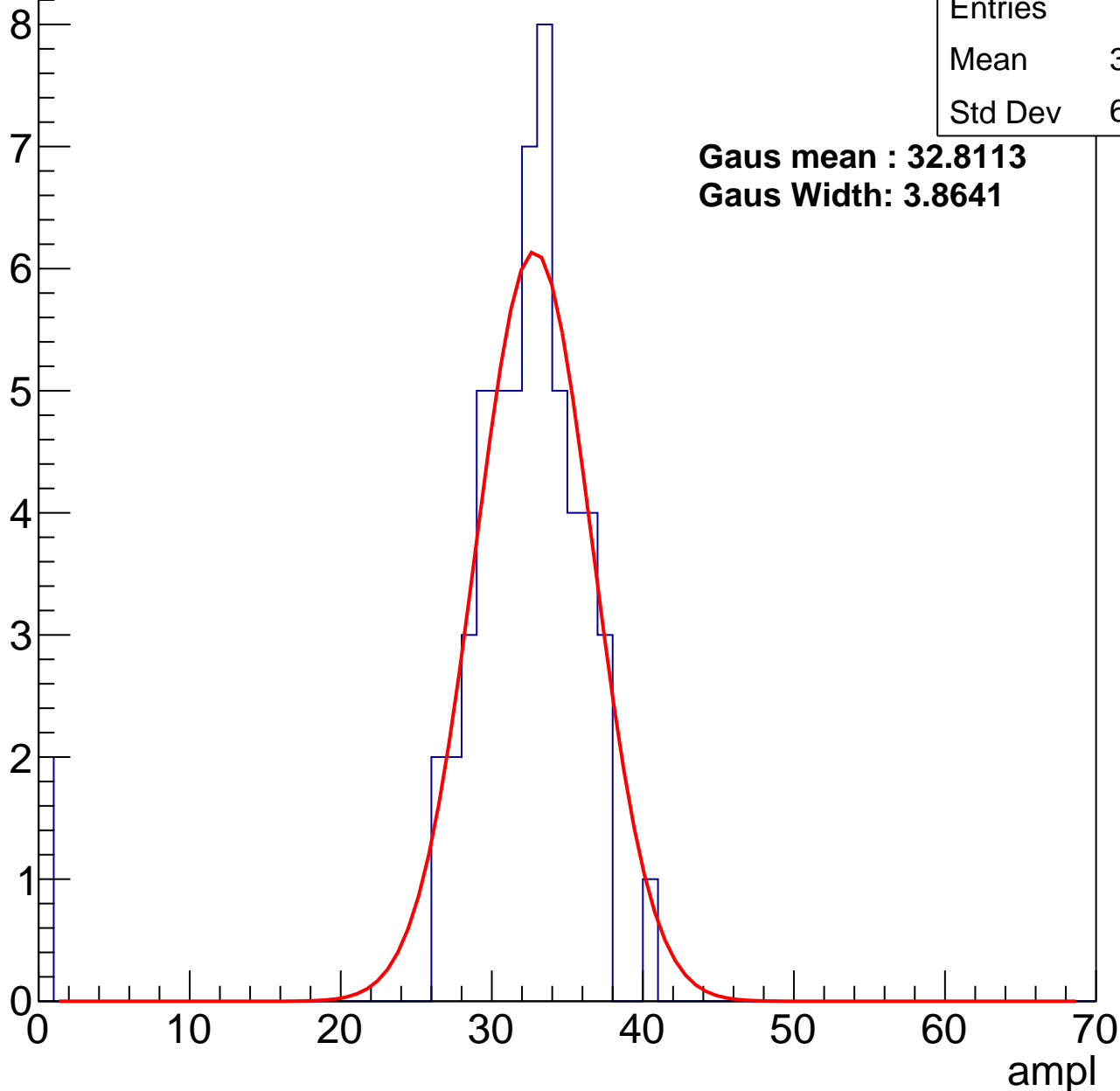
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 30.95 |
| Std Dev | 6.672 |

Gaus mean : 32.8113

Gaus Width: 3.8641



B1L100S, U9-ch99, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 38.68 |
| Std Dev | 3.668 |

Gaus mean : 39.4213

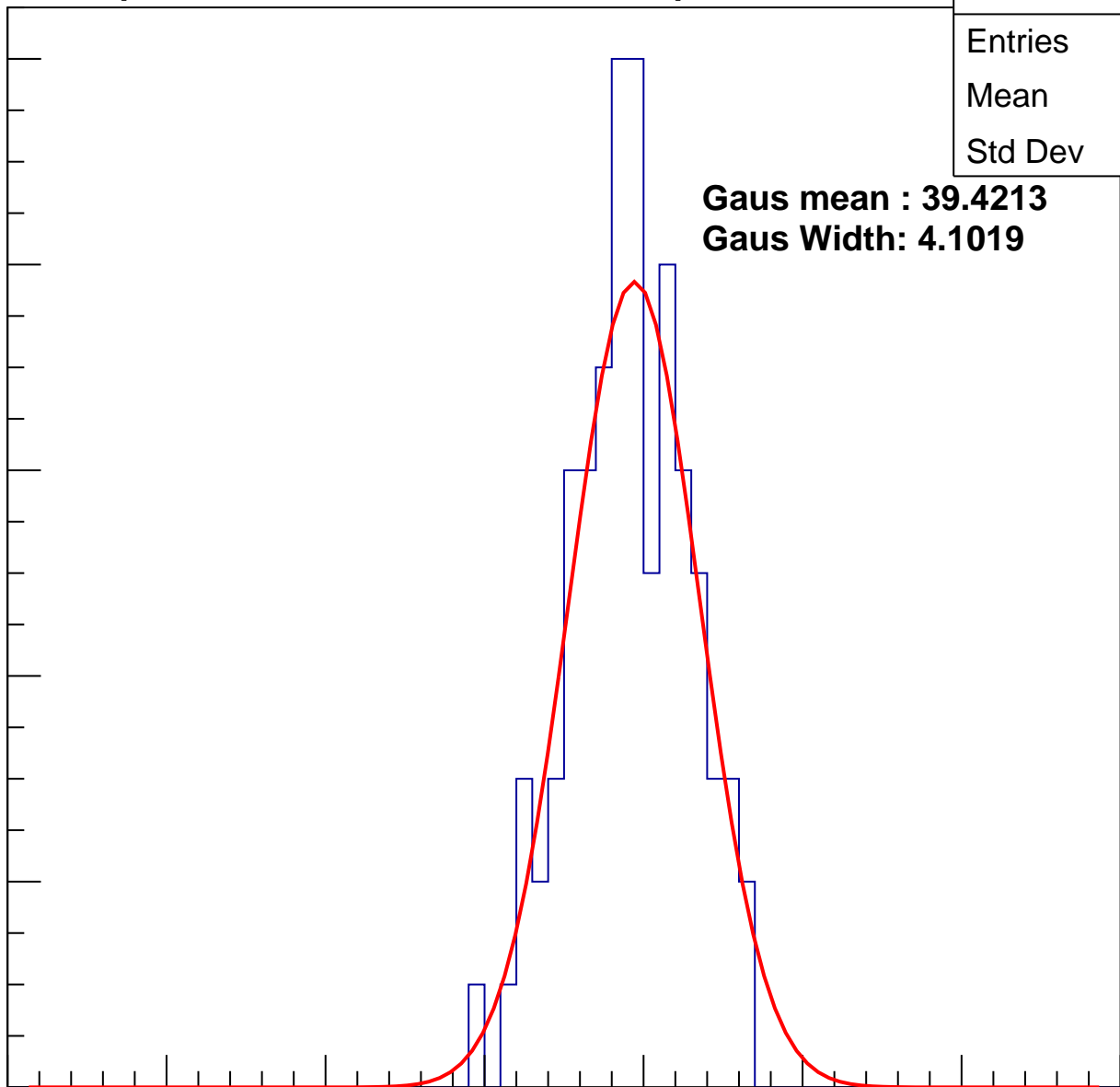
Gaus Width: 4.1019

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch99, adc2

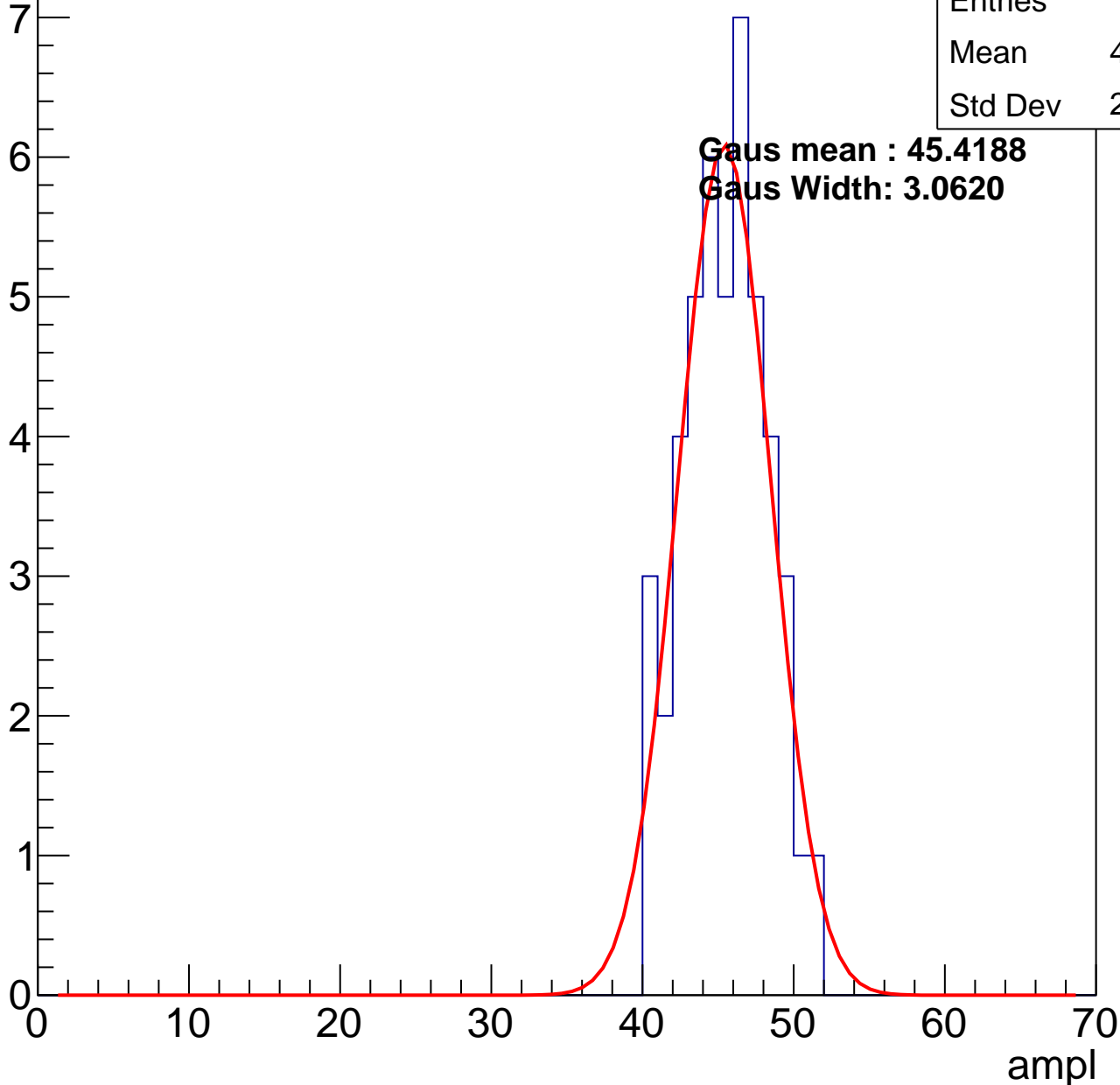
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 45.02 |
| Std Dev | 2.723 |

Gaus mean : 45.4188

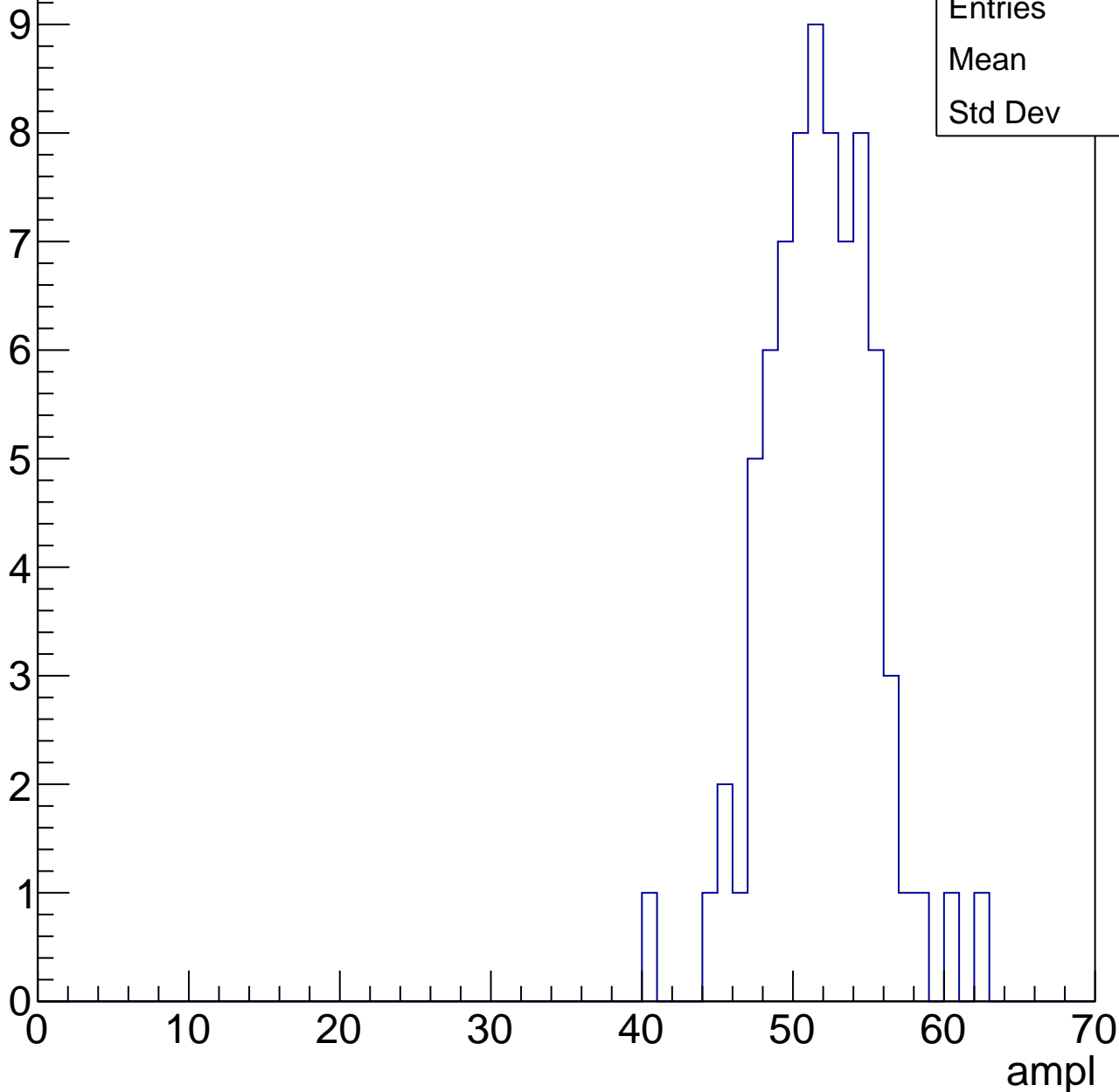
Gaus Width: 3.0620



B1L100S, U9-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

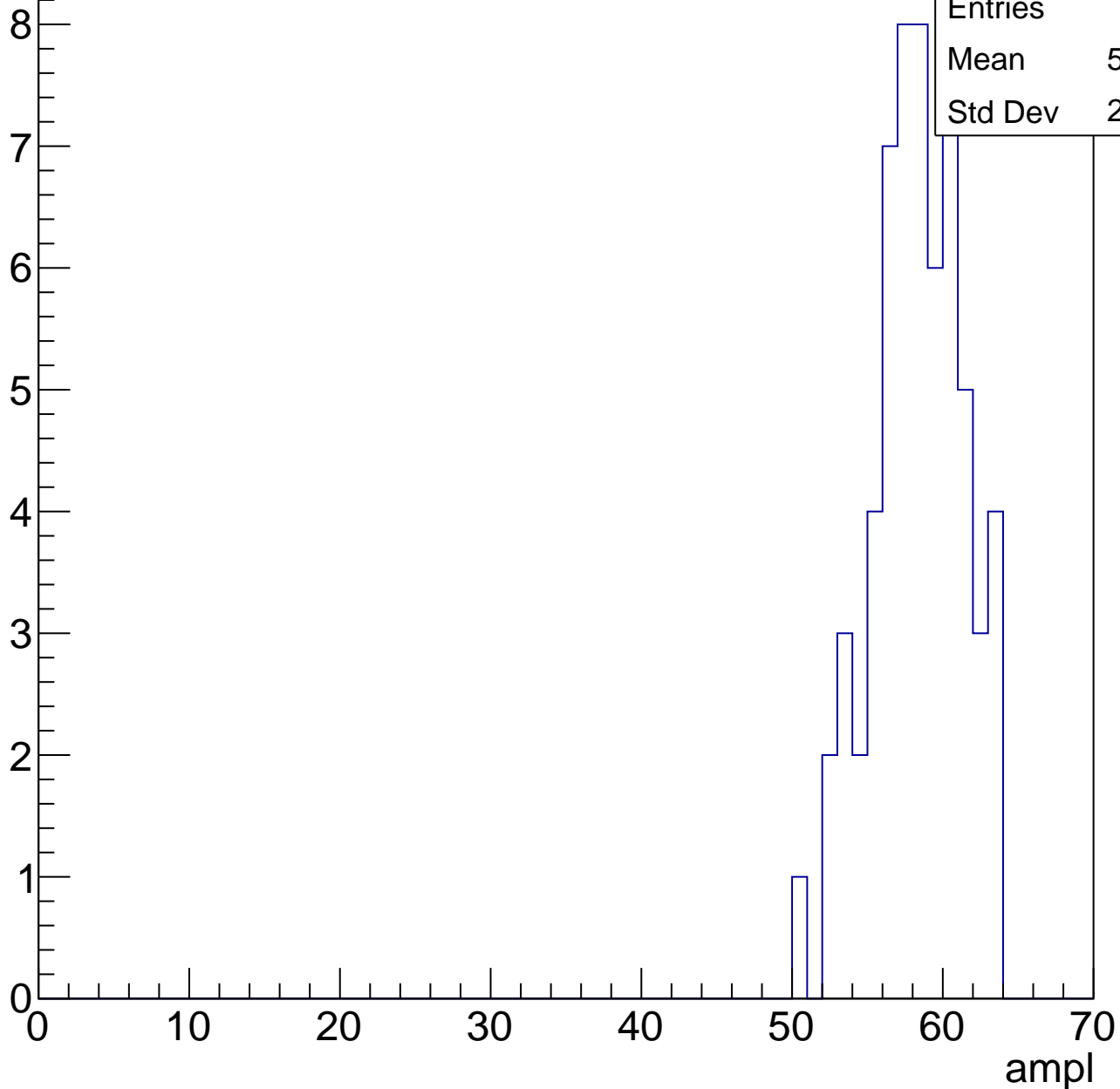
Entry



B1L100S, U9-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

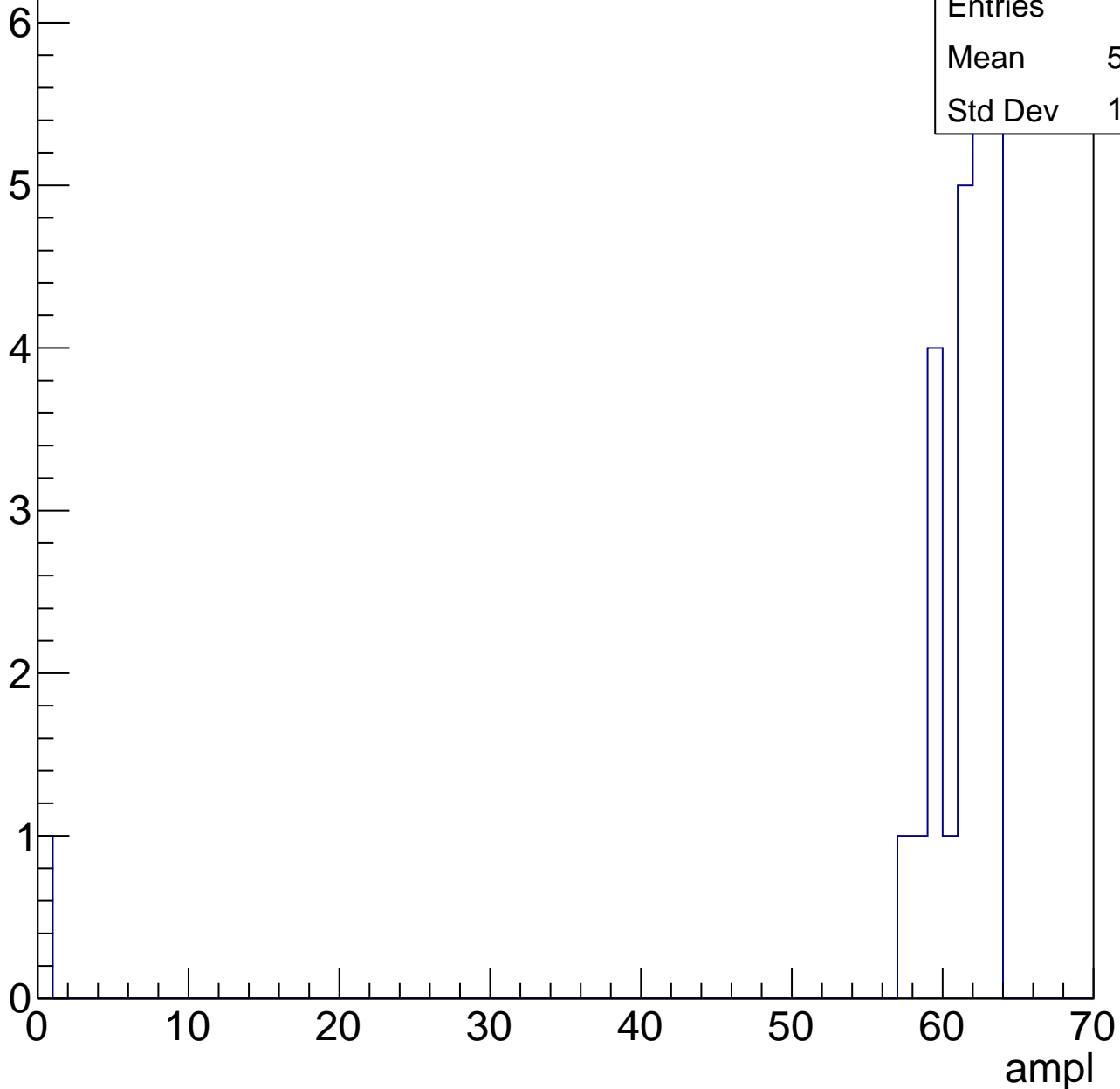


B1L100S, U9-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 25 |
| Mean | 58.64 |
| Std Dev | 12.09 |



B1L100S, U9-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

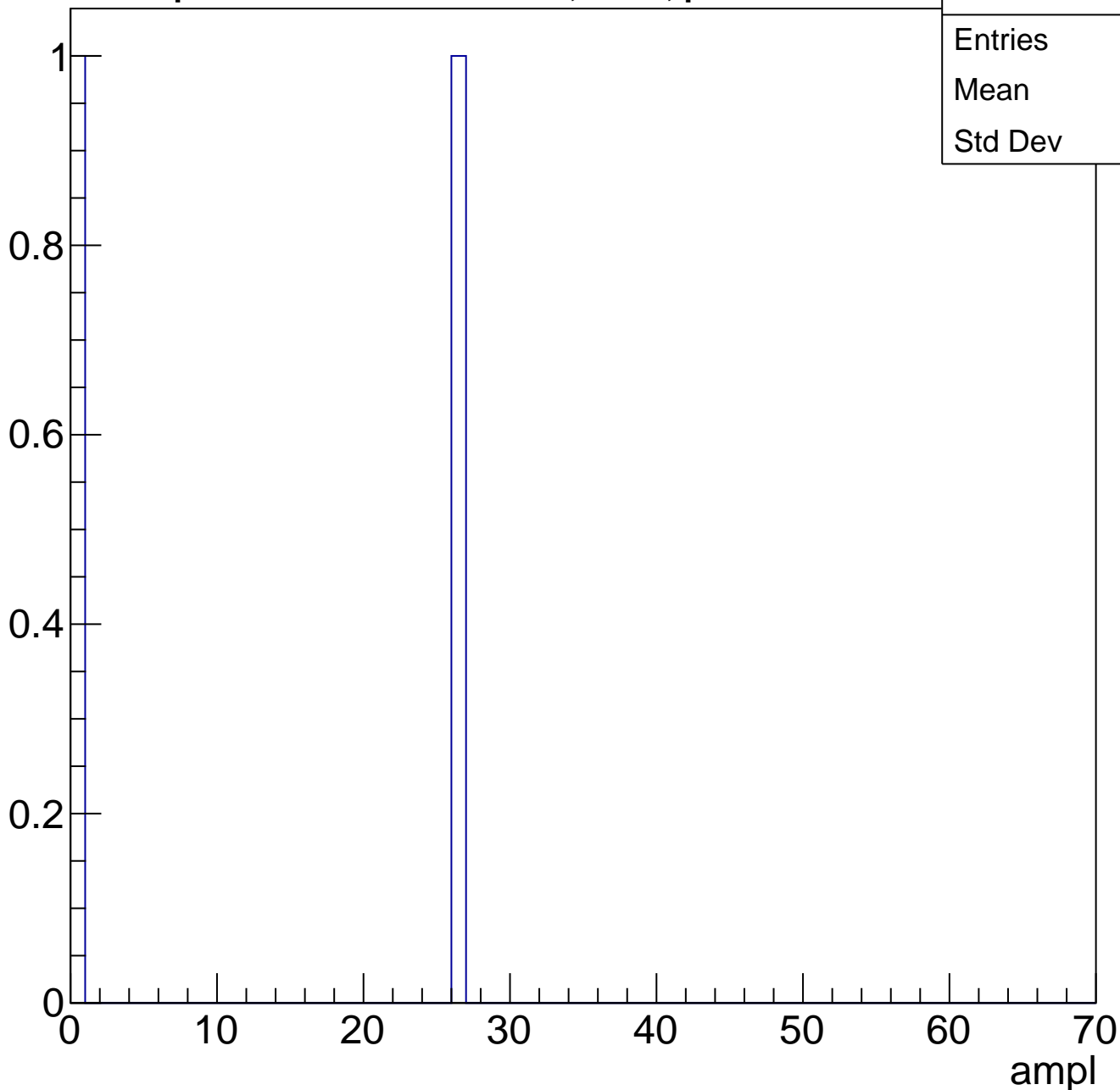


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 13 |
| Std Dev | 13 |

B1L100S, U9-ch100, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 31.03 |
| Std Dev | 3.561 |

Gaus mean : 31.9800

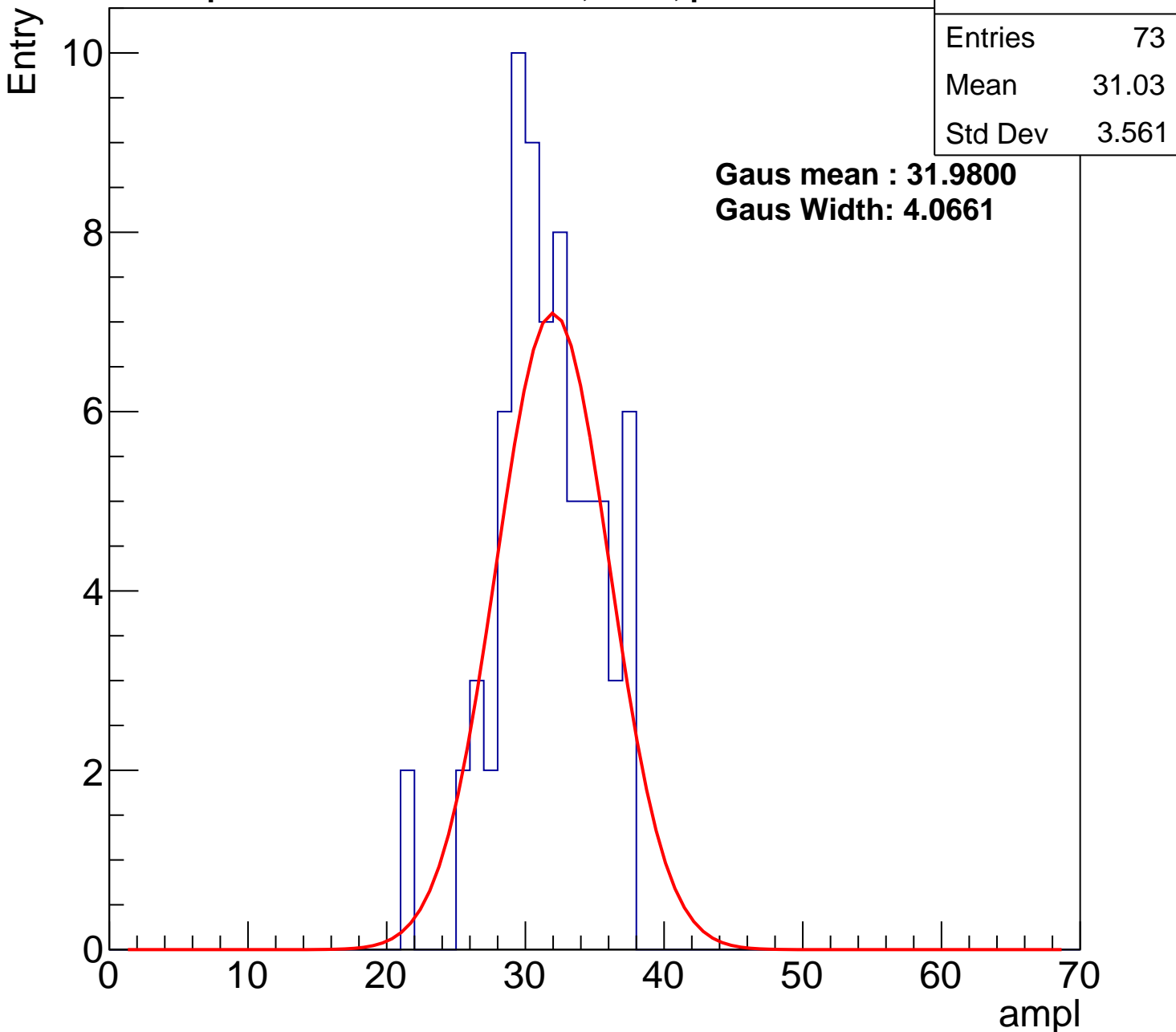
Gaus Width: 4.0661

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch100, adc1

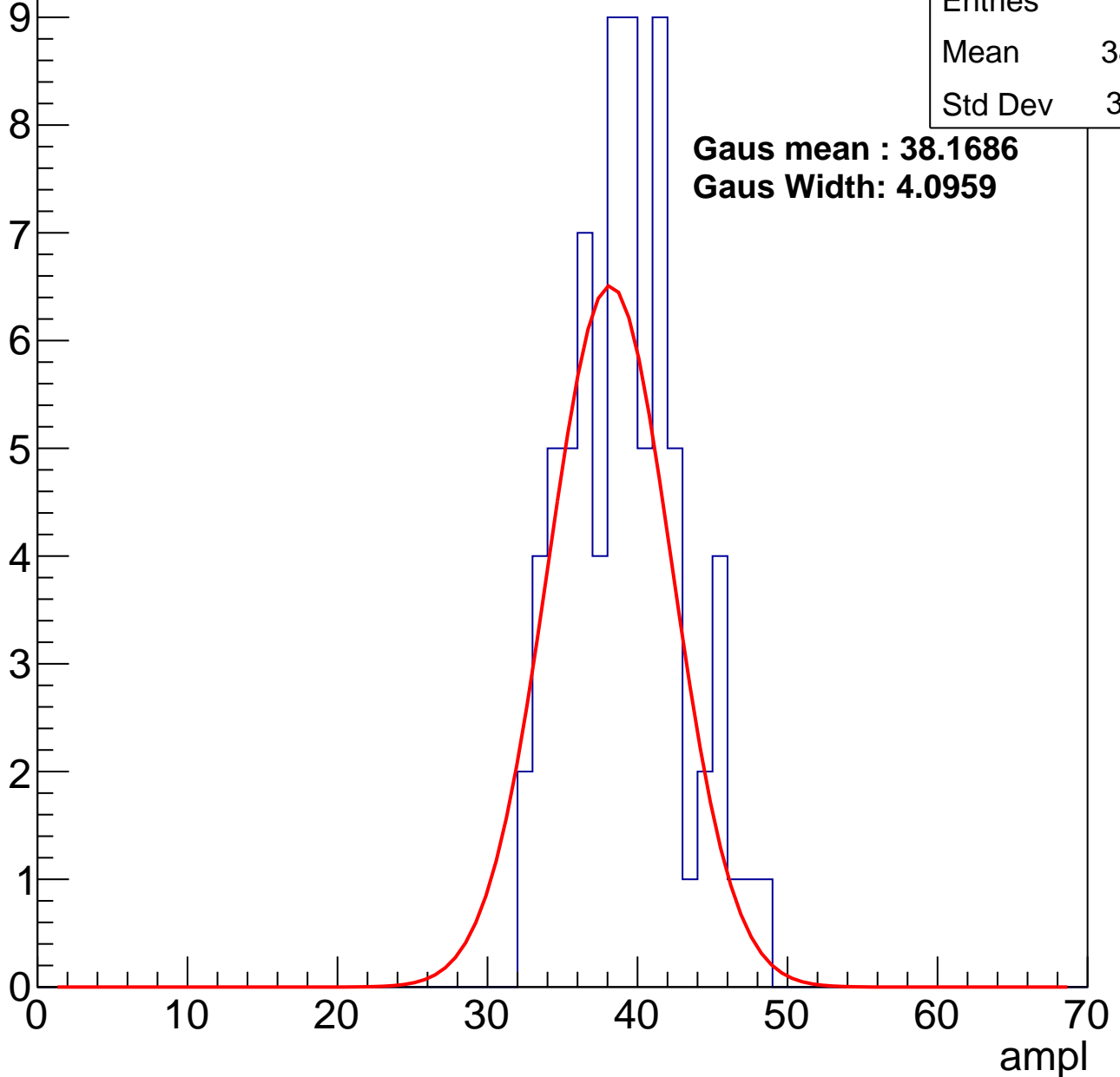
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 38.72 |
| Std Dev | 3.711 |

Gaus mean : 38.1686

Gaus Width: 4.0959



B1L100S, U9-ch100, adc2

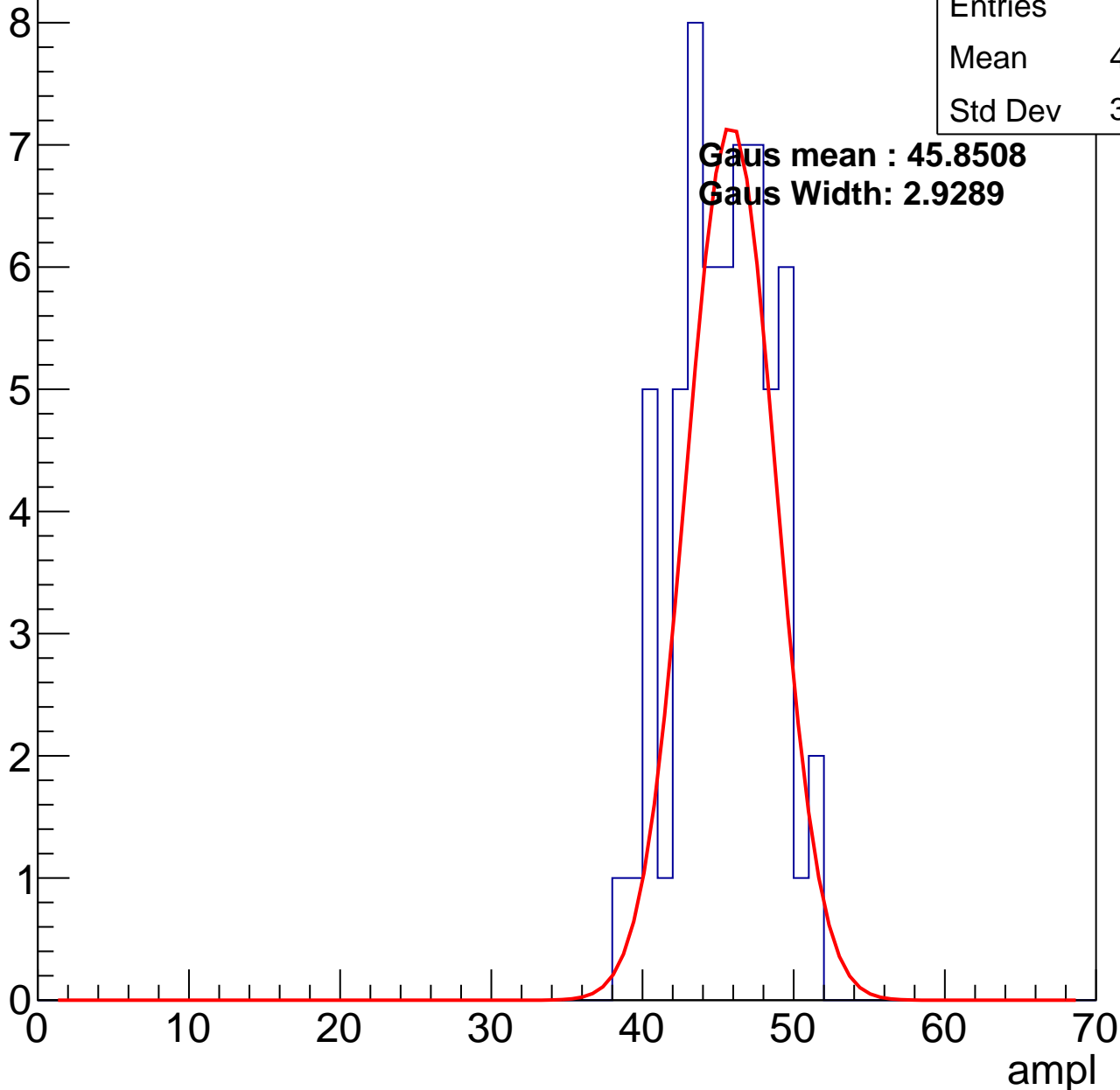
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 44.97 |
| Std Dev | 3.089 |

Gaus mean : 45.8508

Gaus Width: 2.9289

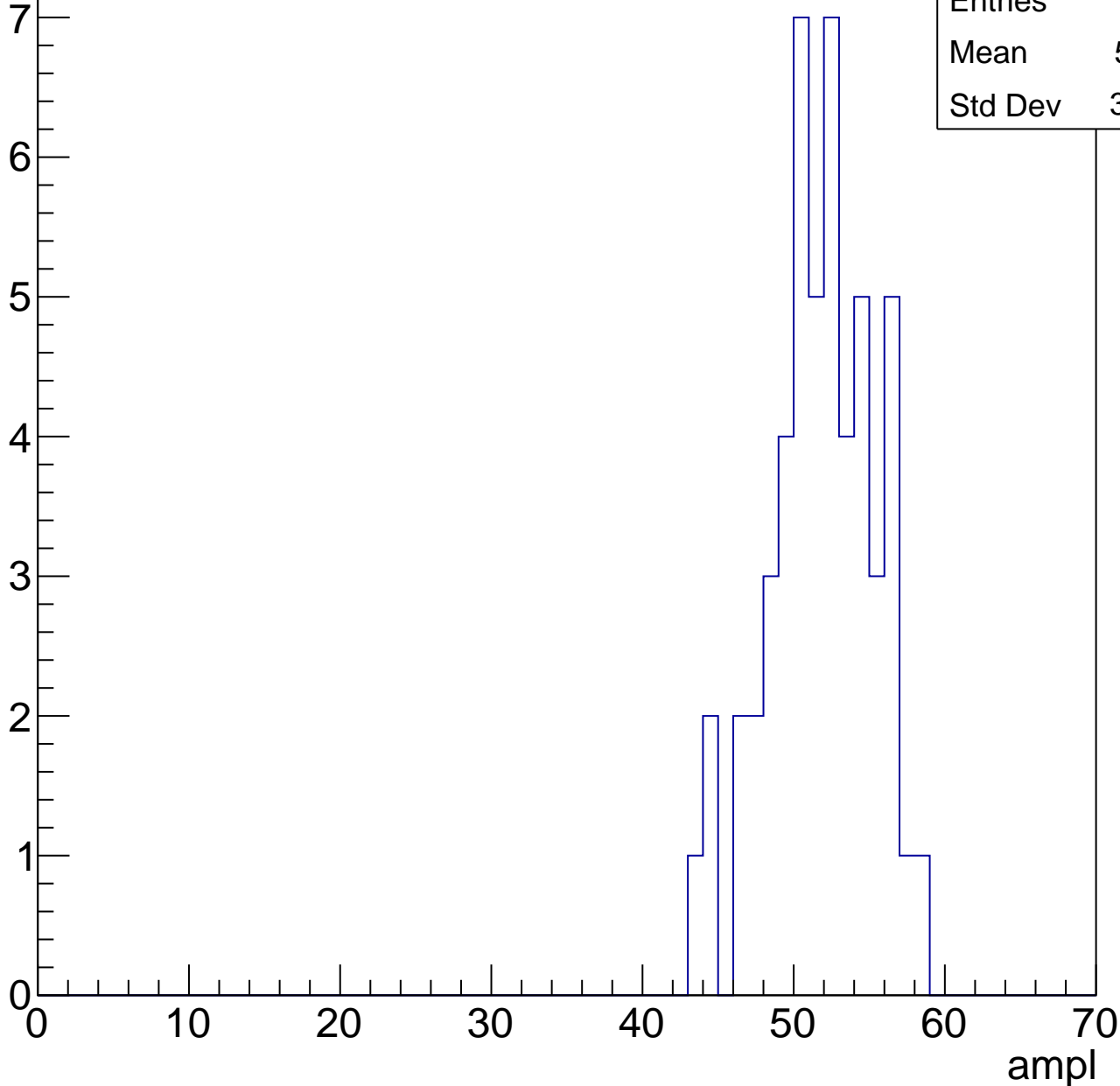


B1L100S, U9-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

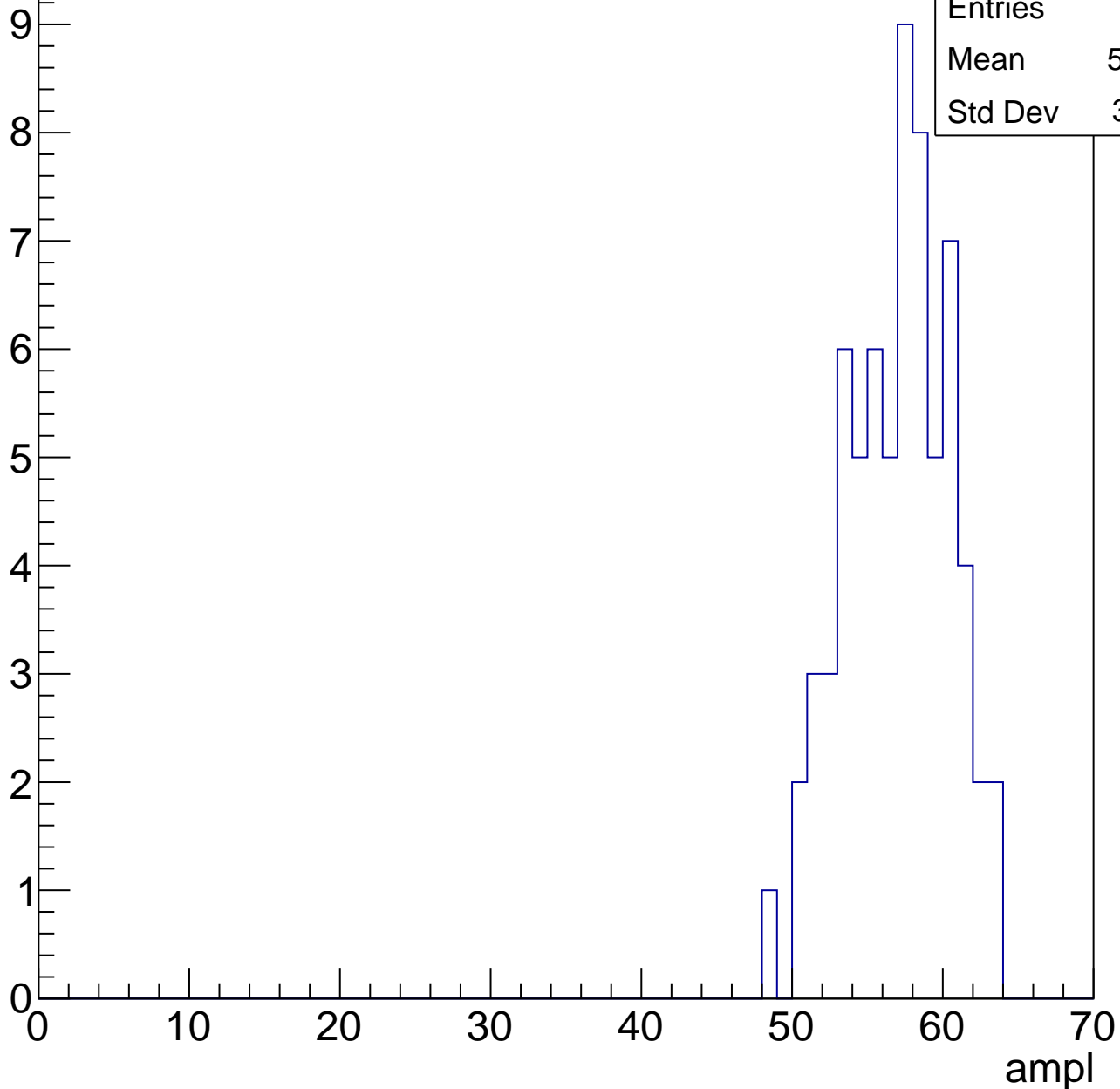
| | |
|---------|-------|
| Entries | 52 |
| Mean | 51.31 |
| Std Dev | 3.439 |



B1L100S, U9-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

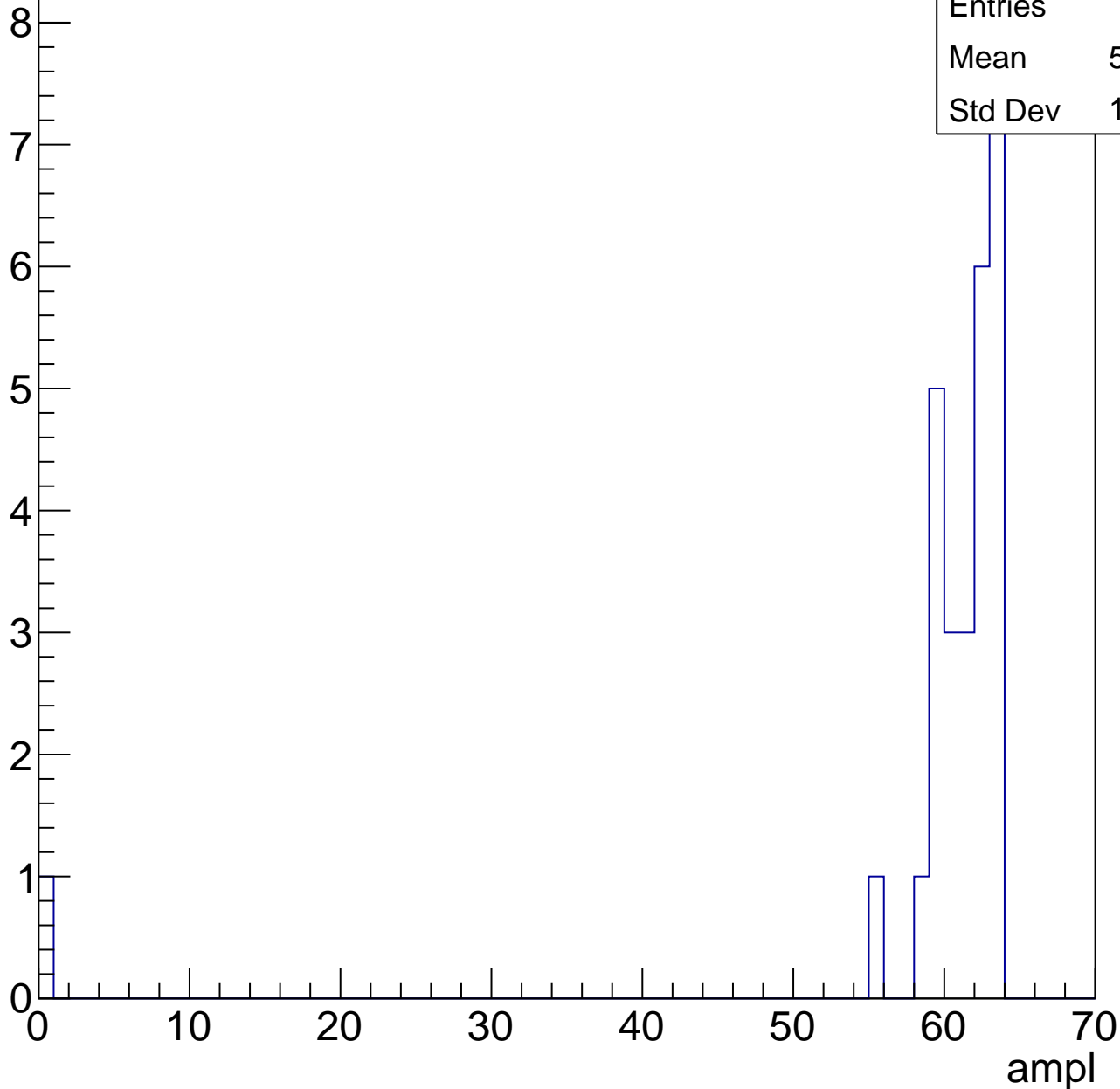


B1L100S, U9-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

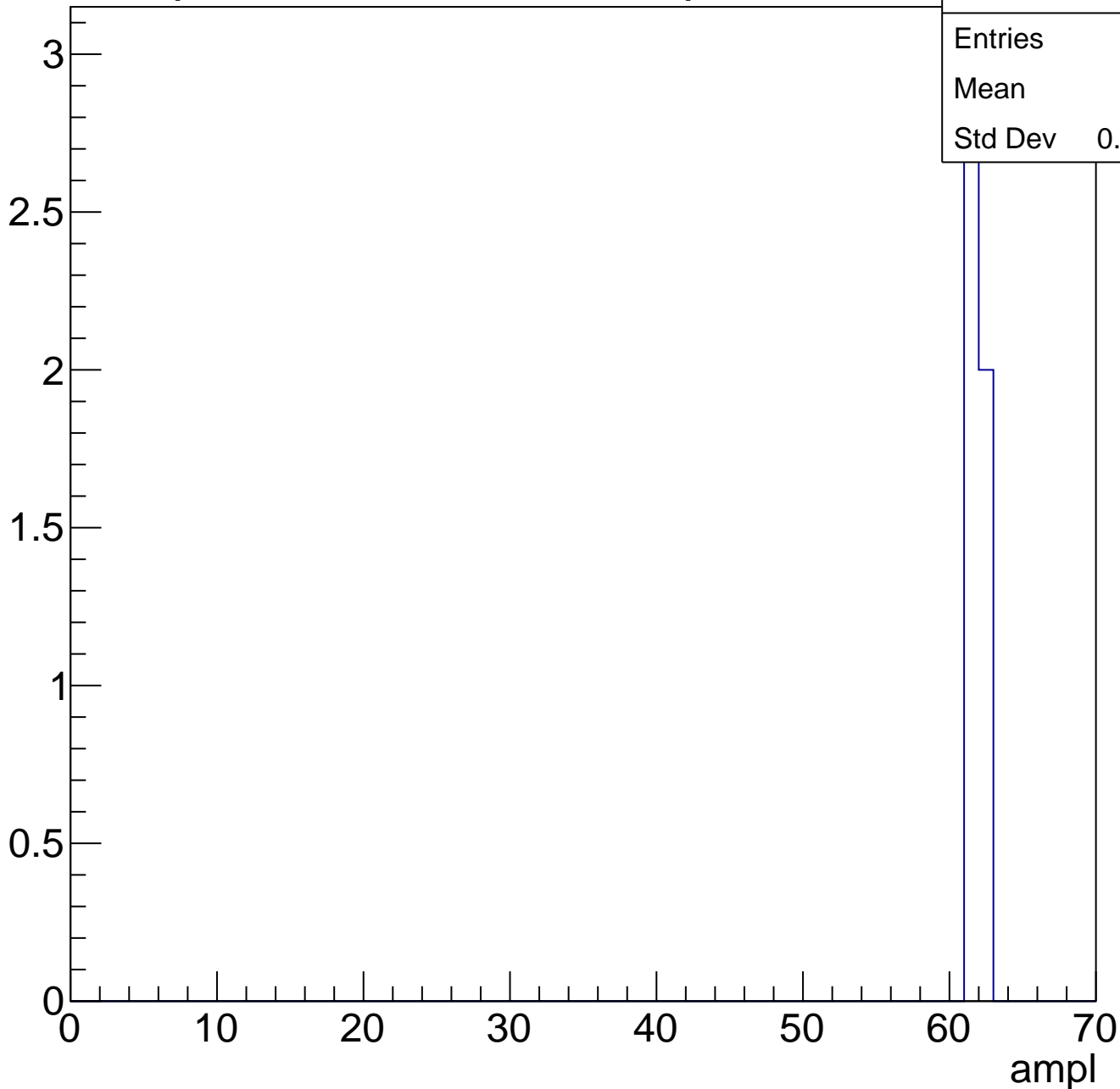
| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.82 |
| Std Dev | 11.49 |



B1L100S, U9-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch101, adc0

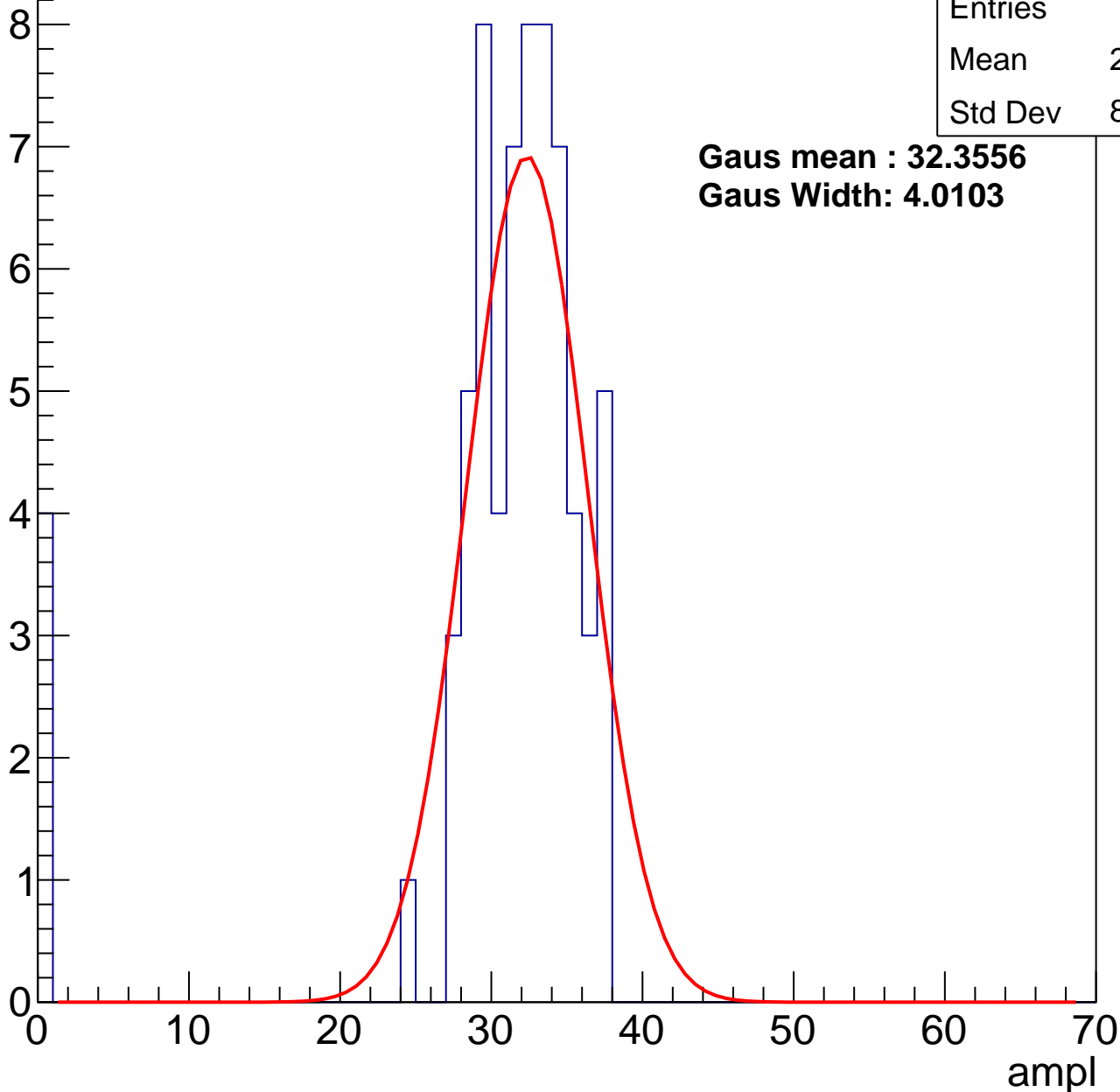
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 29.93 |
| Std Dev | 8.073 |

Gaus mean : 32.3556

Gaus Width: 4.0103



B1L100S, U9-ch101, adc1

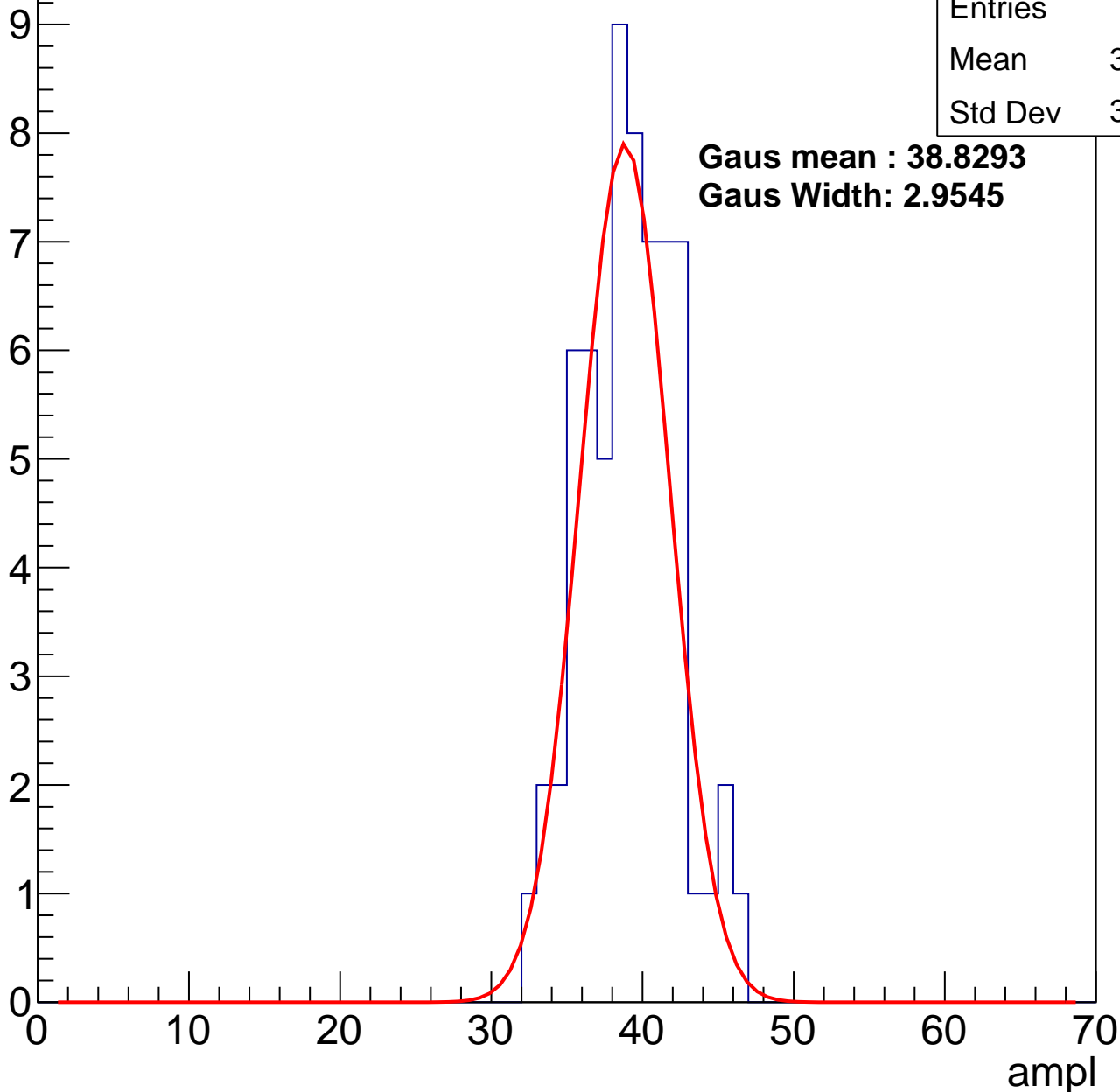
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 38.69 |
| Std Dev | 3.048 |

Gaus mean : 38.8293

Gaus Width: 2.9545



B1L100S, U9-ch101, adc2

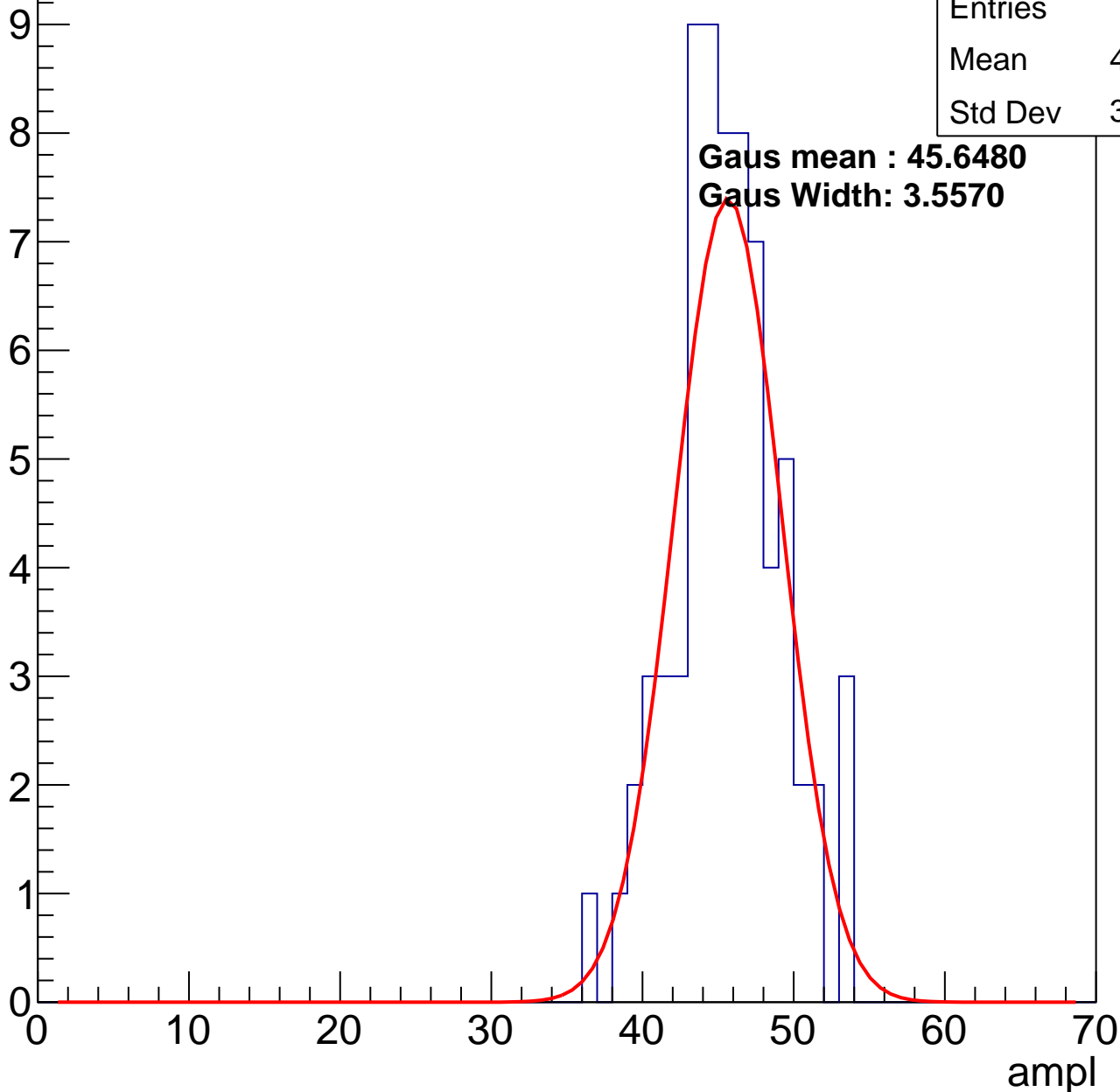
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 45.13 |
| Std Dev | 3.509 |

Gaus mean : 45.6480

Gaus Width: 3.5570

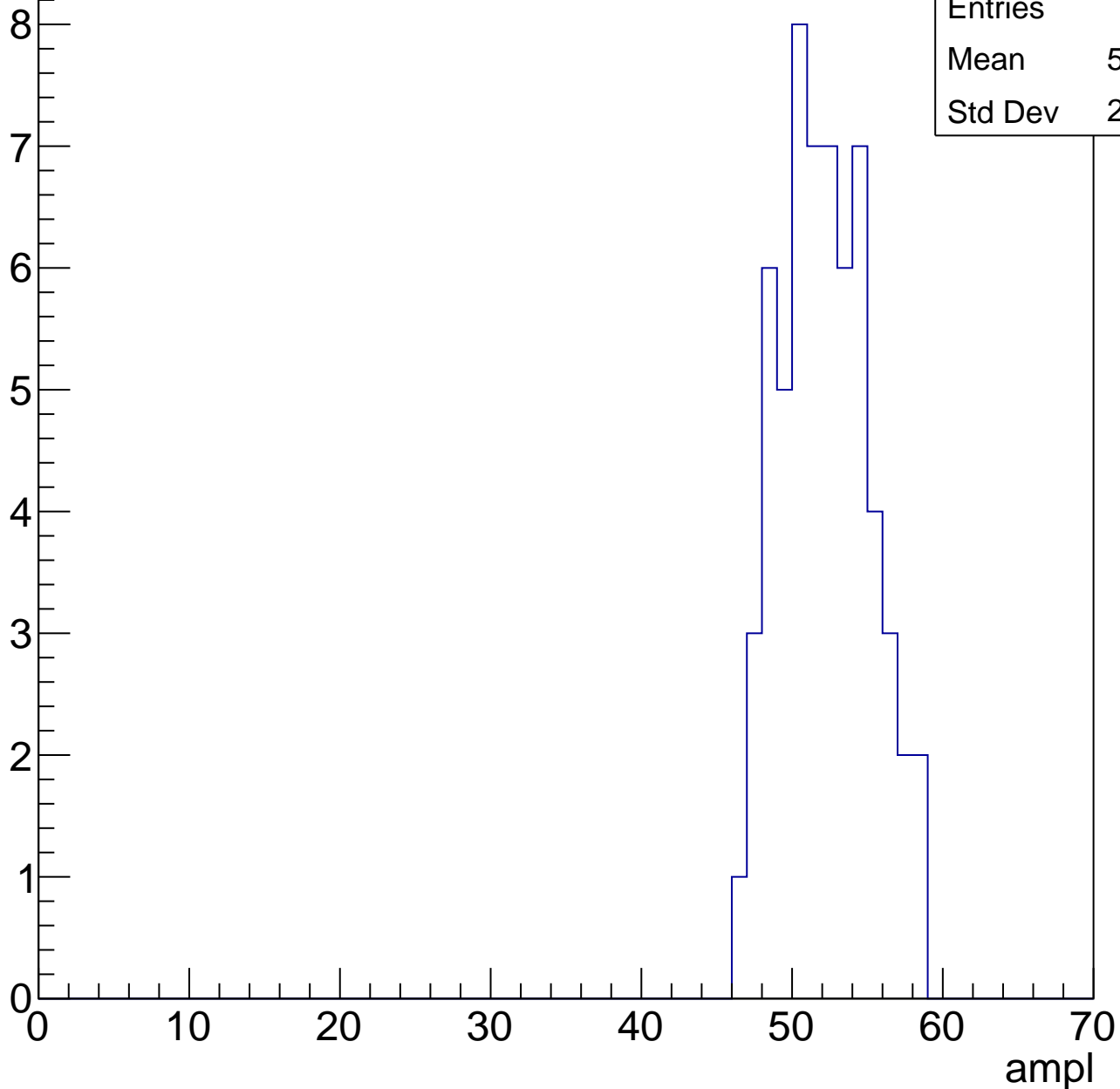


B1L100S, U9-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

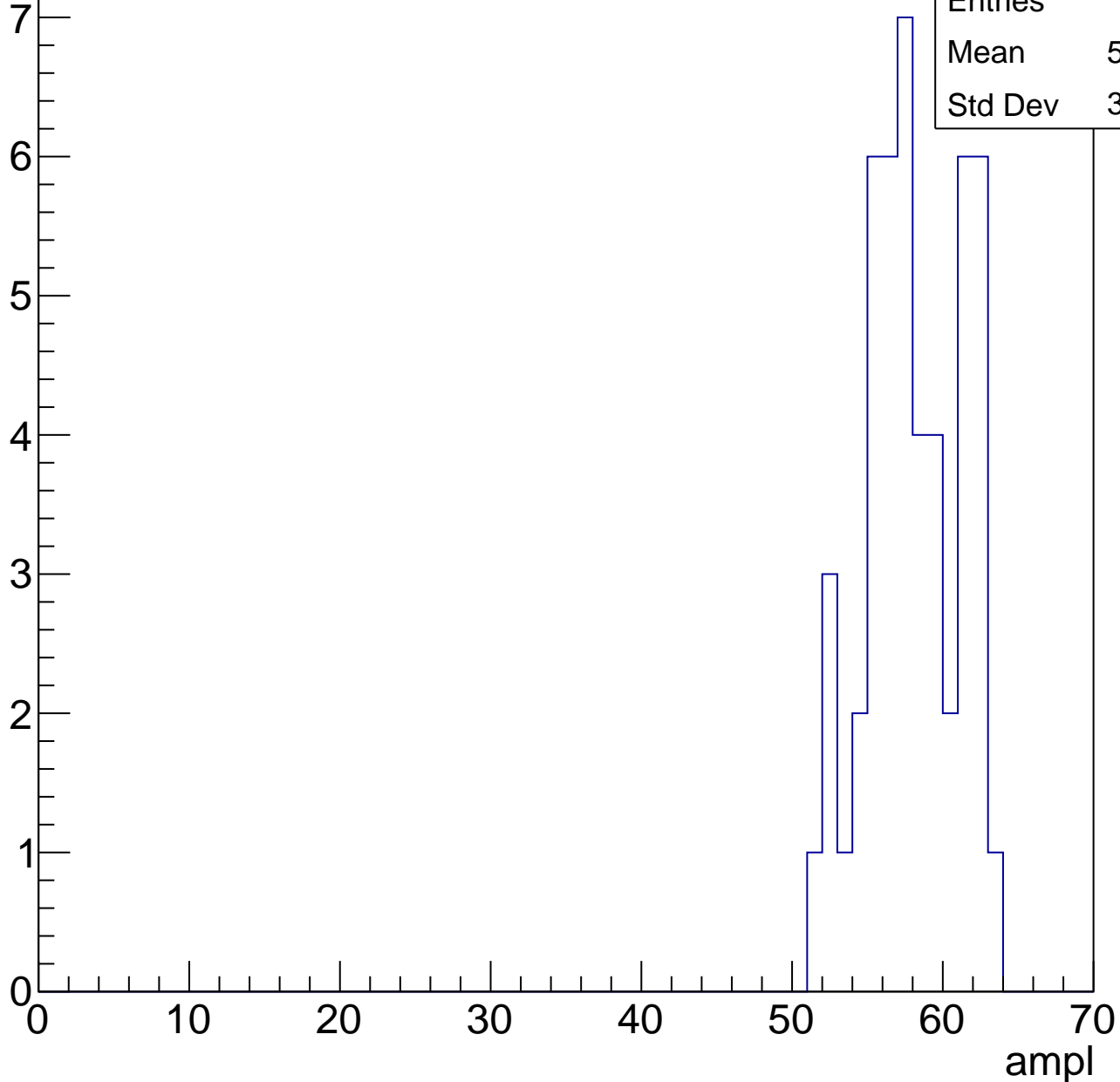
| | |
|---------|-------|
| Entries | 61 |
| Mean | 51.72 |
| Std Dev | 2.937 |



B1L100S, U9-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



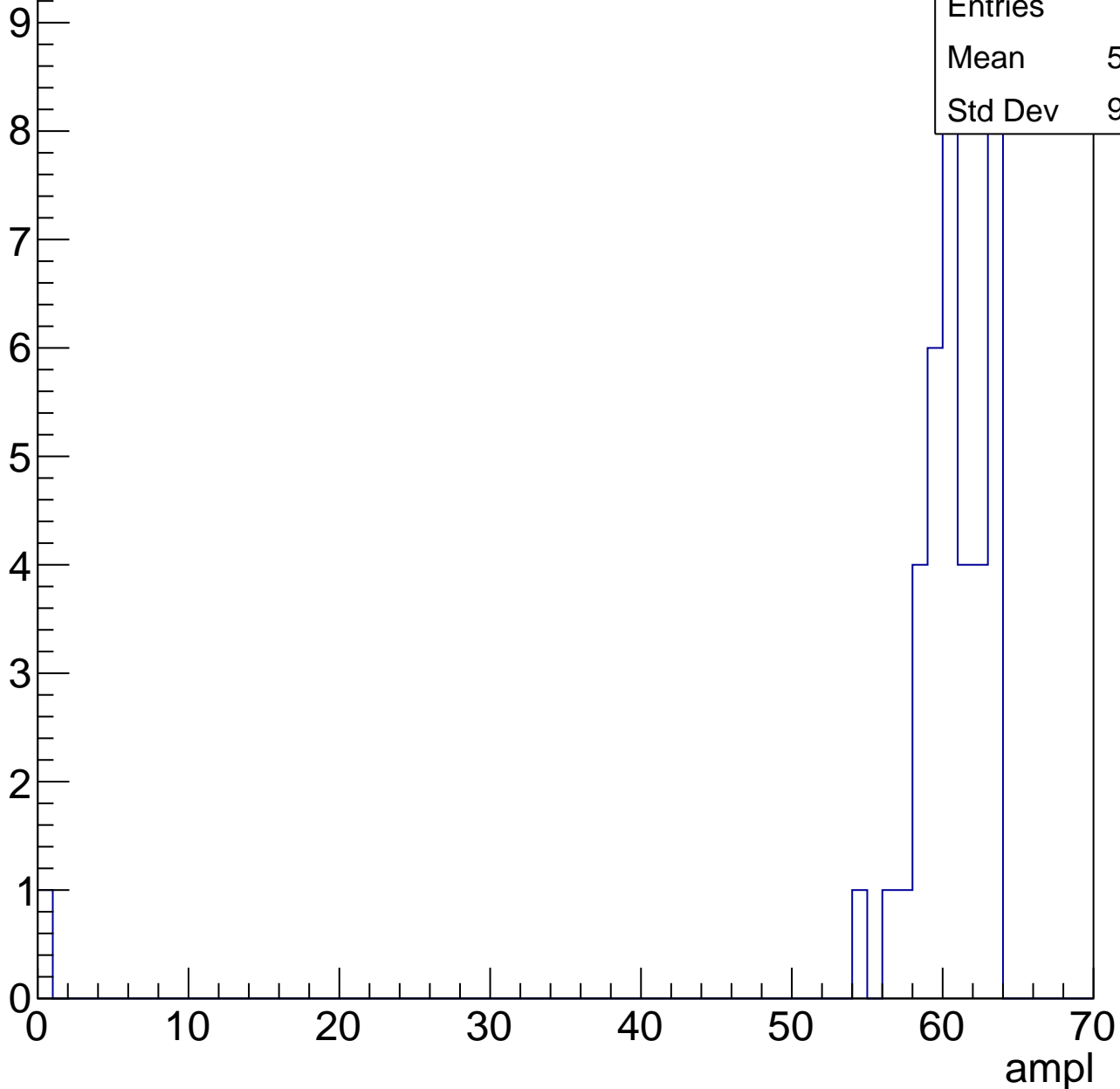
| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.59 |
| Std Dev | 3.123 |

B1L100S, U9-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.77 |
| Std Dev | 9.773 |



B1L100S, U9-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch102, adc0

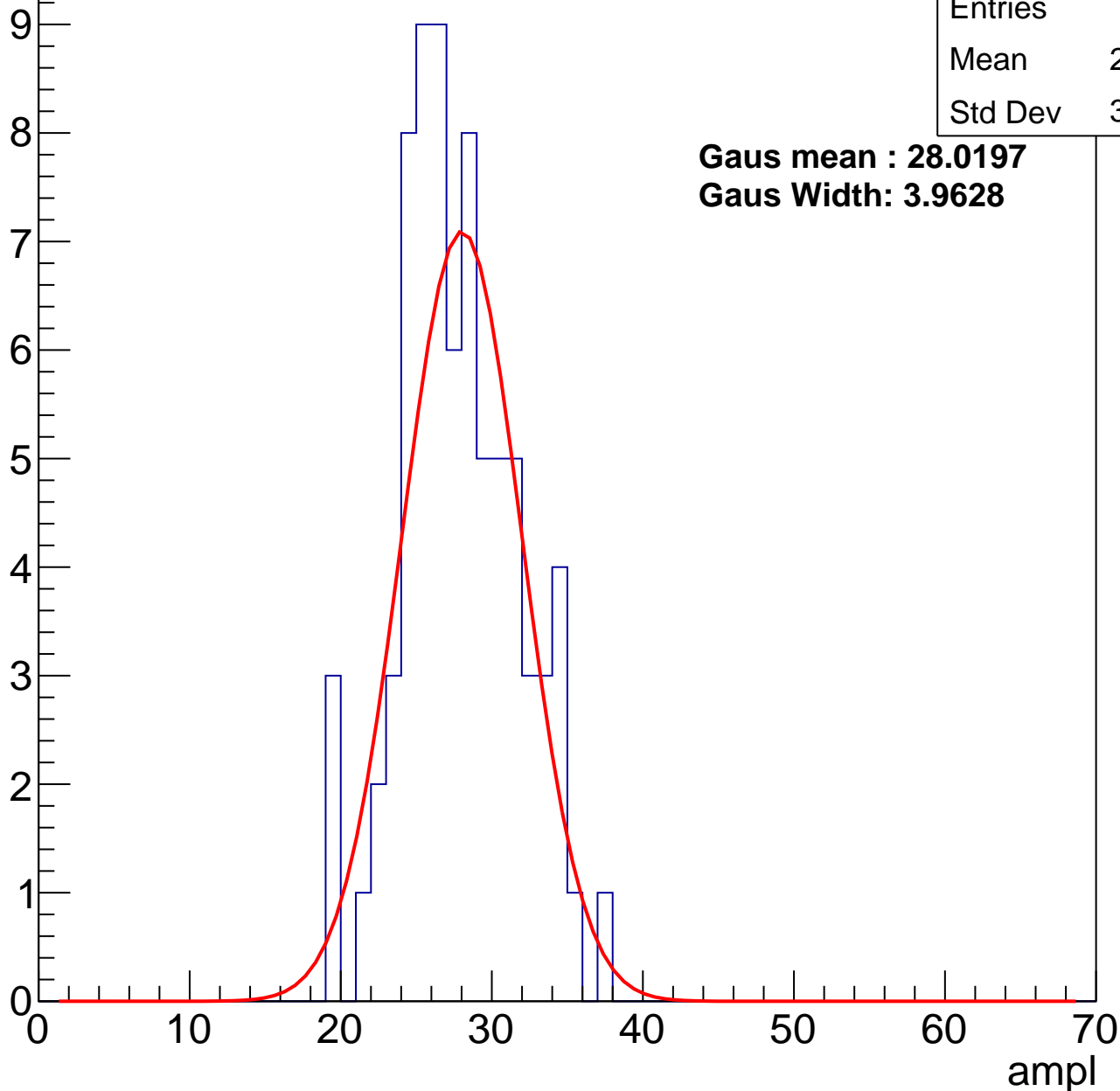
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 27.38 |
| Std Dev | 3.852 |

Gaus mean : 28.0197

Gaus Width: 3.9628



B1L100S, U9-ch102, adc1

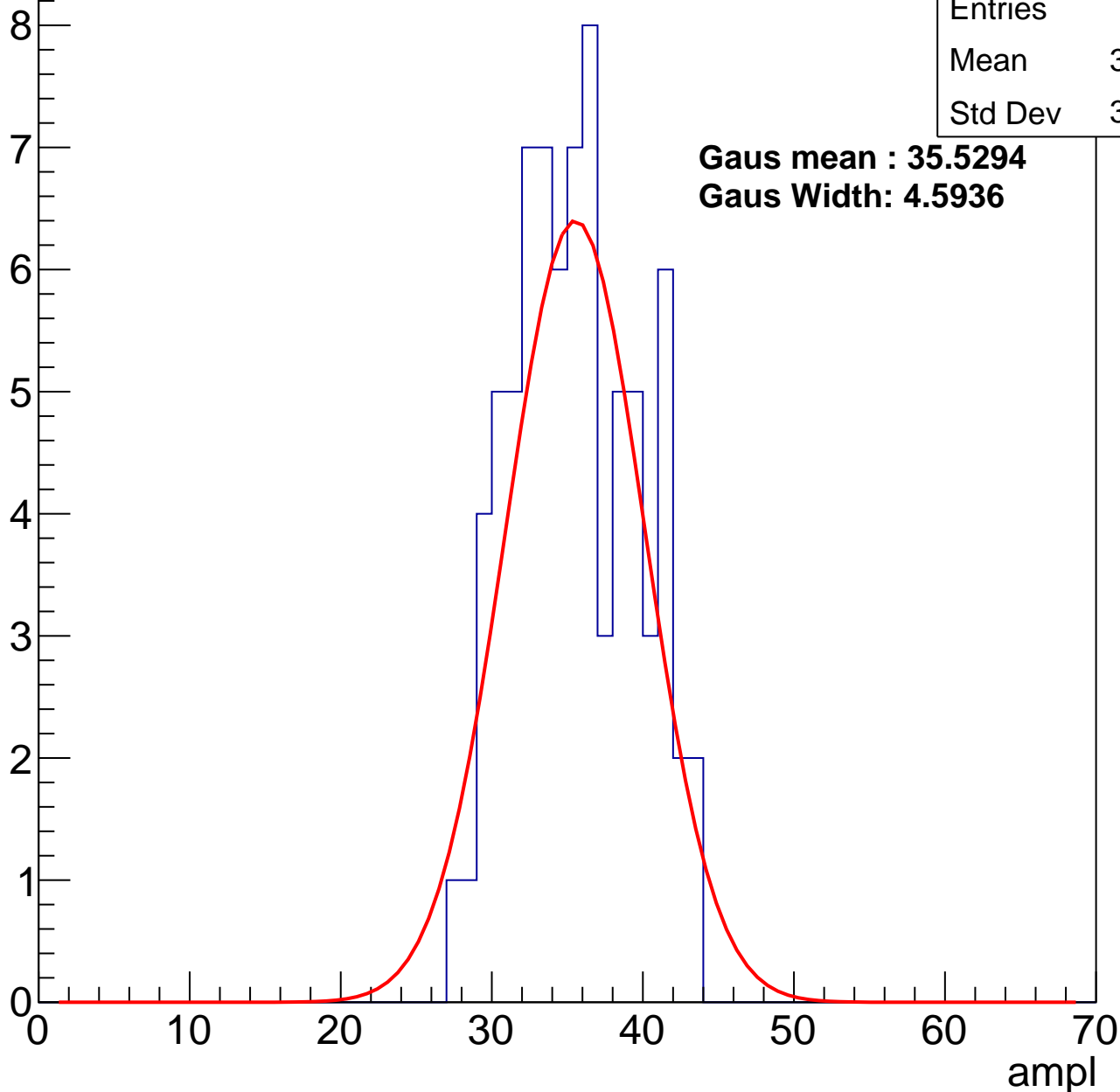
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 35.06 |
| Std Dev | 3.982 |

Gaus mean : 35.5294

Gaus Width: 4.5936



B1L100S, U9-ch102, adc2

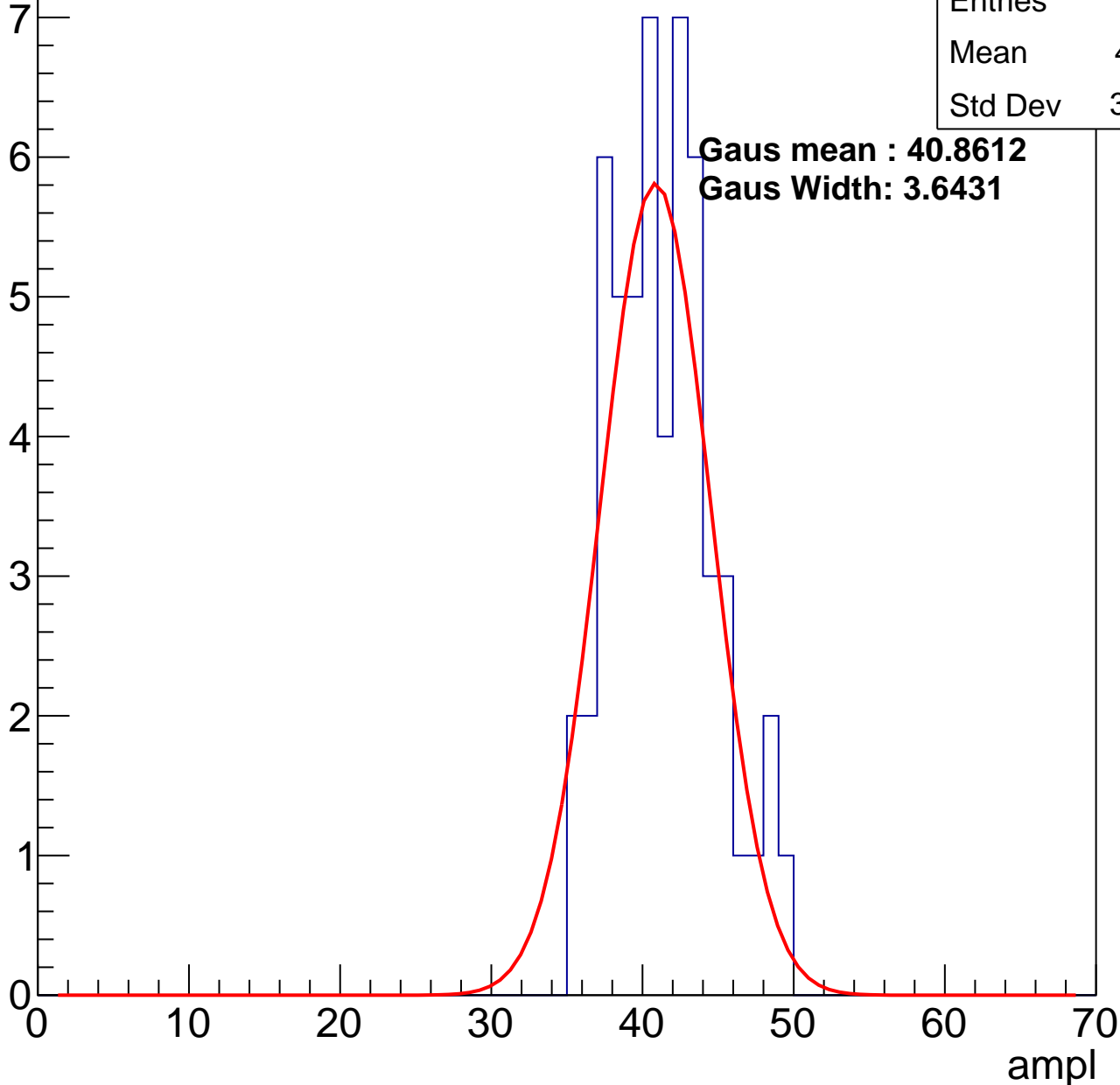
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 40.91 |
| Std Dev | 3.354 |

Gaus mean : 40.8612

Gaus Width: 3.6431

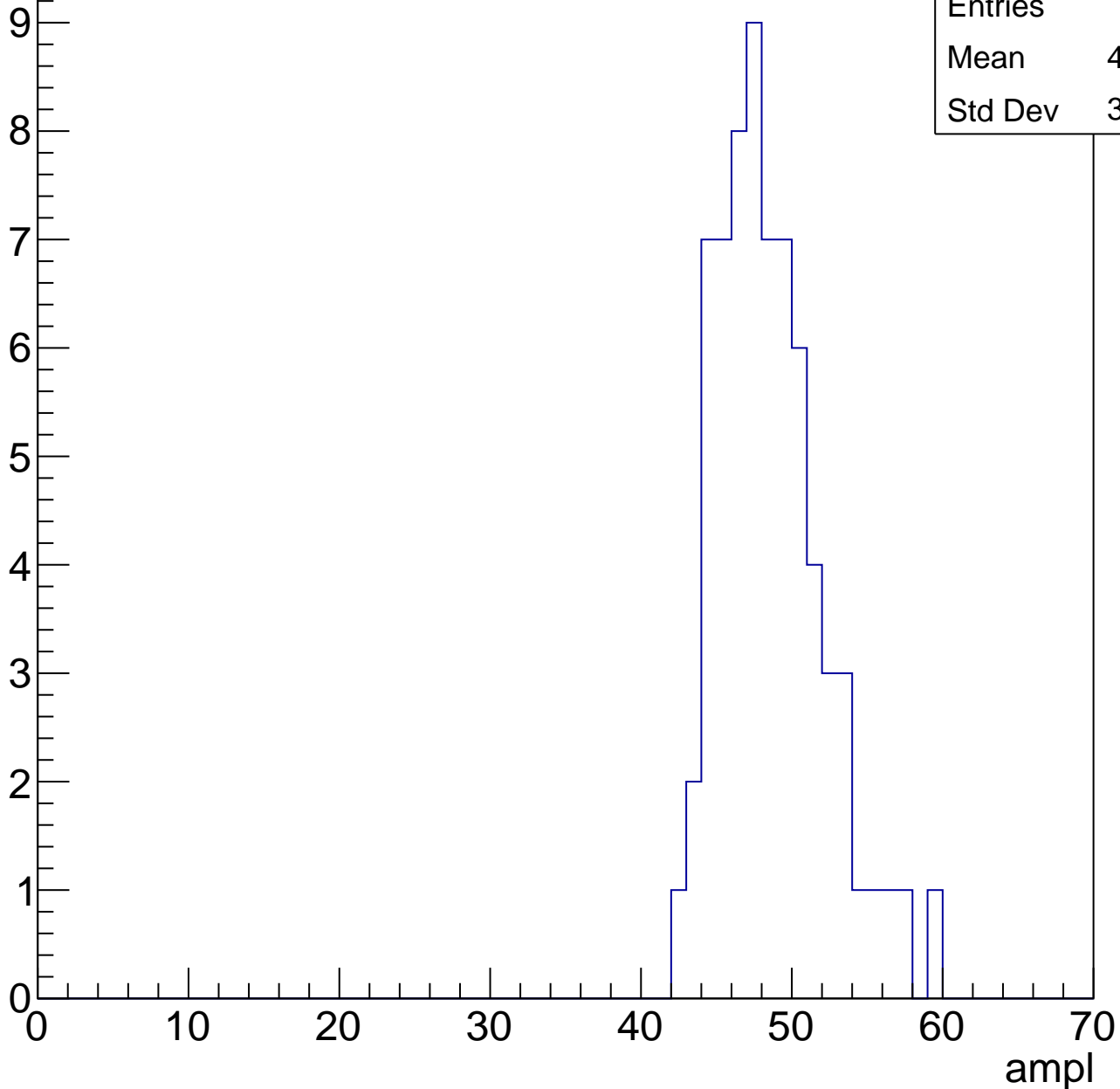


B1L100S, U9-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

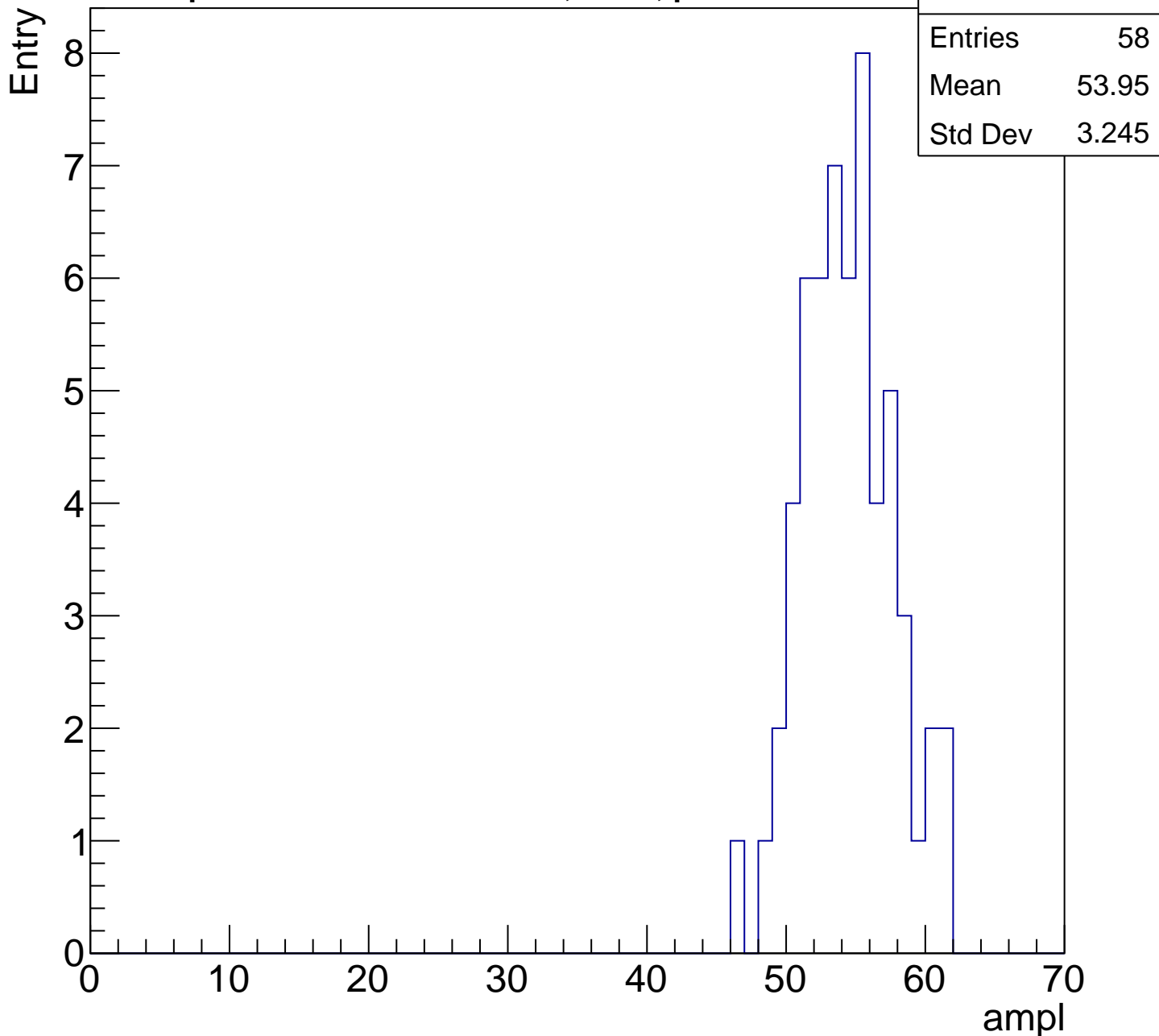
Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.13 |
| Std Dev | 3.493 |



B1L100S, U9-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U9-ch102, adc5

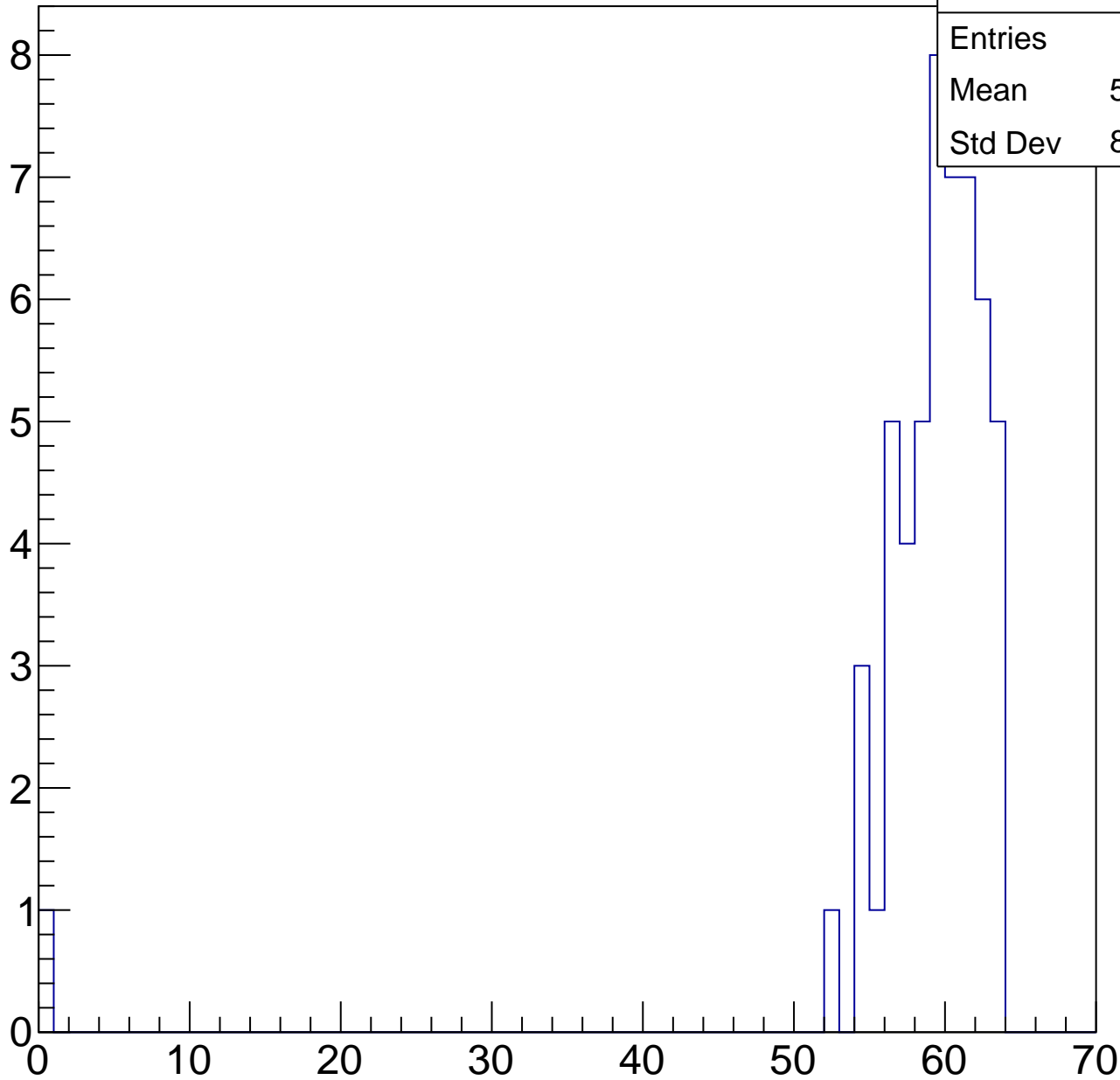
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.98 |
| Std Dev | 8.469 |

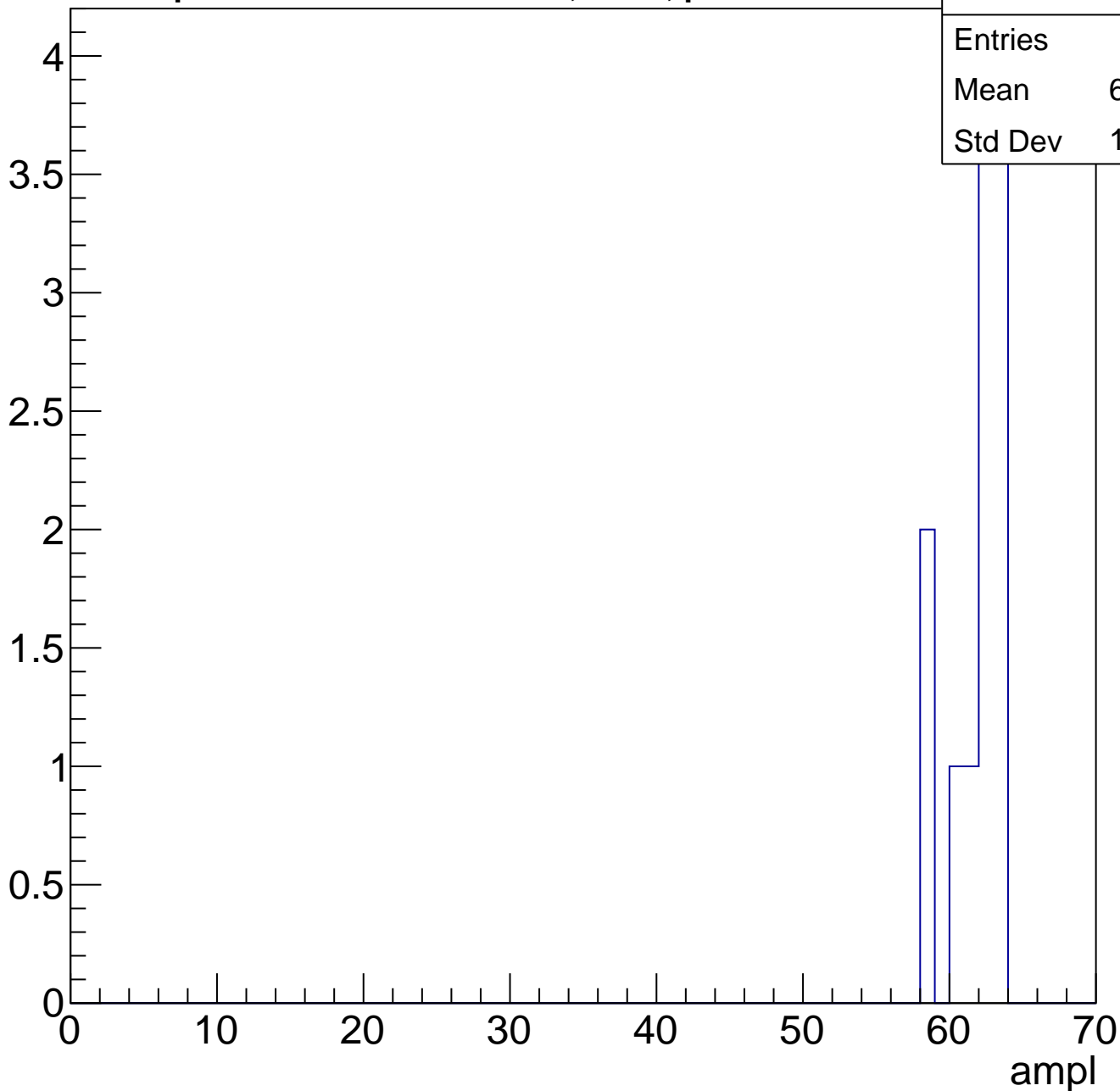
ampl



B1L100S, U9-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch103, adc0

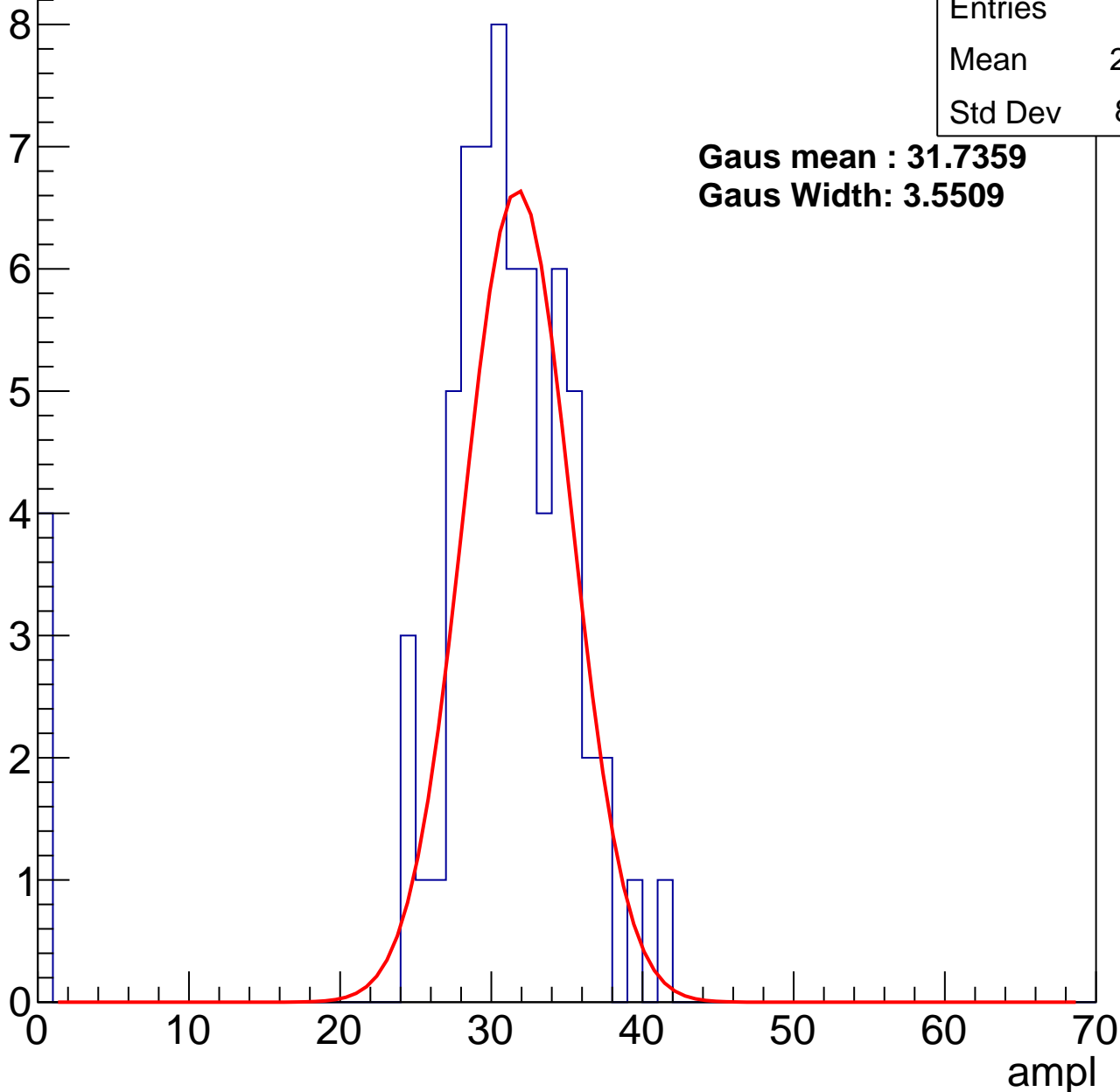
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 29.16 |
| Std Dev | 8.021 |

Gaus mean : 31.7359

Gaus Width: 3.5509



B1L100S, U9-ch103, adc1

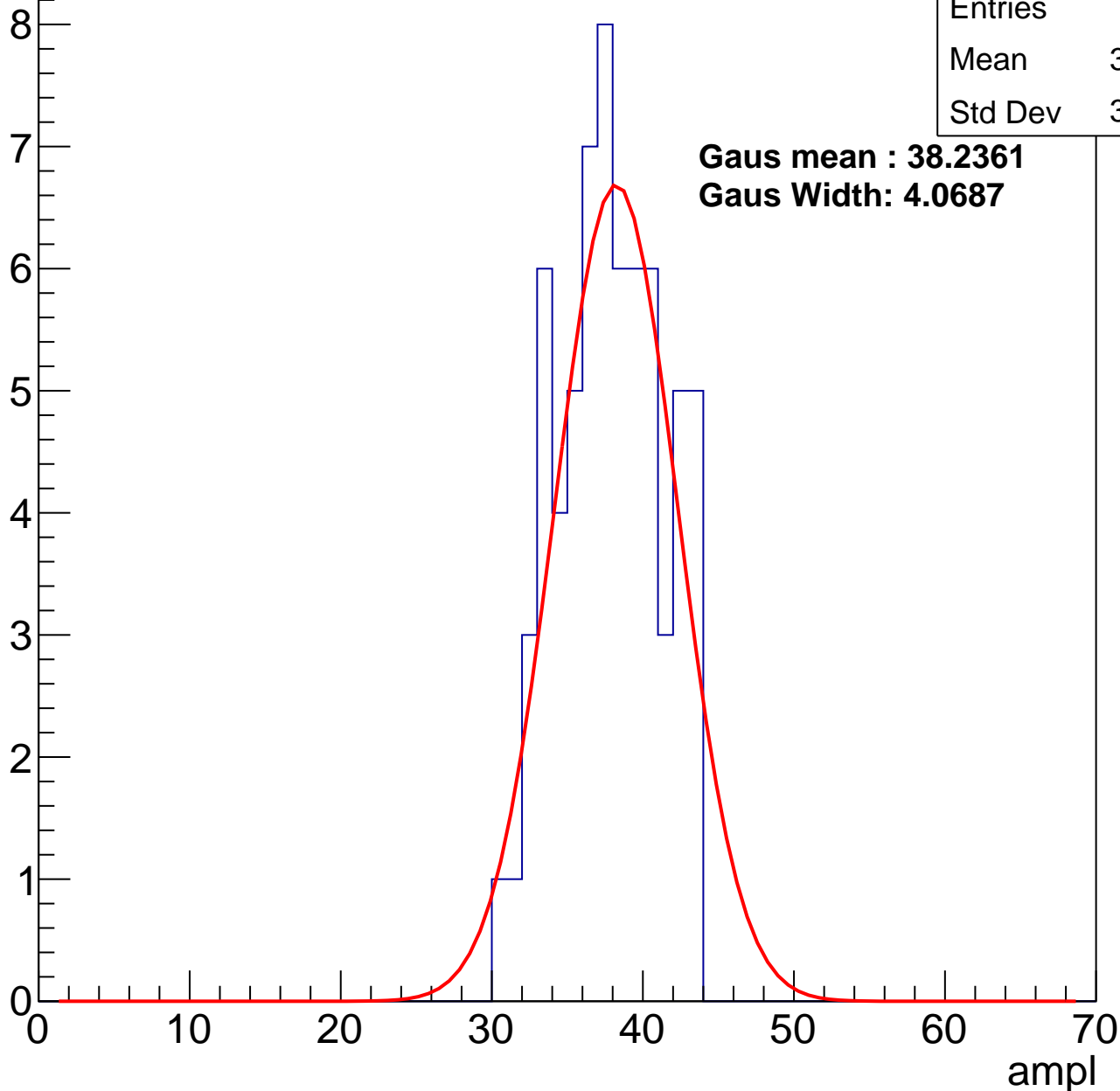
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 37.33 |
| Std Dev | 3.368 |

Gaus mean : 38.2361

Gaus Width: 4.0687



B1L100S, U9-ch103, adc2

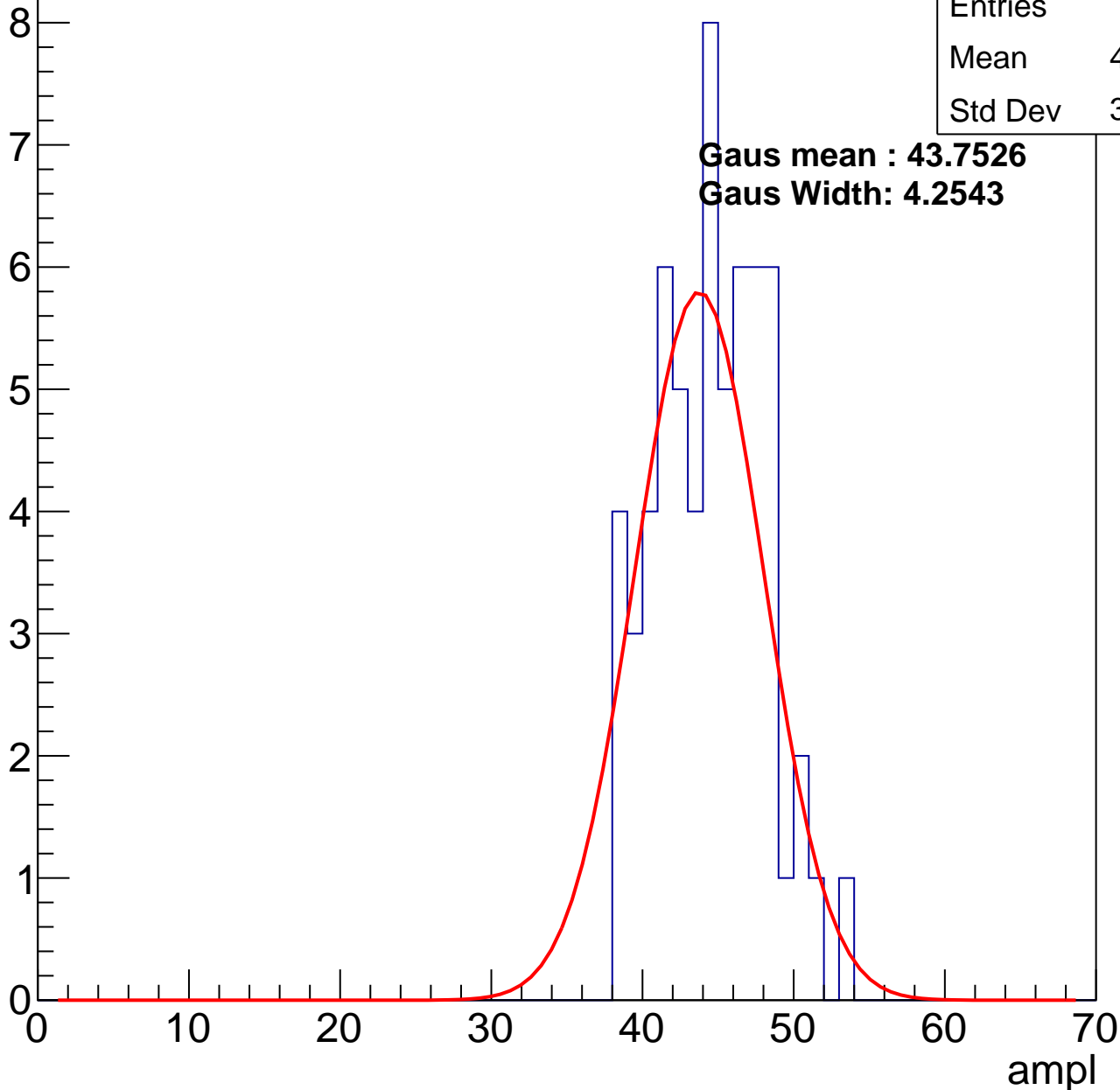
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 44.08 |
| Std Dev | 3.516 |

Gaus mean : 43.7526

Gaus Width: 4.2543

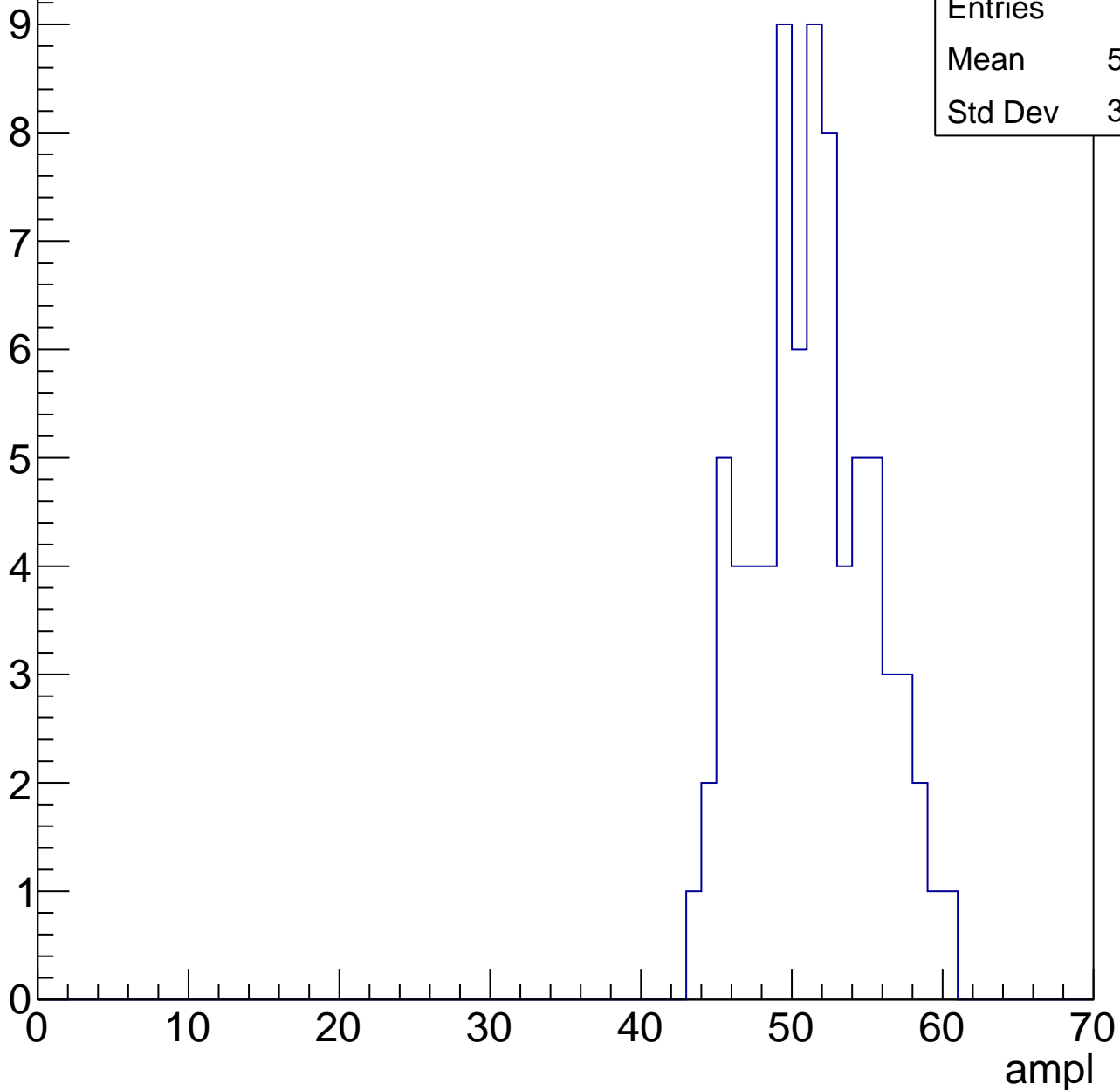


B1L100S, U9-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 50.88 |
| Std Dev | 3.903 |

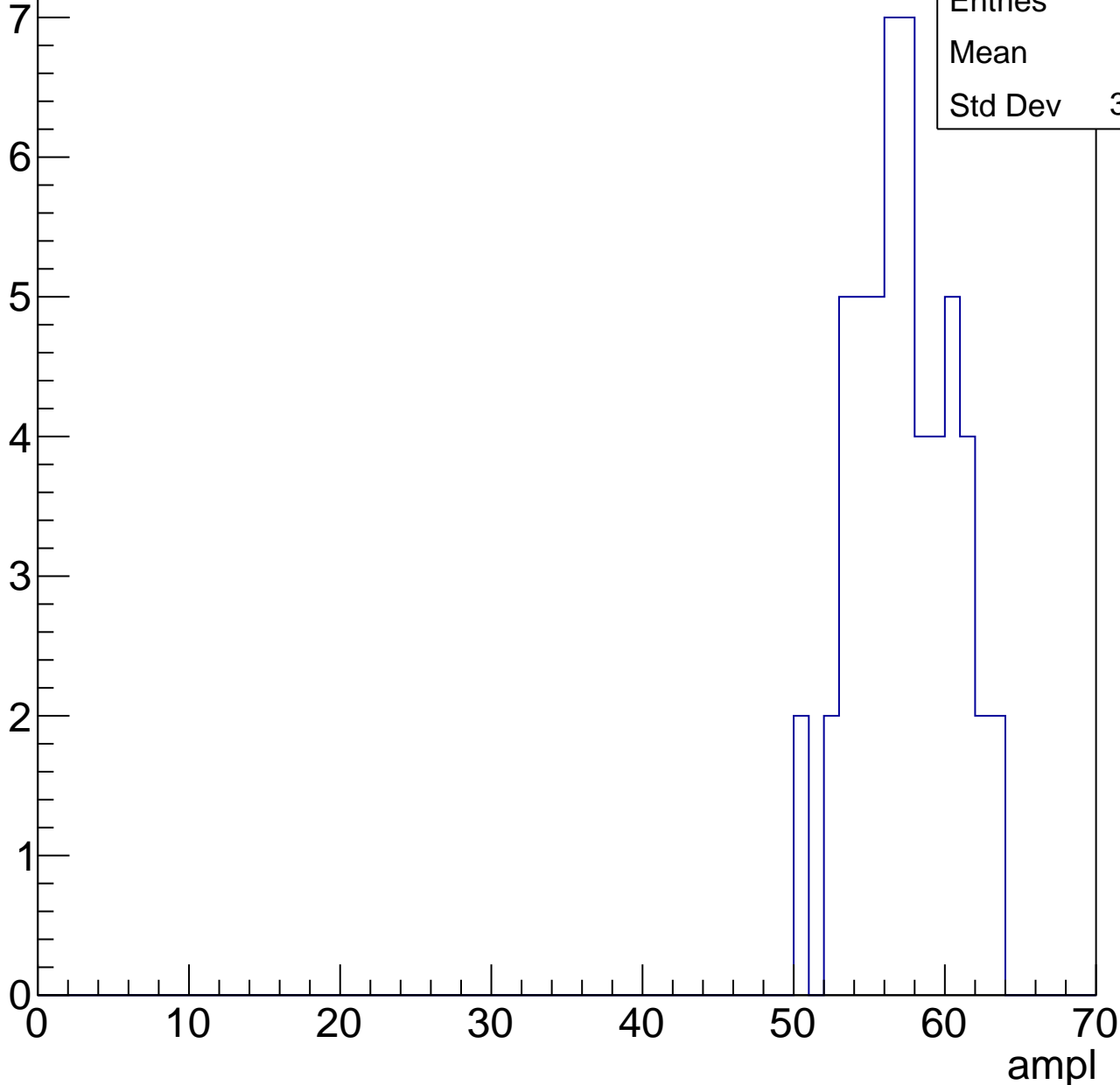


B1L100S, U9-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

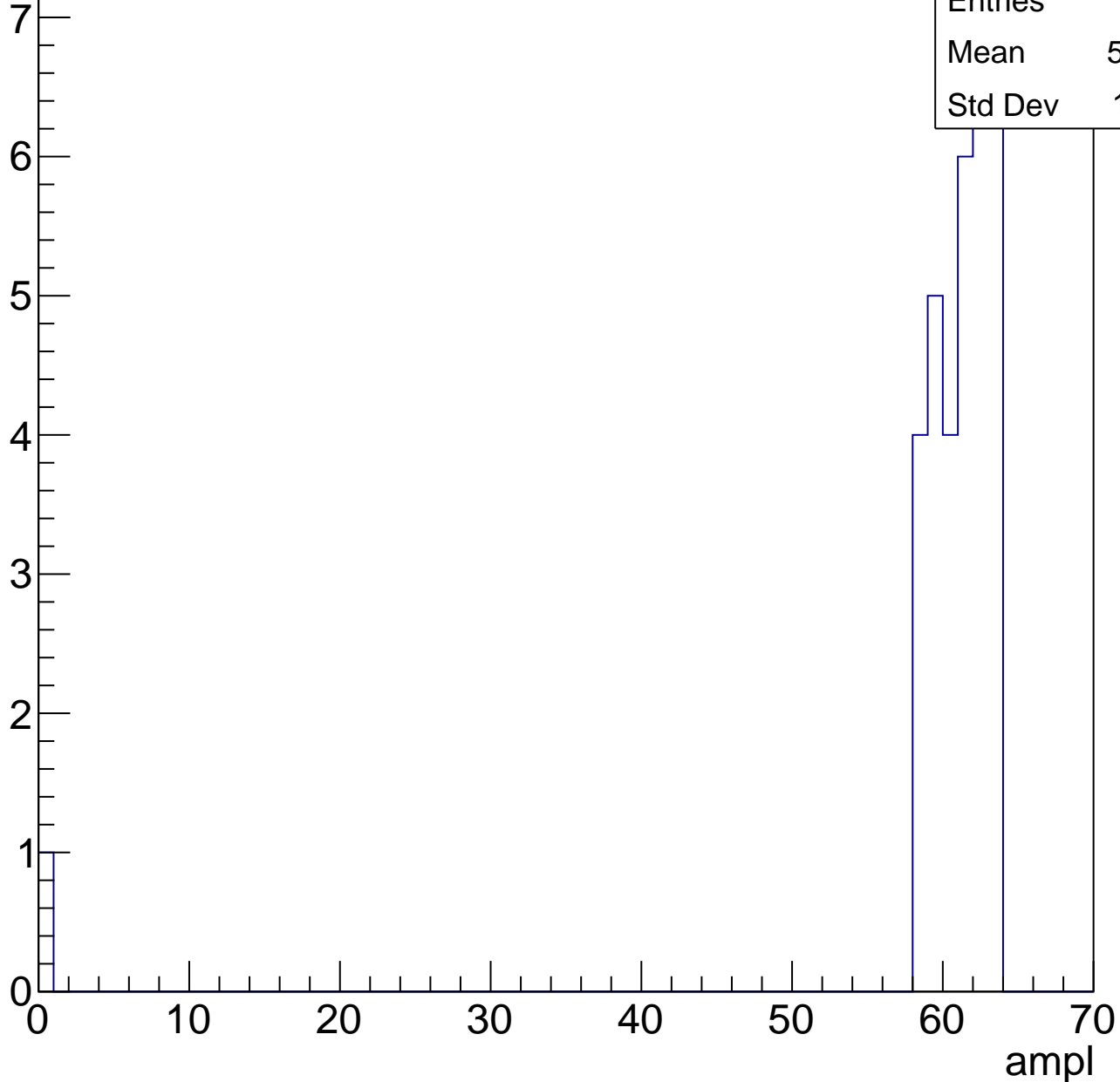
| | |
|---------|-------|
| Entries | 54 |
| Mean | 56.8 |
| Std Dev | 3.188 |



B1L100S, U9-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch104, adc0

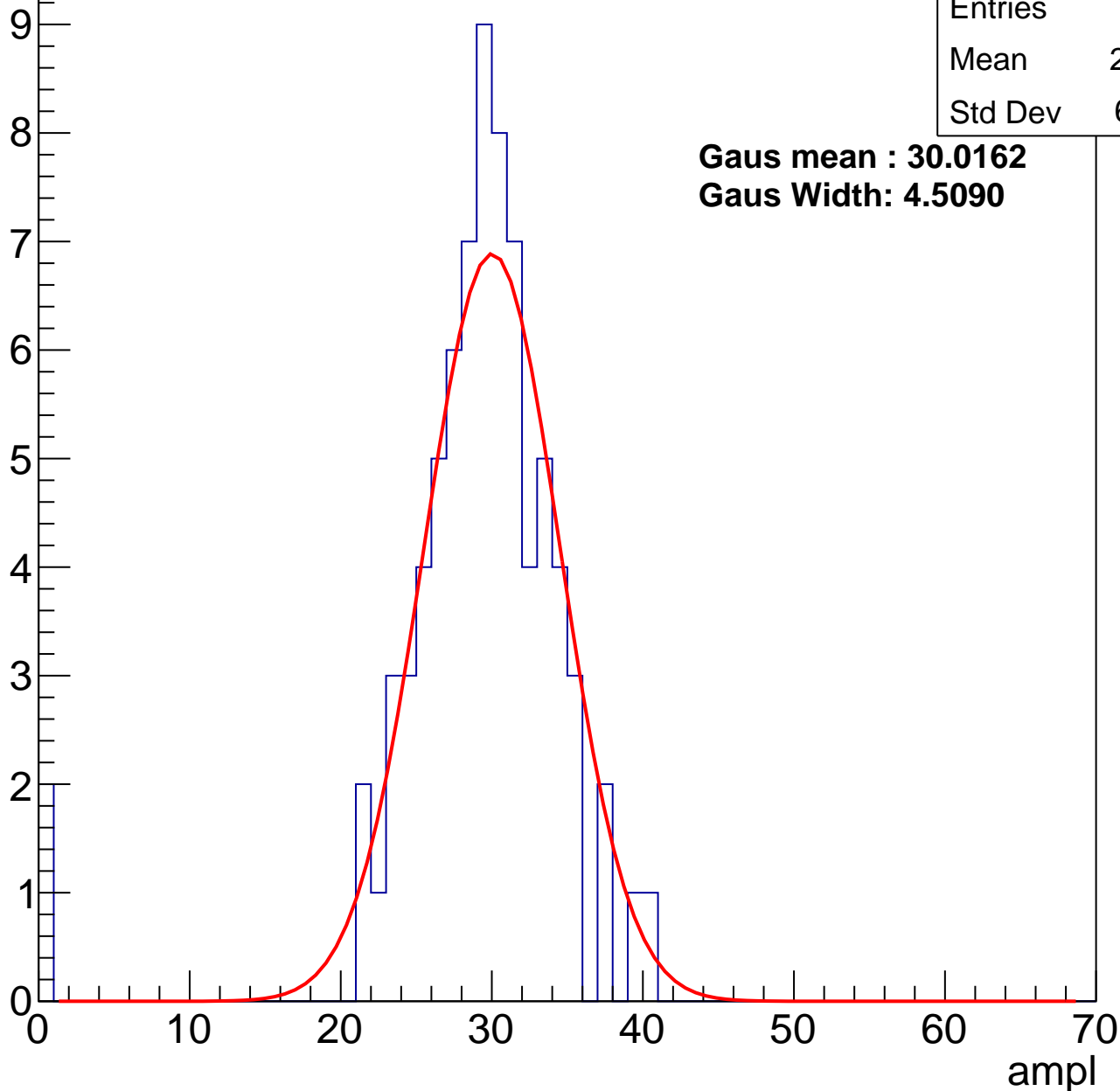
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 28.55 |
| Std Dev | 6.111 |

Gaus mean : 30.0162

Gaus Width: 4.5090



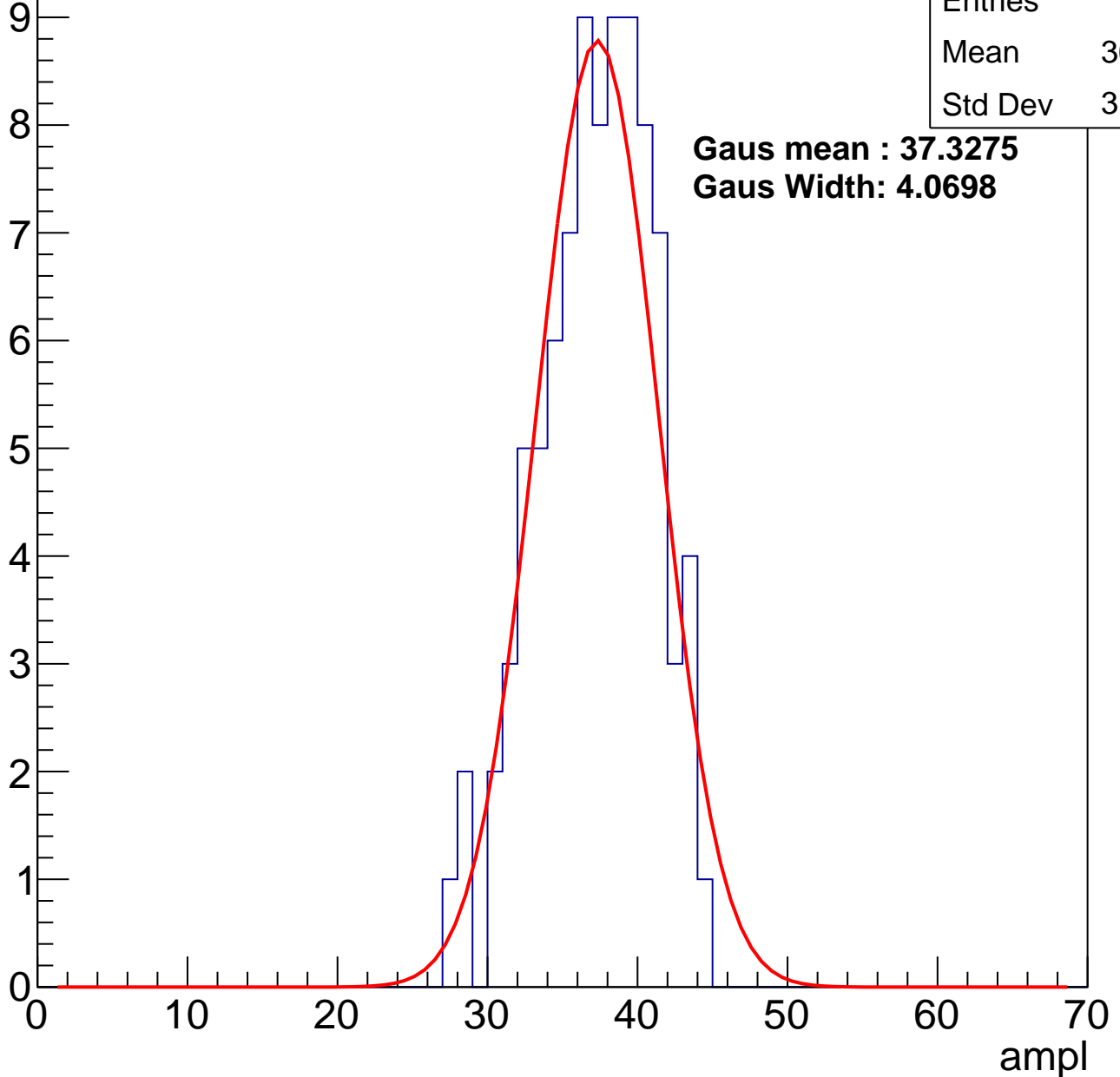
B1L100S, U9-ch104, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 36.76 |
| Std Dev | 3.757 |

Gaus mean : 37.3275
Gaus Width: 4.0698



B1L100S, U9-ch104, adc2

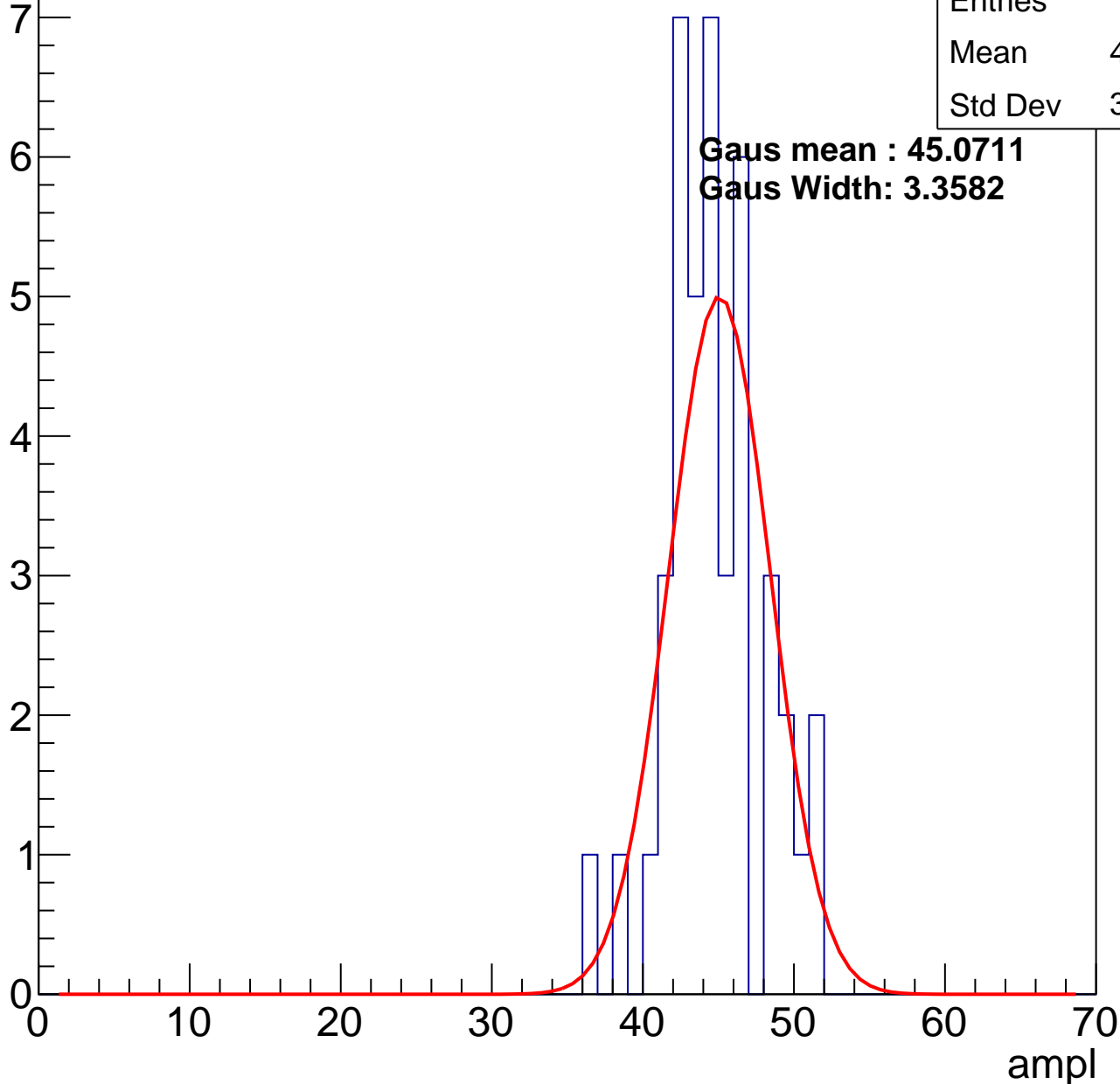
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 44.26 |
| Std Dev | 3.208 |

Gaus mean : 45.0711

Gaus Width: 3.3582

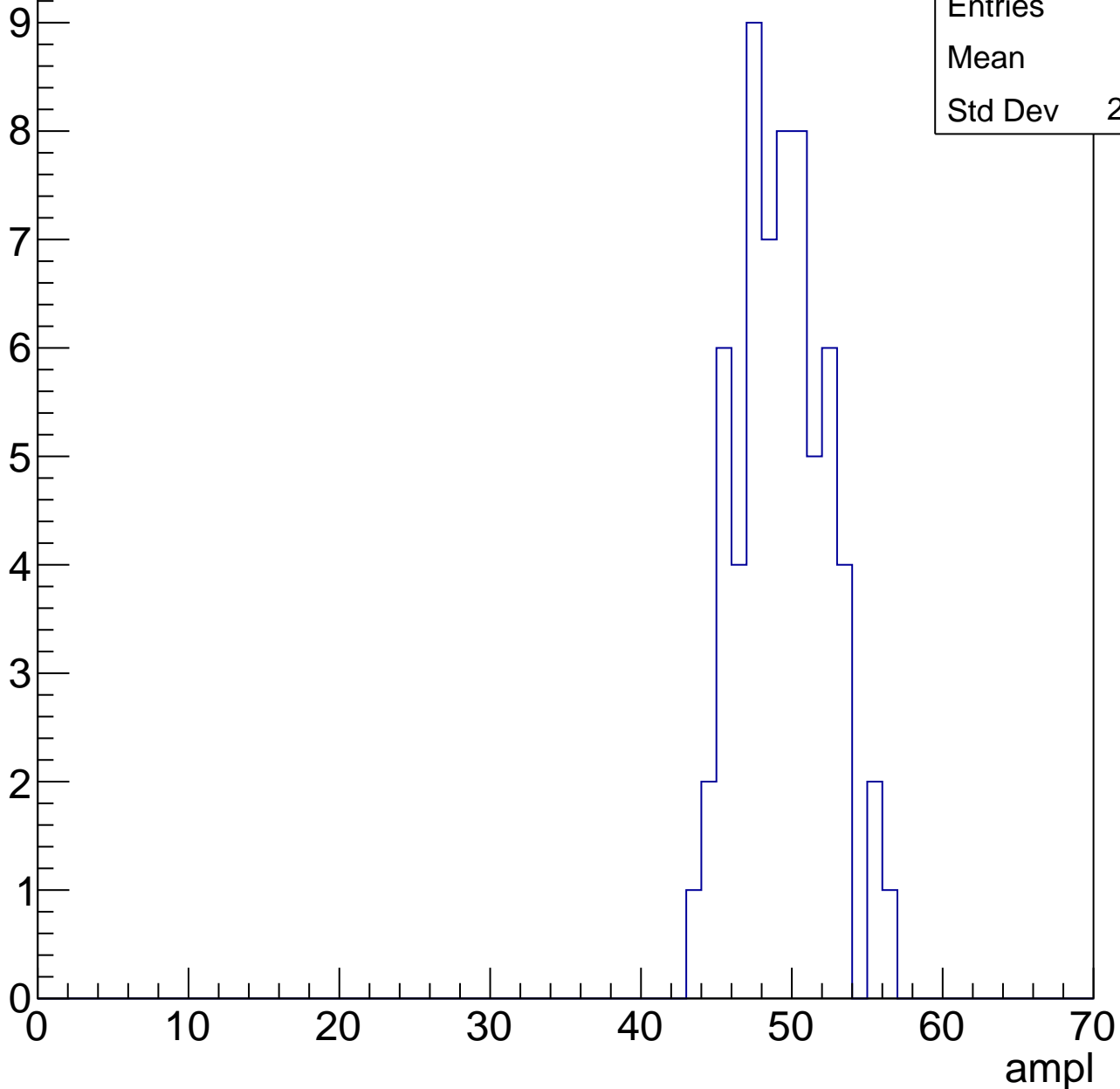


B1L100S, U9-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.9 |
| Std Dev | 2.893 |



B1L100S, U9-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

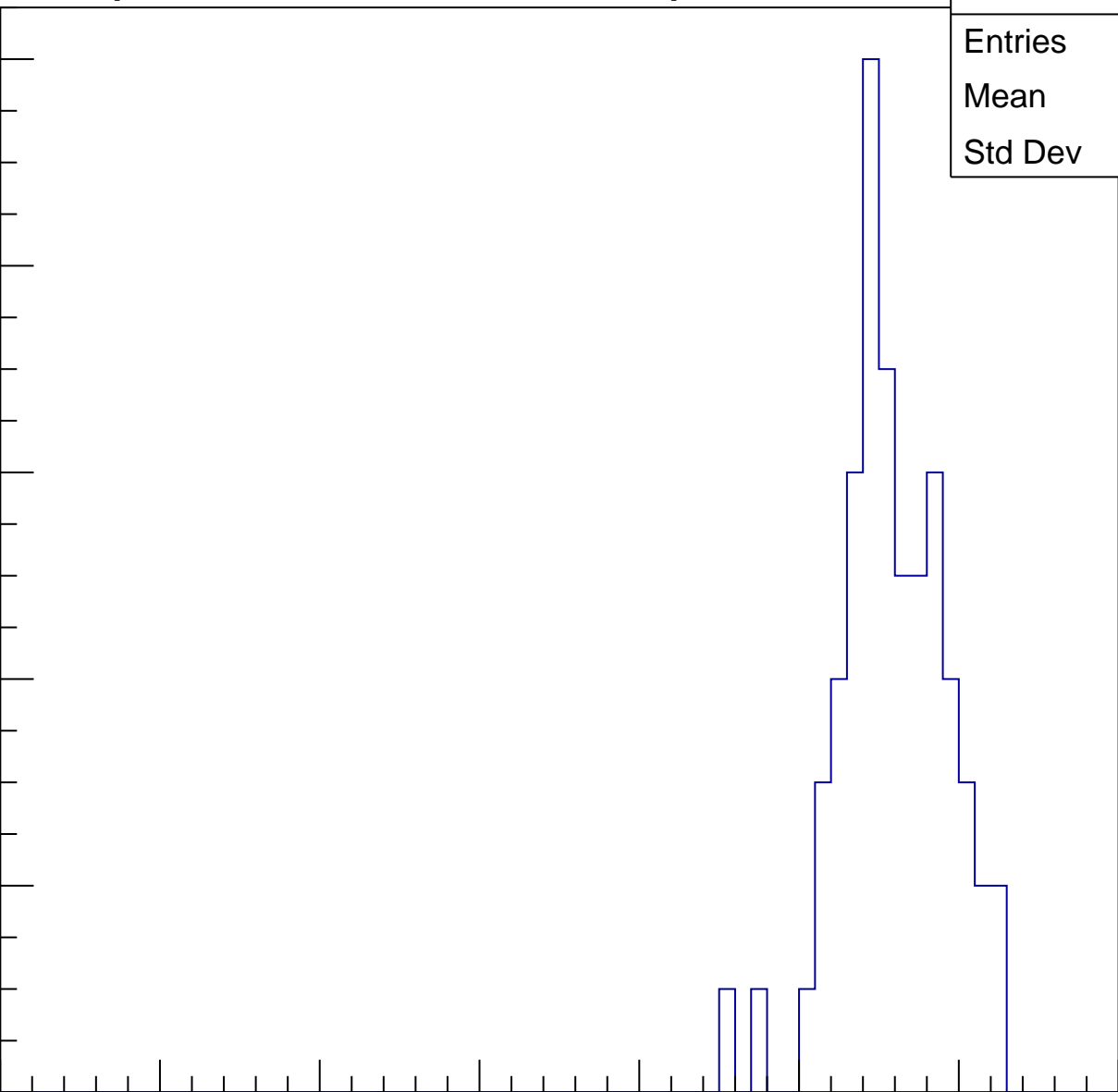
| | |
|---------|-------|
| Entries | 60 |
| Mean | 55.35 |
| Std Dev | 3.385 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

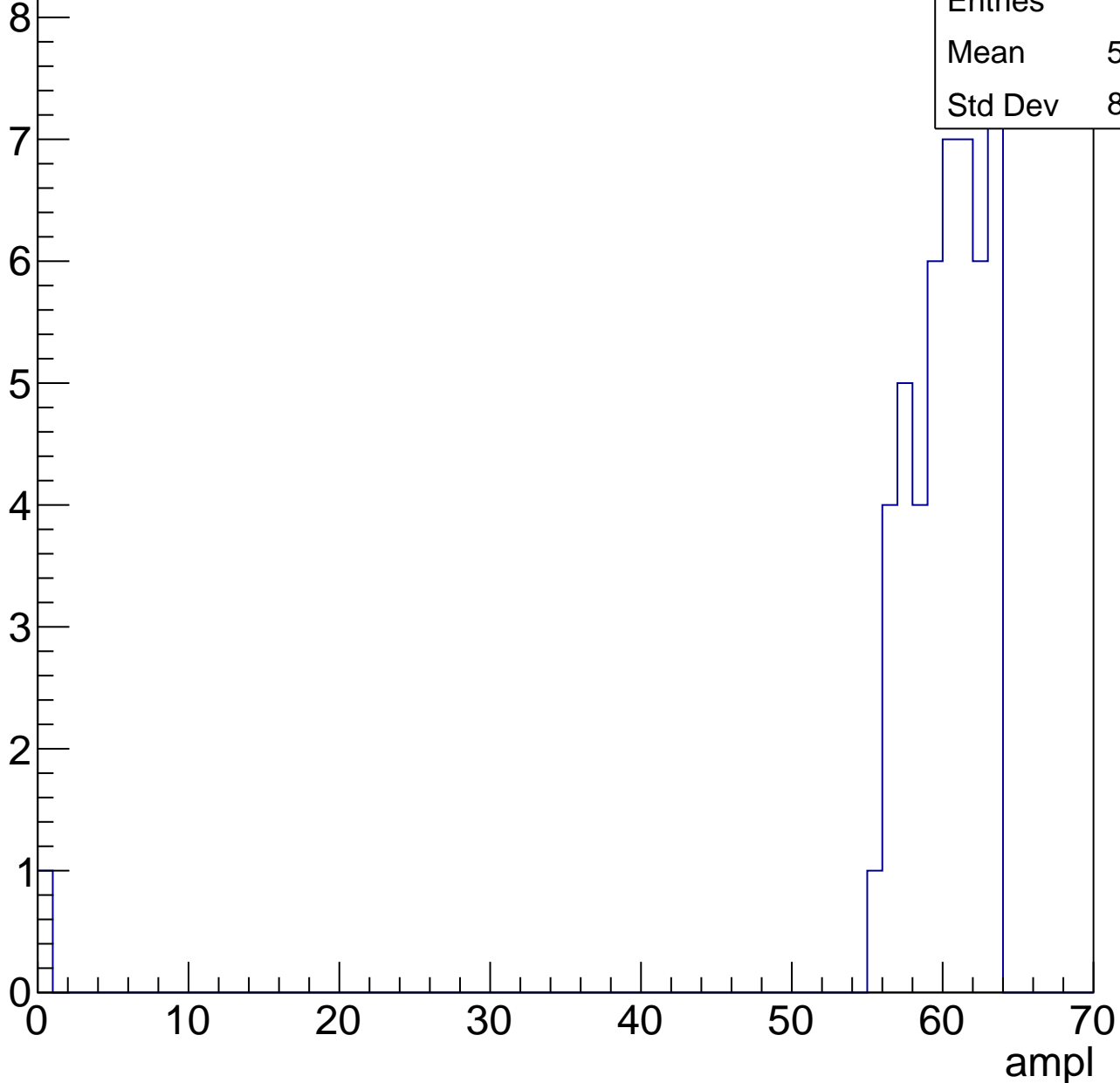


B1L100S, U9-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.63 |
| Std Dev | 8.768 |



B1L100S, U9-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62.2 |
| Std Dev | 0.7483 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch105, adc0

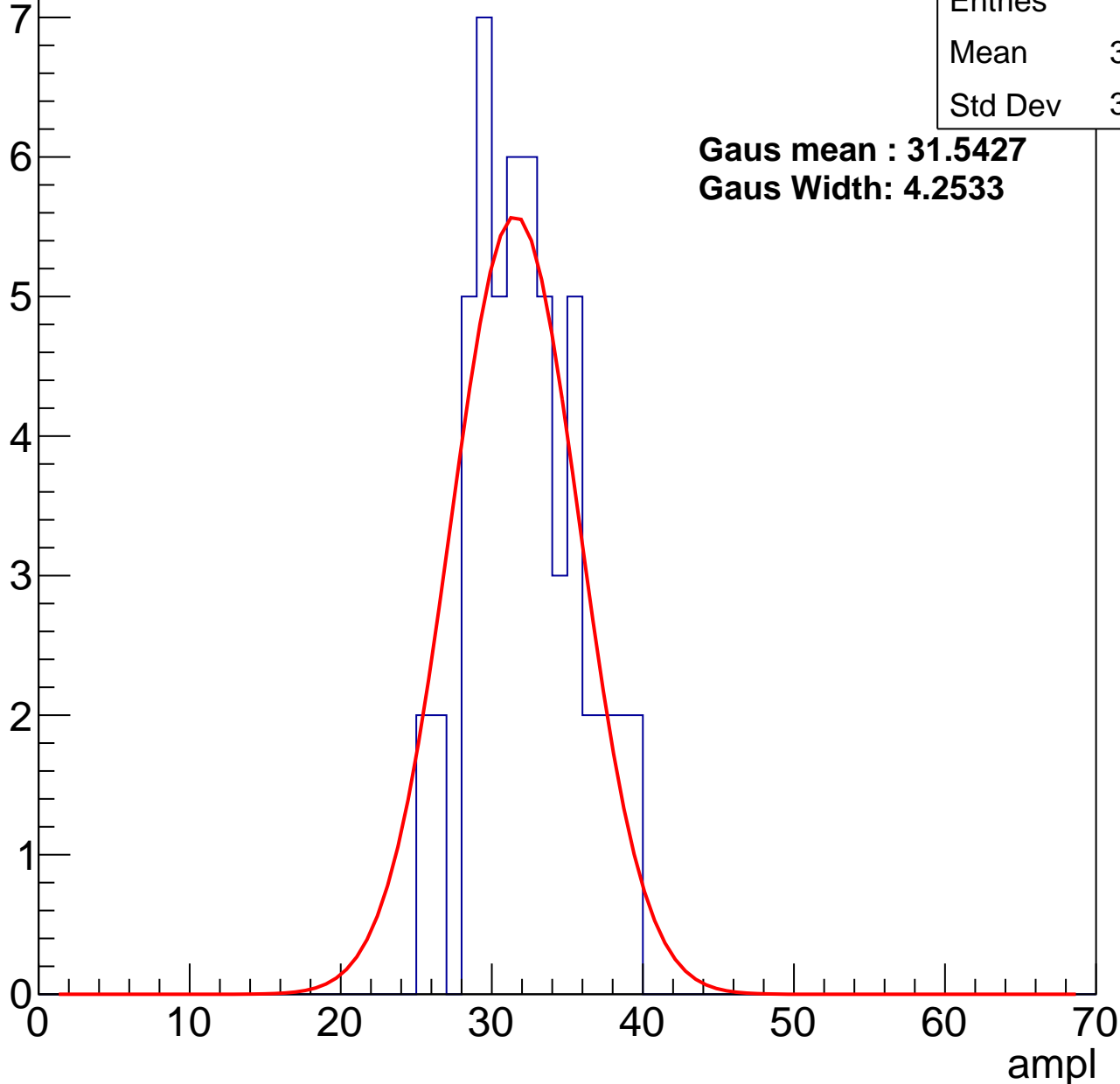
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 31.76 |
| Std Dev | 3.469 |

Gaus mean : 31.5427

Gaus Width: 4.2533



B1L100S, U9-ch105, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 84 |
| Mean | 38.01 |
| Std Dev | 3.527 |

Gaus mean : 38.4354

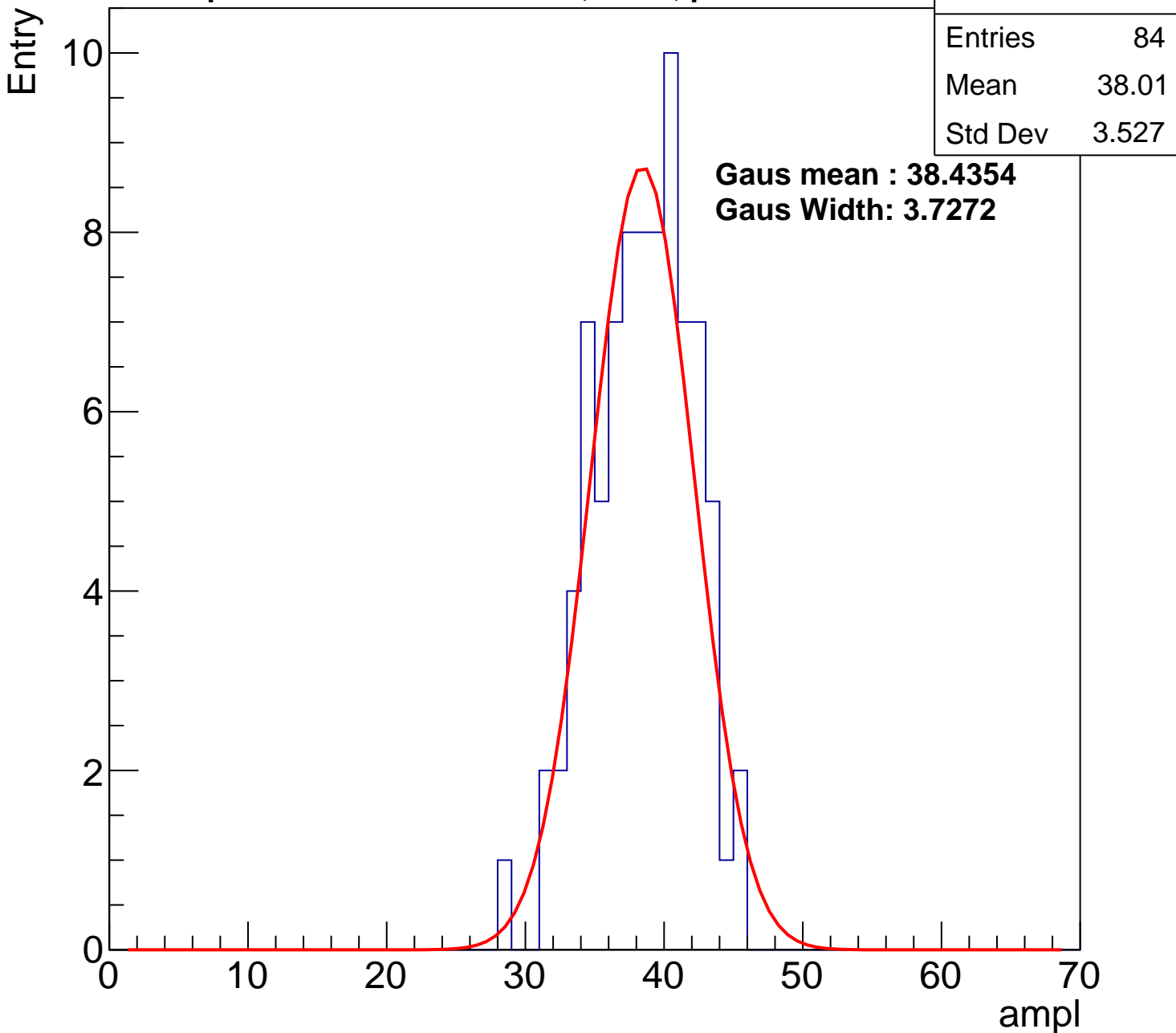
Gaus Width: 3.7272

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U9-ch105, adc2

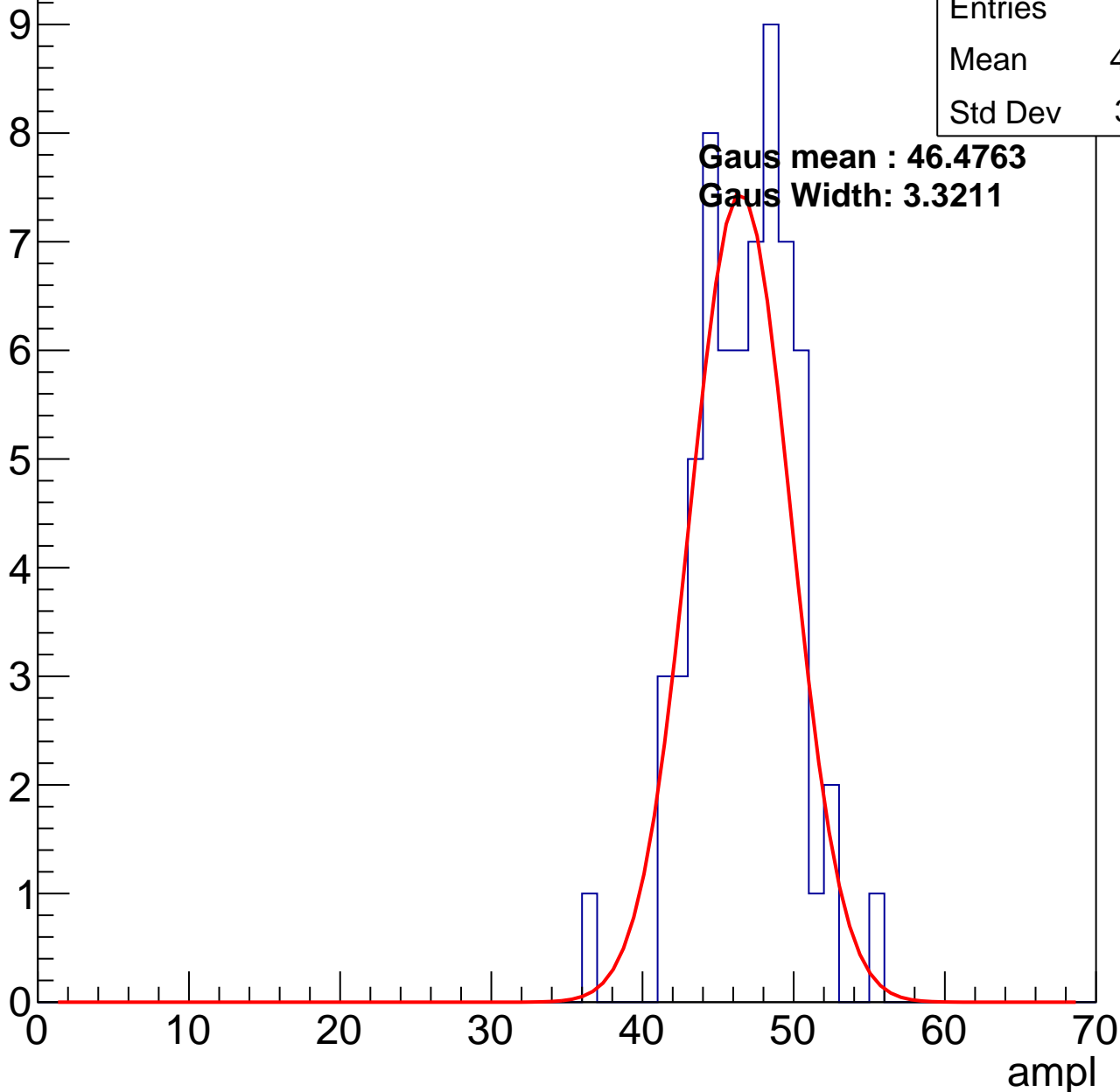
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 46.34 |
| Std Dev | 3.231 |

Gaus mean : 46.4763

Gaus Width: 3.3211

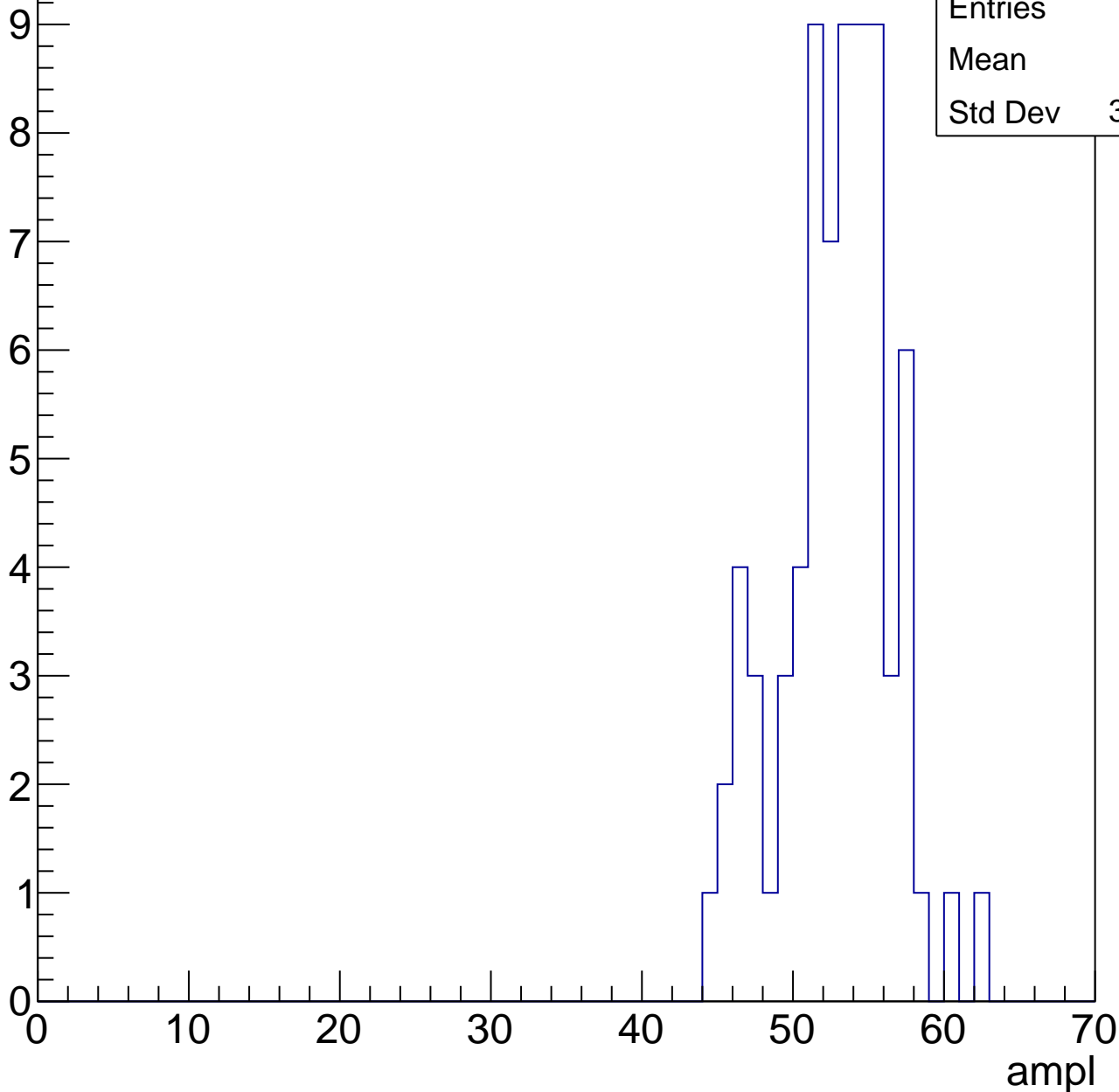


B1L100S, U9-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 52.4 |
| Std Dev | 3.629 |



B1L100S, U9-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.89 |
| Std Dev | 2.595 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

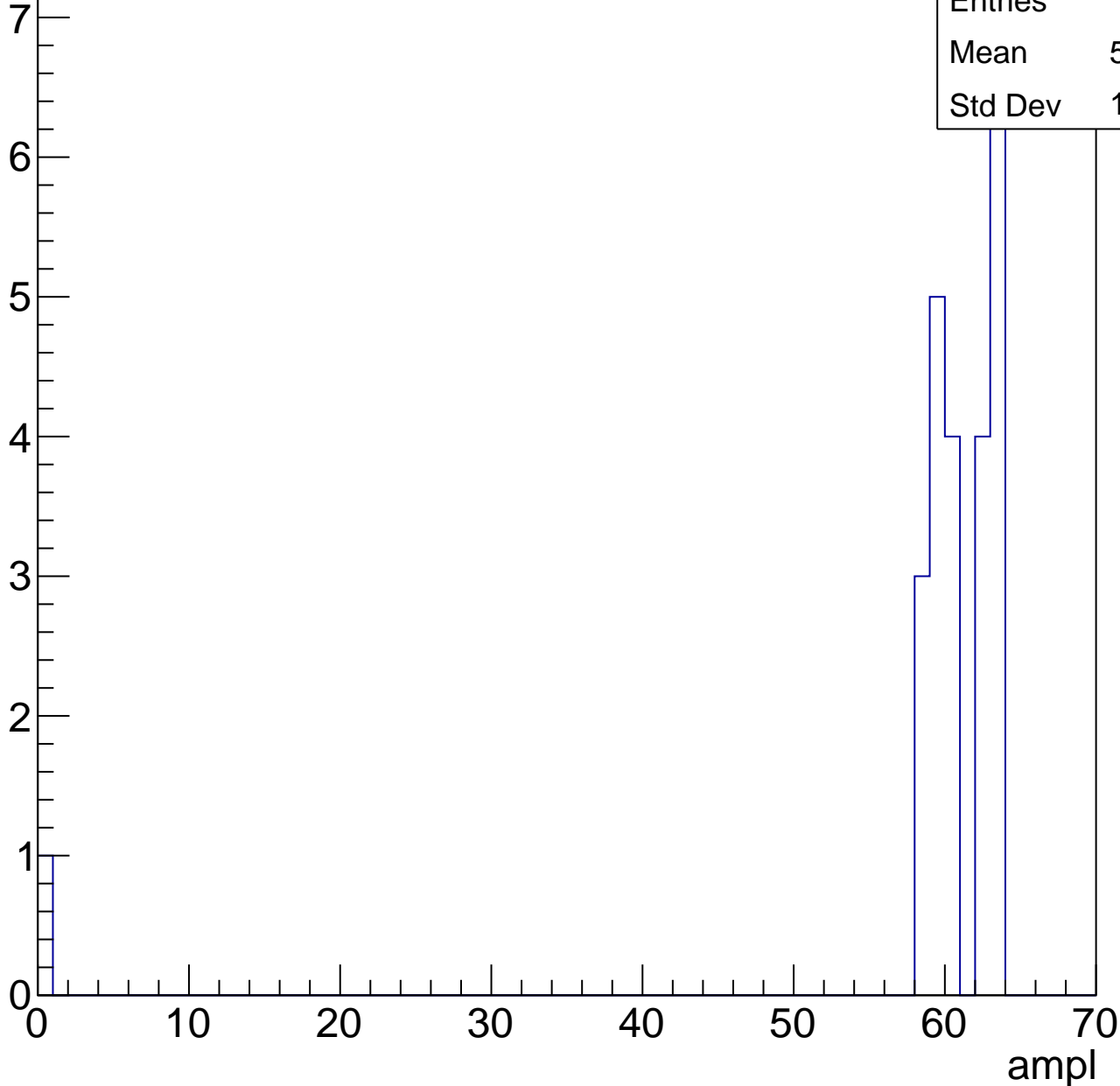
10

B1L100S, U9-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.25 |
| Std Dev | 12.29 |



B1L100S, U9-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch106, adc0

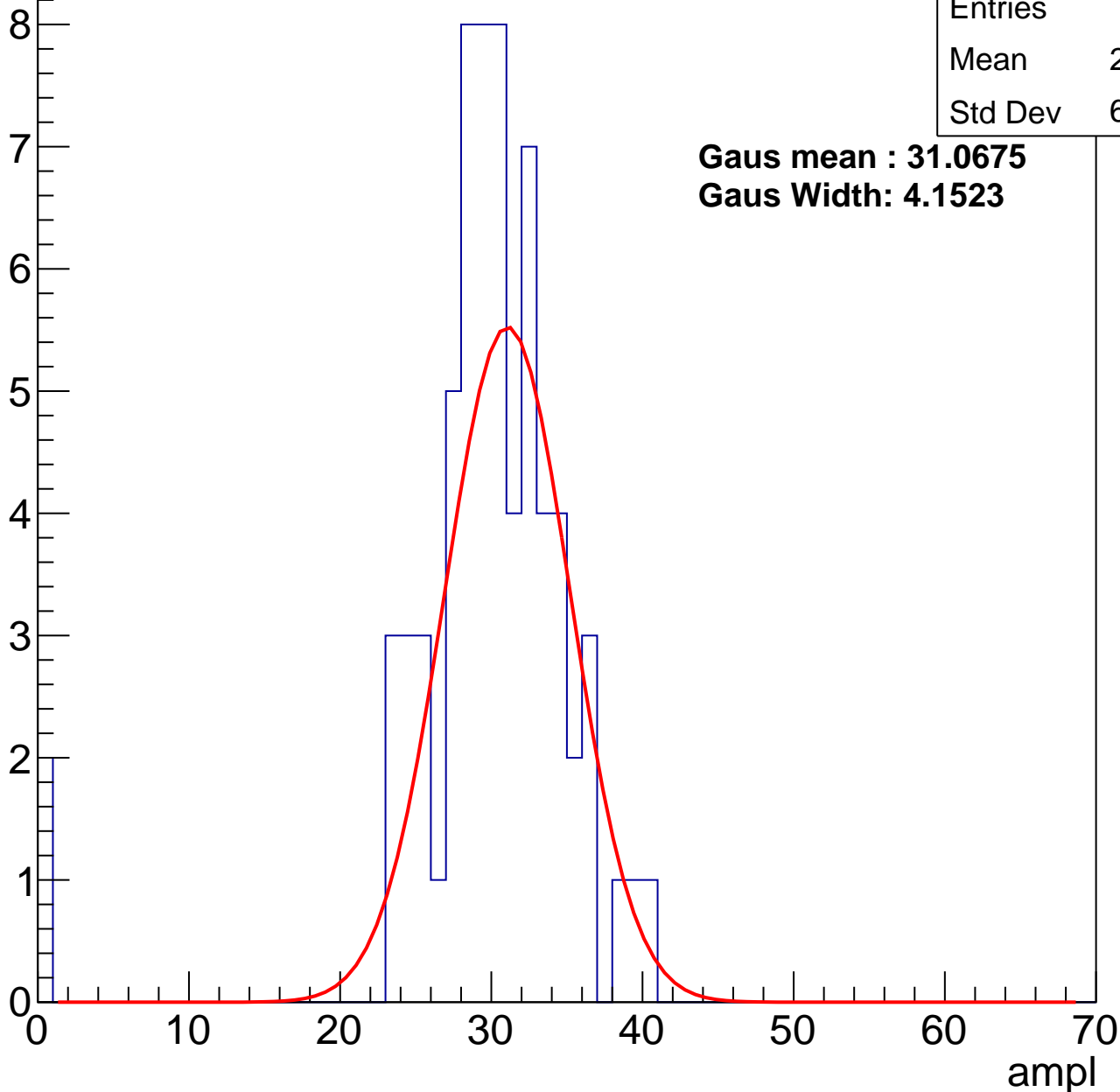
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.18 |
| Std Dev | 6.324 |

Gaus mean : 31.0675

Gaus Width: 4.1523



B1L100S, U9-ch106, adc1

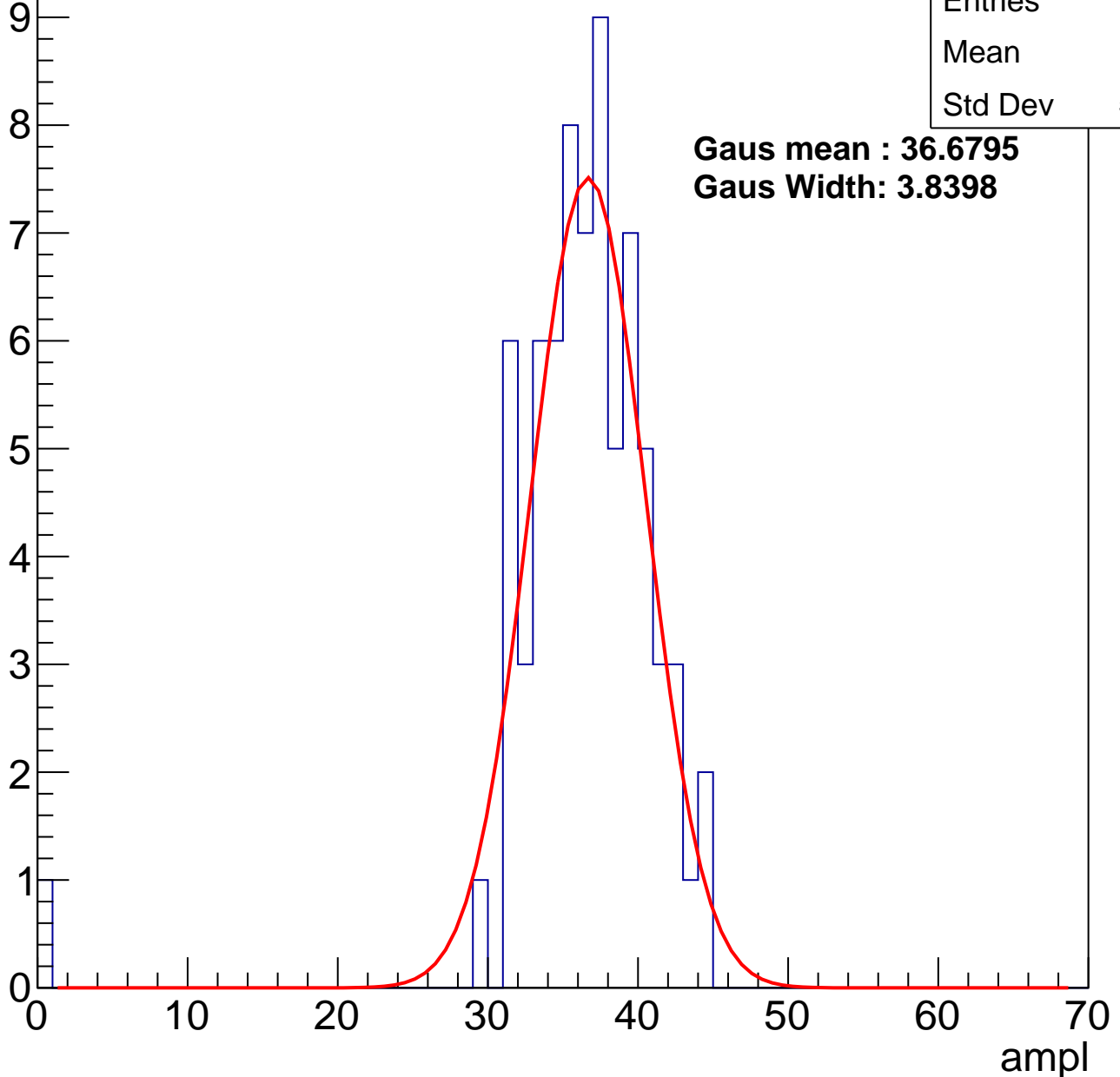
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 73 |
| Mean | 35.9 |
| Std Dev | 5.44 |

Gaus mean : 36.6795

Gaus Width: 3.8398



B1L100S, U9-ch106, adc2

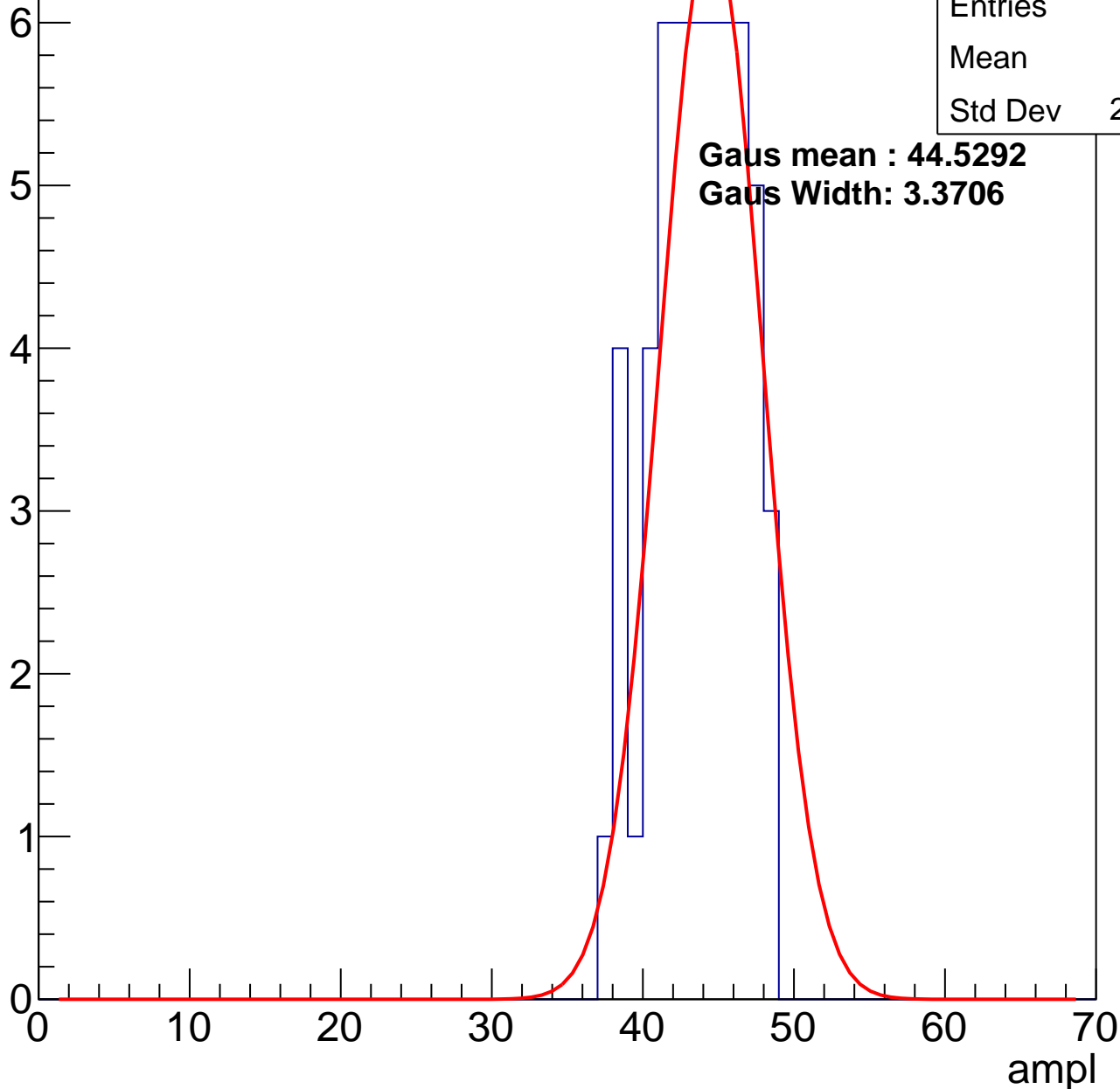
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.2 |
| Std Dev | 2.902 |

Gaus mean : 44.5292

Gaus Width: 3.3706

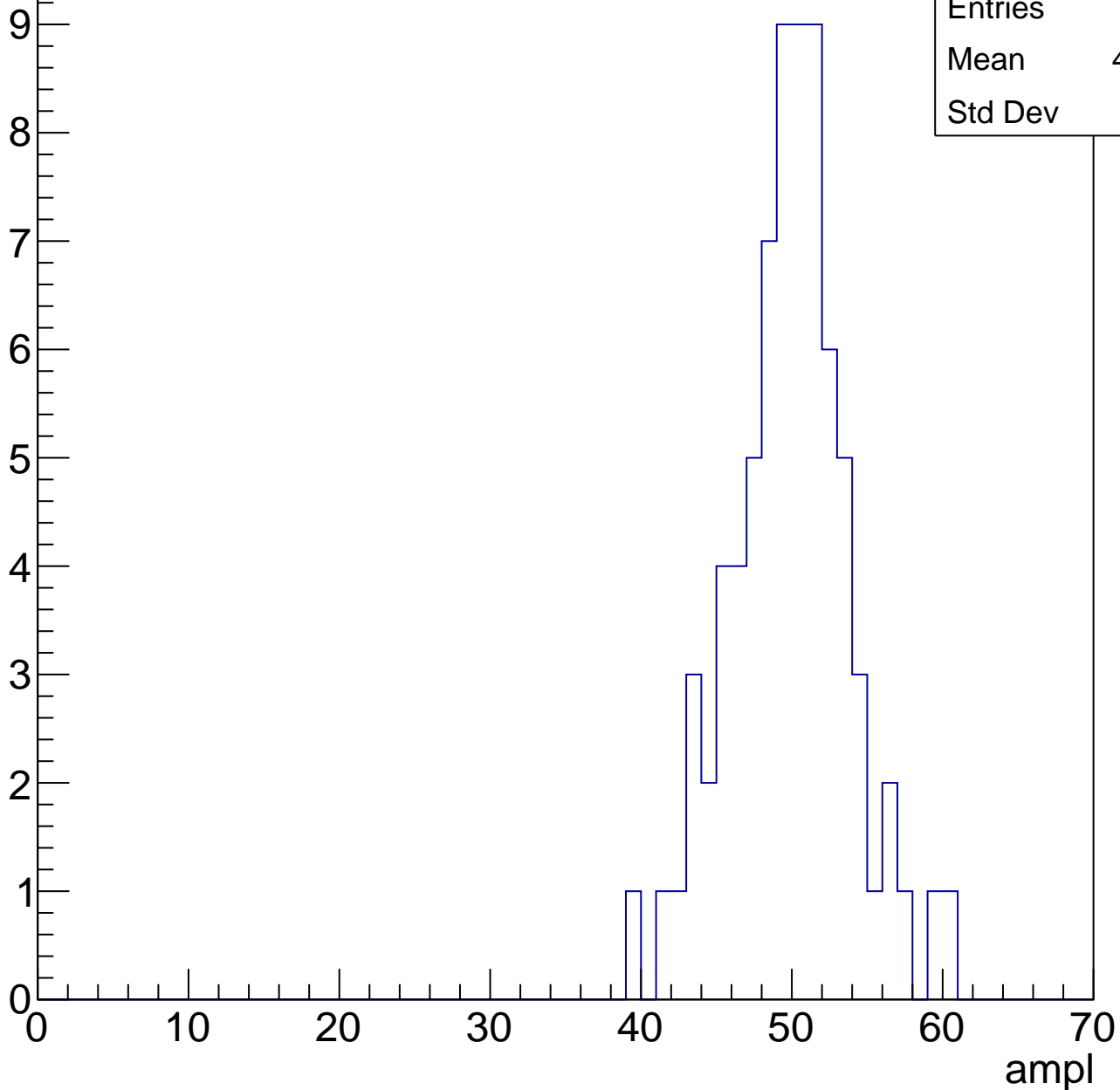


B1L100S, U9-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.41 |
| Std Dev | 3.93 |

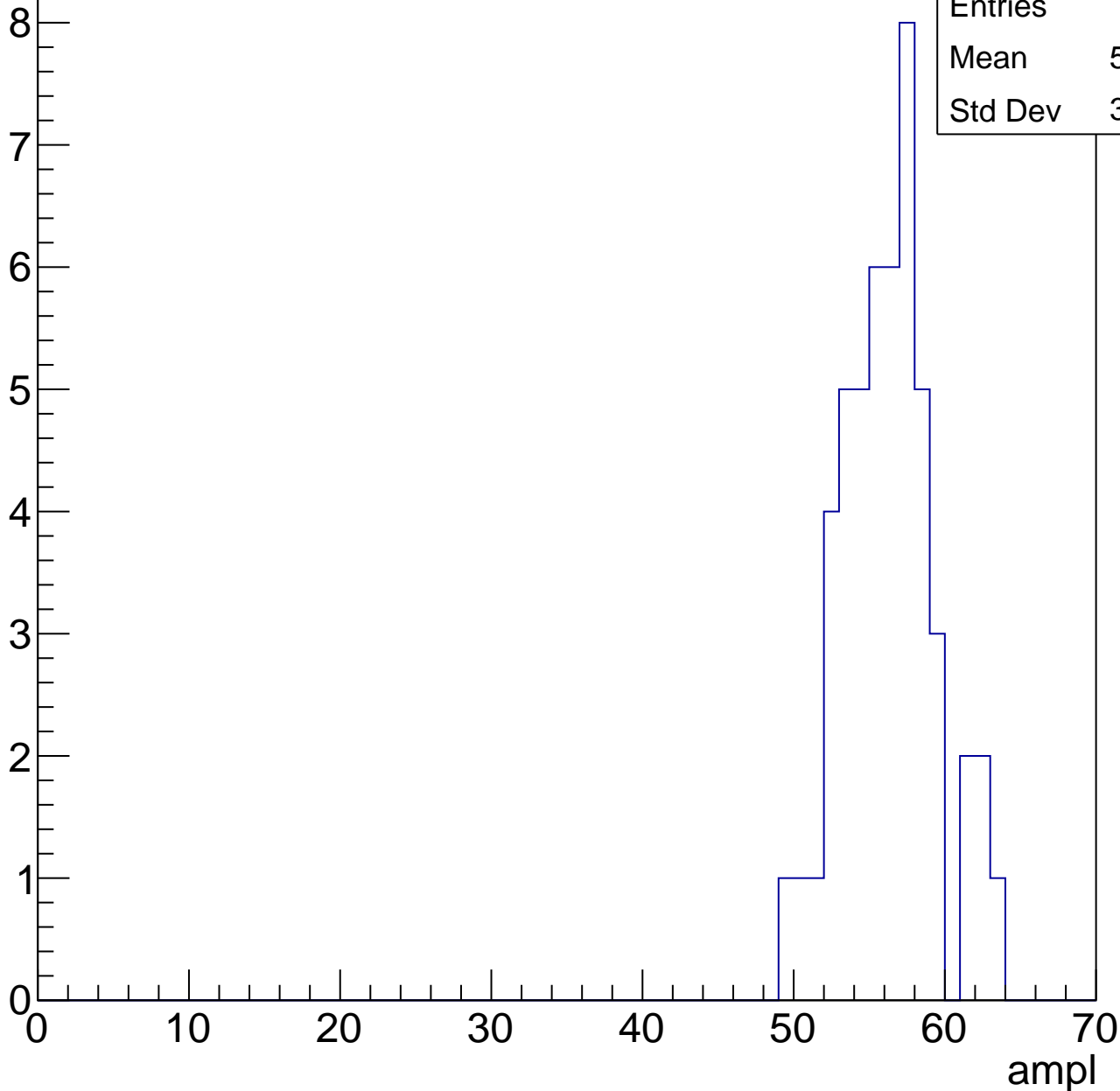


B1L100S, U9-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 55.82 |
| Std Dev | 3.064 |



B1L100S, U9-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

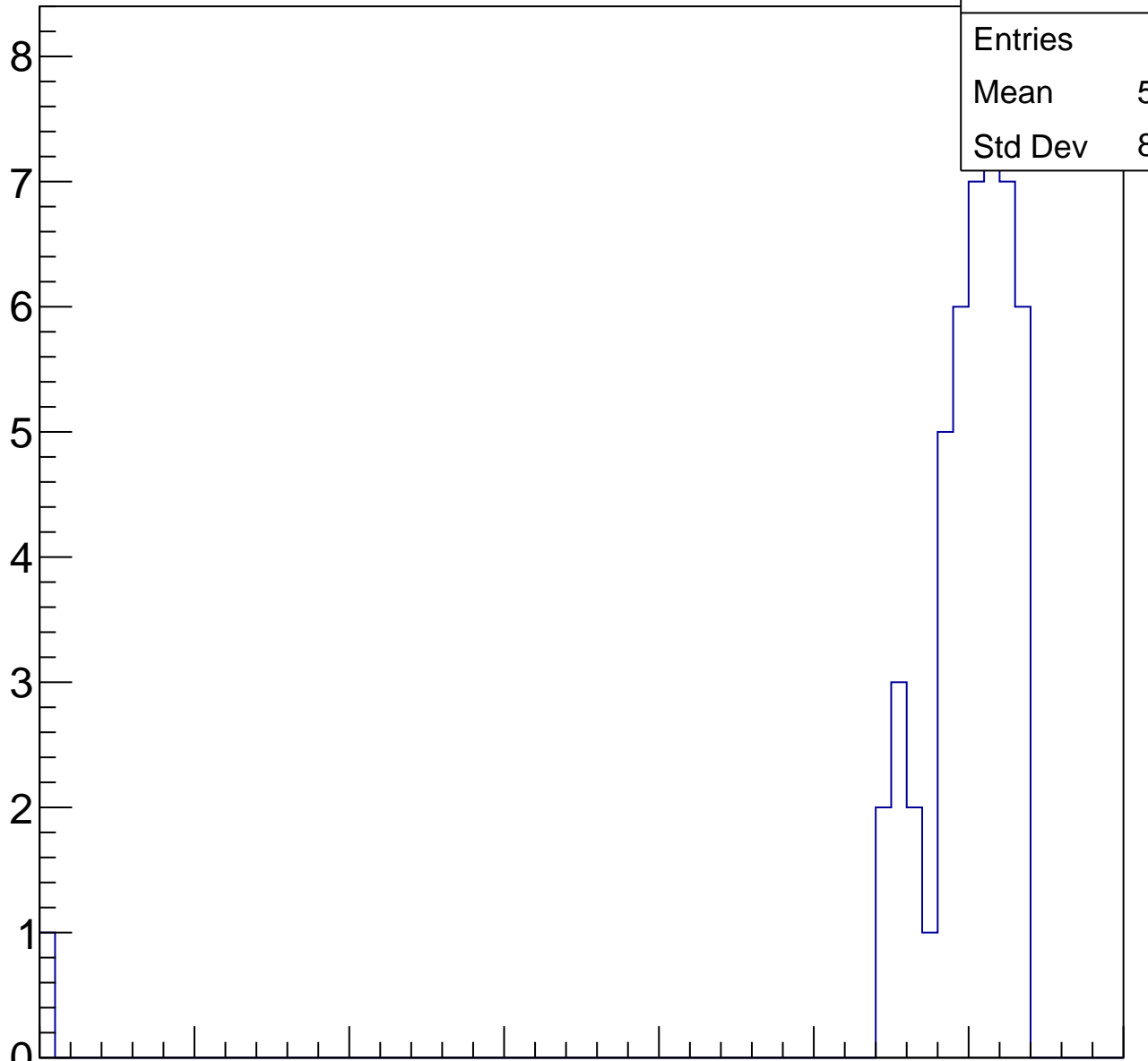
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.46 |
| Std Dev | 8.886 |

ampl

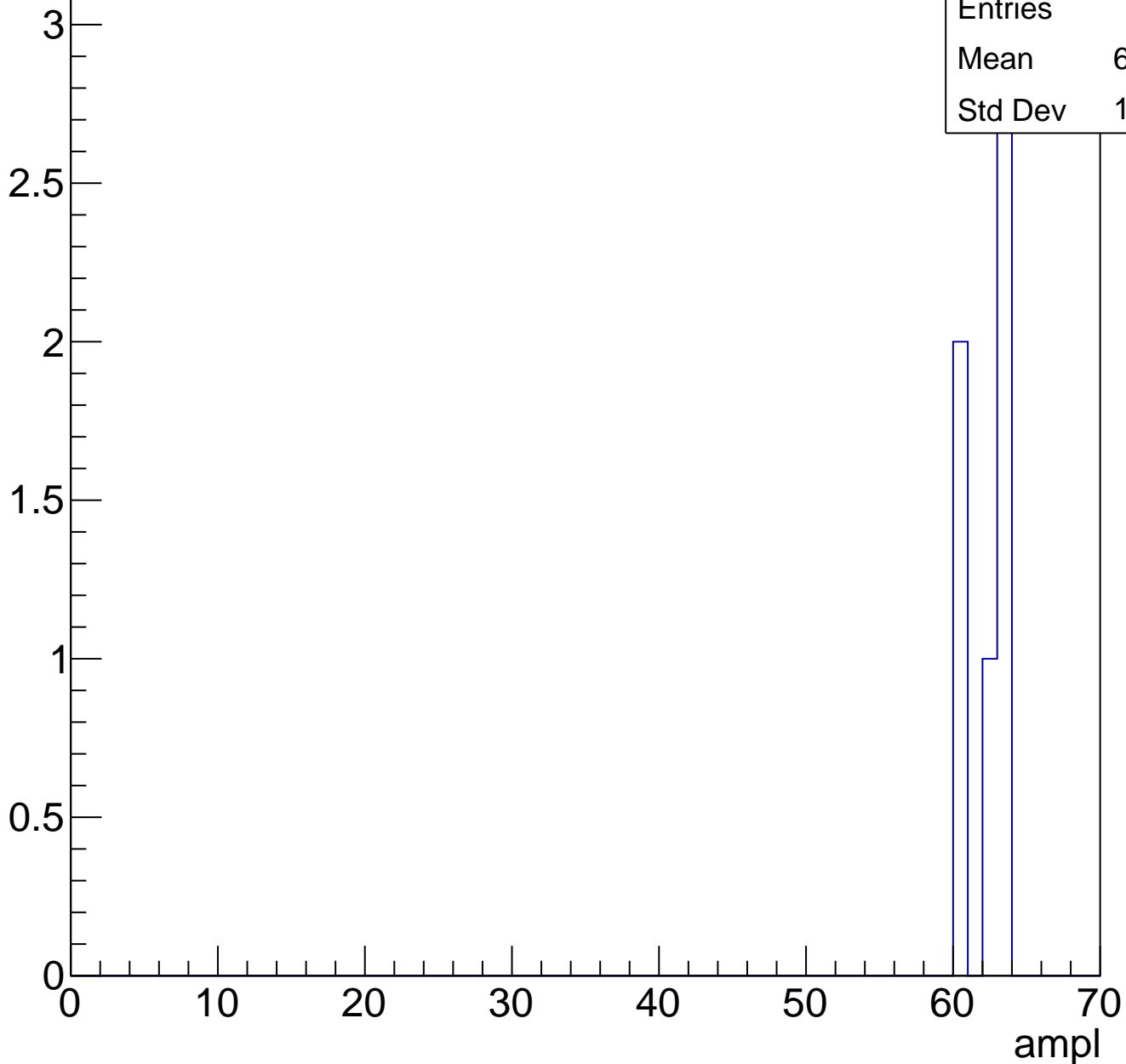
0 10 20 30 40 50 60 70



B1L100S, U9-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch107, adc0

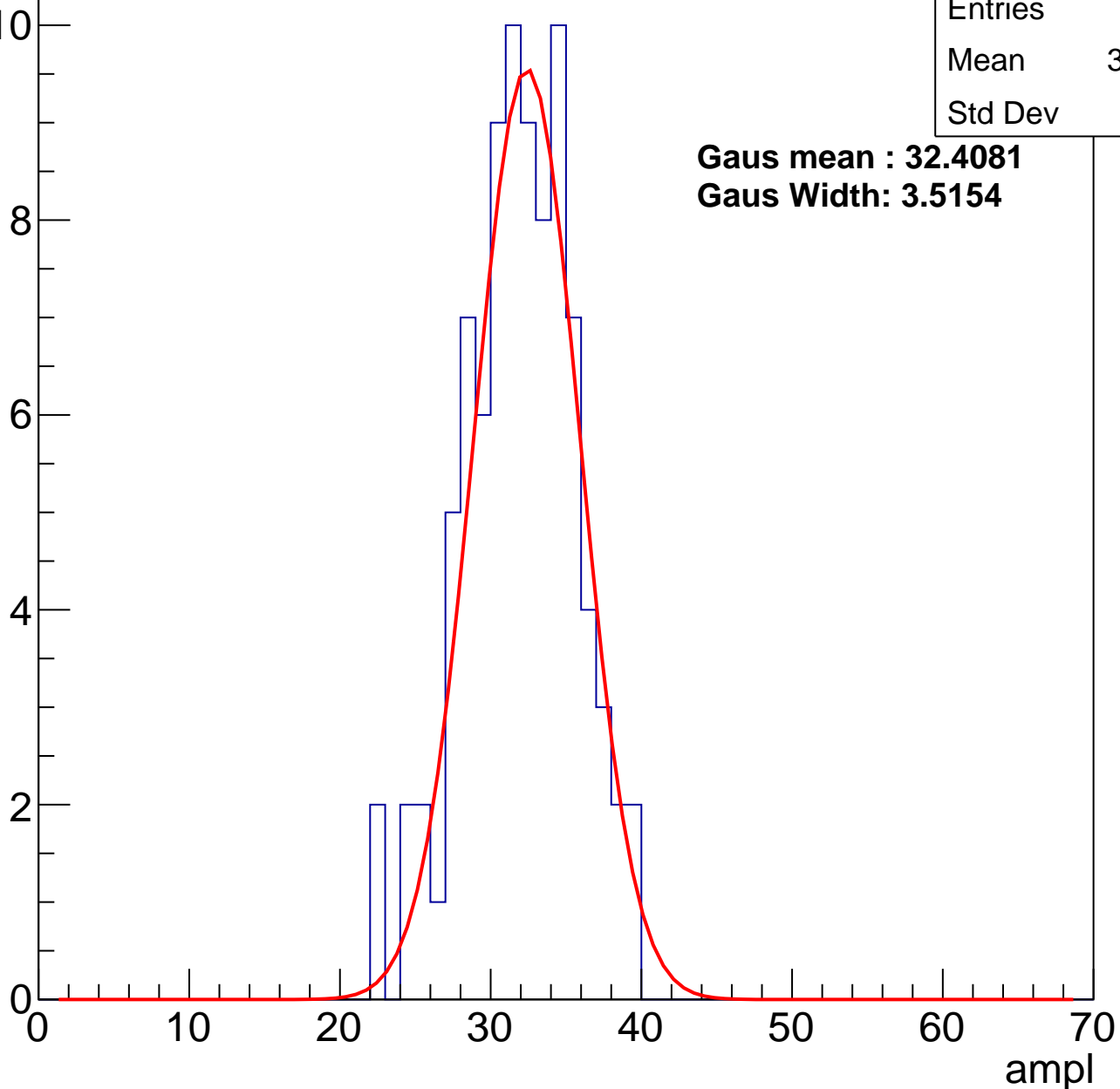
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 31.45 |
| Std Dev | 3.66 |

Gaus mean : 32.4081

Gaus Width: 3.5154



B1L100S, U9-ch107, adc1

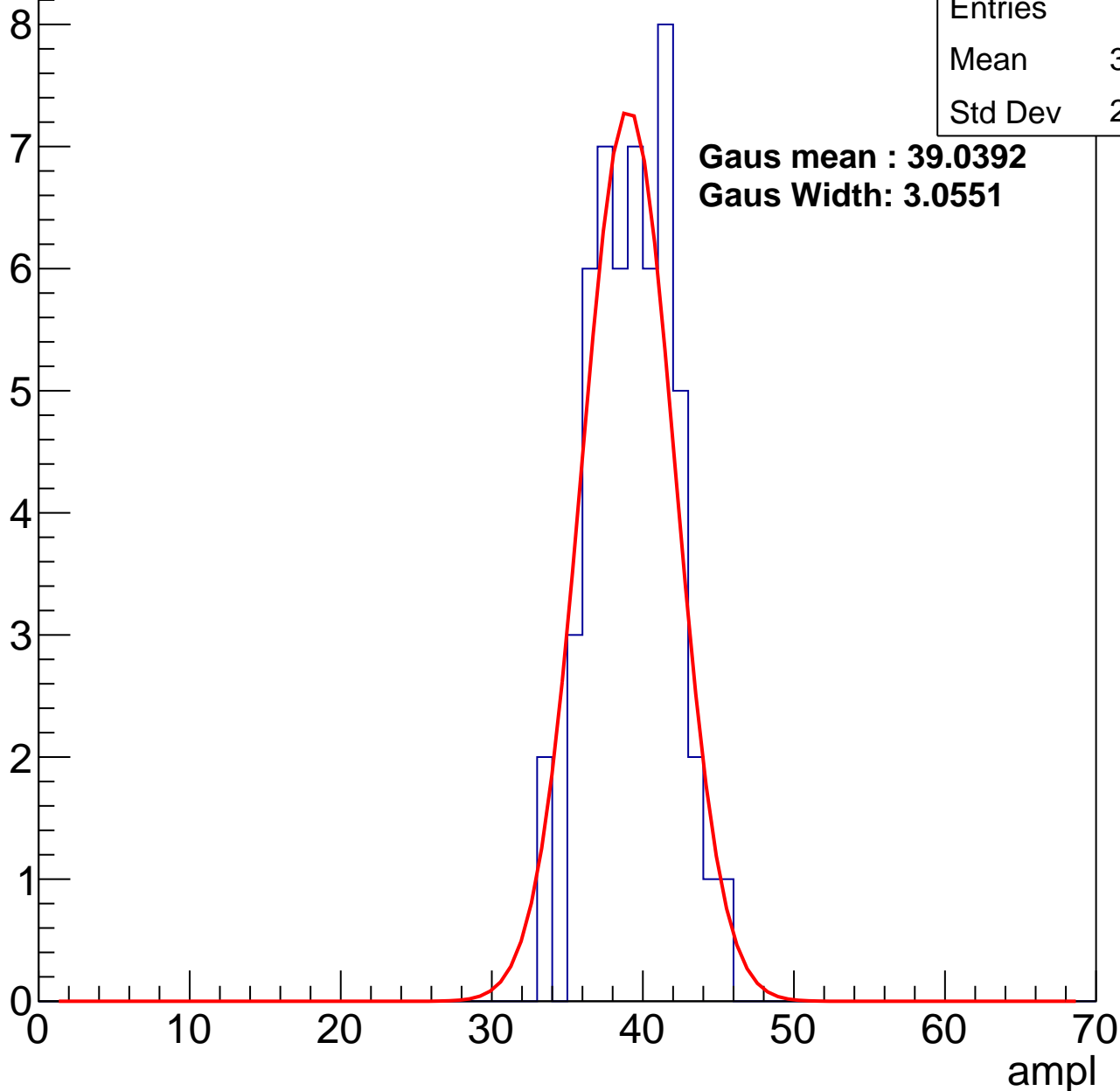
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 38.89 |
| Std Dev | 2.664 |

Gaus mean : 39.0392

Gaus Width: 3.0551



B1L100S, U9-ch107, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 45.1 |
| Std Dev | 3.135 |

Gaus mean : 46.0649

Gaus Width: 3.4248

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

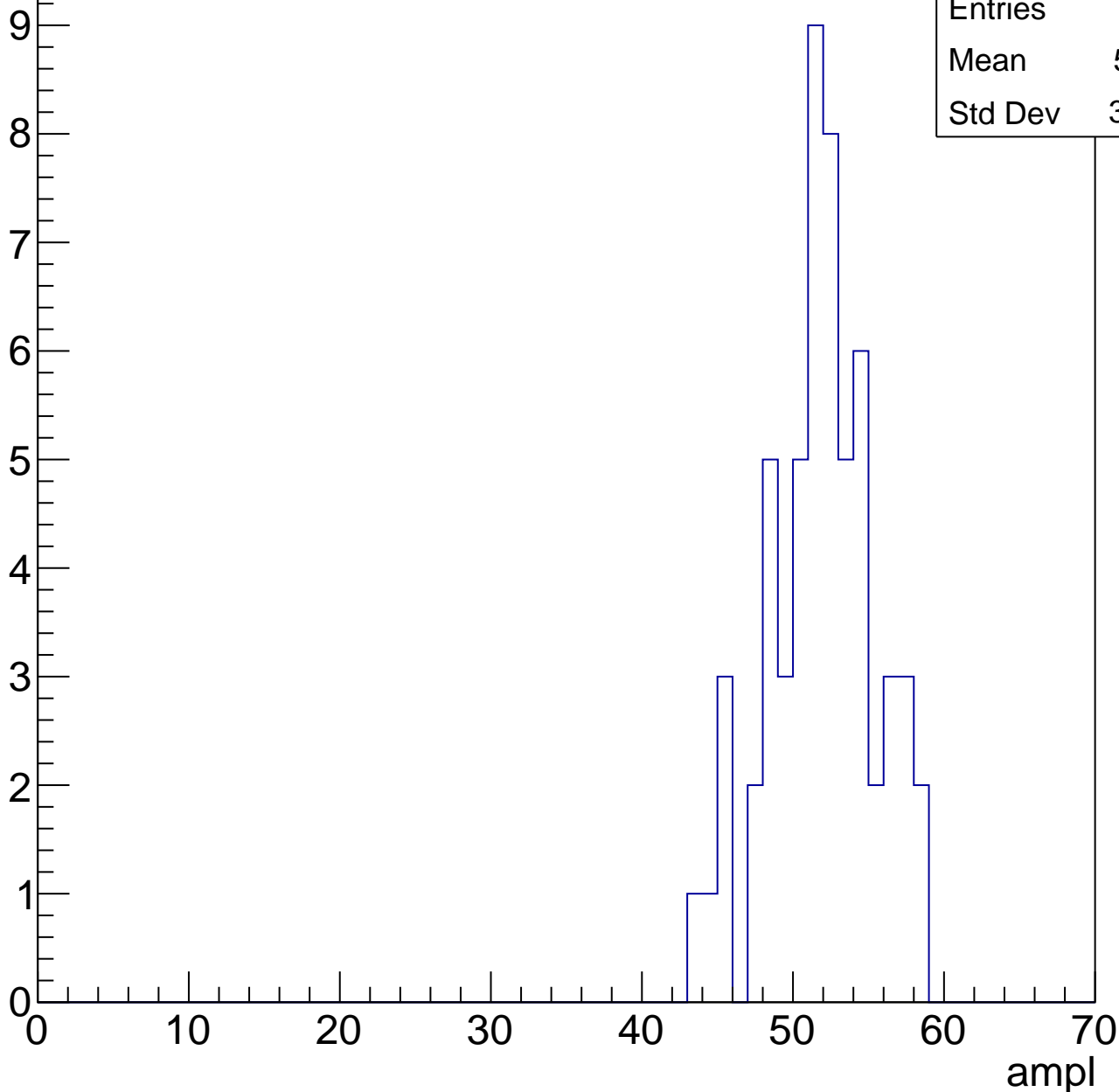
70

B1L100S, U9-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 51.41 |
| Std Dev | 3.474 |

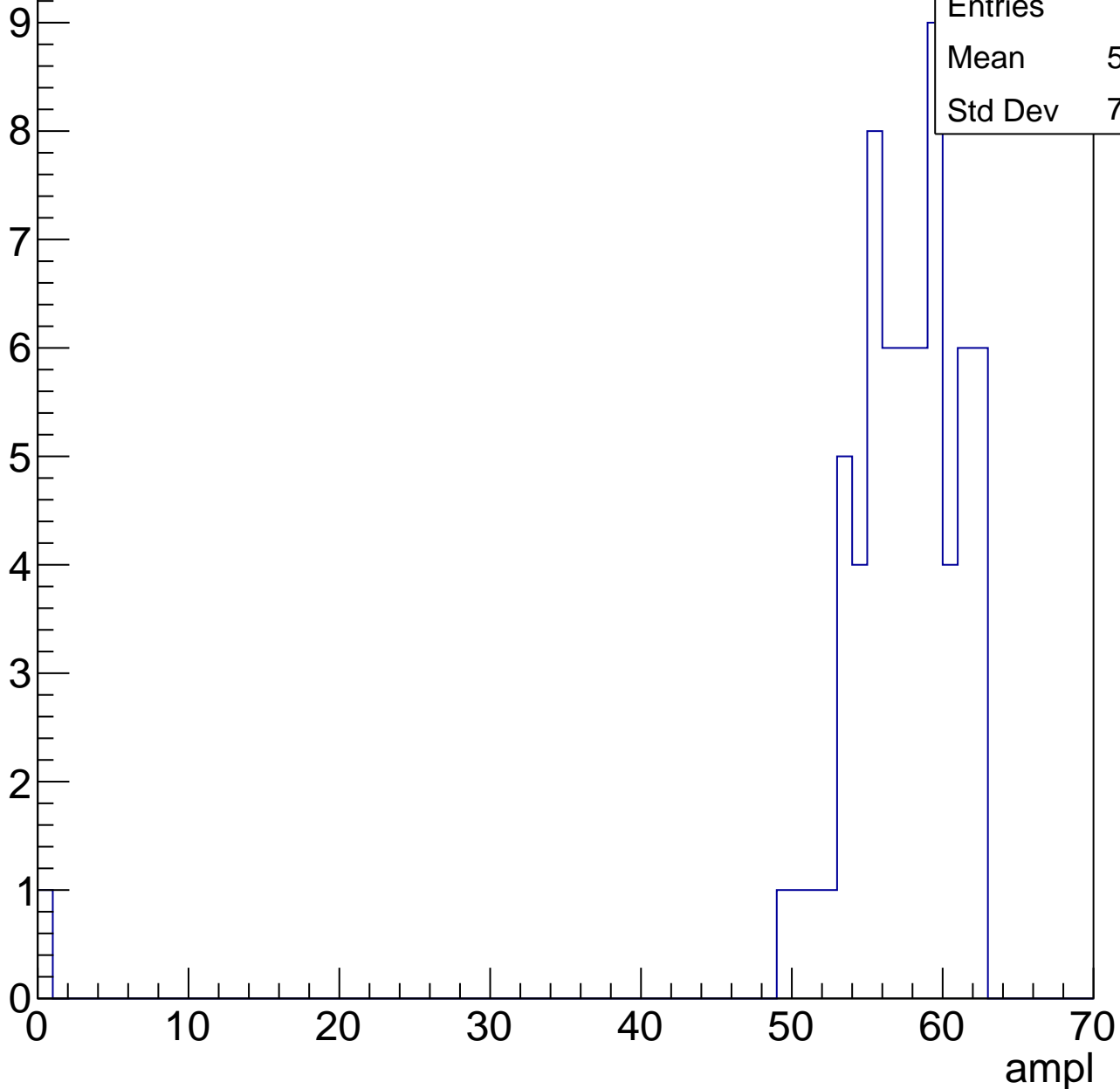


B1L100S, U9-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

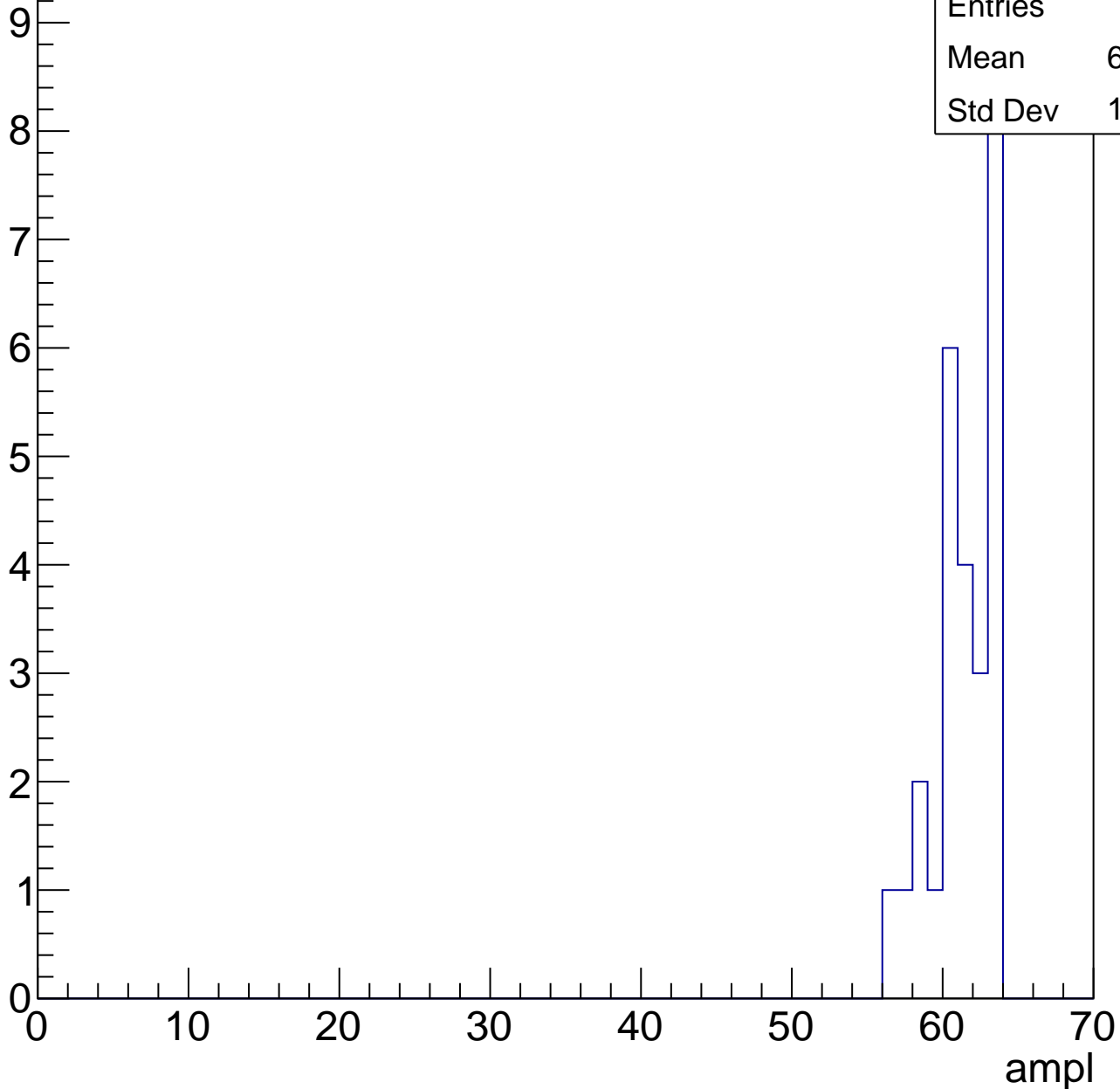
| | |
|---------|-------|
| Entries | 65 |
| Mean | 56.28 |
| Std Dev | 7.713 |



B1L100S, U9-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch108, adc0

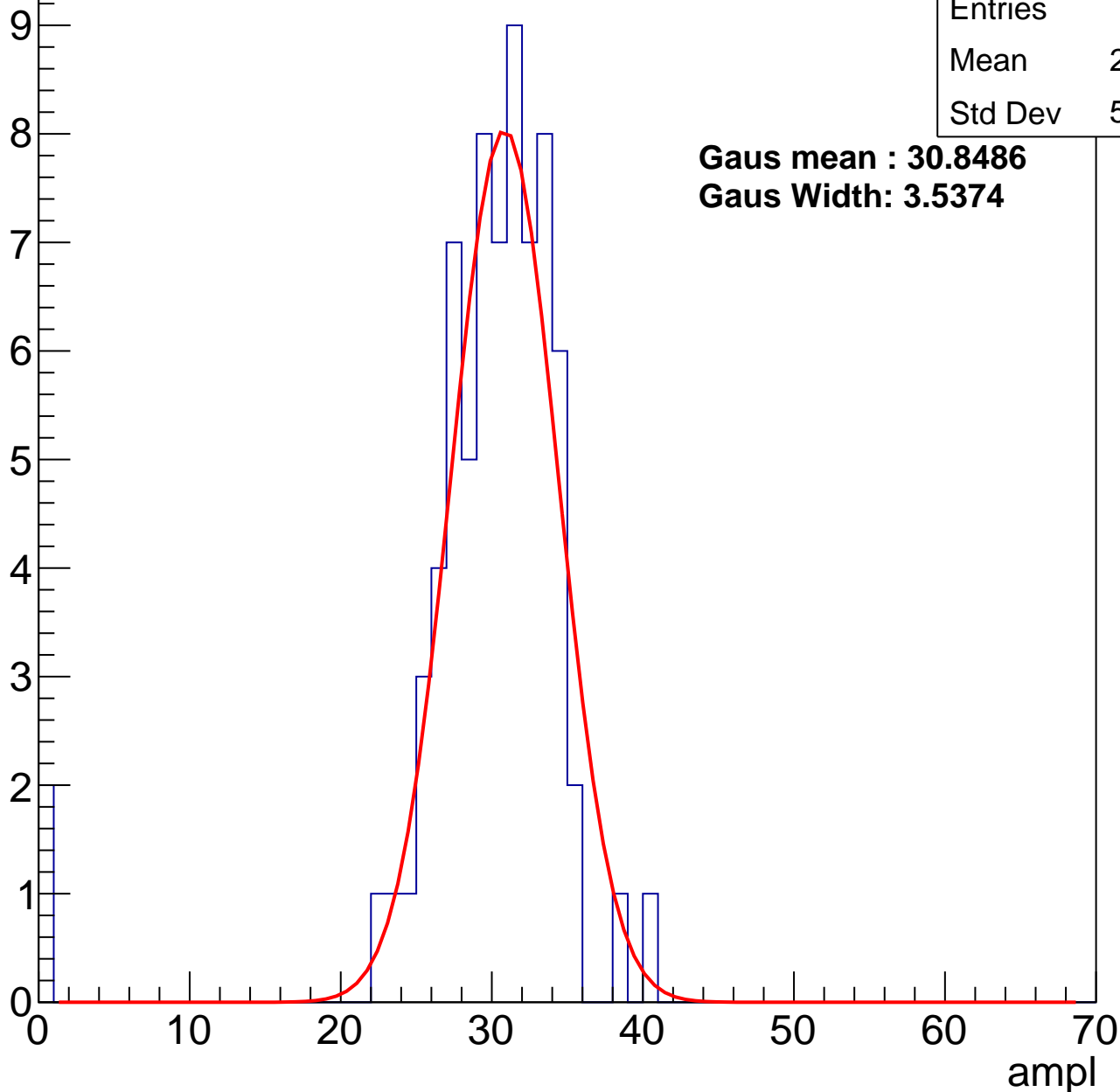
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.29 |
| Std Dev | 5.923 |

Gaus mean : 30.8486

Gaus Width: 3.5374



B1L100S, U9-ch108, adc1

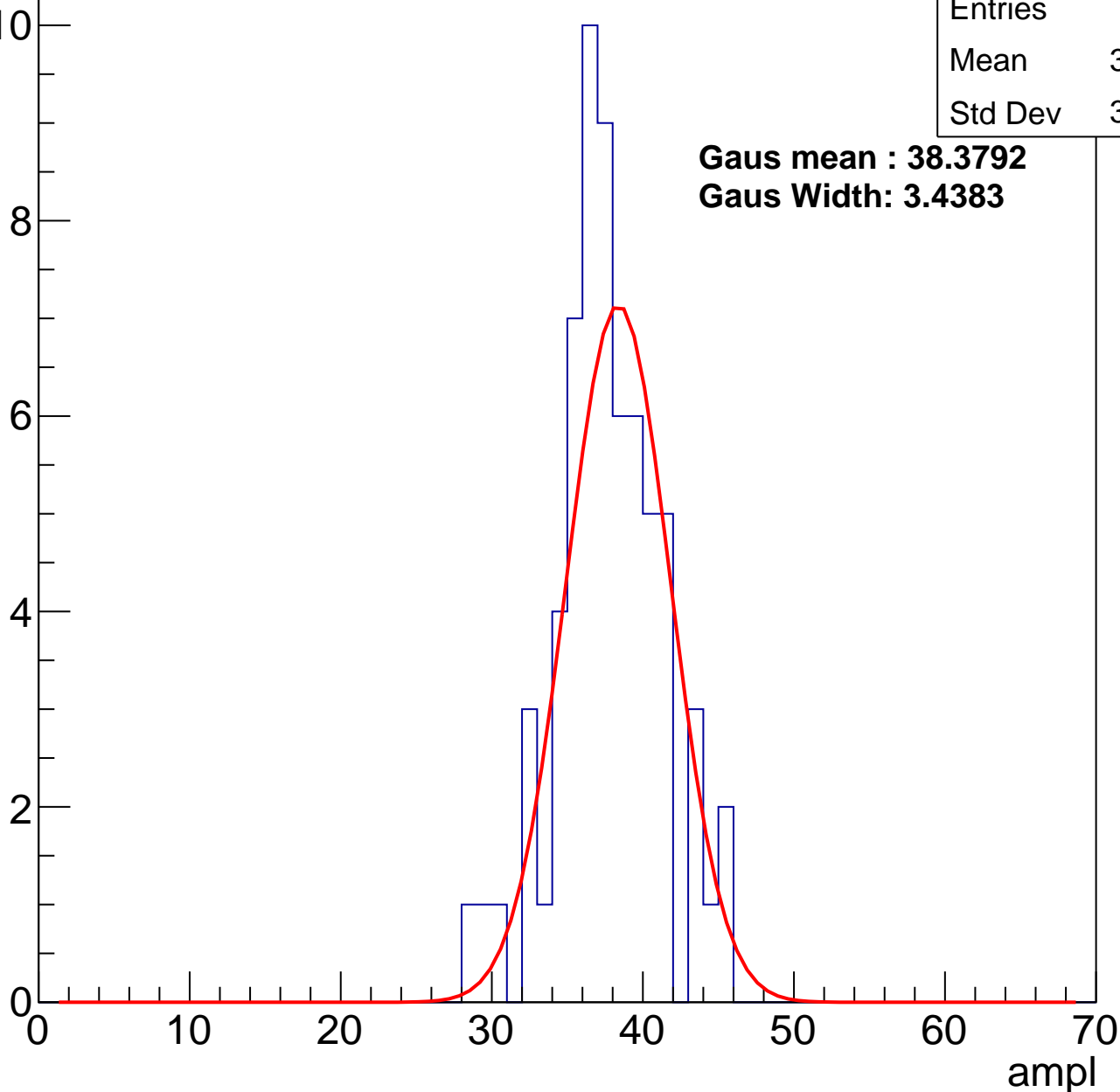
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 37.23 |
| Std Dev | 3.498 |

Gaus mean : 38.3792

Gaus Width: 3.4383



B1L100S, U9-ch108, adc2

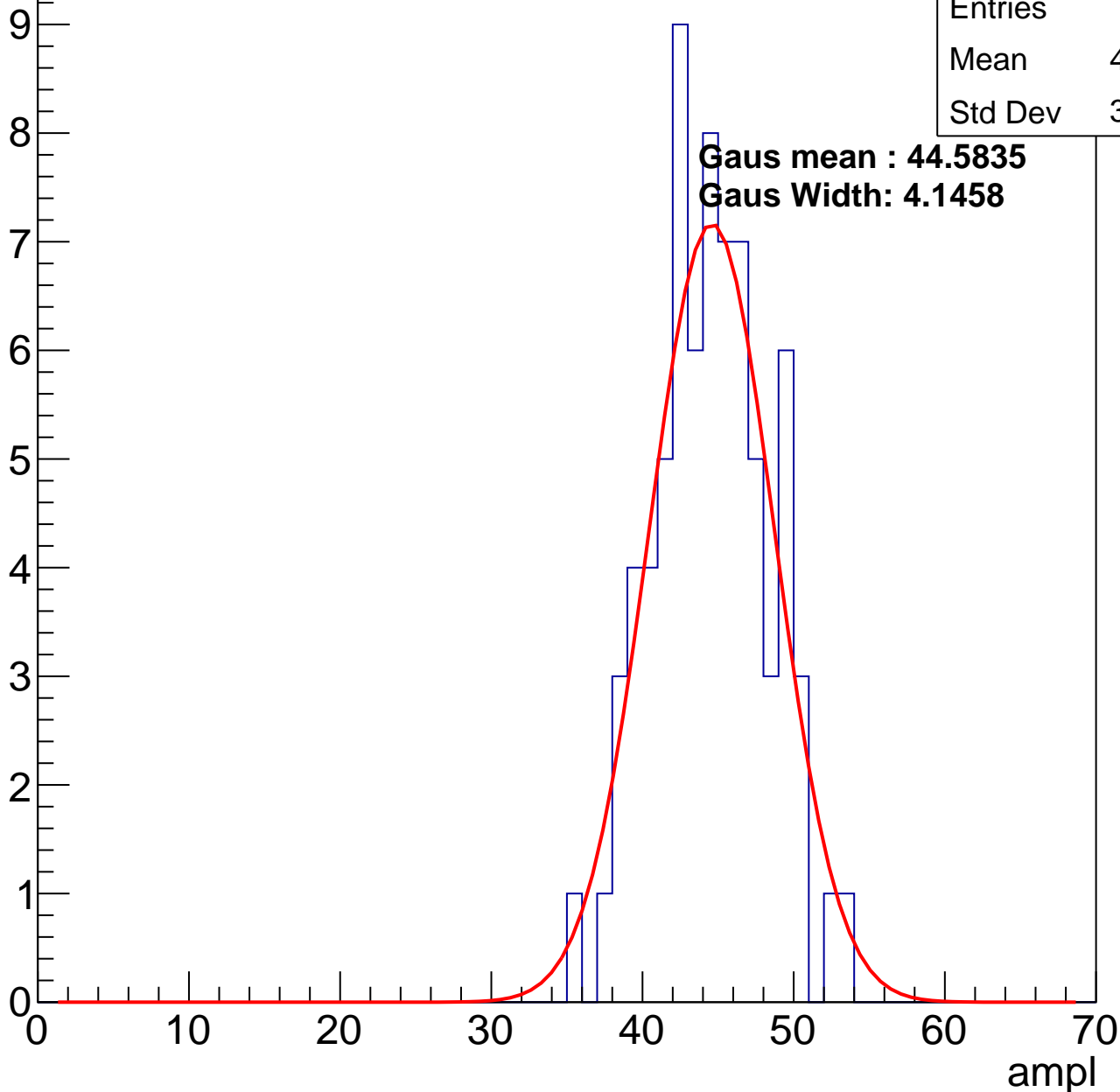
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 44.05 |
| Std Dev | 3.712 |

Gaus mean : 44.5835

Gaus Width: 4.1458

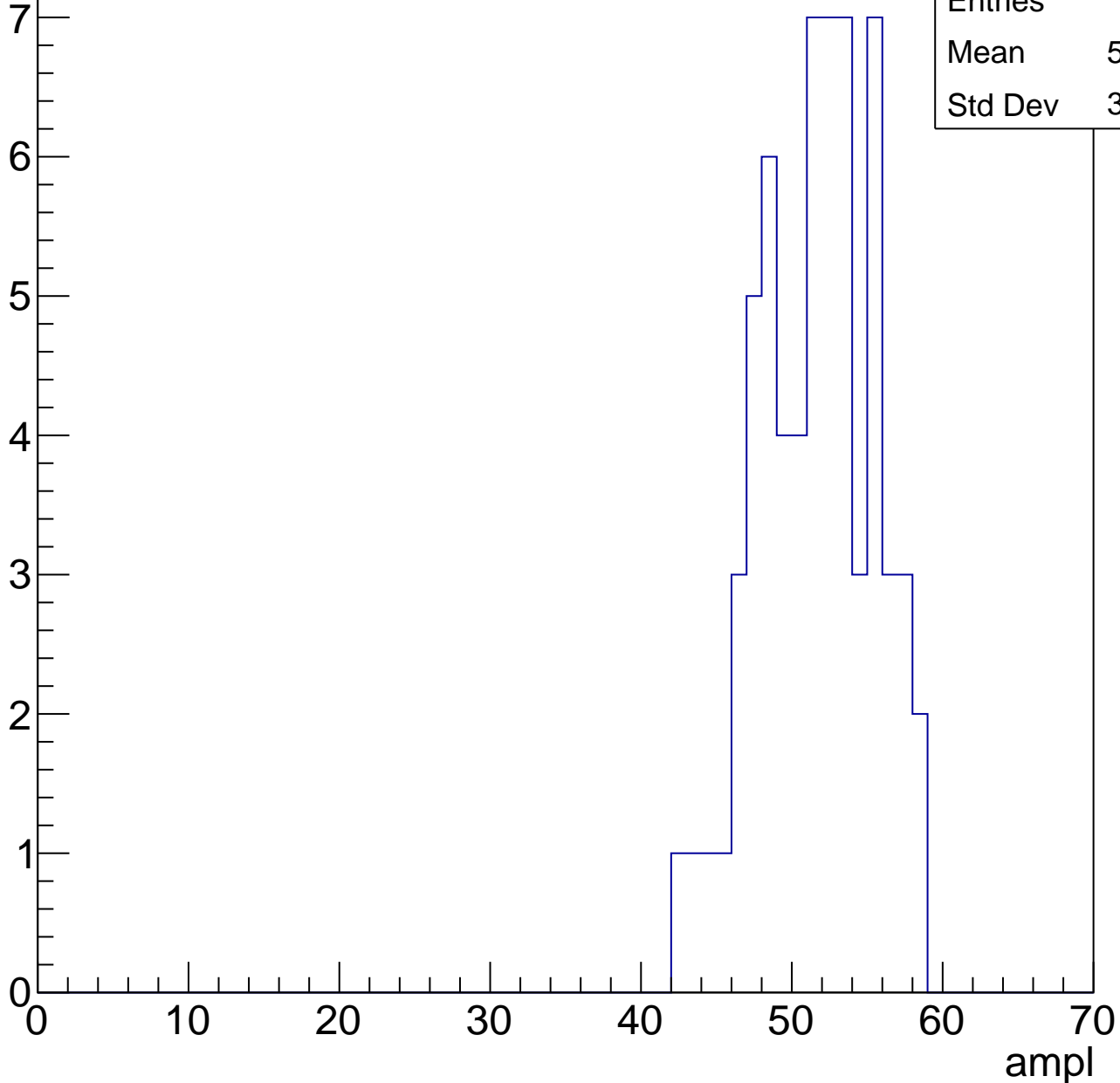


B1L100S, U9-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.15 |
| Std Dev | 3.747 |

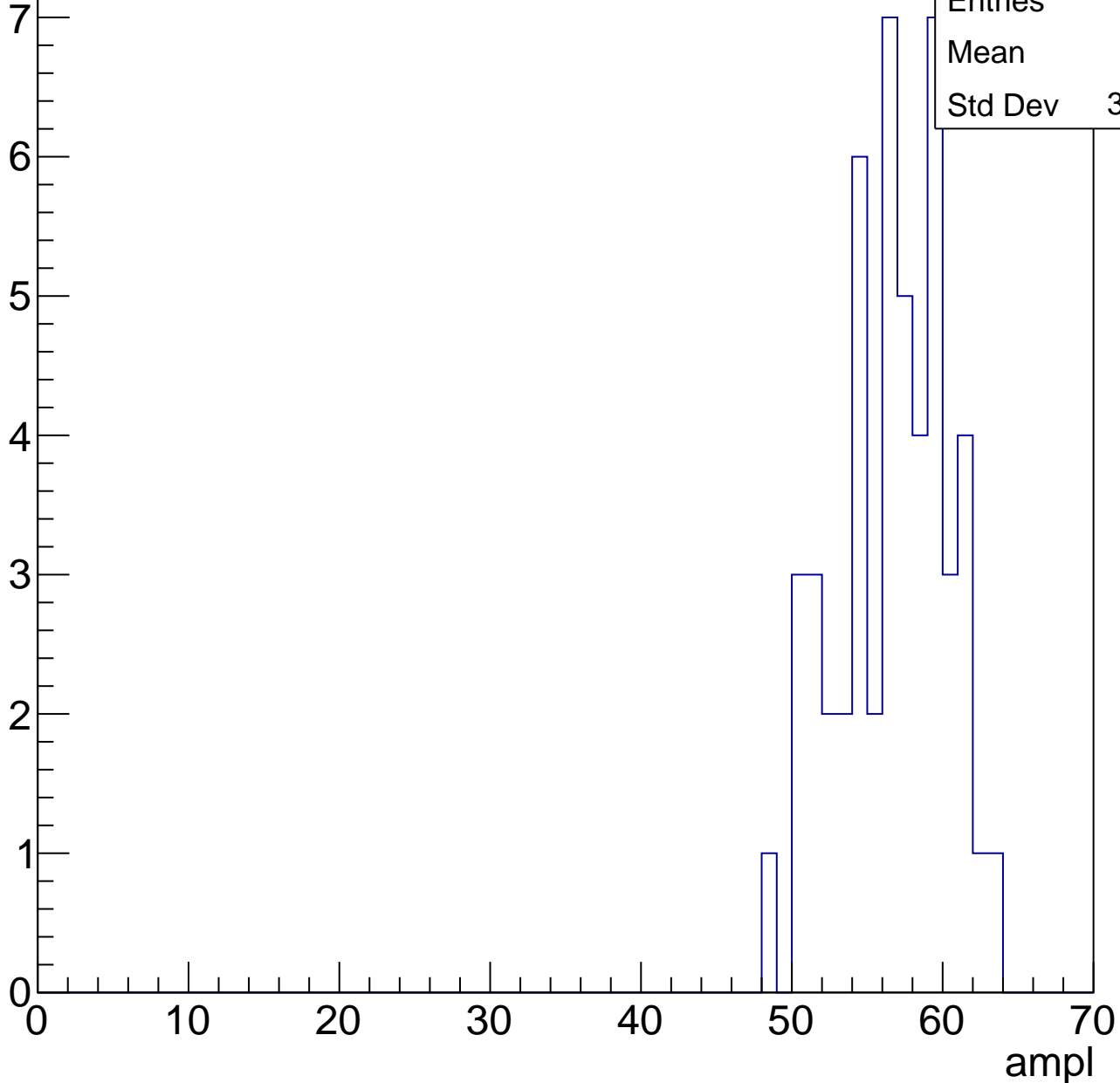


B1L100S, U9-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.2 |
| Std Dev | 3.537 |

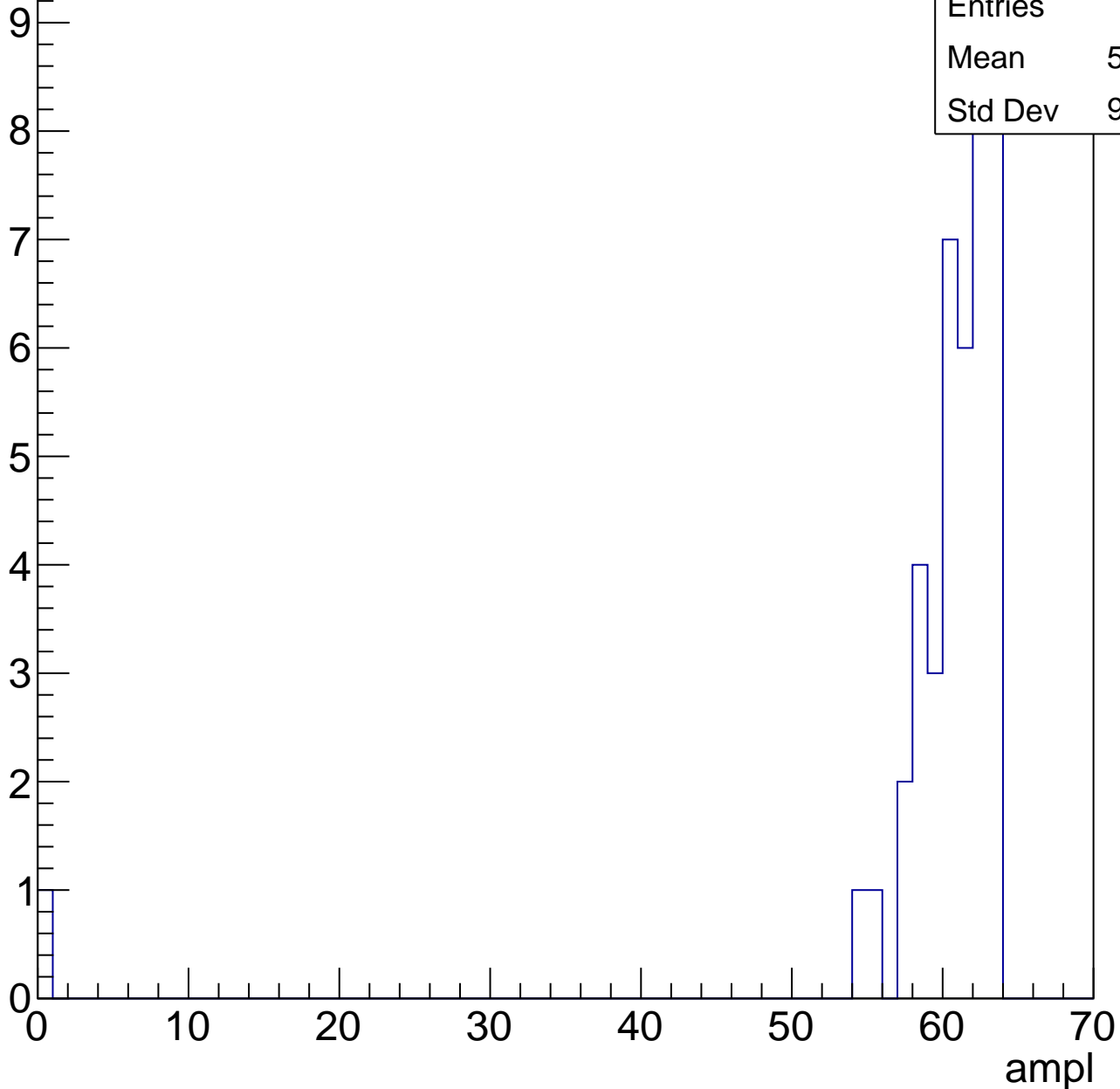


B1L100S, U9-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.07 |
| Std Dev | 9.488 |



B1L100S, U9-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch109, adc0

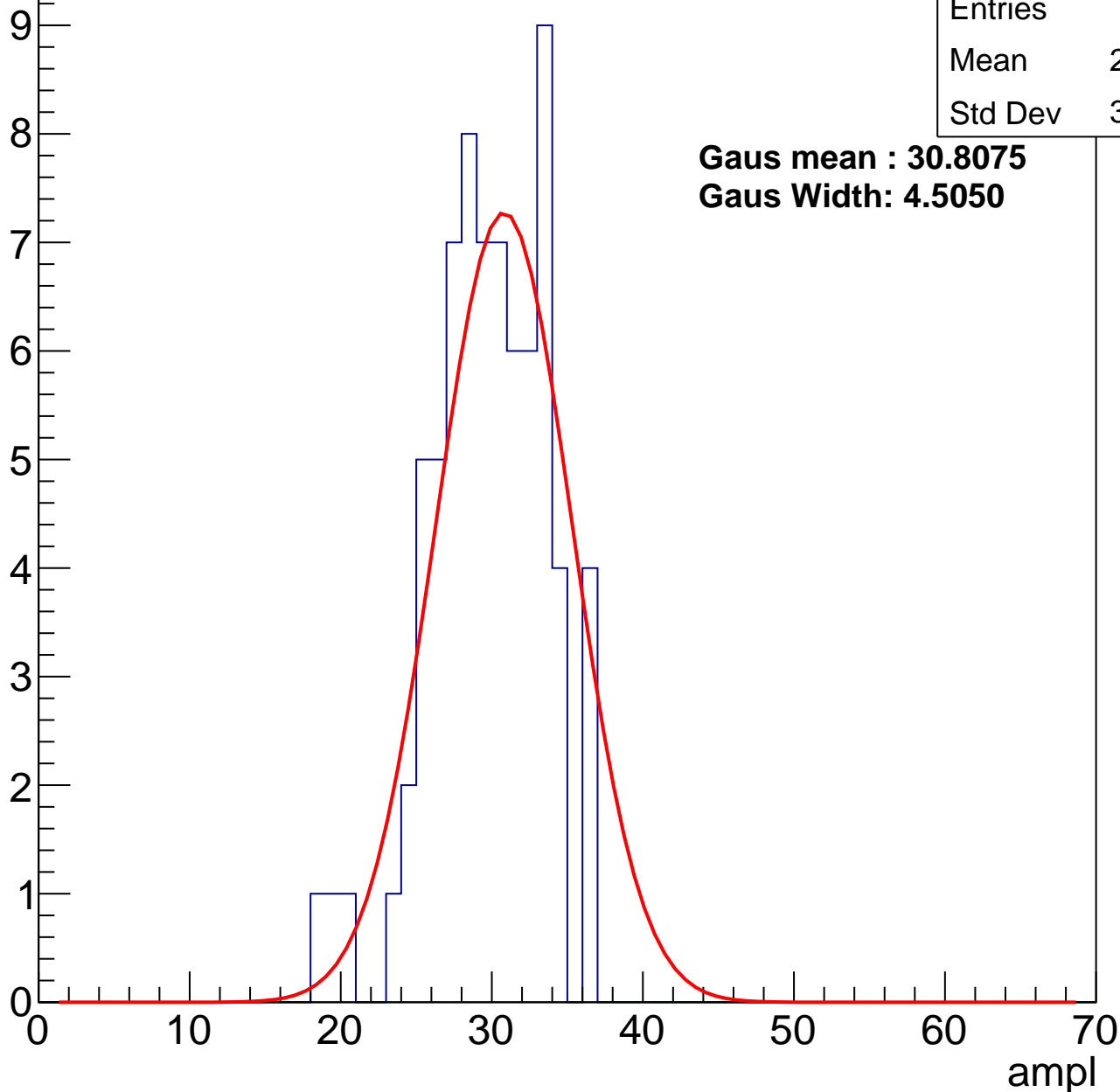
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.24 |
| Std Dev | 3.802 |

Gaus mean : 30.8075

Gaus Width: 4.5050



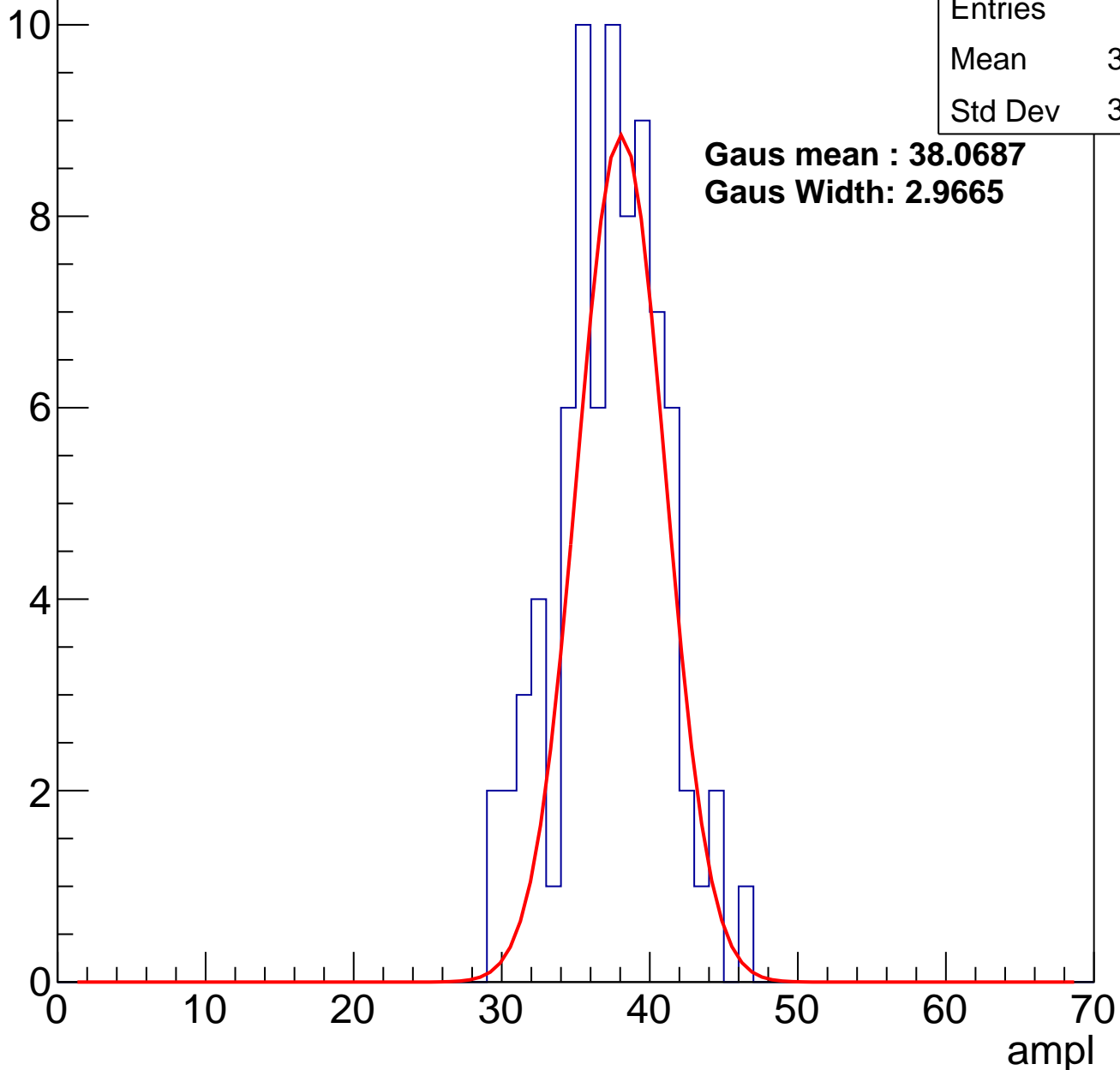
B1L100S, U9-ch109, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.92 |
| Std Dev | 3.566 |

Gaus mean : 38.0687
Gaus Width: 2.9665

Entry



B1L100S, U9-ch109, adc2

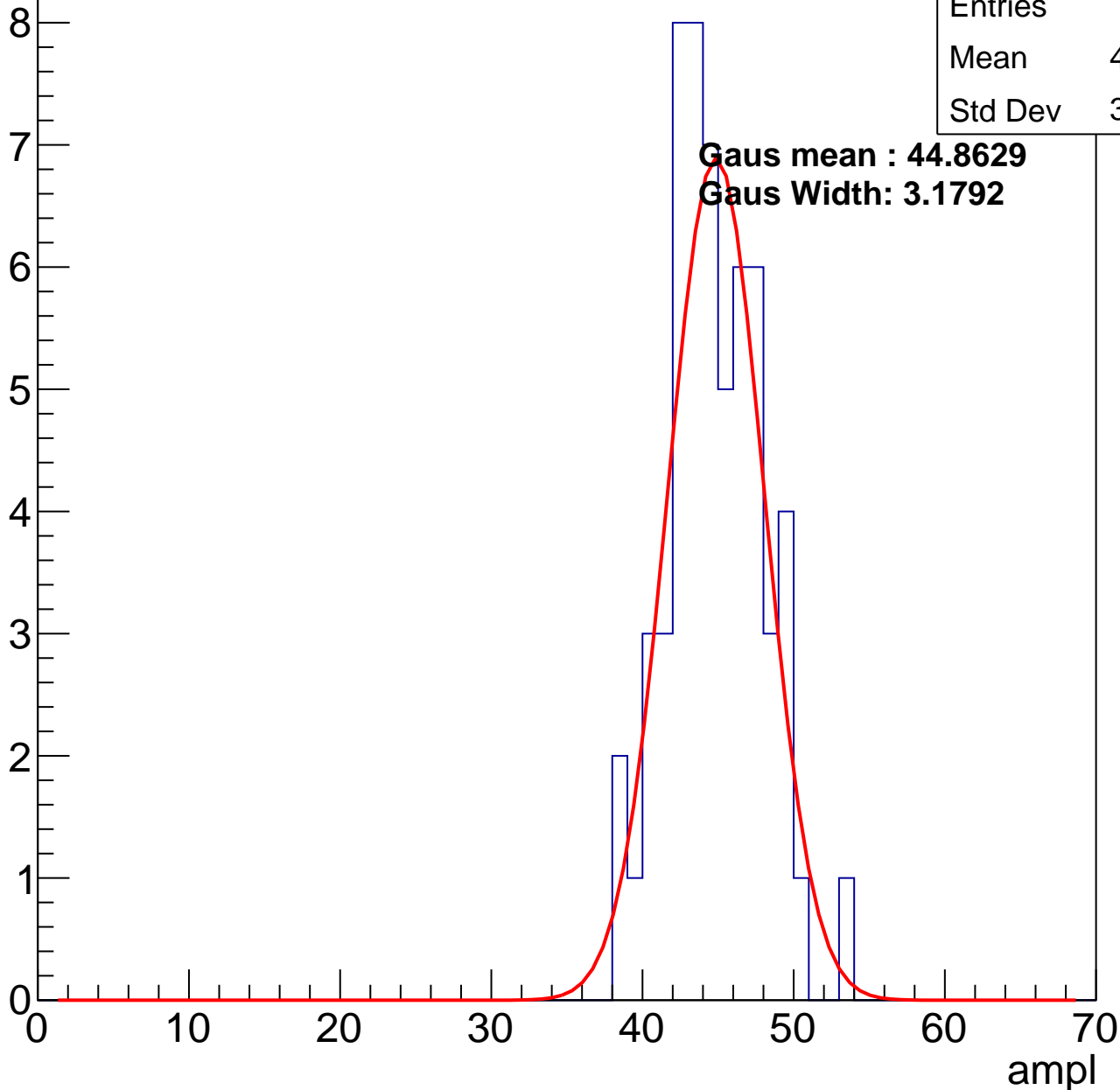
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 44.34 |
| Std Dev | 3.088 |

Gaus mean : 44.8629

Gaus Width: 3.1792

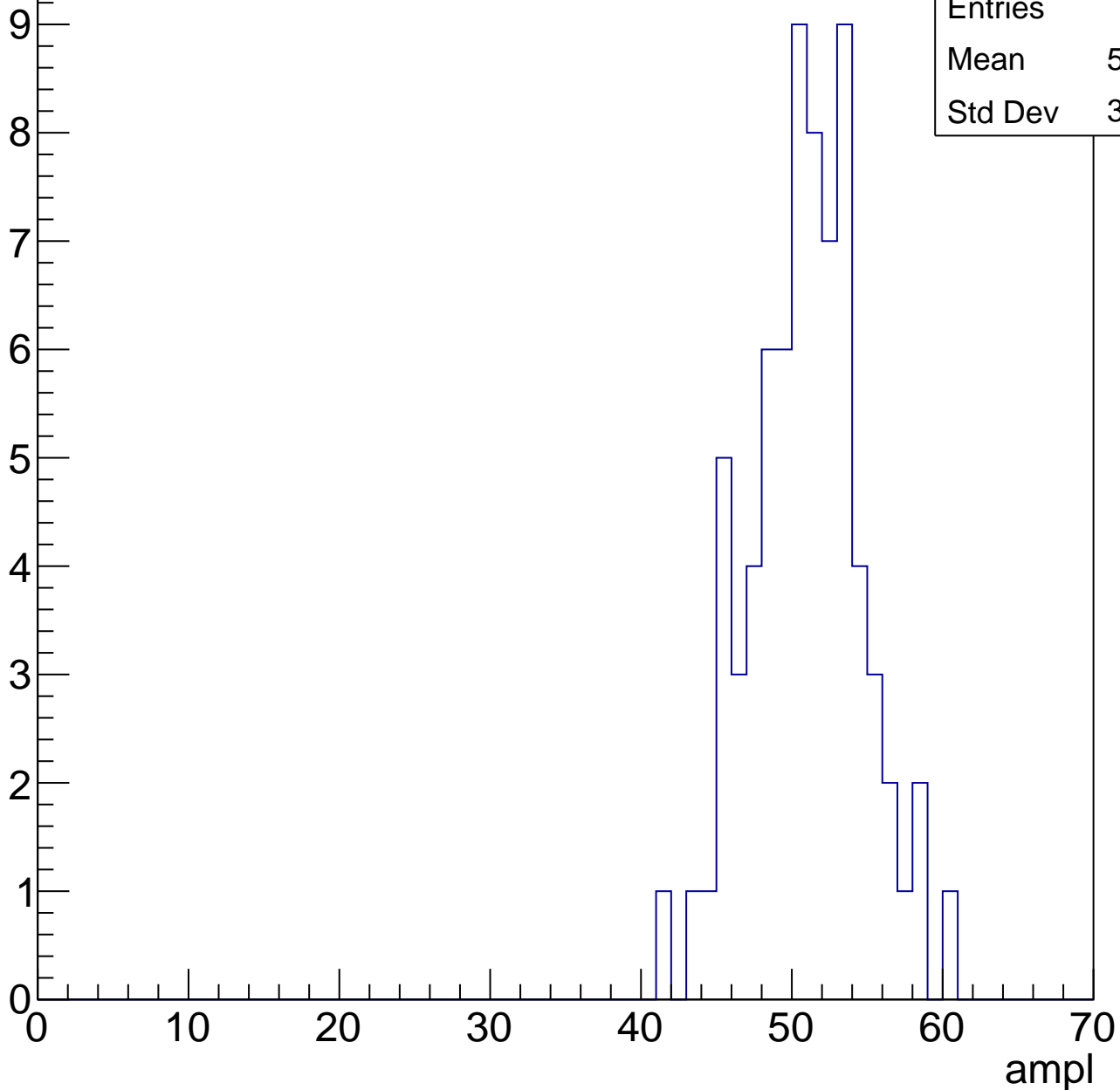


B1L100S, U9-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

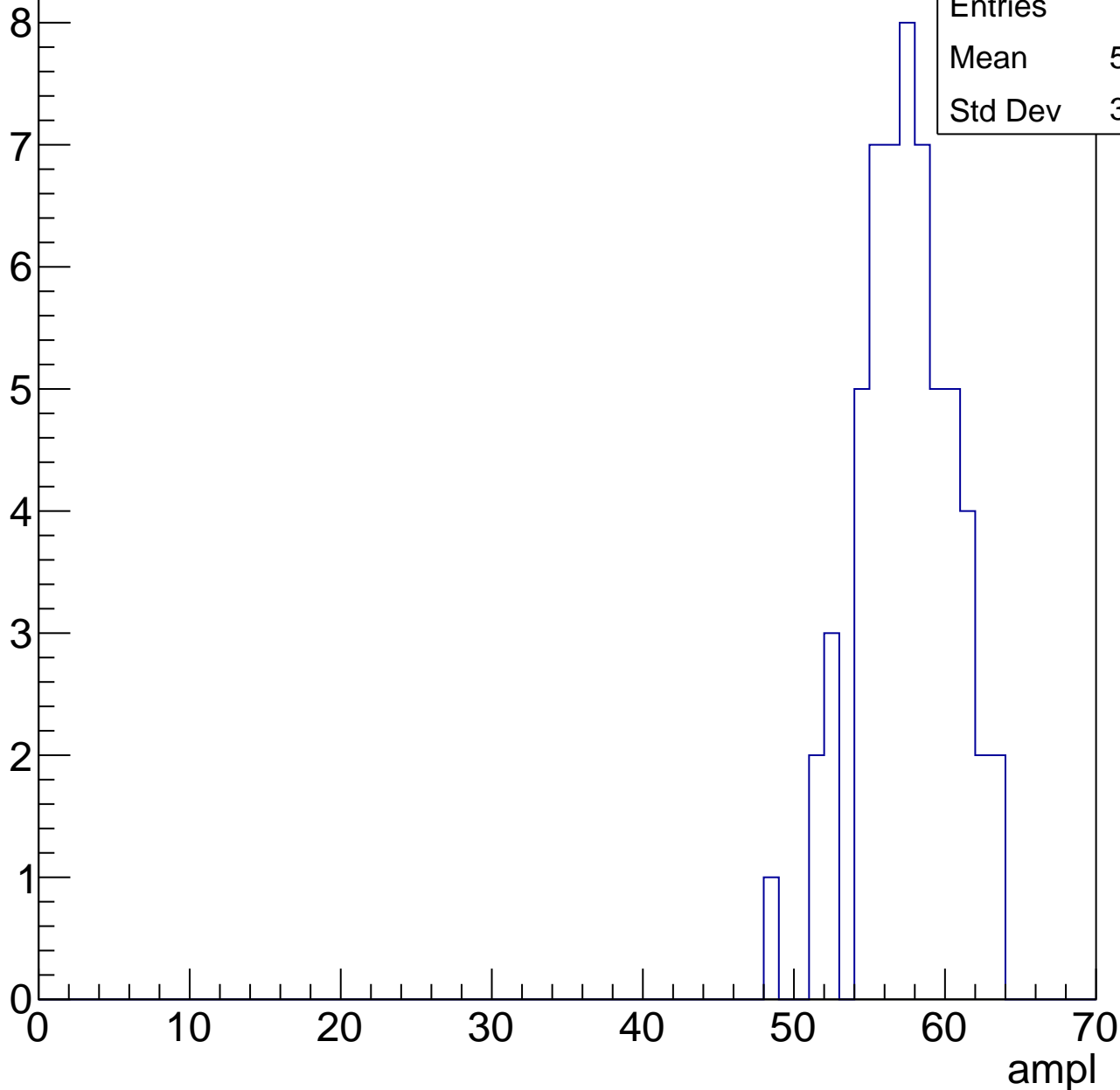
| | |
|---------|-------|
| Entries | 73 |
| Mean | 50.49 |
| Std Dev | 3.687 |



B1L100S, U9-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



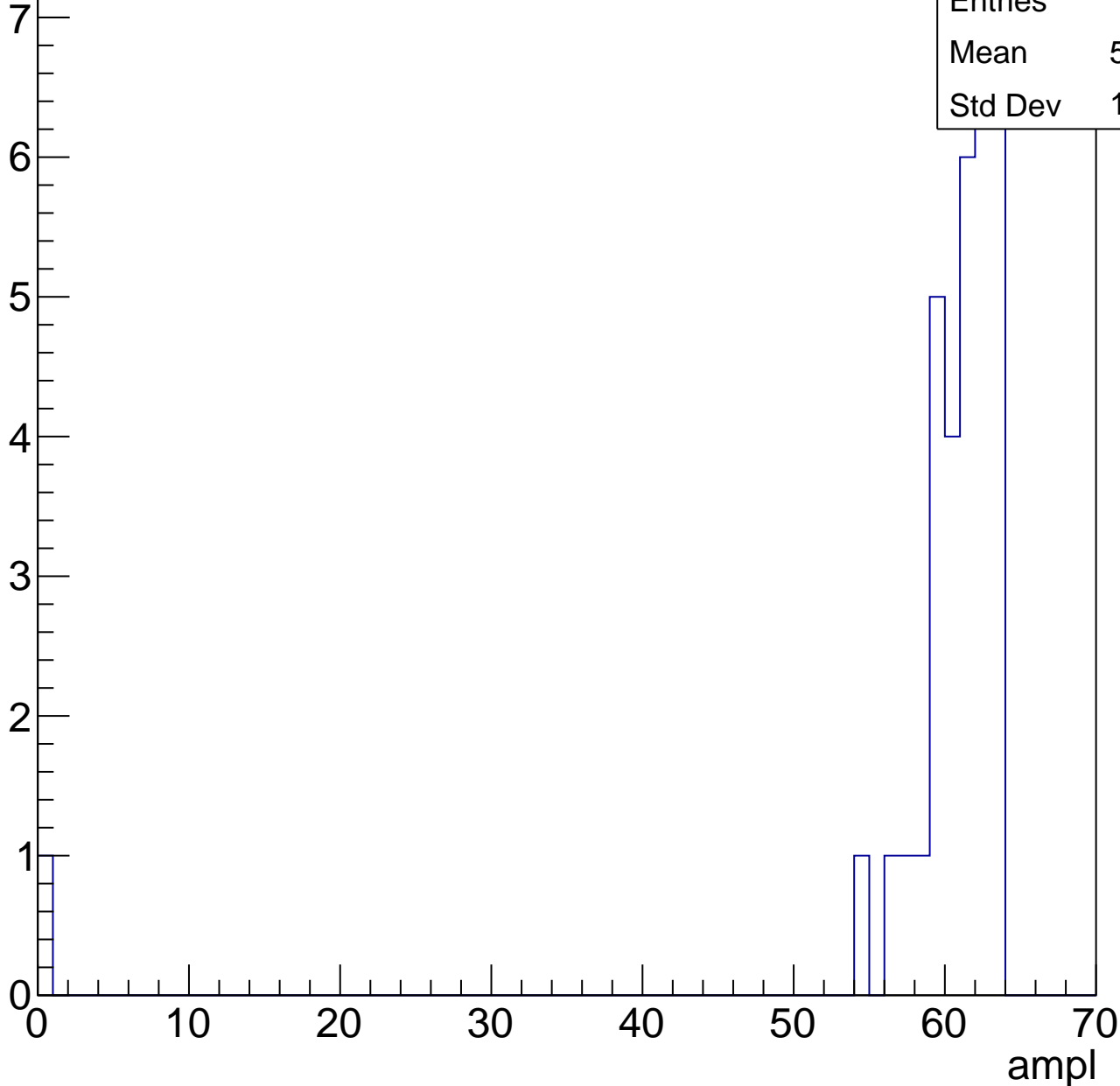
| | |
|---------|-------|
| Entries | 58 |
| Mean | 56.97 |
| Std Dev | 3.124 |

B1L100S, U9-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 58.85 |
| Std Dev | 10.46 |



B1L100S, U9-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch110, adc0

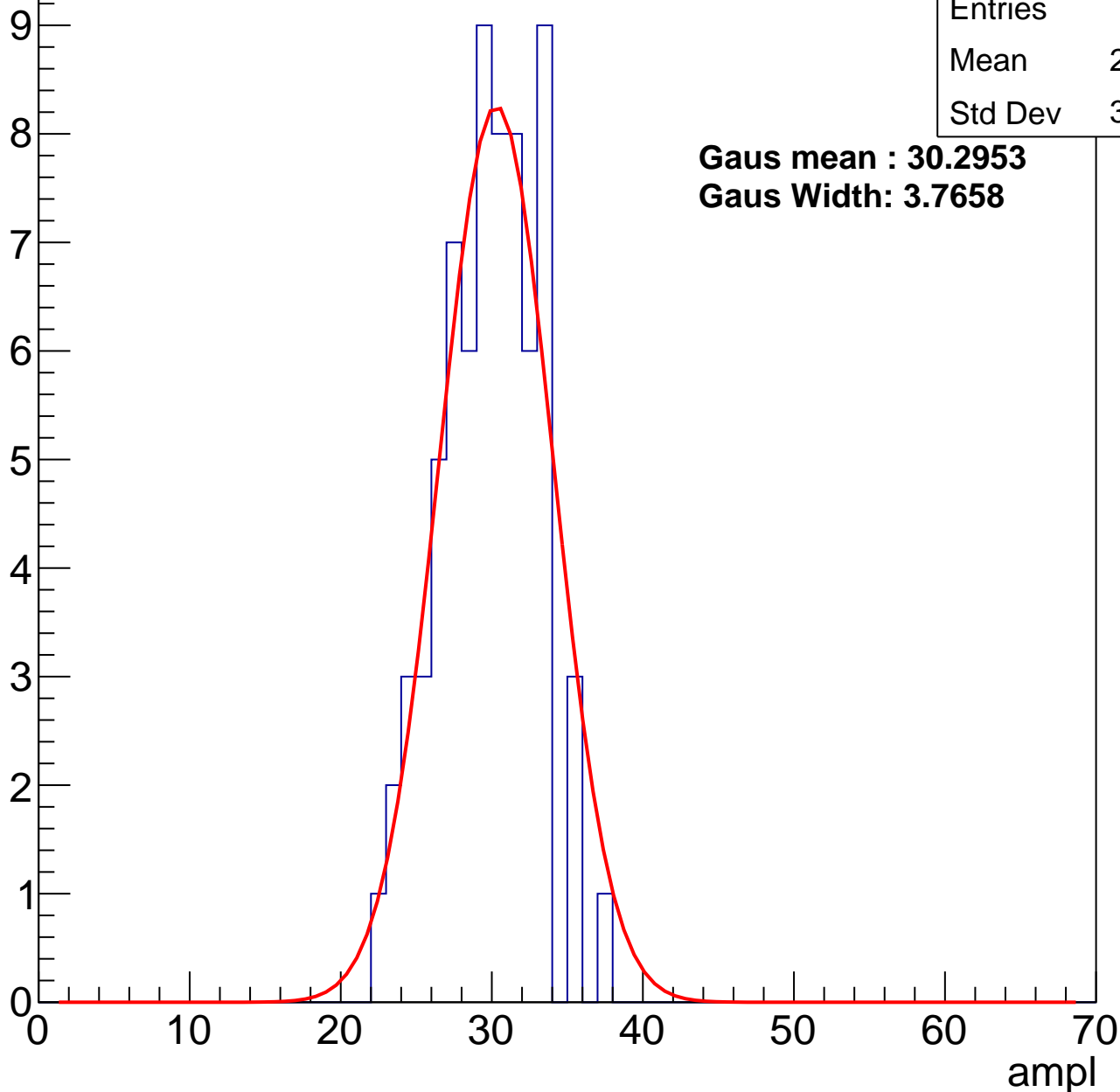
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.32 |
| Std Dev | 3.197 |

Gaus mean : 30.2953

Gaus Width: 3.7658



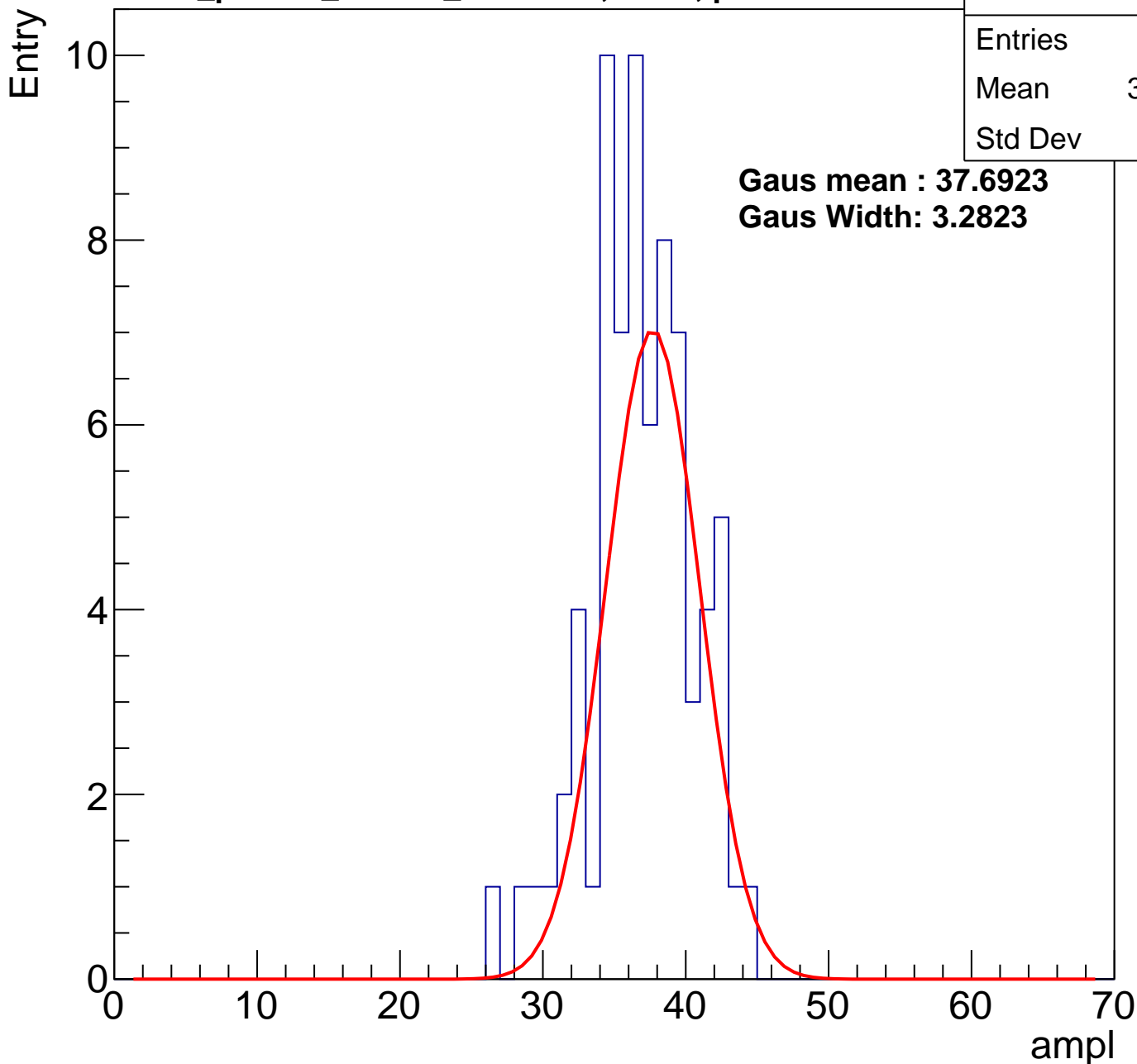
B1L100S, U9-ch110, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 36.45 |
| Std Dev | 3.6 |

Gaus mean : 37.6923

Gaus Width: 3.2823



B1L100S, U9-ch110, adc2

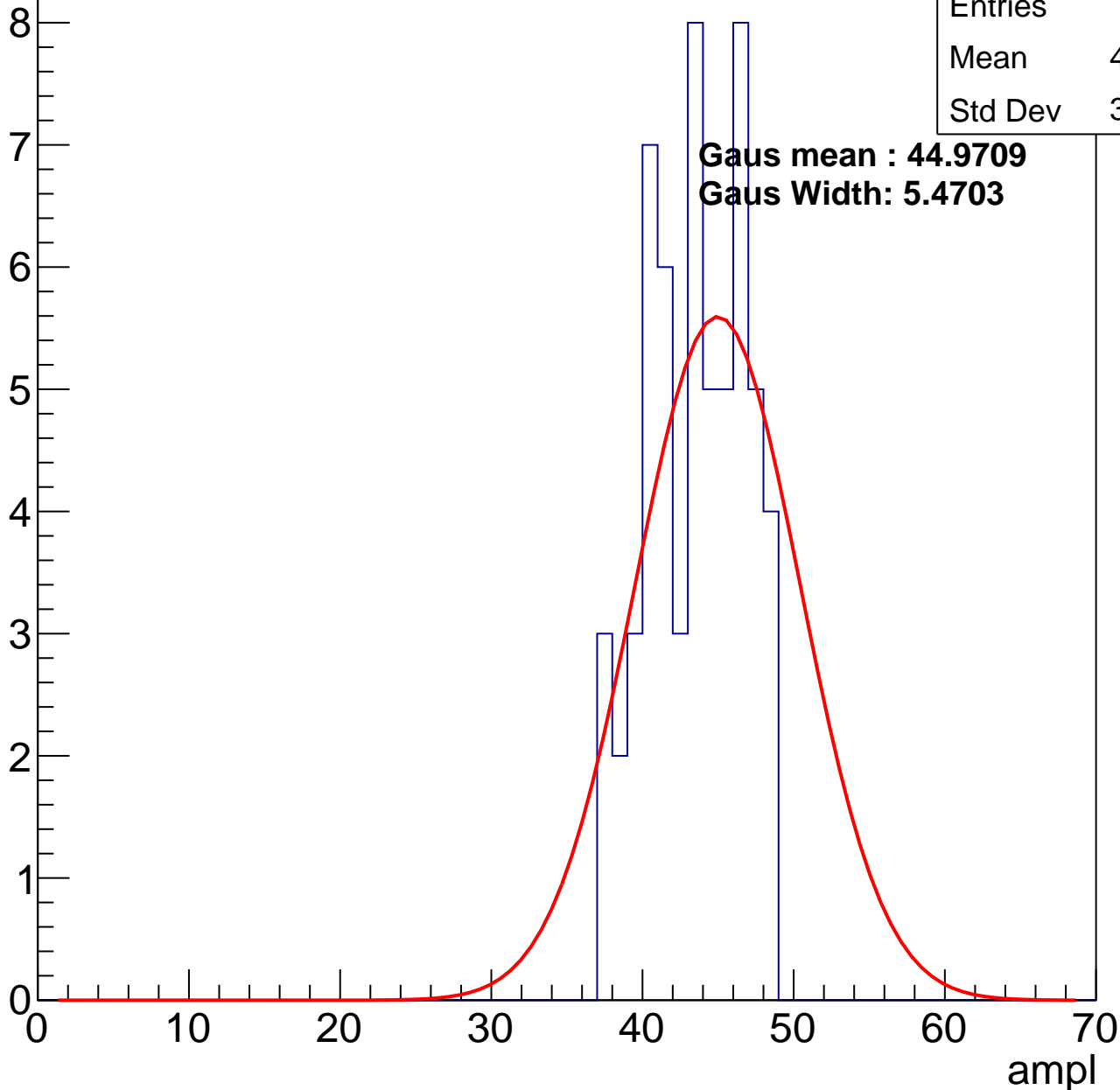
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 43.05 |
| Std Dev | 3.116 |

Gaus mean : 44.9709

Gaus Width: 5.4703



B1L100S, U9-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

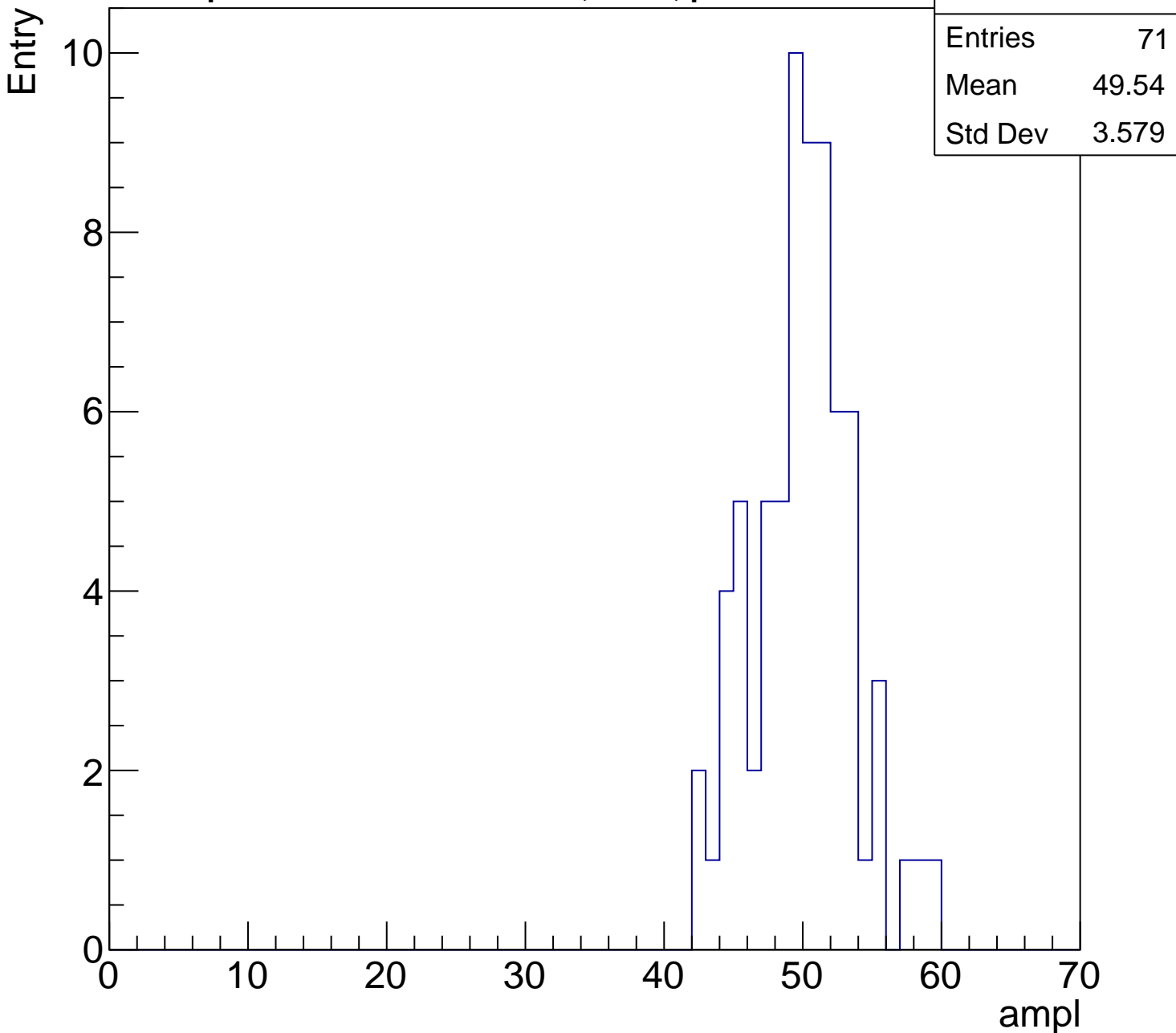
| | |
|---------|-------|
| Entries | 71 |
| Mean | 49.54 |
| Std Dev | 3.579 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

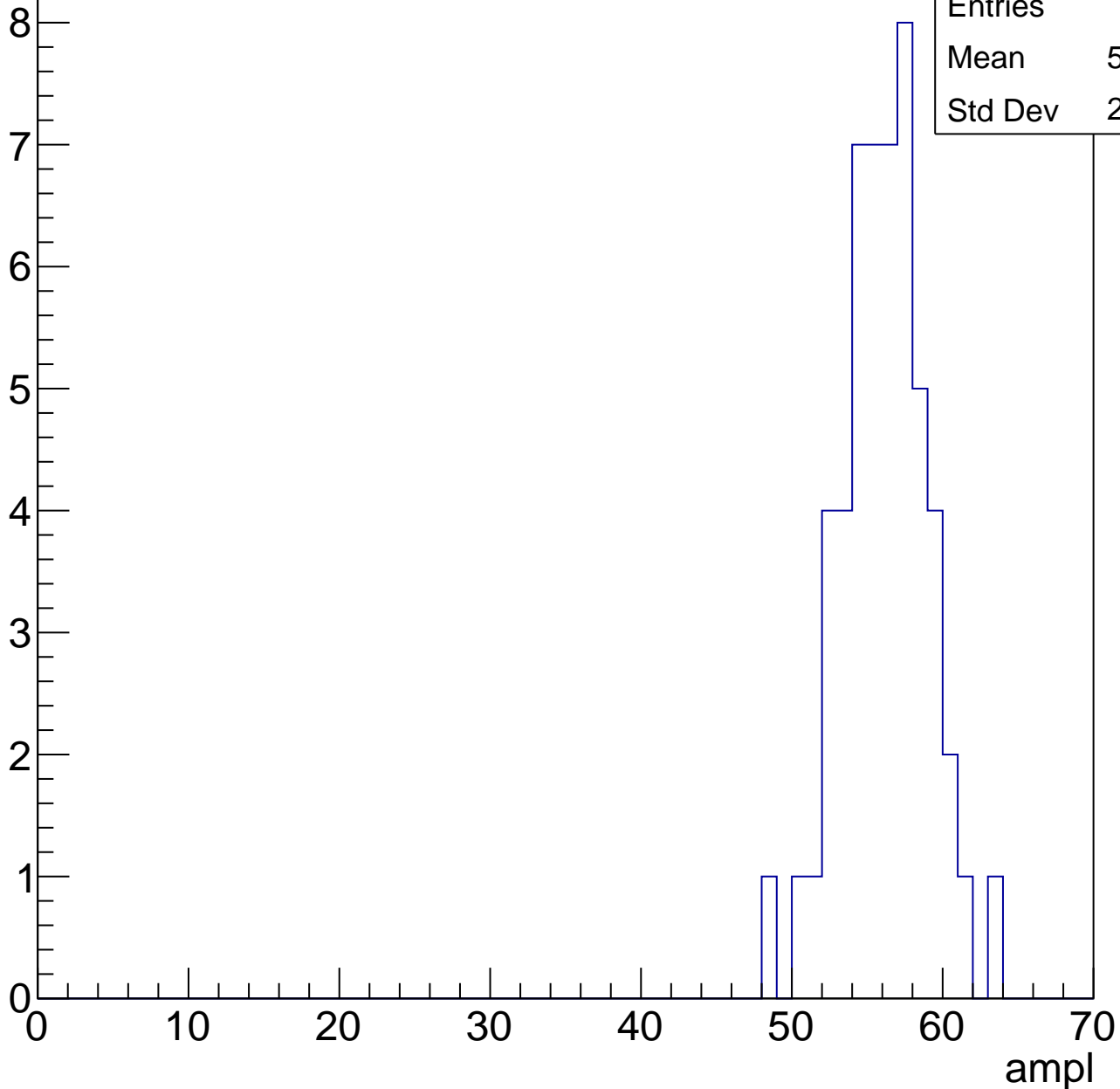


B1L100S, U9-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.66 |
| Std Dev | 2.828 |

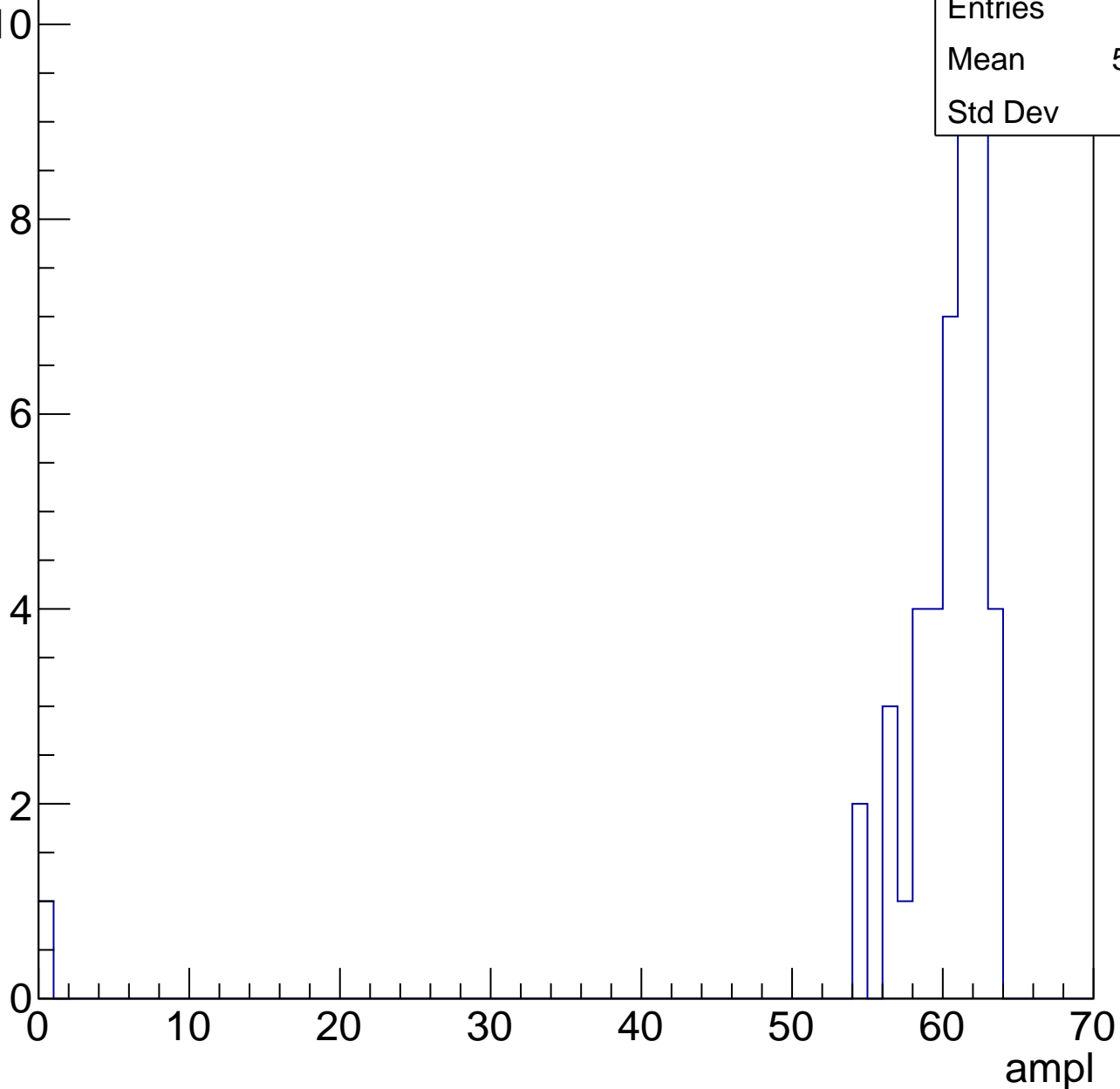


B1L100S, U9-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.71 |
| Std Dev | 9.14 |

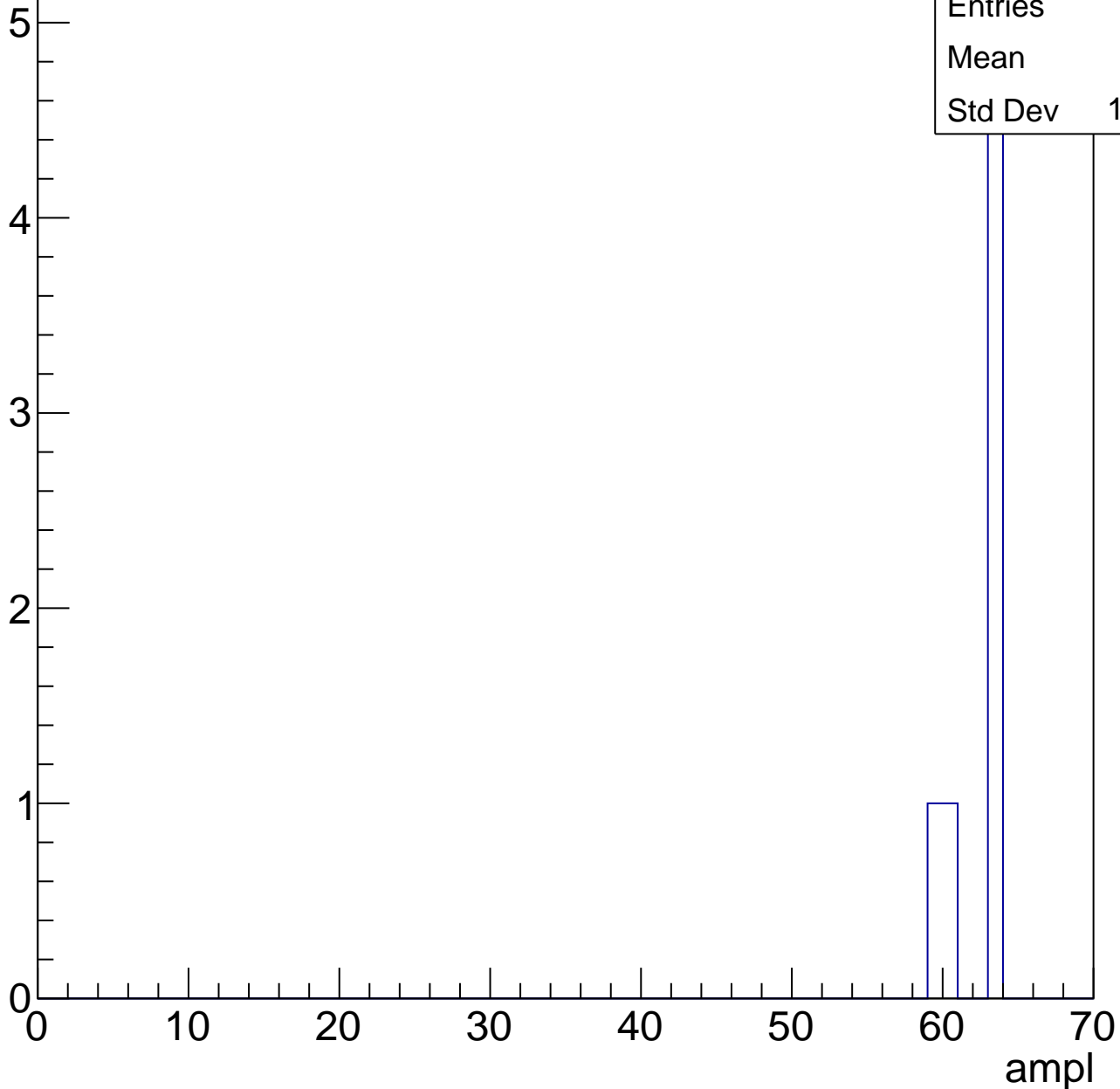


B1L100S, U9-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 7 |
| Mean | 62 |
| Std Dev | 1.604 |



B1L100S, U9-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch111, adc0

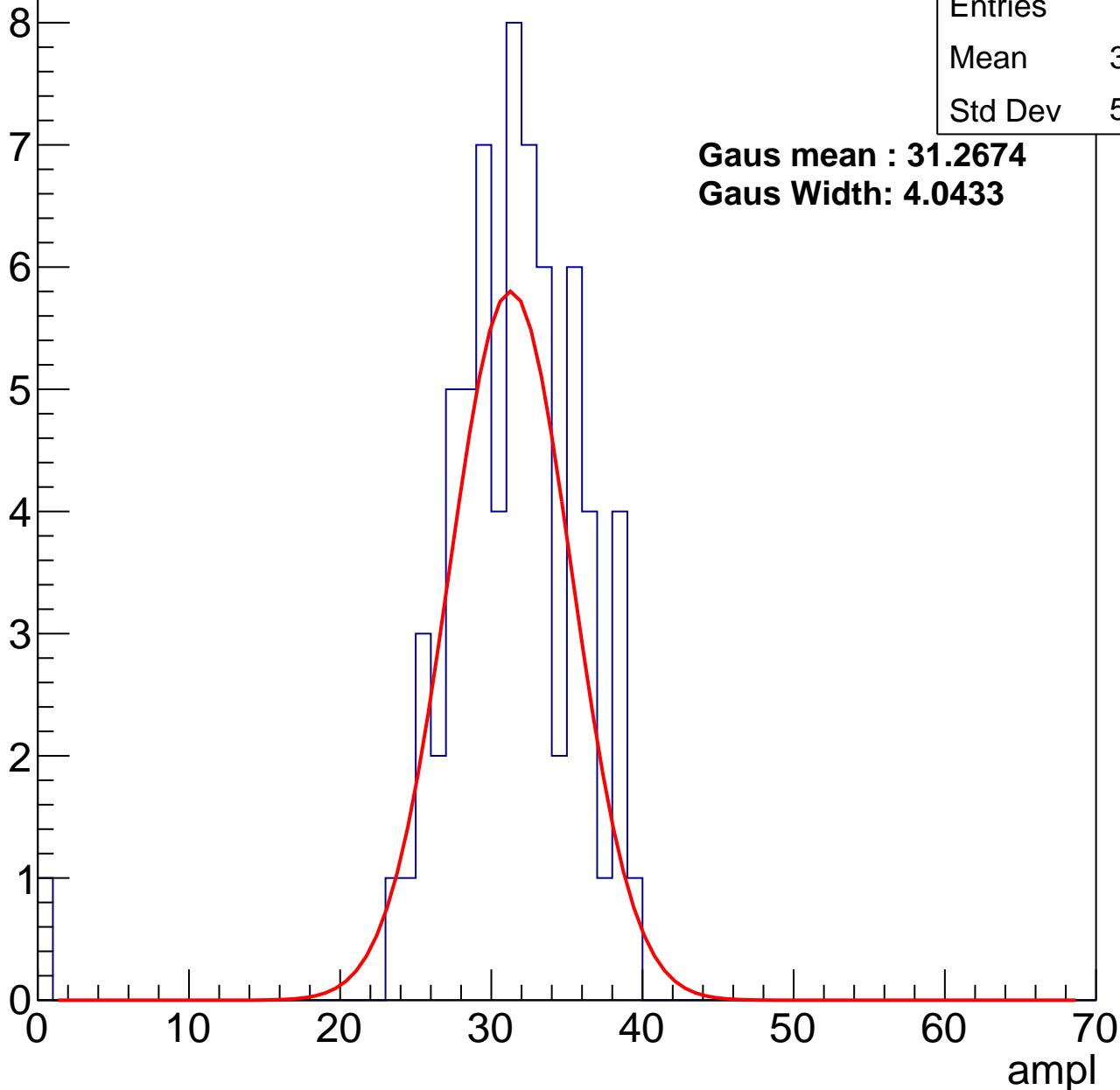
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 30.76 |
| Std Dev | 5.322 |

Gaus mean : 31.2674

Gaus Width: 4.0433



B1L100S, U9-ch111, adc1

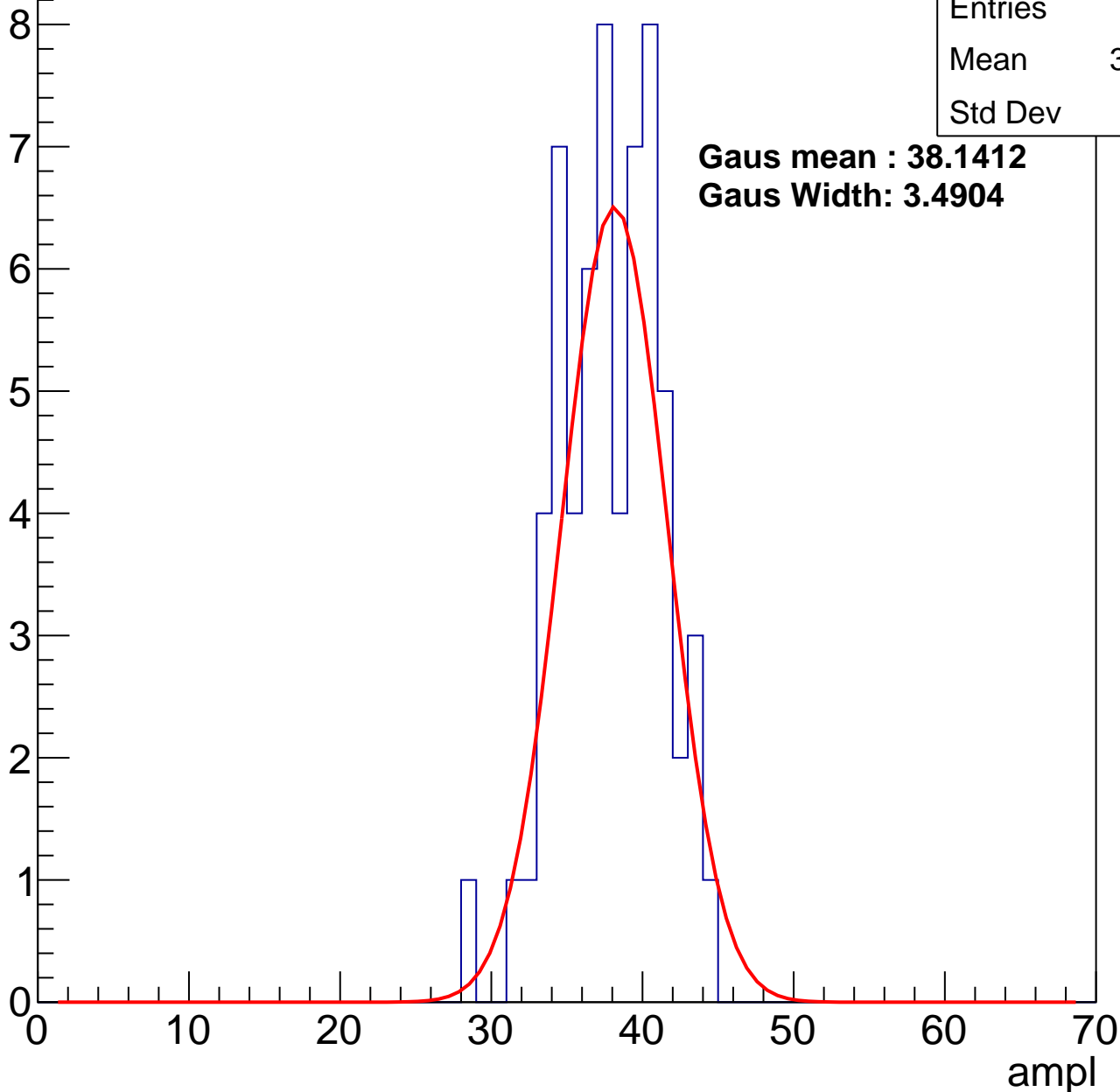
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 37.42 |
| Std Dev | 3.29 |

Gaus mean : 38.1412

Gaus Width: 3.4904



B1L100S, U9-ch111, adc2

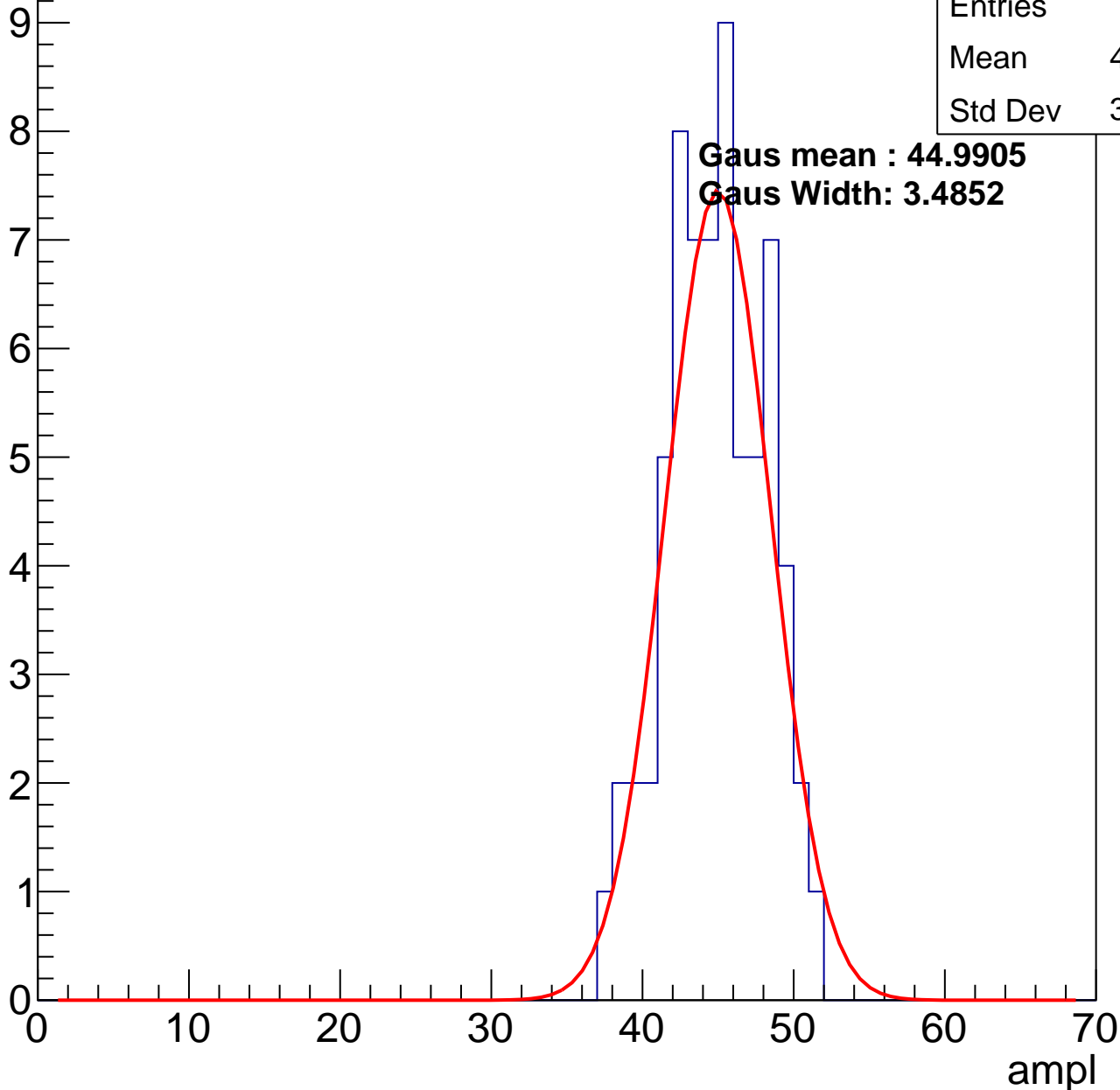
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.39 |
| Std Dev | 3.195 |

Gaus mean : 44.9905

Gaus Width: 3.4852



B1L100S, U9-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

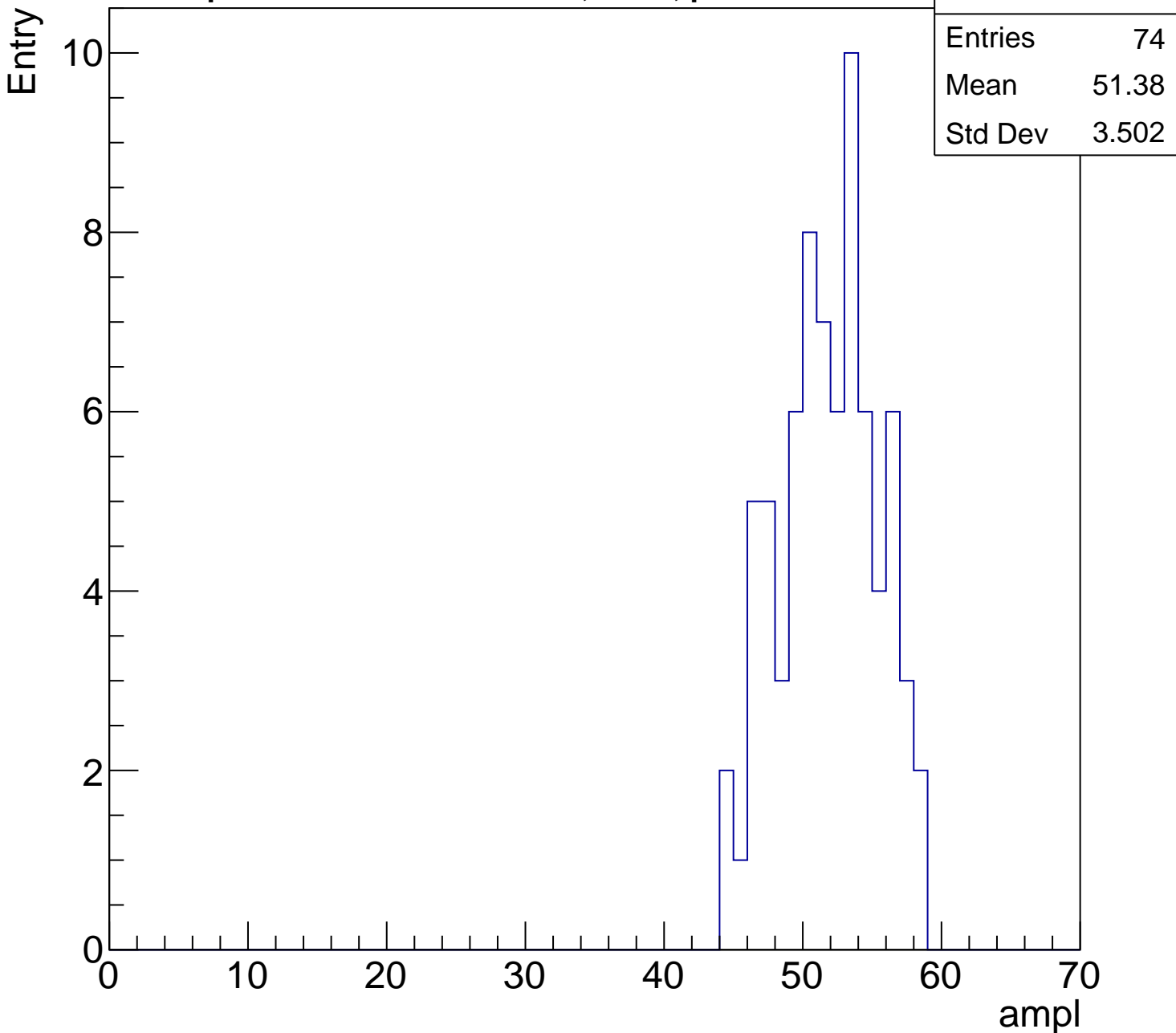
| | |
|---------|-------|
| Entries | 74 |
| Mean | 51.38 |
| Std Dev | 3.502 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

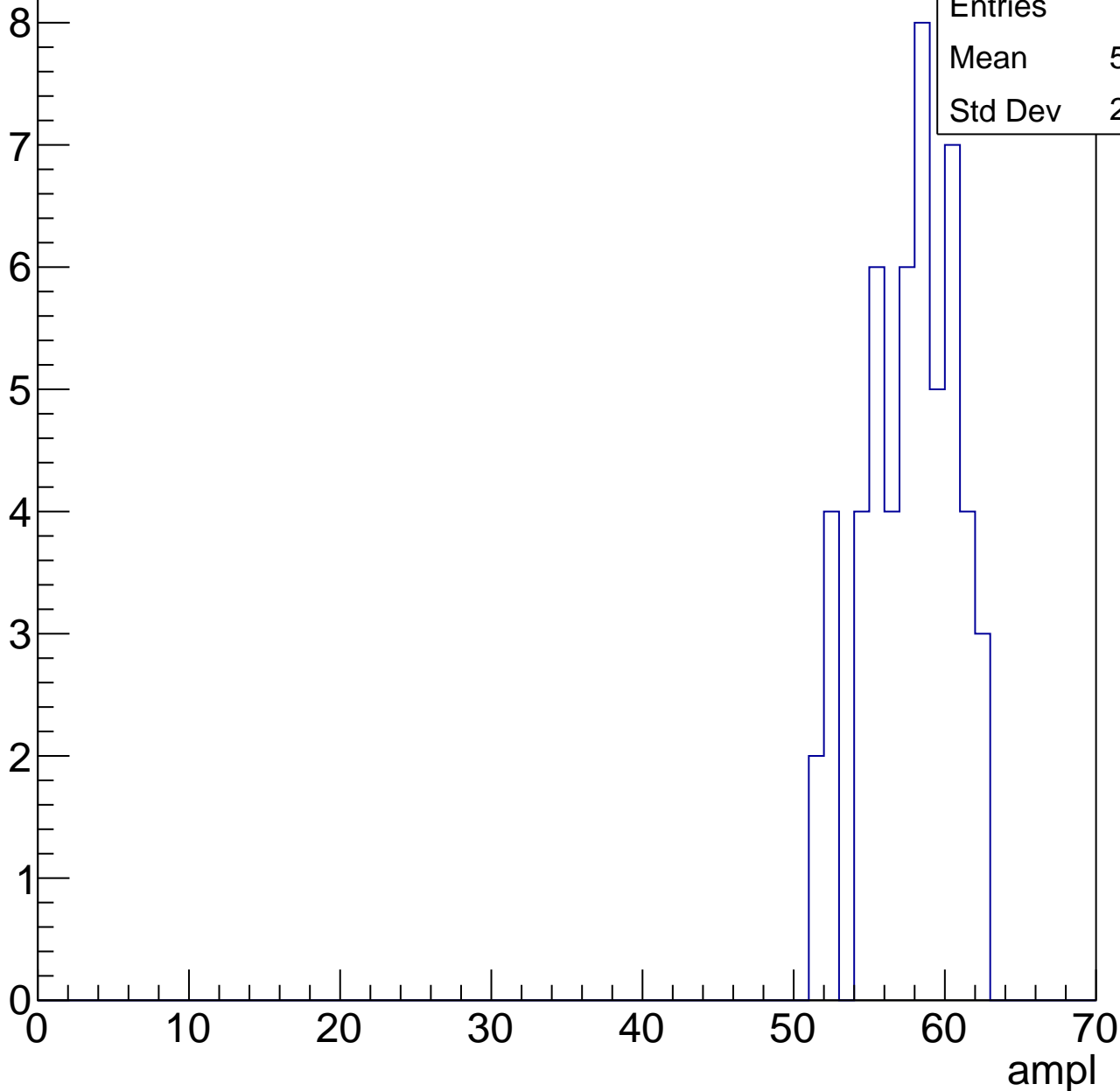


B1L100S, U9-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.19 |
| Std Dev | 2.959 |

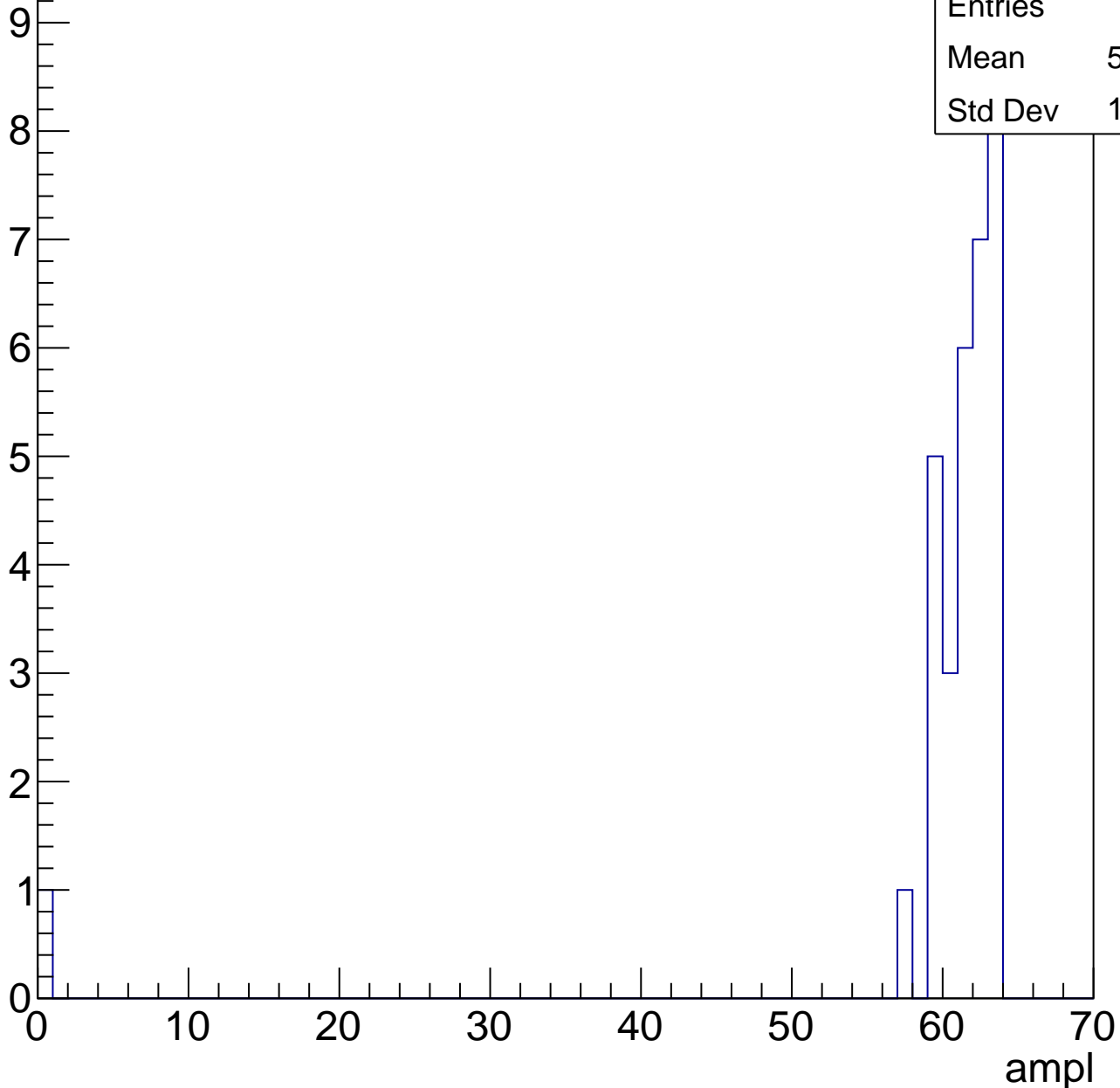


B1L100S, U9-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 59.34 |
| Std Dev | 10.77 |



B1L100S, U9-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B1L100S, U9-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch112, adc0

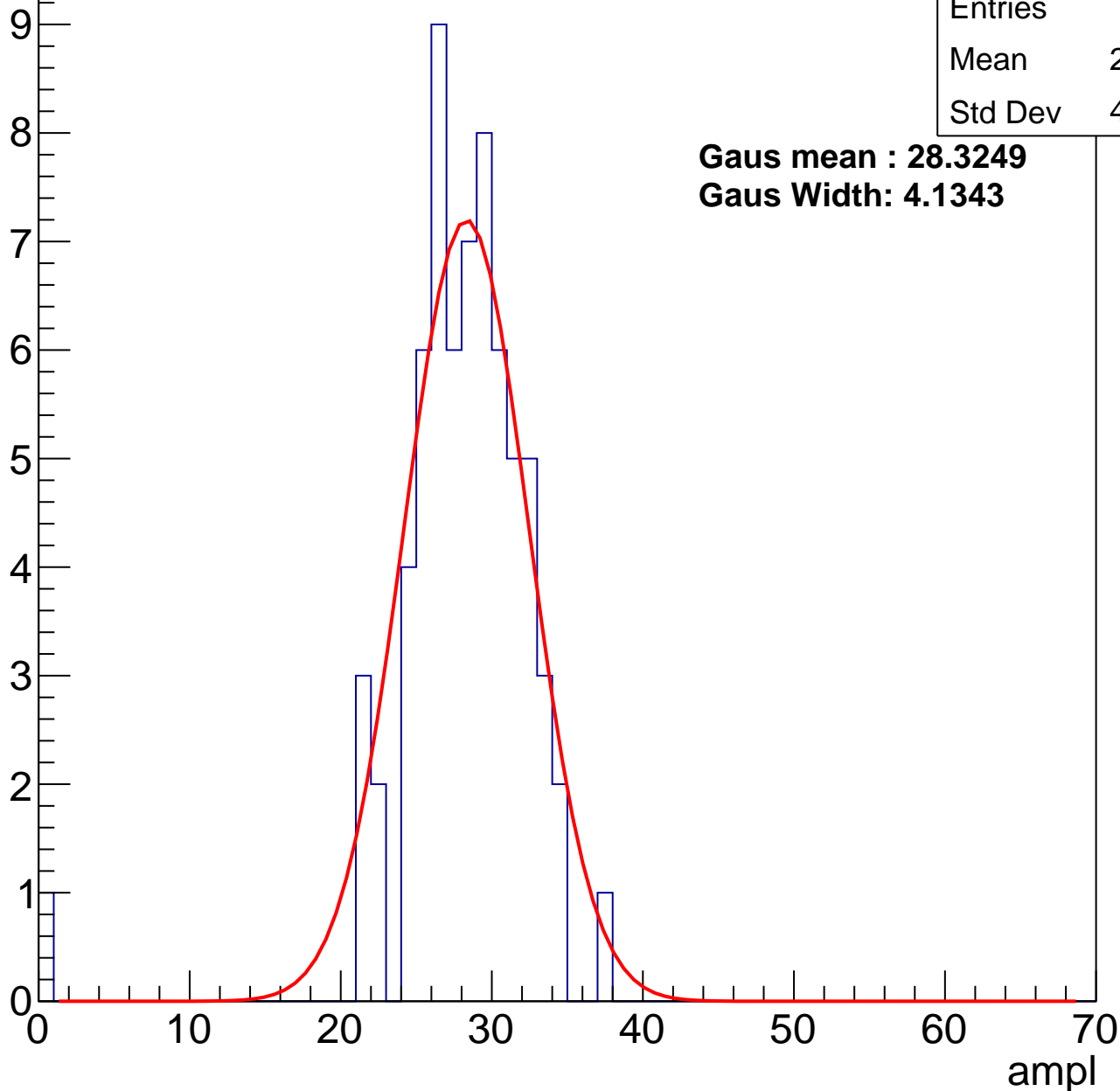
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 27.59 |
| Std Dev | 4.753 |

Gaus mean : 28.3249

Gaus Width: 4.1343



B1L100S, U9-ch112, adc1

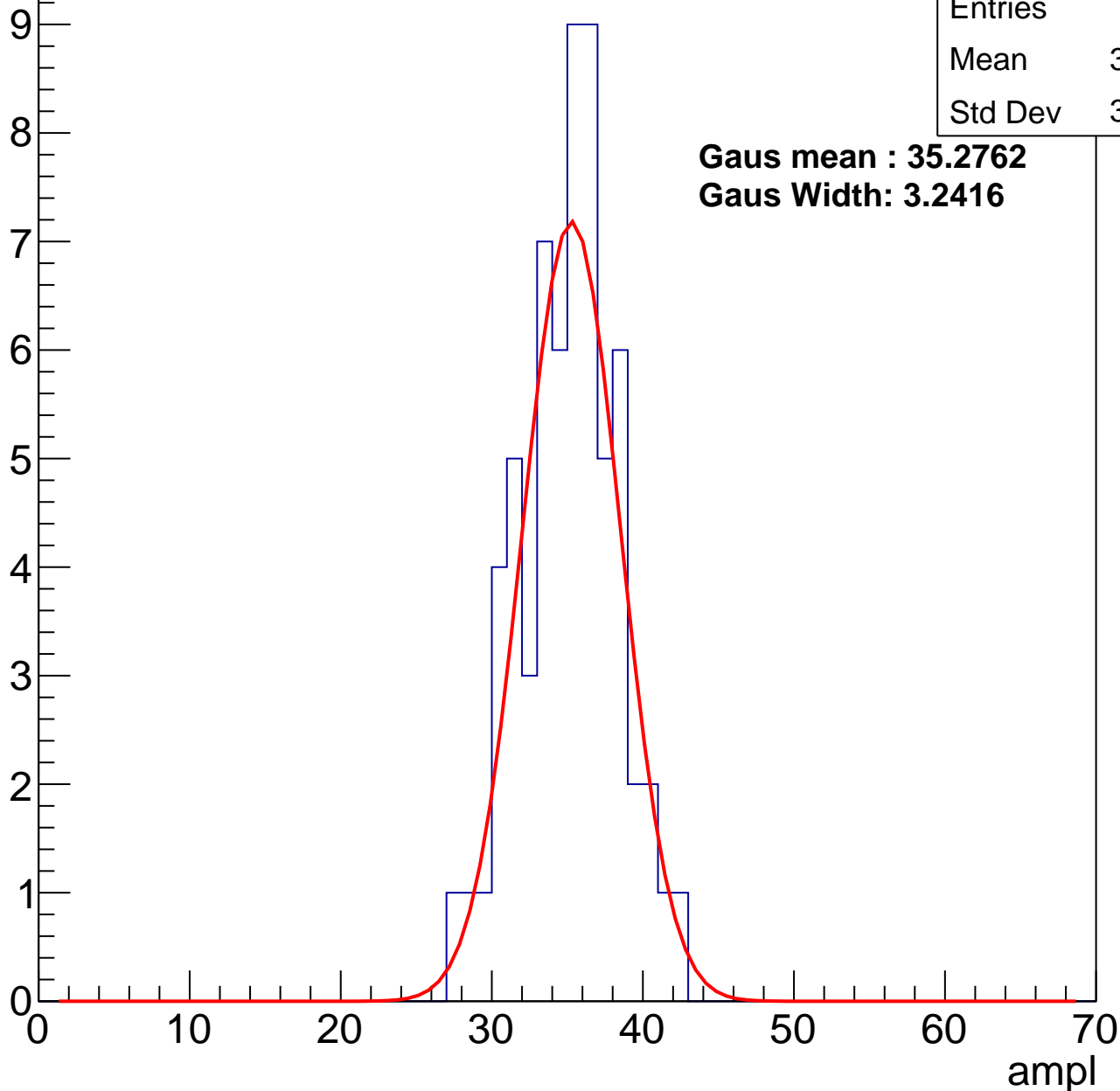
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 34.65 |
| Std Dev | 3.168 |

Gaus mean : 35.2762

Gaus Width: 3.2416



B1L100S, U9-ch112, adc2

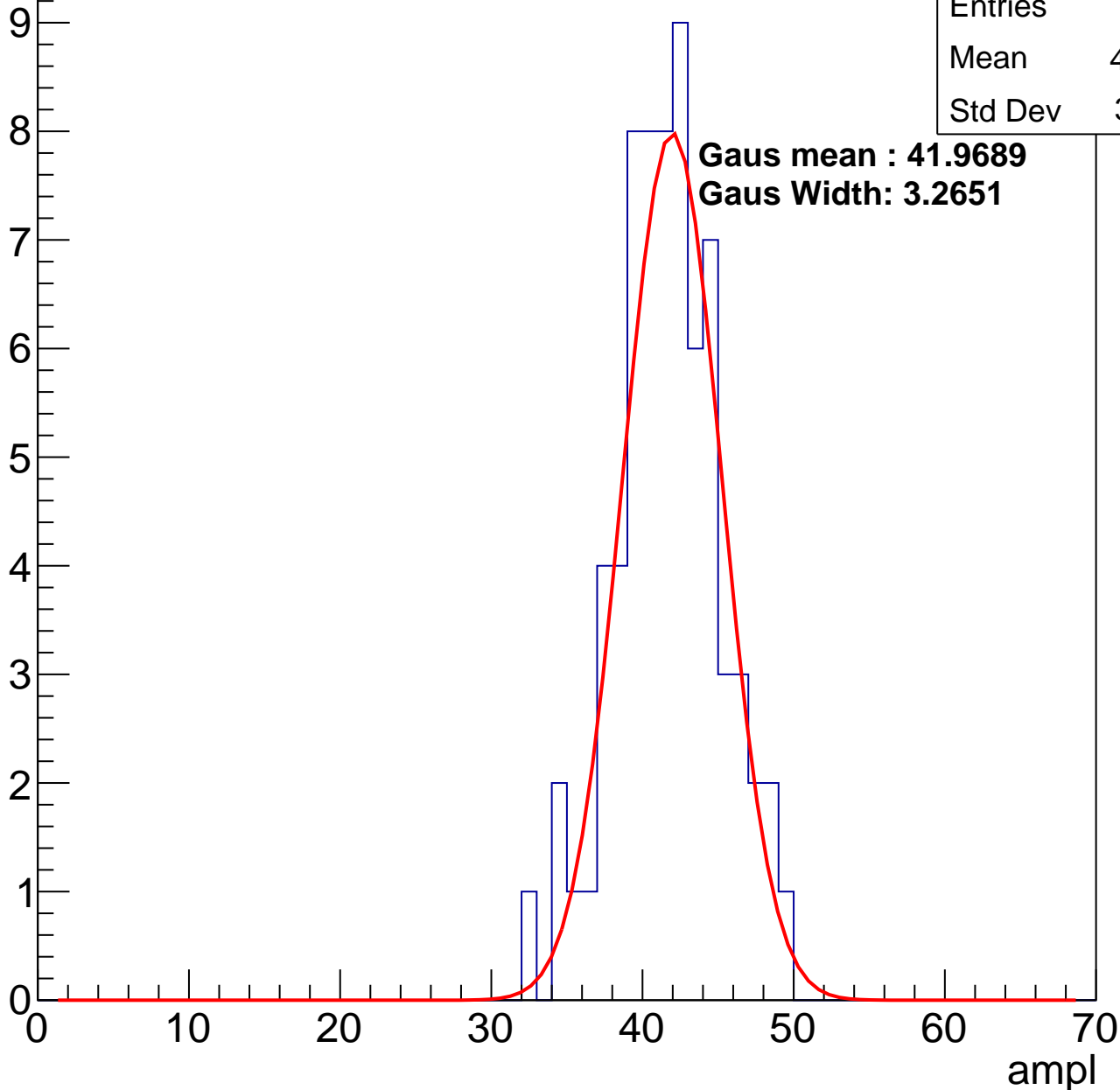
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 41.24 |
| Std Dev | 3.441 |

Gaus mean : 41.9689

Gaus Width: 3.2651

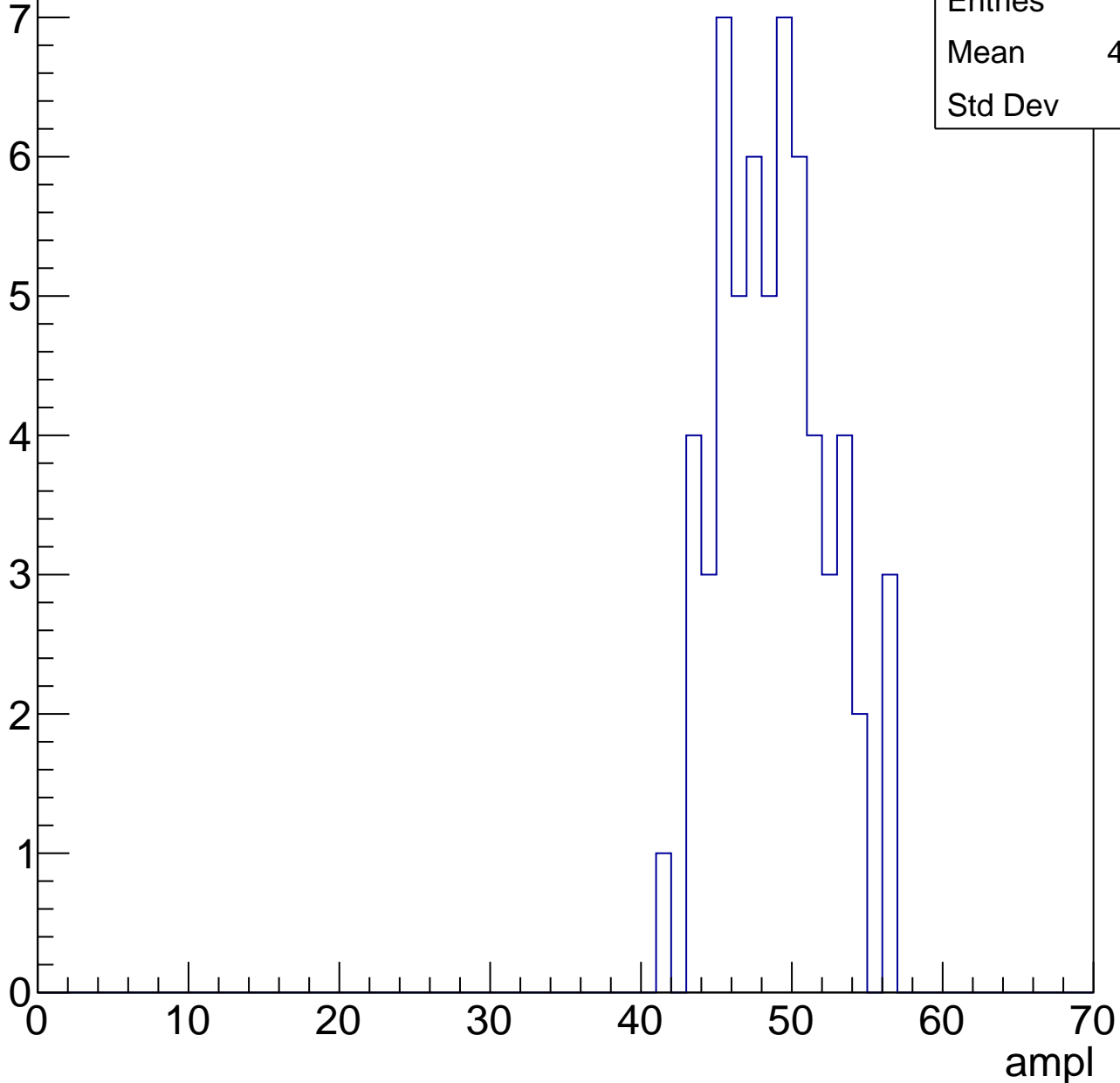


B1L100S, U9-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.38 |
| Std Dev | 3.55 |



B1L100S, U9-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

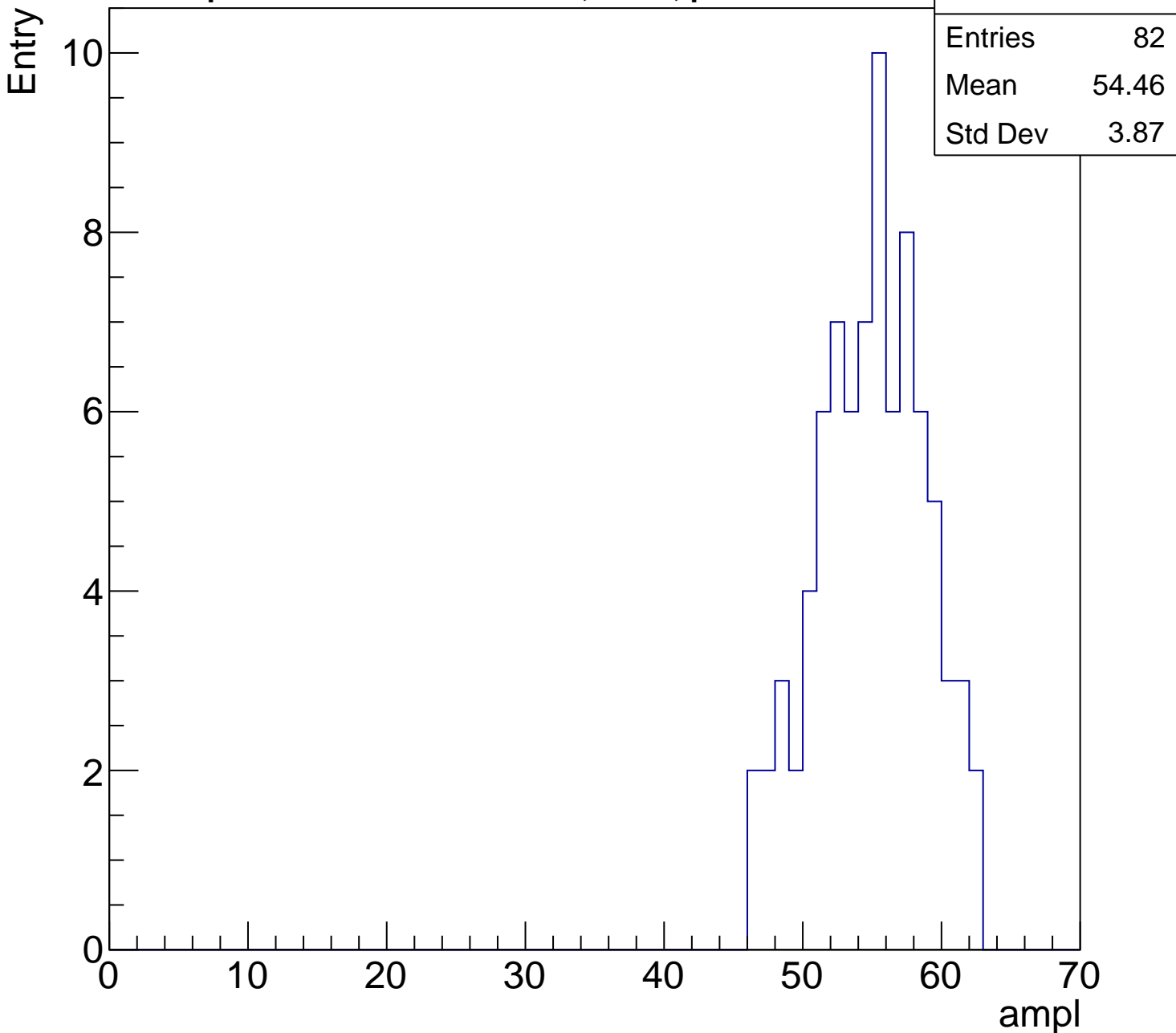
| | |
|---------|-------|
| Entries | 82 |
| Mean | 54.46 |
| Std Dev | 3.87 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

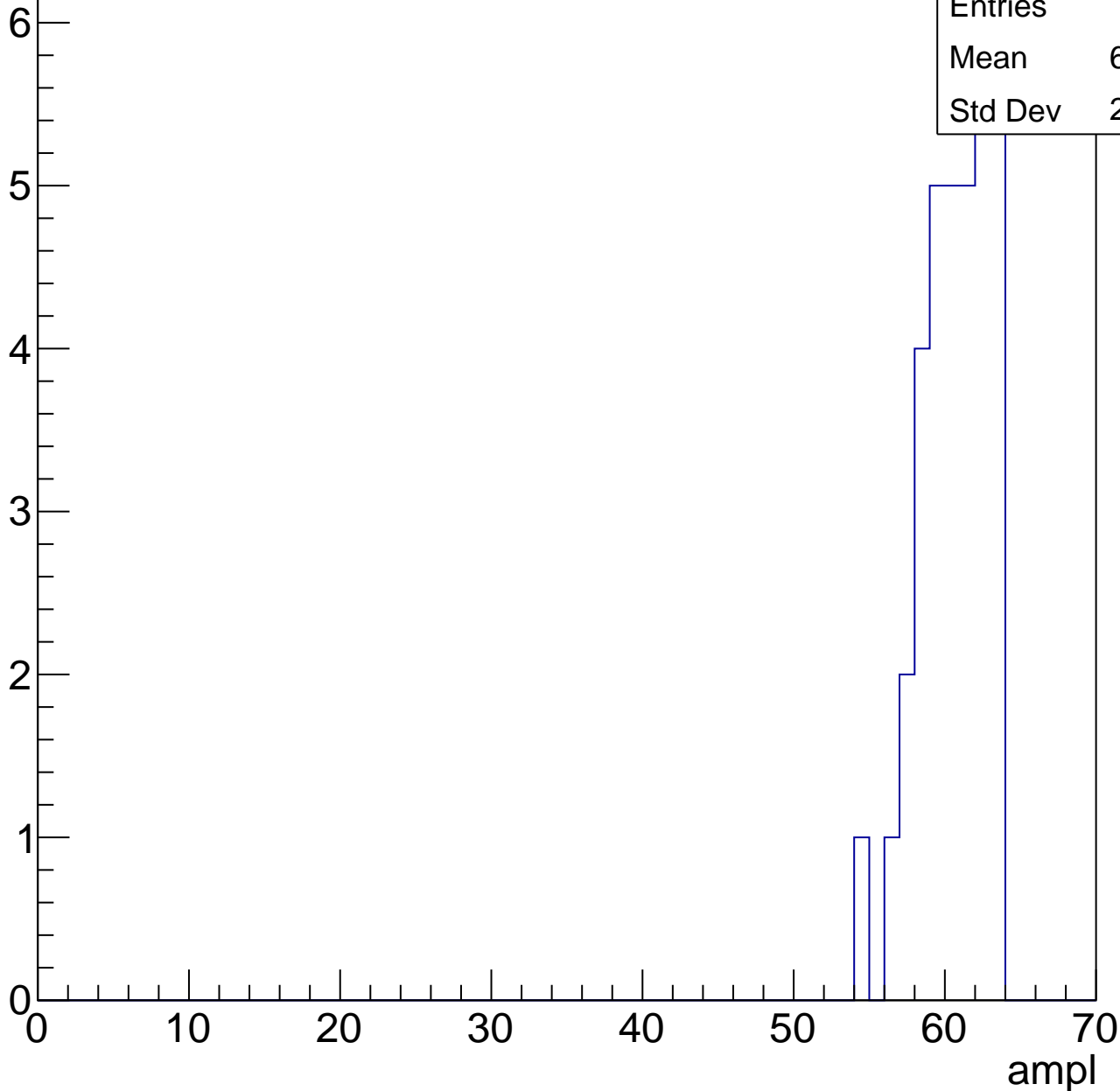


B1L100S, U9-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.17 |
| Std Dev | 2.223 |



B1L100S, U9-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54 |
| Std Dev | 20.43 |

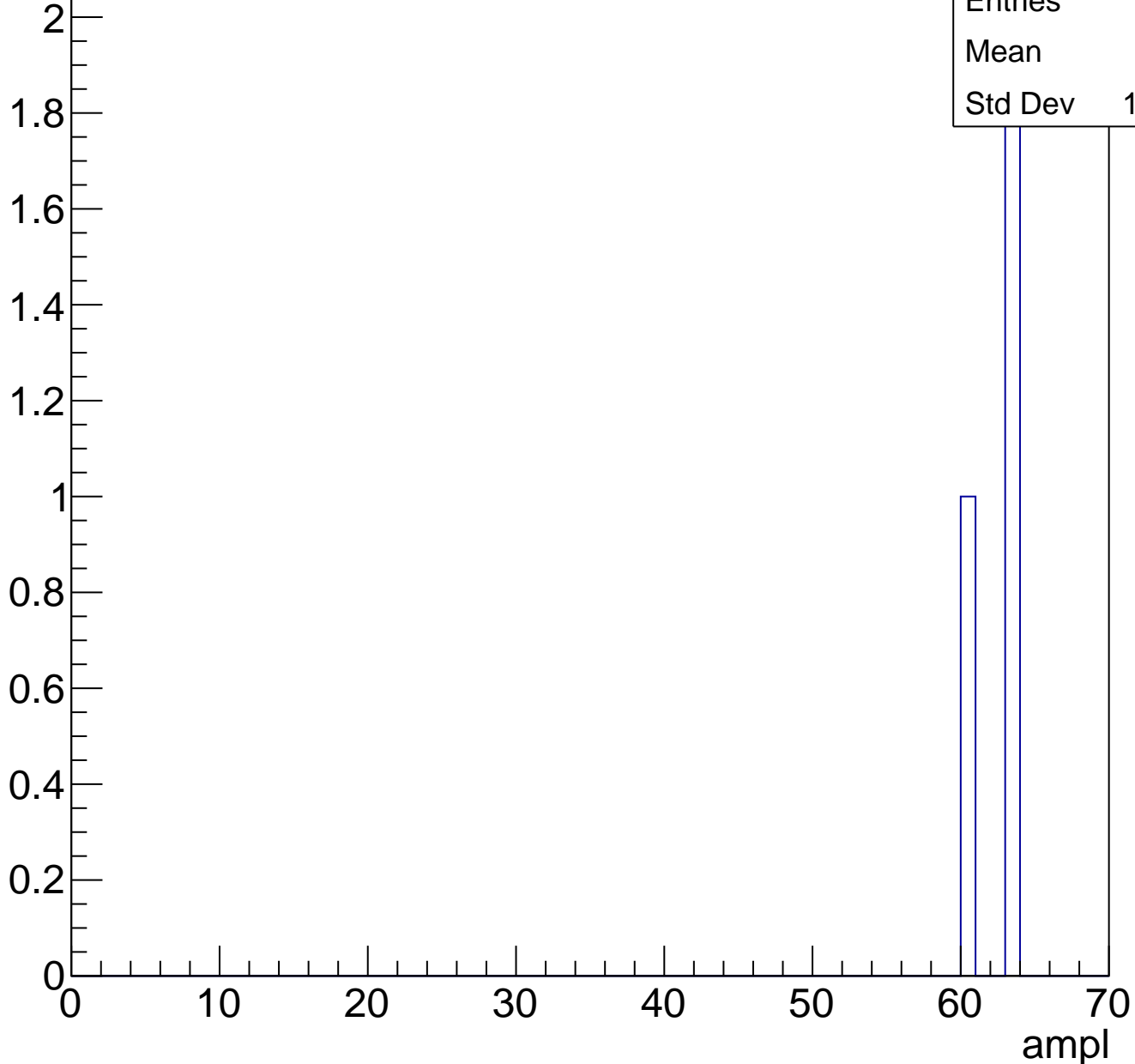
ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch113, adc0

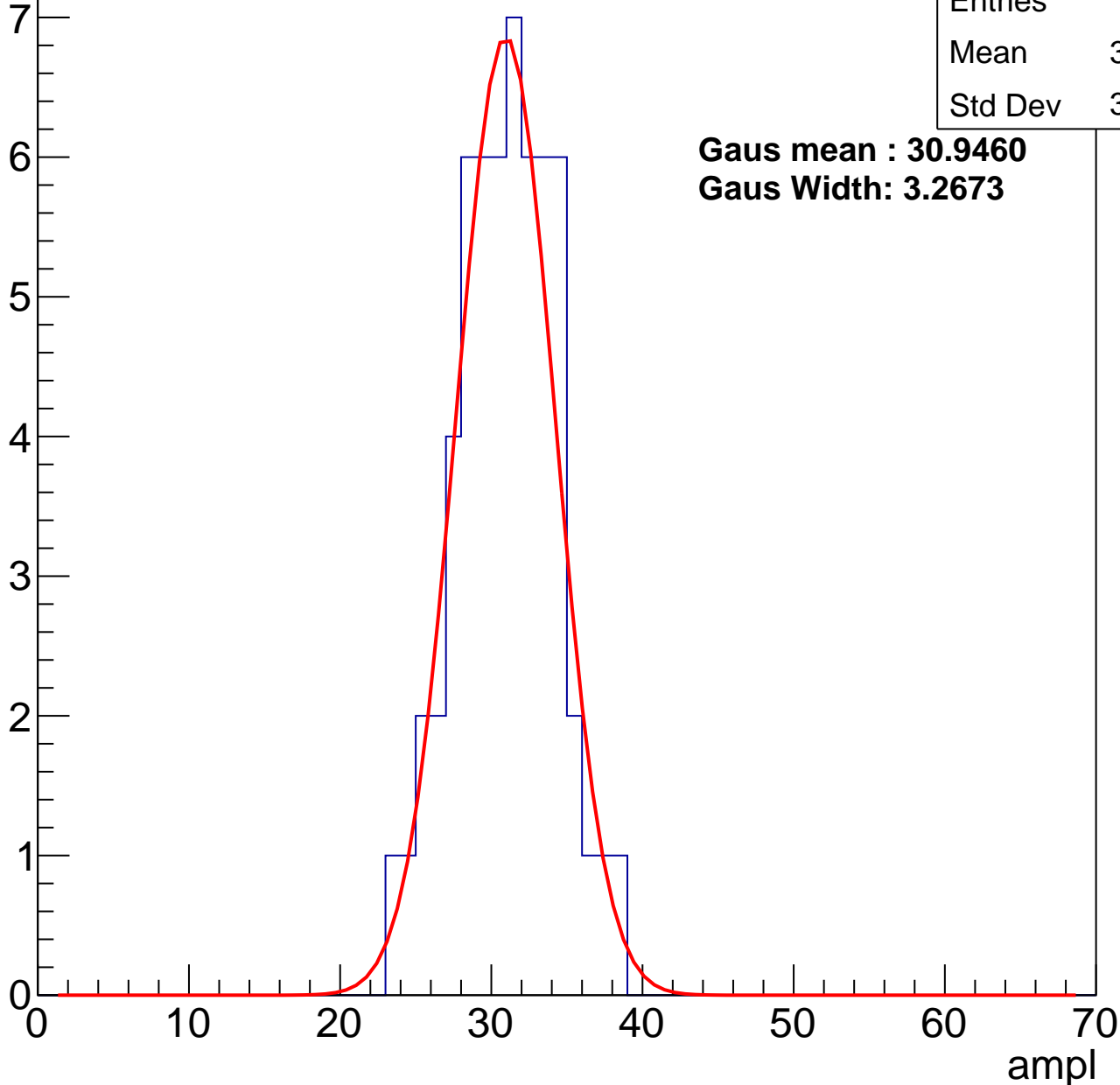
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 30.53 |
| Std Dev | 3.207 |

Gaus mean : 30.9460

Gaus Width: 3.2673



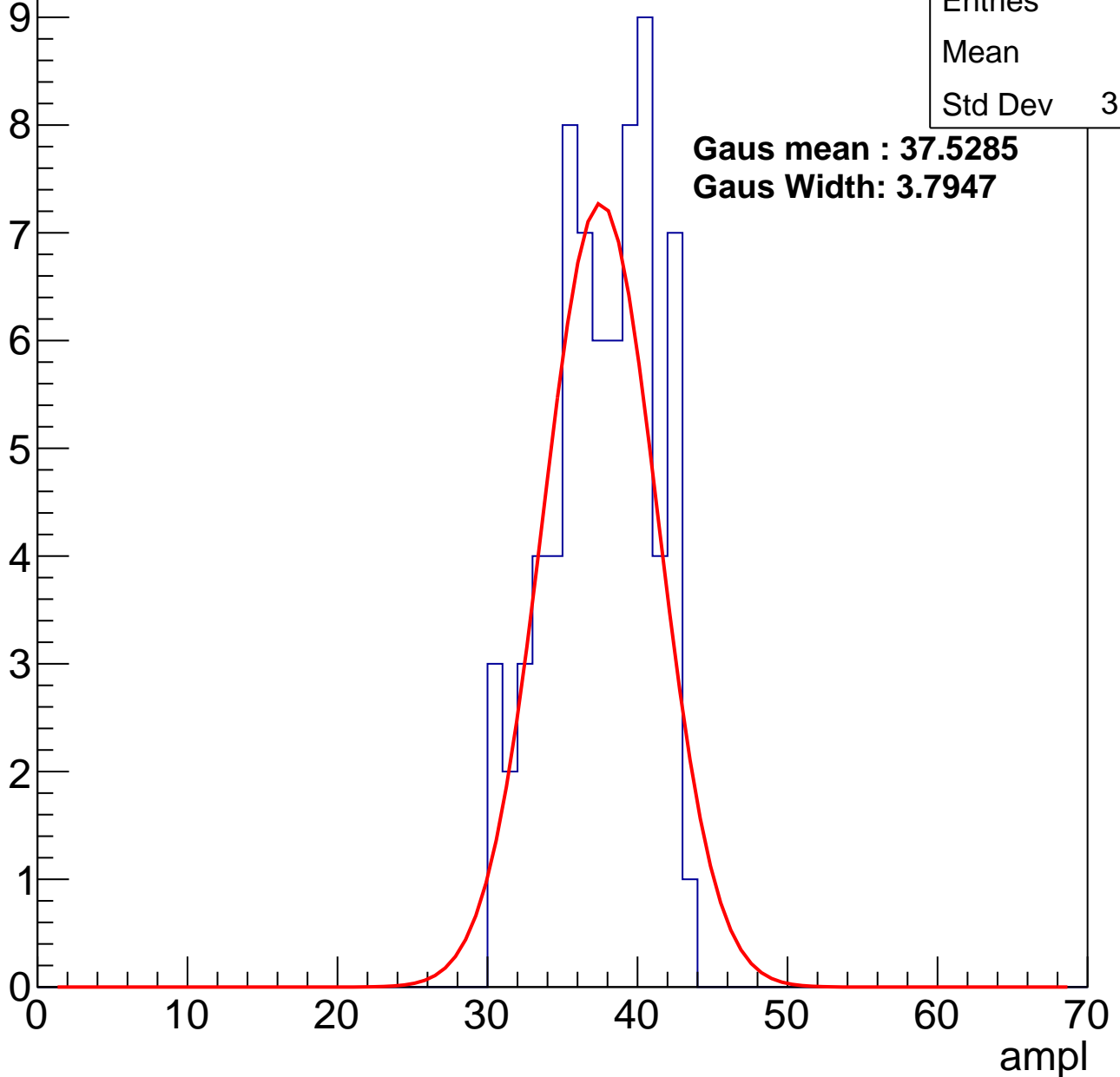
B1L100S, U9-ch113, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.1 |
| Std Dev | 3.388 |

Gaus mean : 37.5285
Gaus Width: 3.7947



B1L100S, U9-ch113, adc2

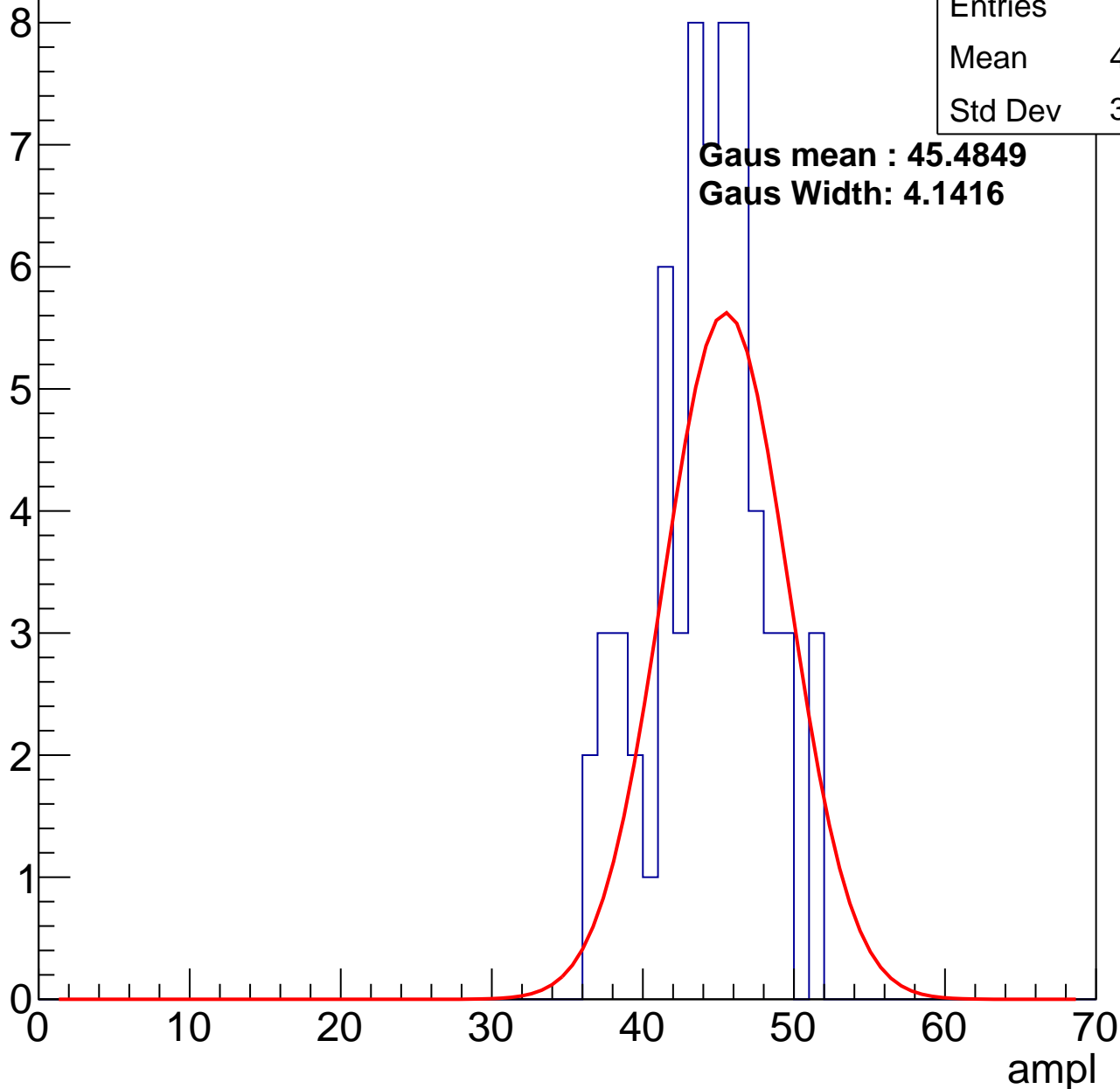
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.73 |
| Std Dev | 3.675 |

Gaus mean : 45.4849

Gaus Width: 4.1416

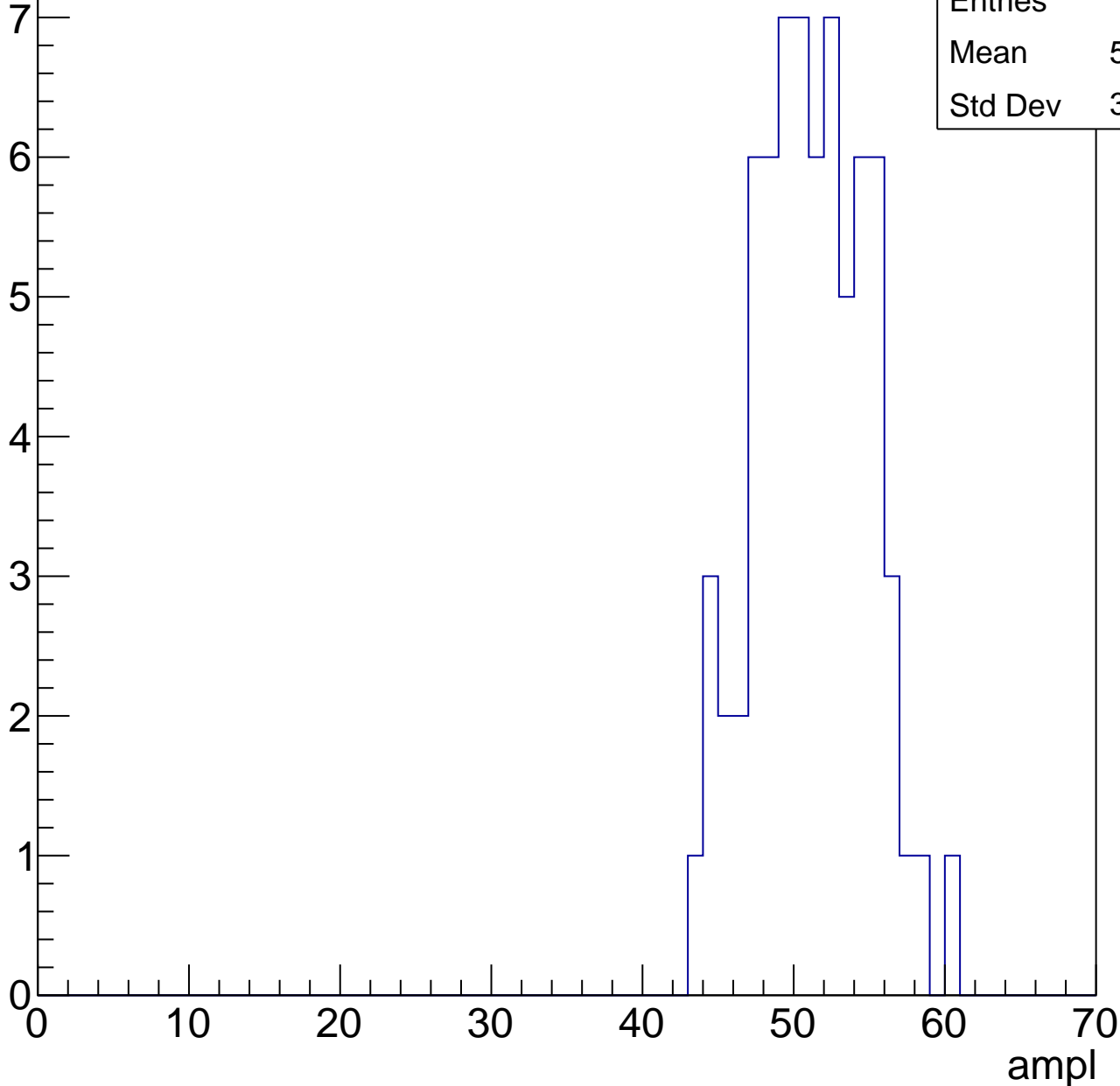


B1L100S, U9-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 50.74 |
| Std Dev | 3.648 |

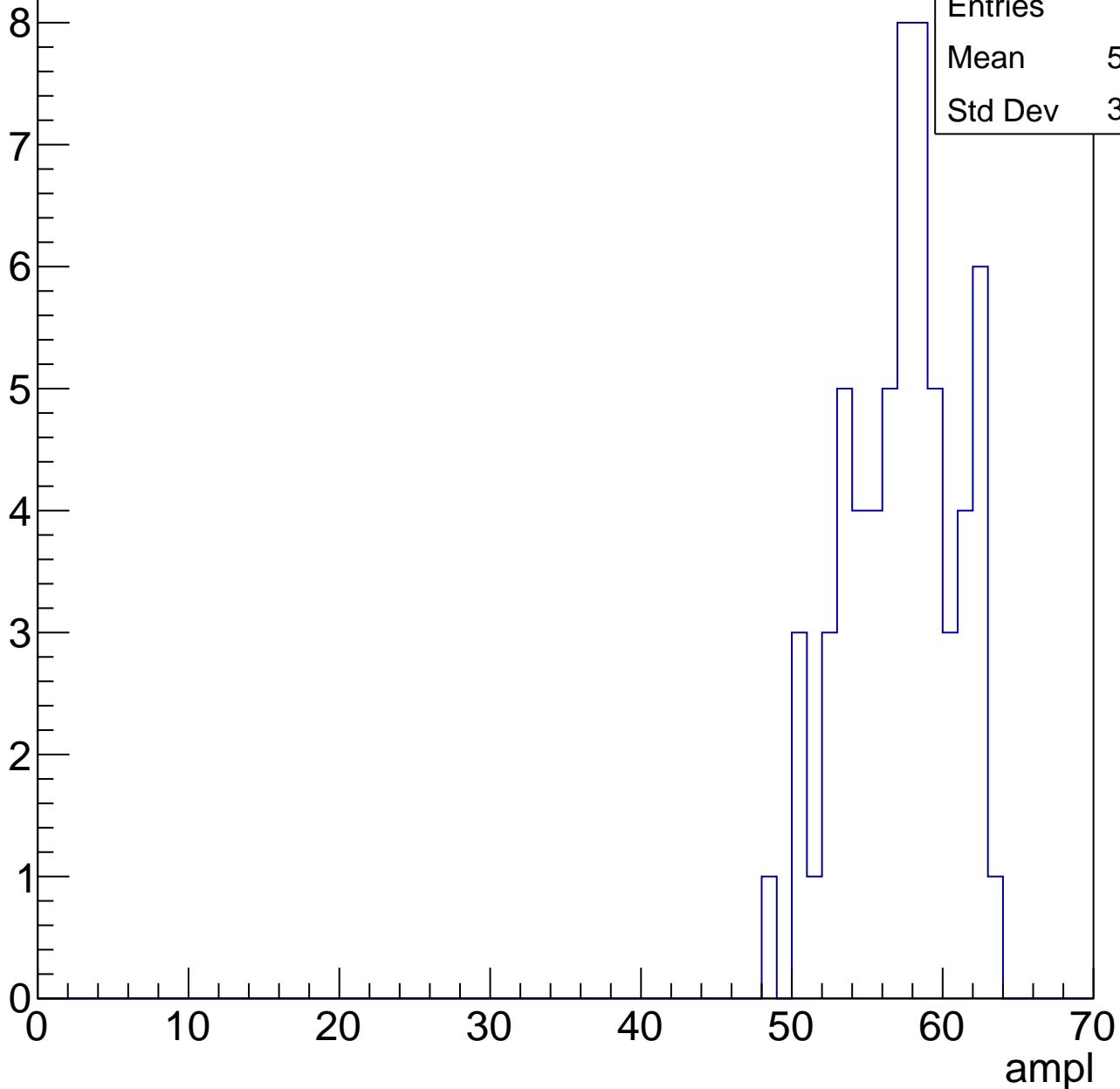


B1L100S, U9-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

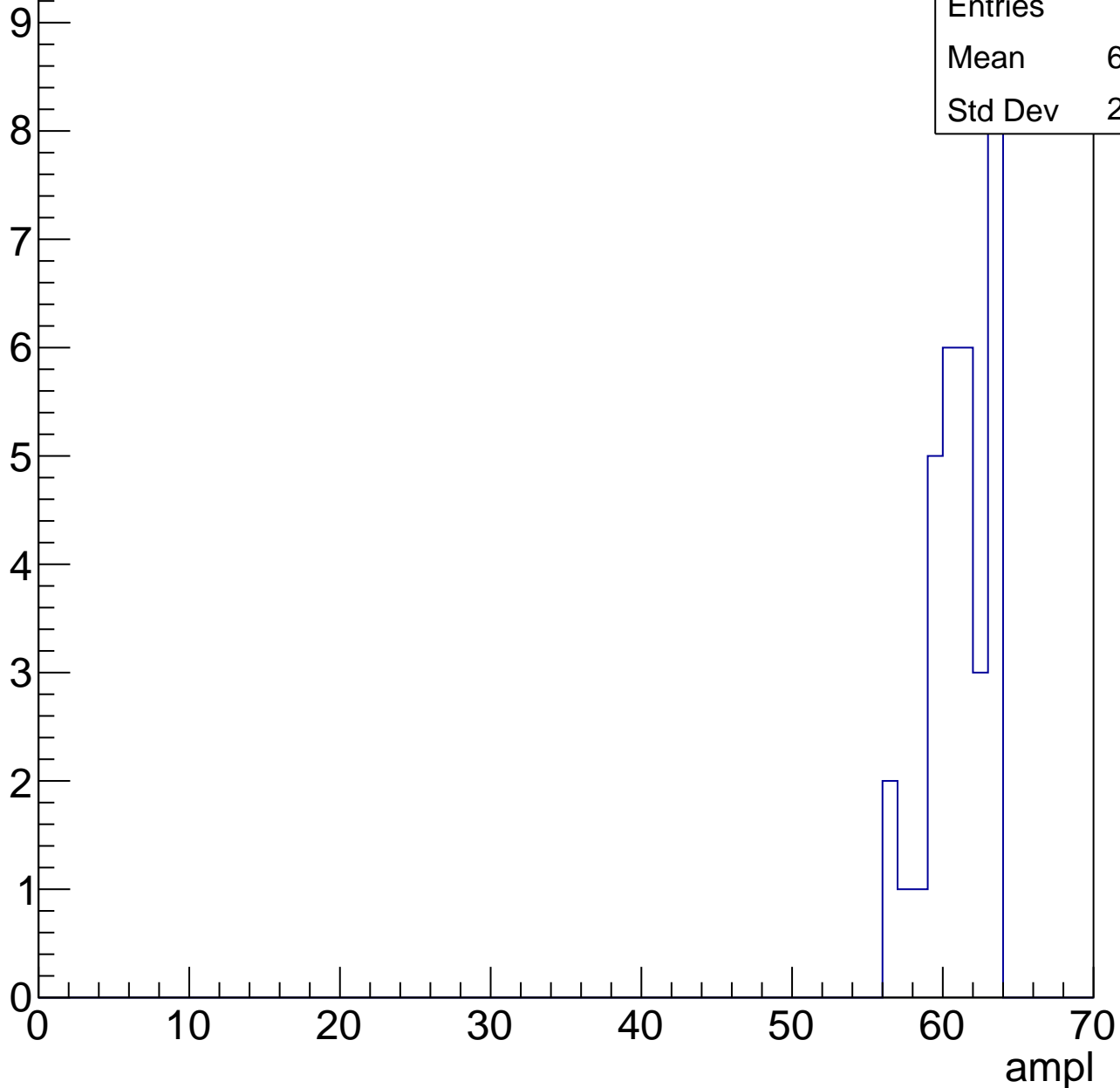
| | |
|---------|-------|
| Entries | 61 |
| Mean | 56.72 |
| Std Dev | 3.567 |



B1L100S, U9-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

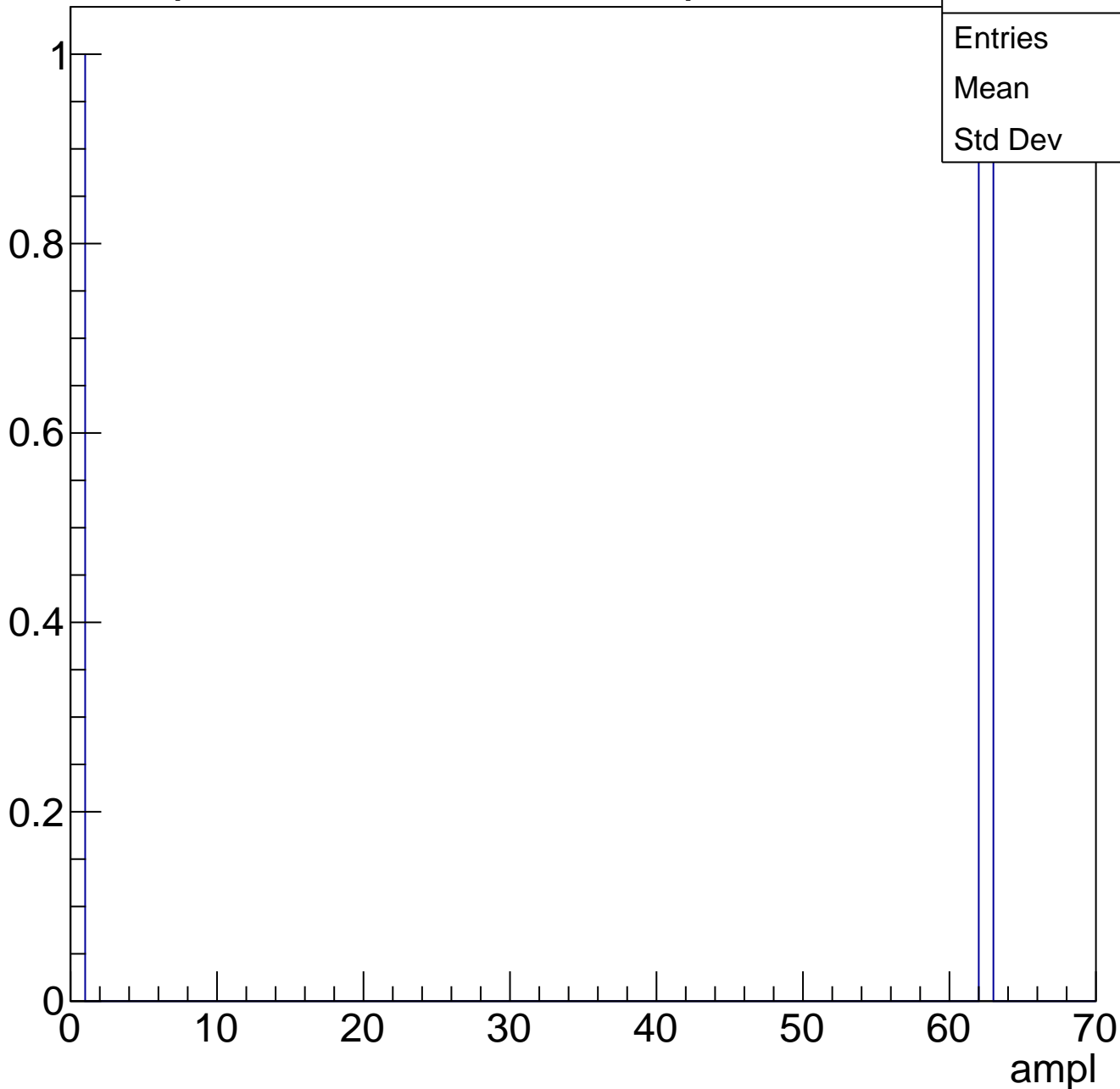
Entry



B1L100S, U9-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch114, adc0

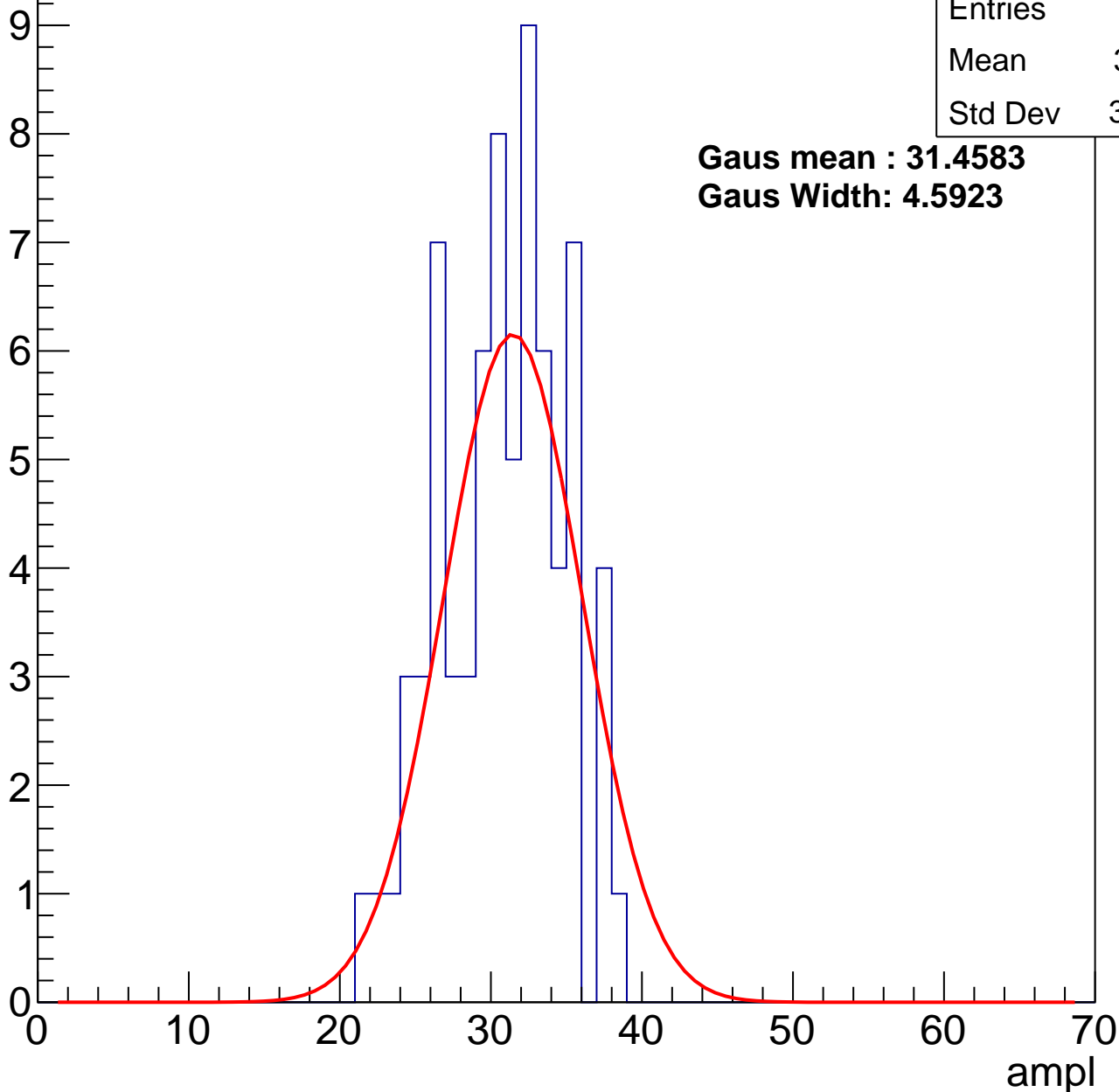
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 30.31 |
| Std Dev | 3.946 |

Gaus mean : 31.4583

Gaus Width: 4.5923



B1L100S, U9-ch114, adc1

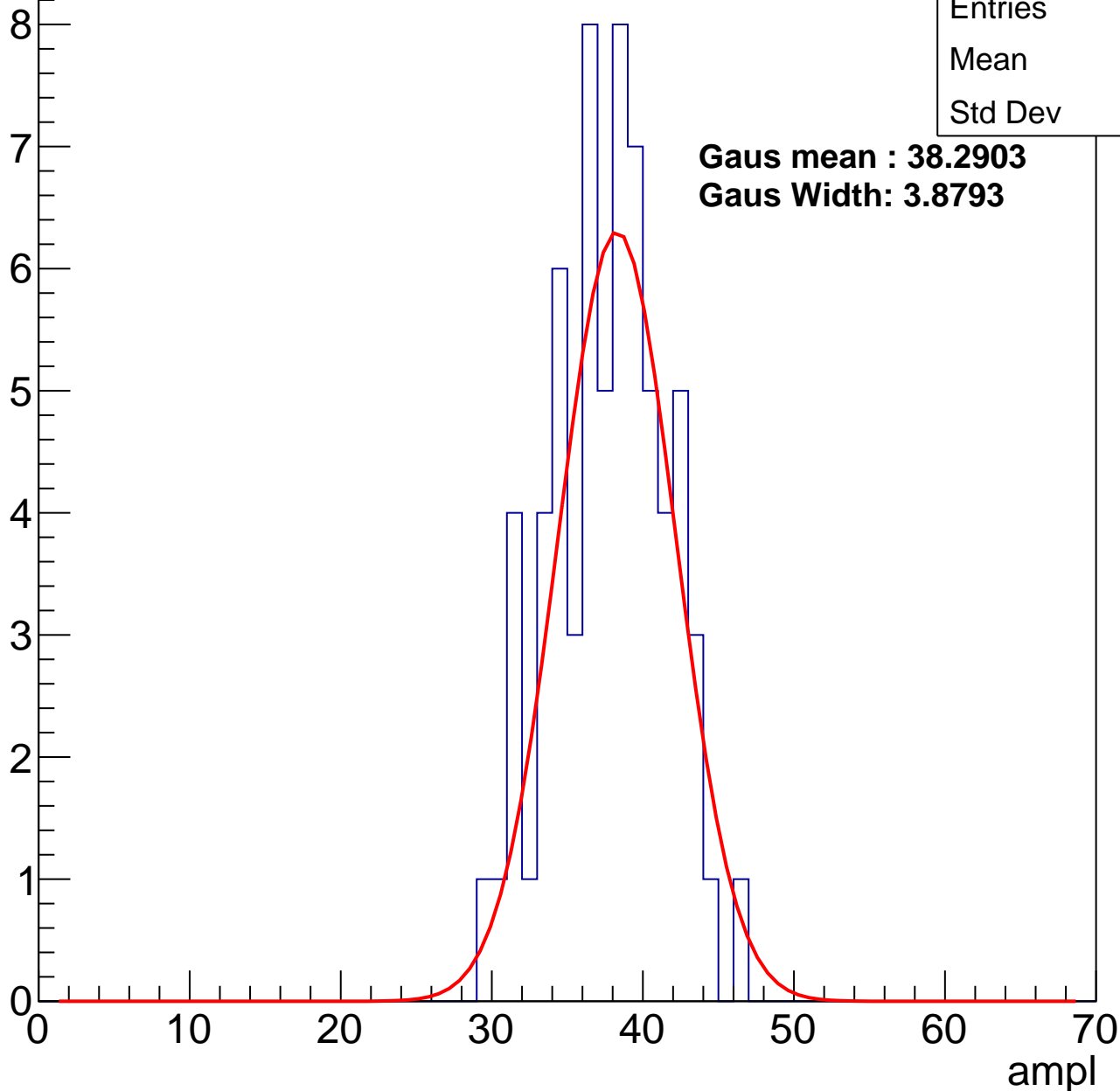
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 67 |
| Mean | 37.3 |
| Std Dev | 3.71 |

Gaus mean : 38.2903

Gaus Width: 3.8793



B1L100S, U9-ch114, adc2

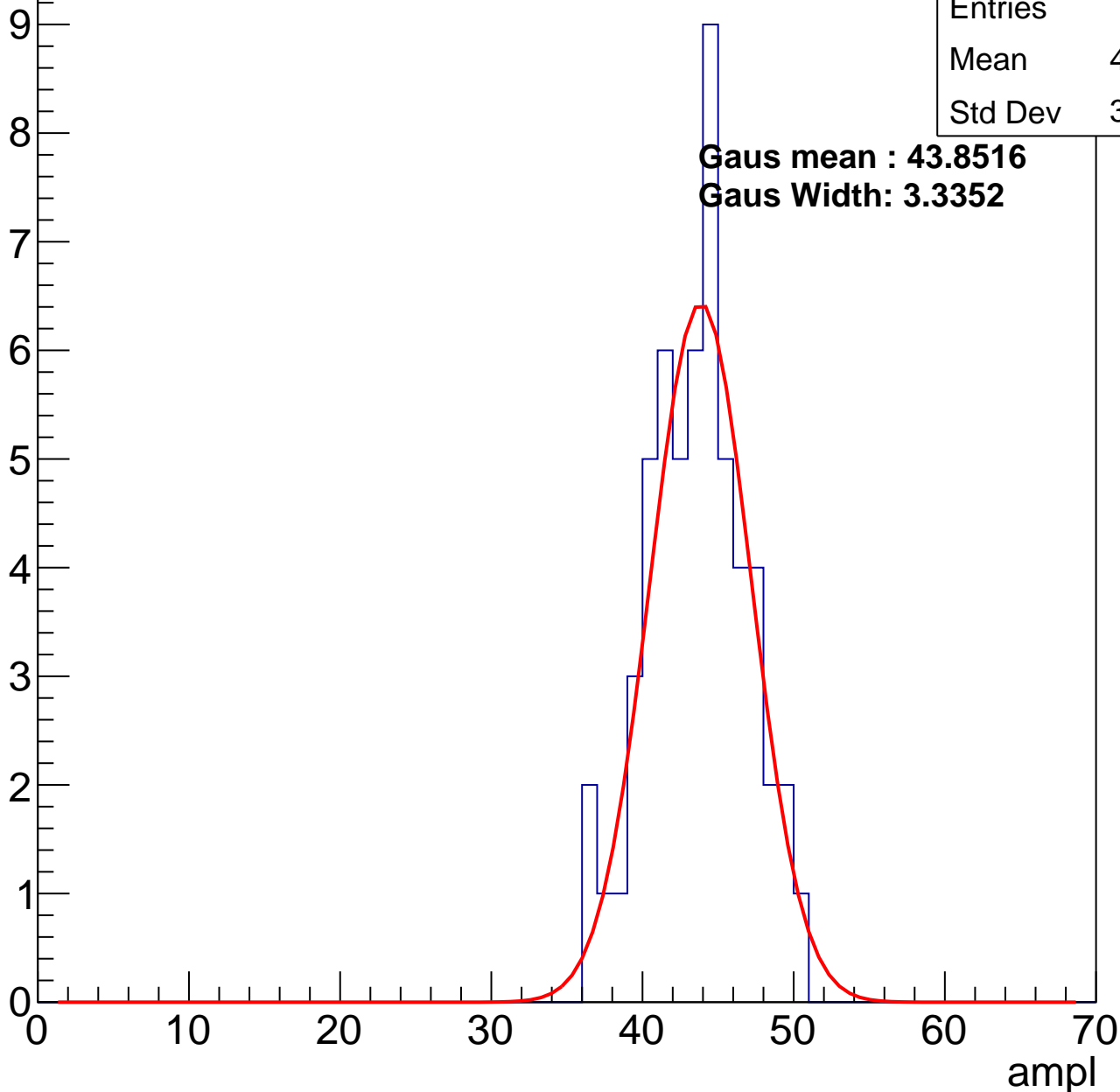
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43.12 |
| Std Dev | 3.219 |

Gaus mean : 43.8516

Gaus Width: 3.3352



B1L100S, U9-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 85 |
| Mean | 50.79 |
| Std Dev | 3.512 |

Entry

10

8

6

4

2

0

0

10

20

30

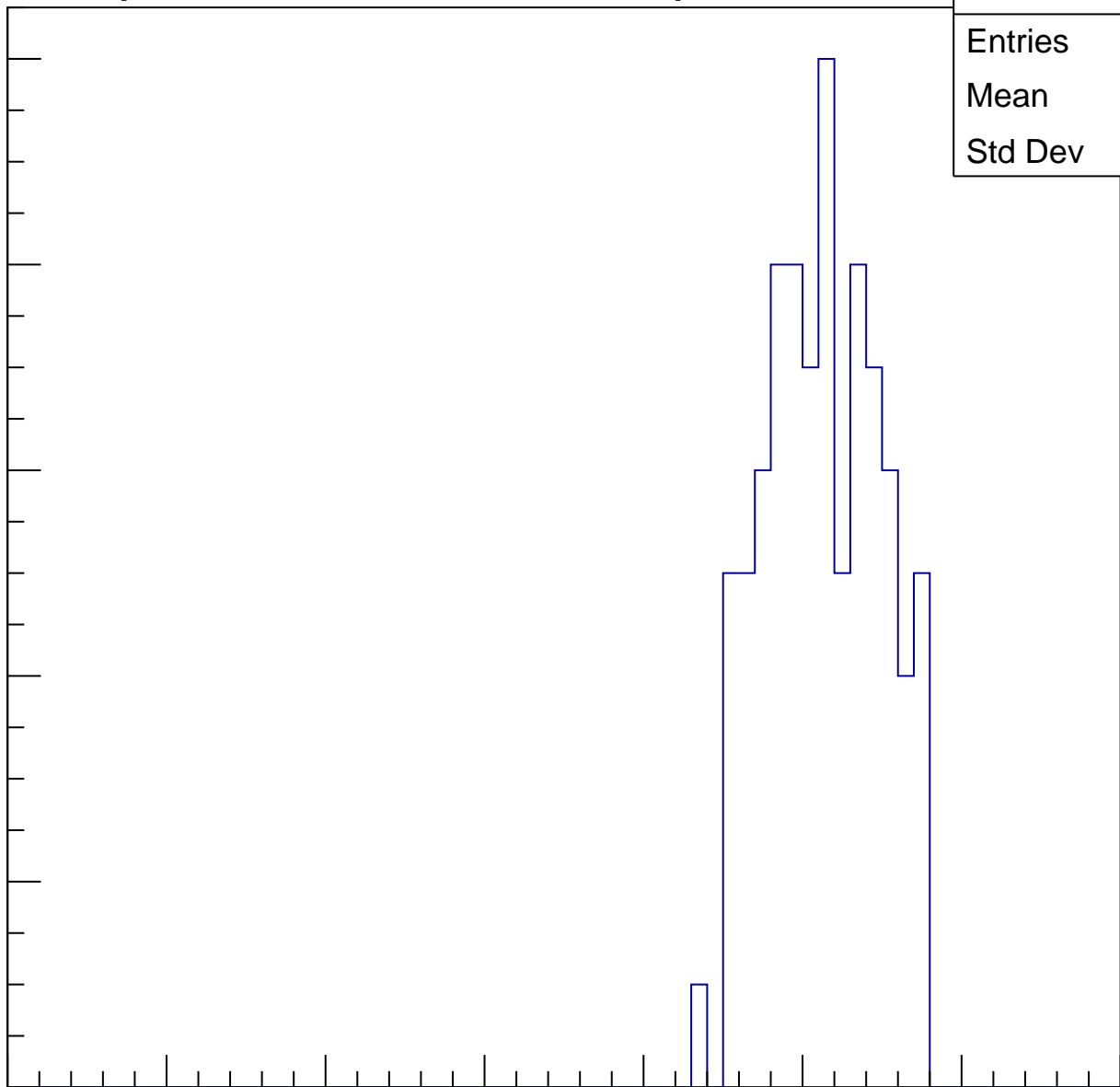
40

50

60

70

ampl

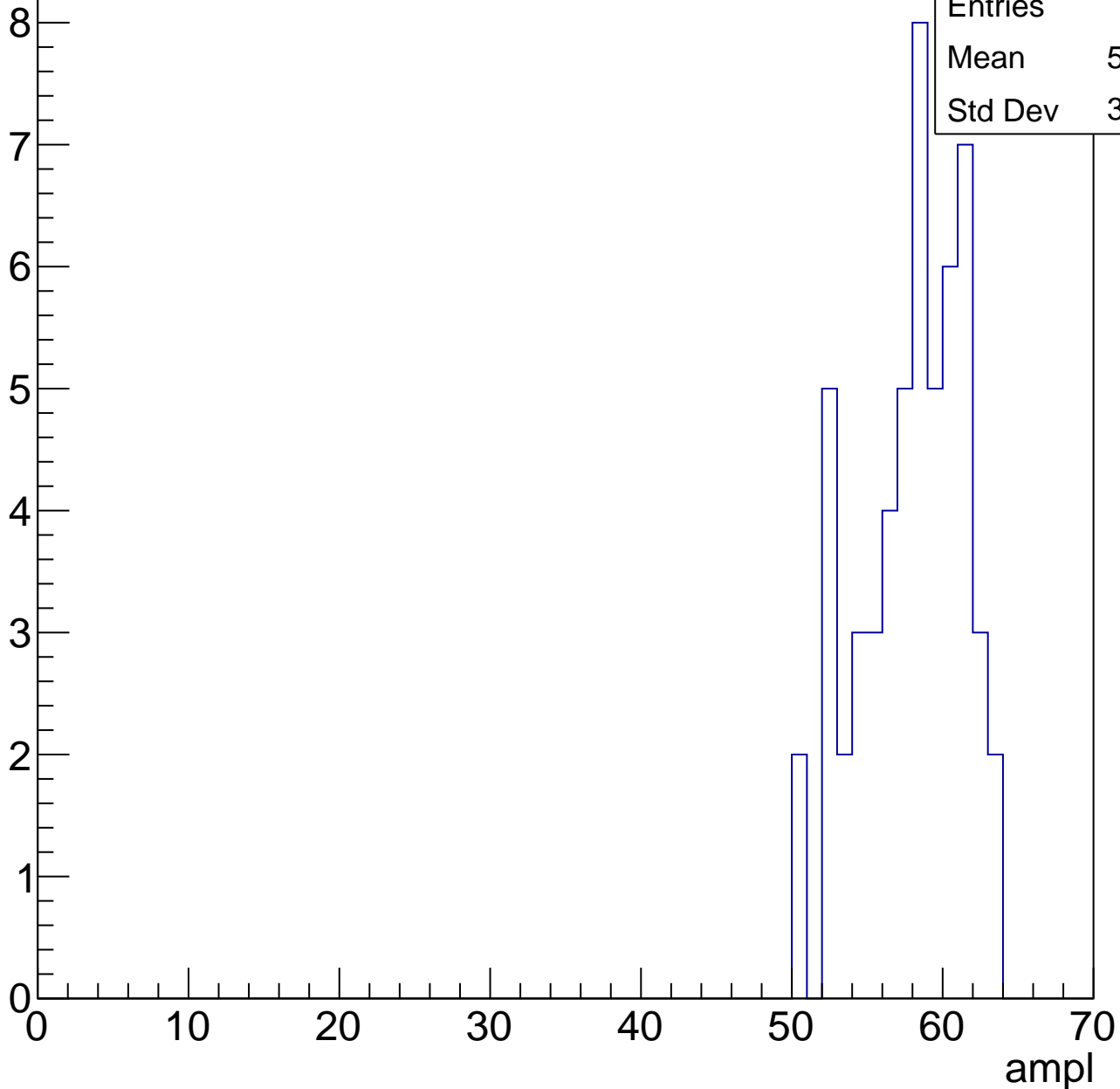


B1L100S, U9-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 57.45 |
| Std Dev | 3.378 |

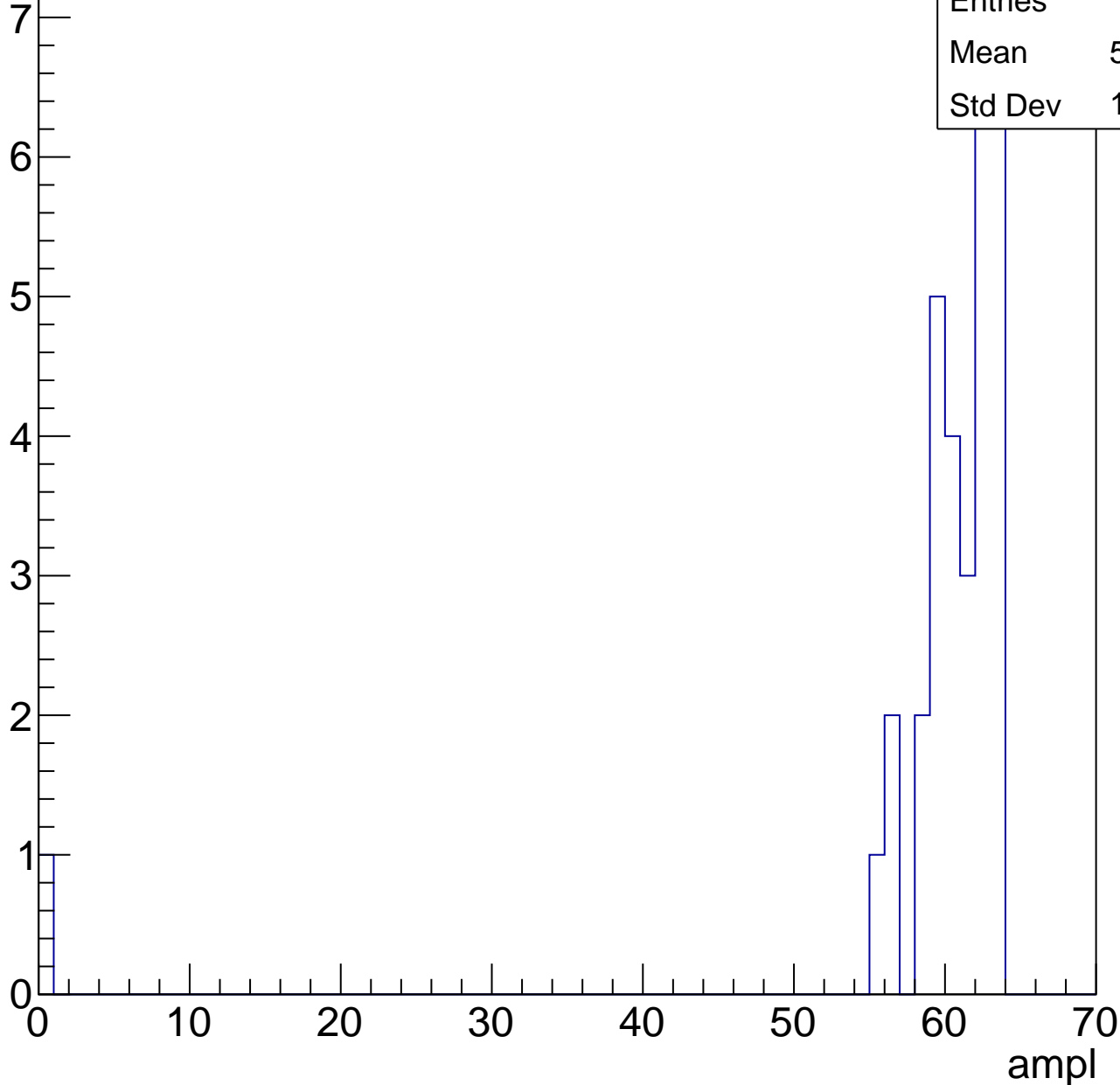


B1L100S, U9-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.62 |
| Std Dev | 10.76 |



B1L100S, U9-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch115, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 101 |
| Mean | 27.17 |
| Std Dev | 6.398 |

Gaus mean : 28.2372

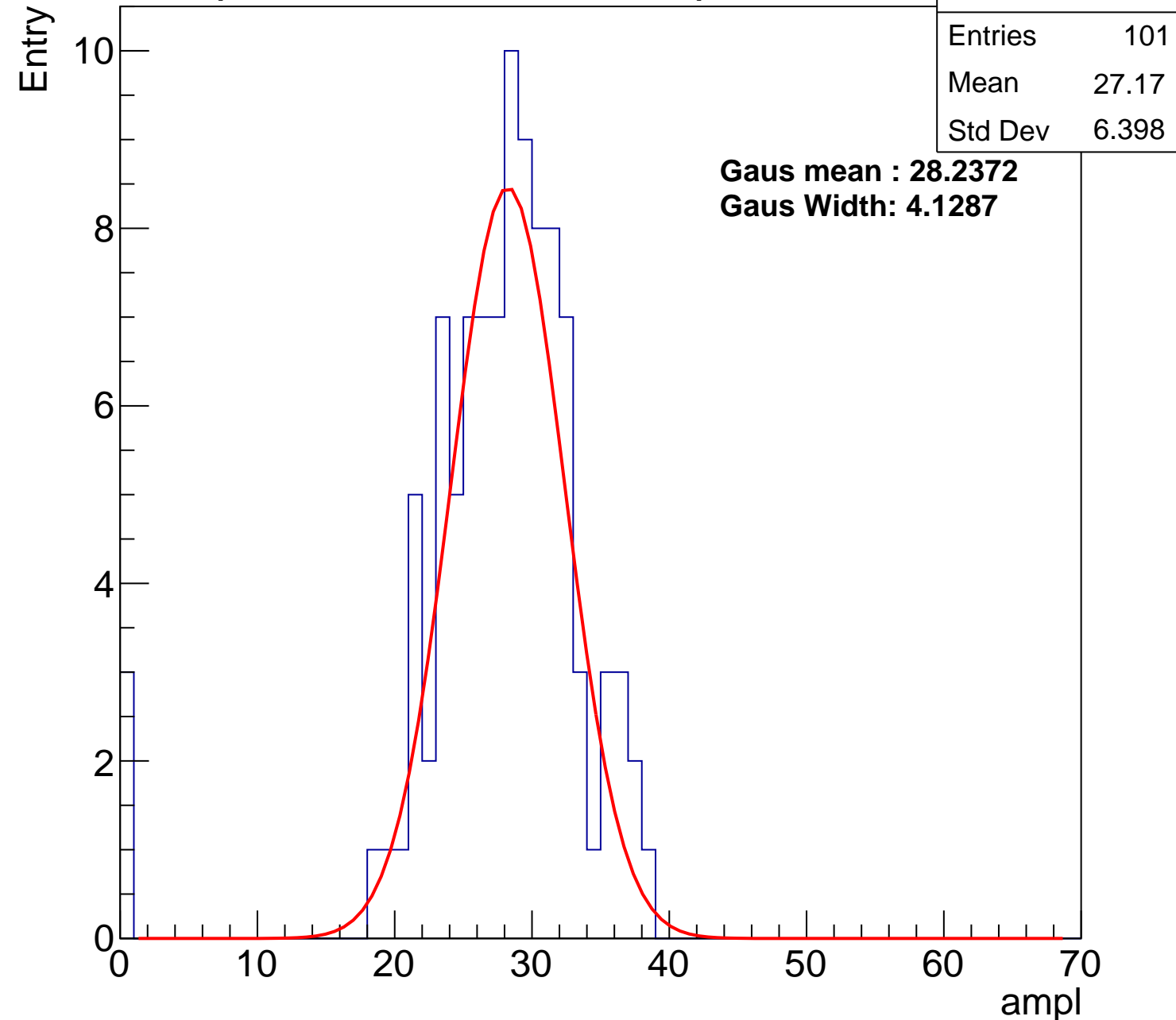
Gaus Width: 4.1287

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch115, adc1

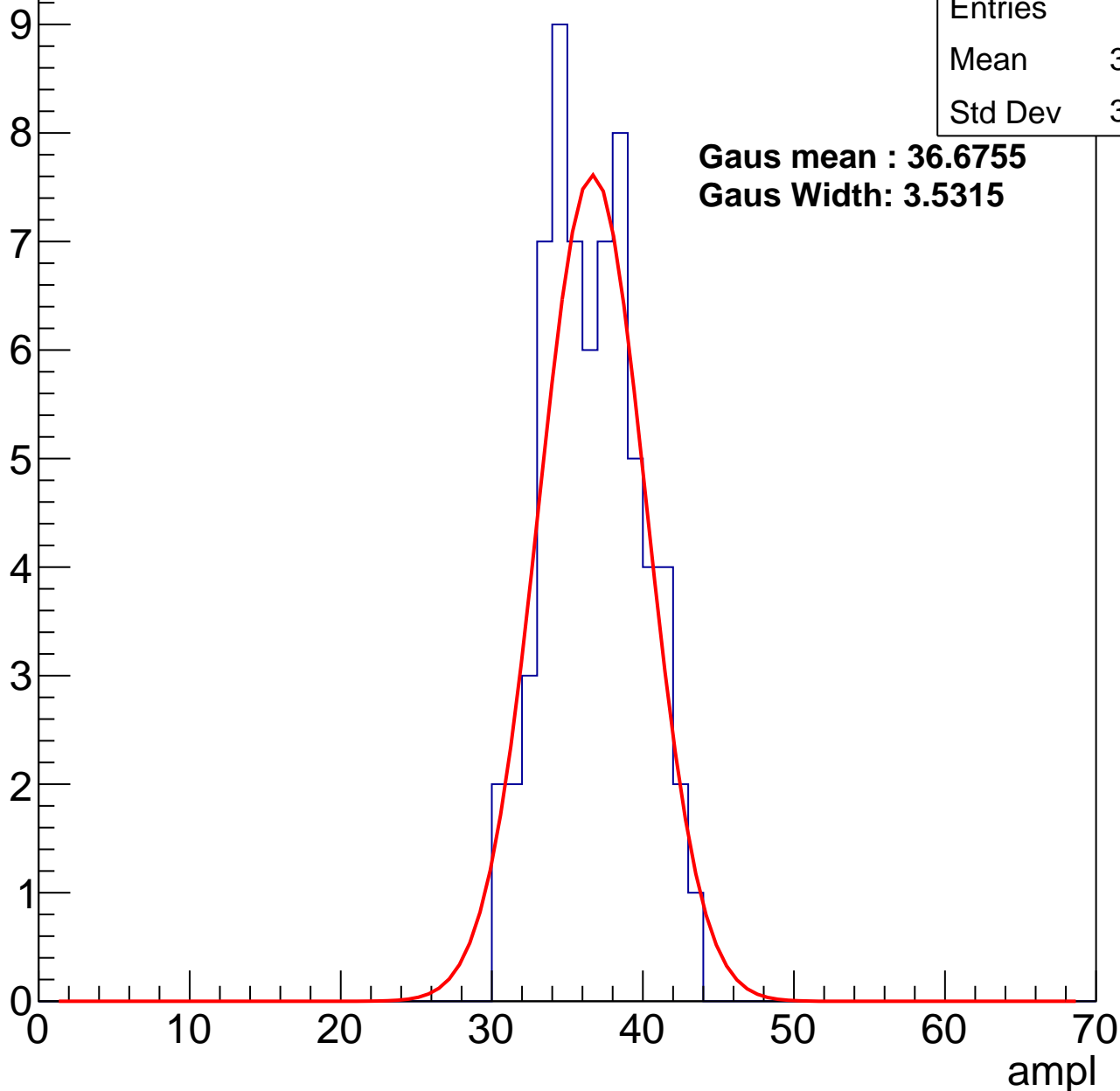
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.19 |
| Std Dev | 3.097 |

Gaus mean : 36.6755

Gaus Width: 3.5315



B1L100S, U9-ch115, adc2

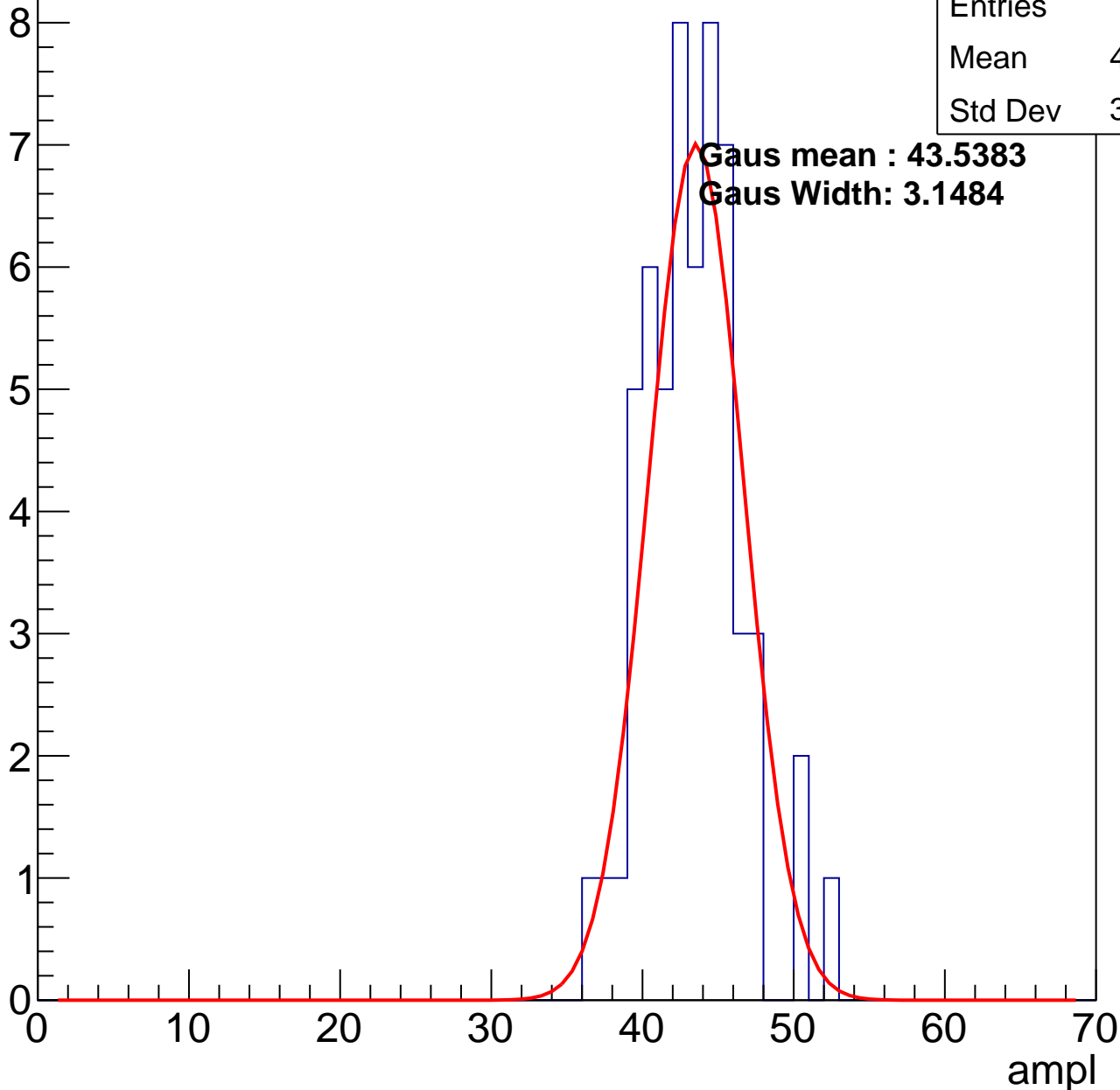
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 42.86 |
| Std Dev | 3.137 |

Gaus mean : 43.5383

Gaus Width: 3.1484



B1L100S, U9-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 49.07 |
| Std Dev | 3.38 |

Entry

10

8

6

4

2

0

0

10

20

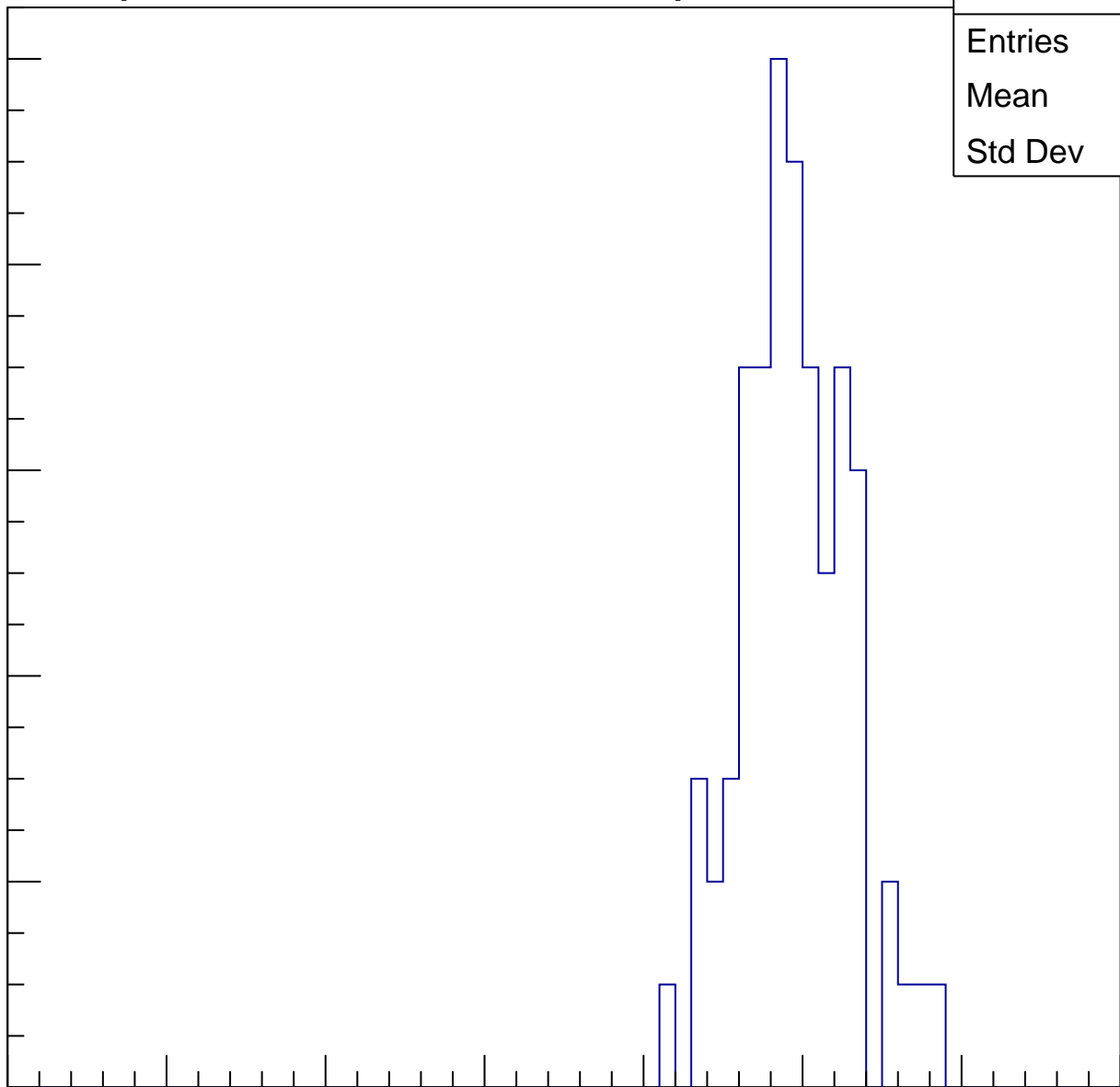
30

40

50

60

ampl

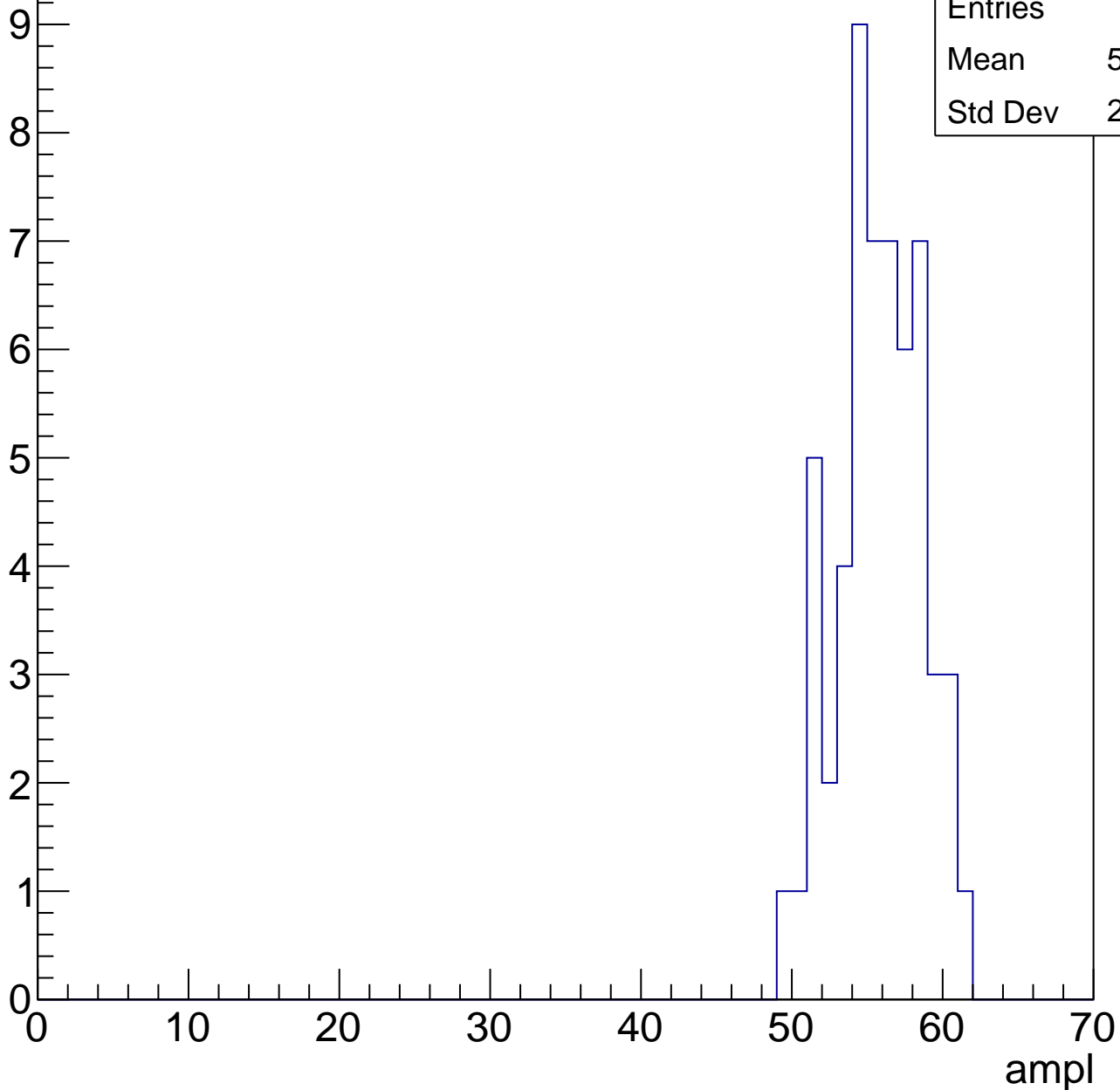


B1L100S, U9-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.34 |
| Std Dev | 2.773 |

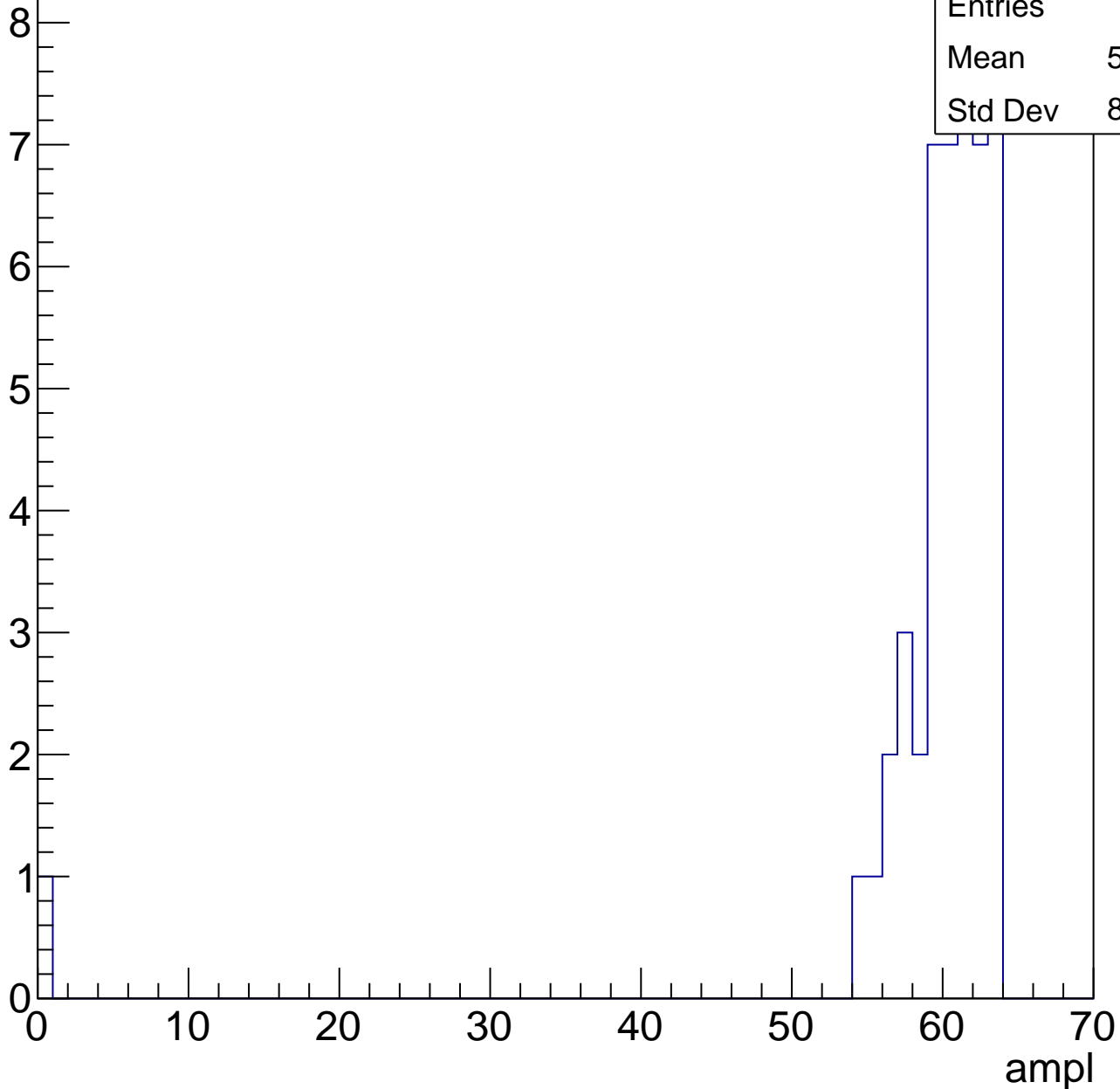


B1L100S, U9-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.87 |
| Std Dev | 8.972 |



B1L100S, U9-ch115, adc6

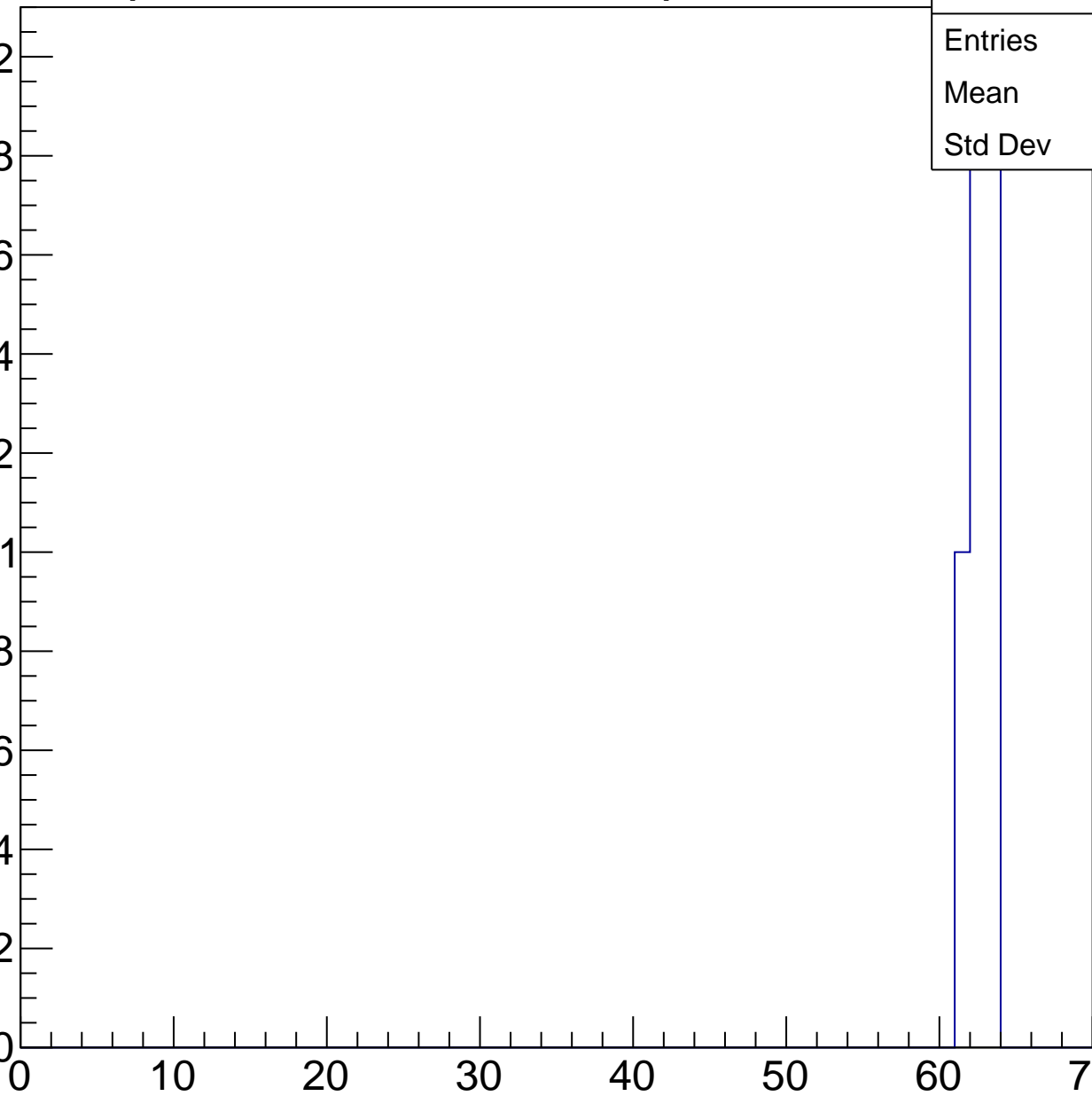
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62.2 |
| Std Dev | 0.7483 |

ampl



B1L100S, U9-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch116, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 30.65 |
| Std Dev | 3.806 |

Gaus mean : 31.1607

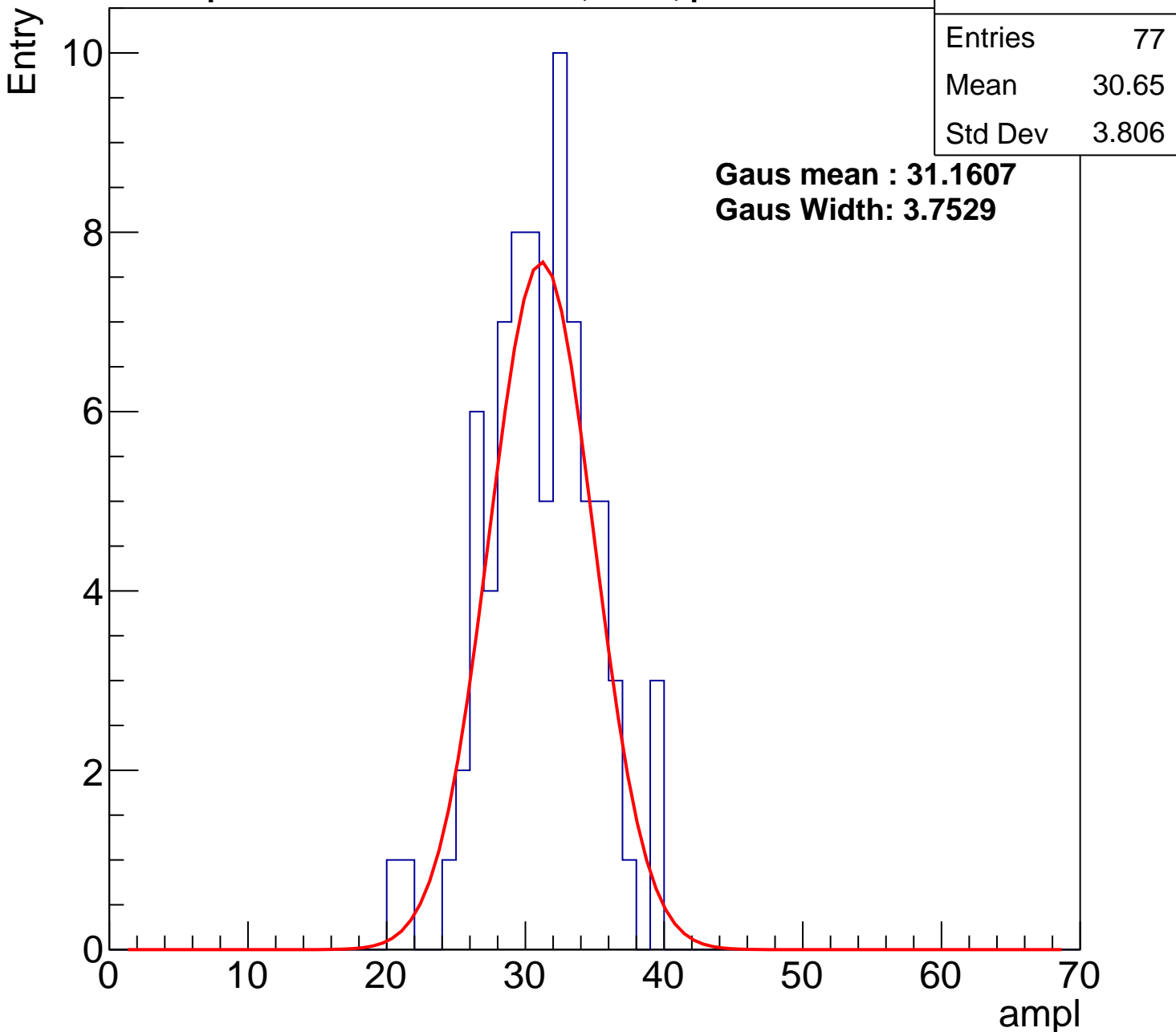
Gaus Width: 3.7529

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch116, adc1

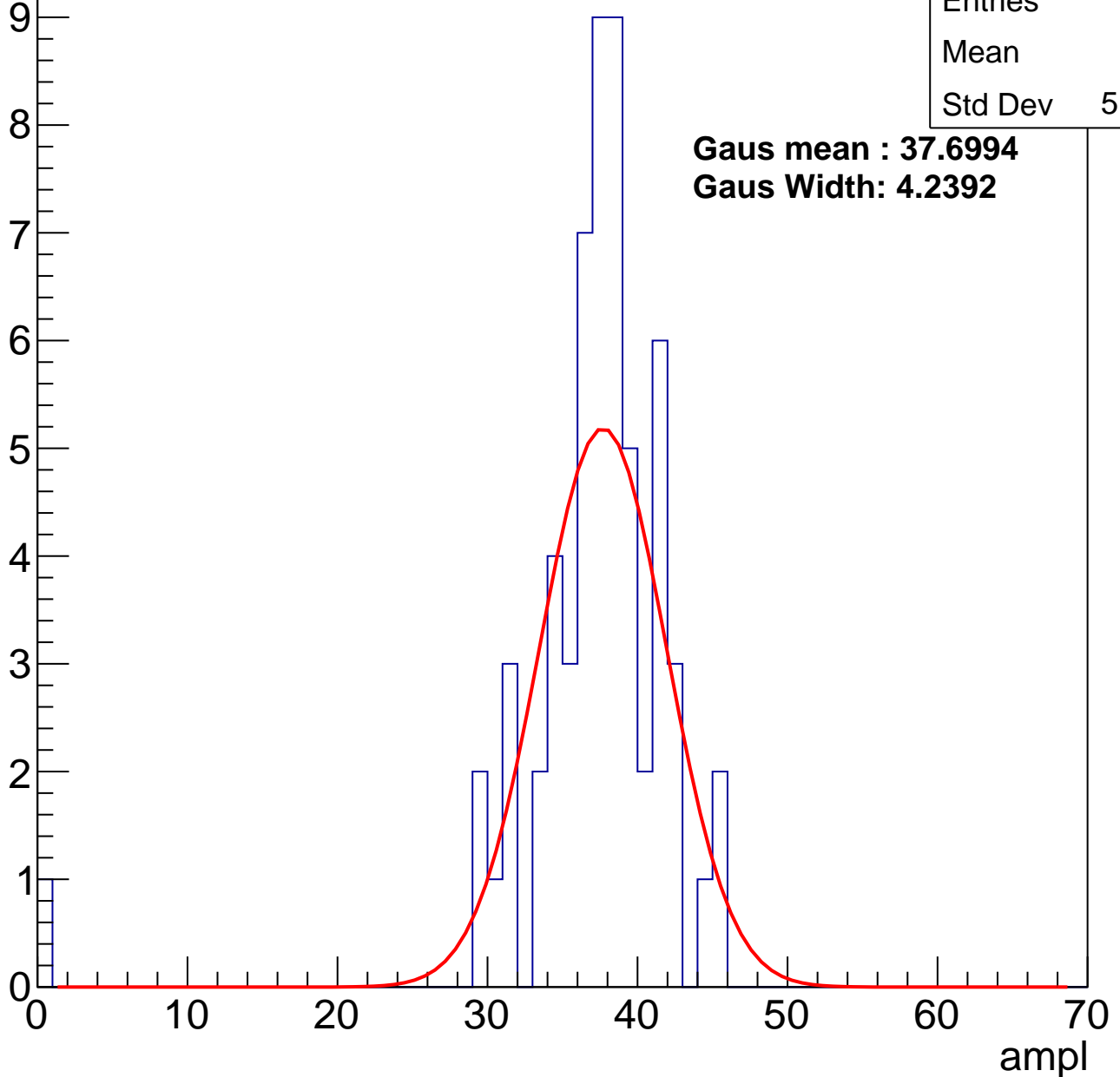
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 36.6 |
| Std Dev | 5.953 |

Gaus mean : 37.6994

Gaus Width: 4.2392



B1L100S, U9-ch116, adc2

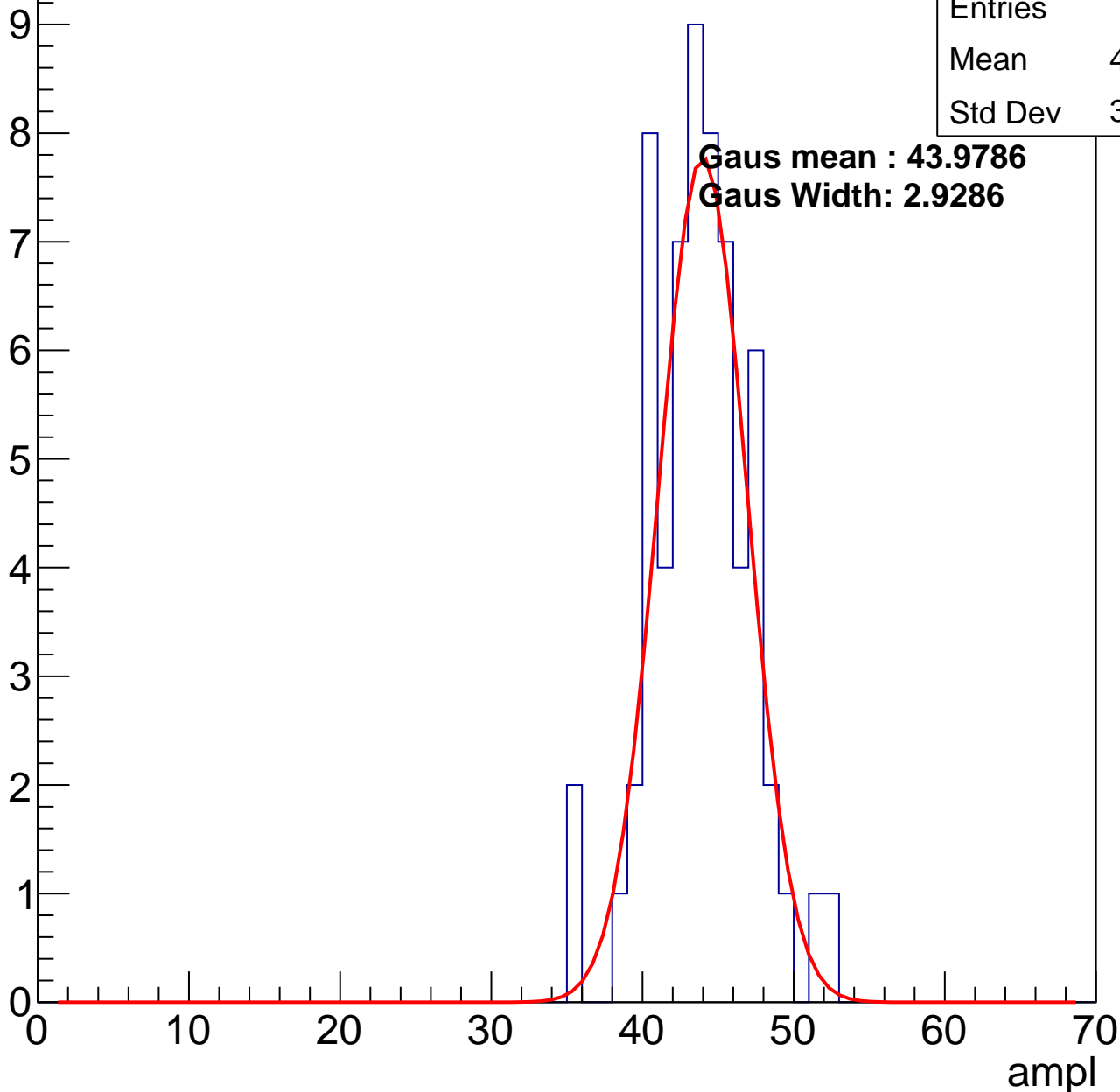
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.37 |
| Std Dev | 3.263 |

Gaus mean : 43.9786

Gaus Width: 2.9286

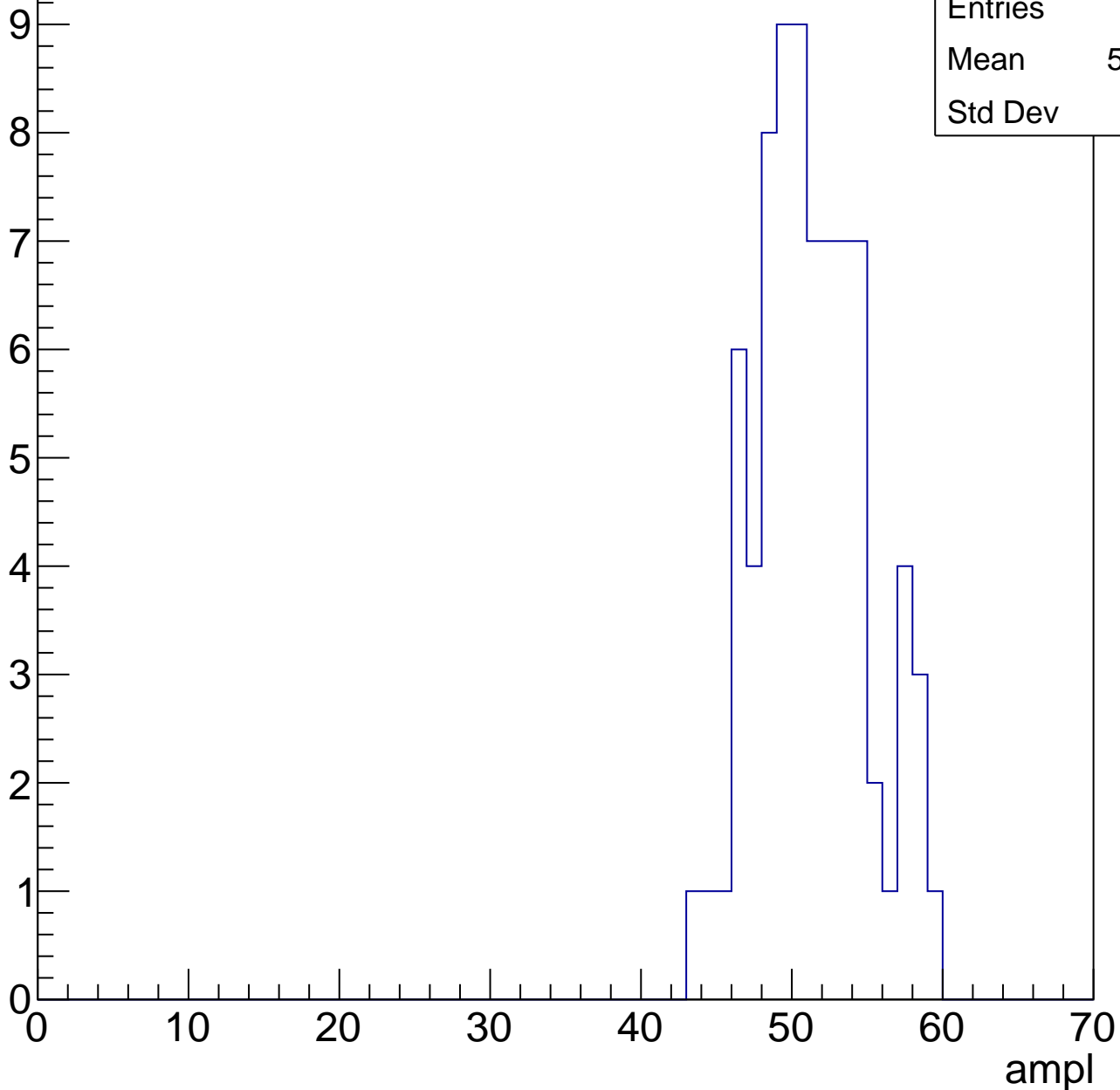


B1L100S, U9-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 50.87 |
| Std Dev | 3.56 |

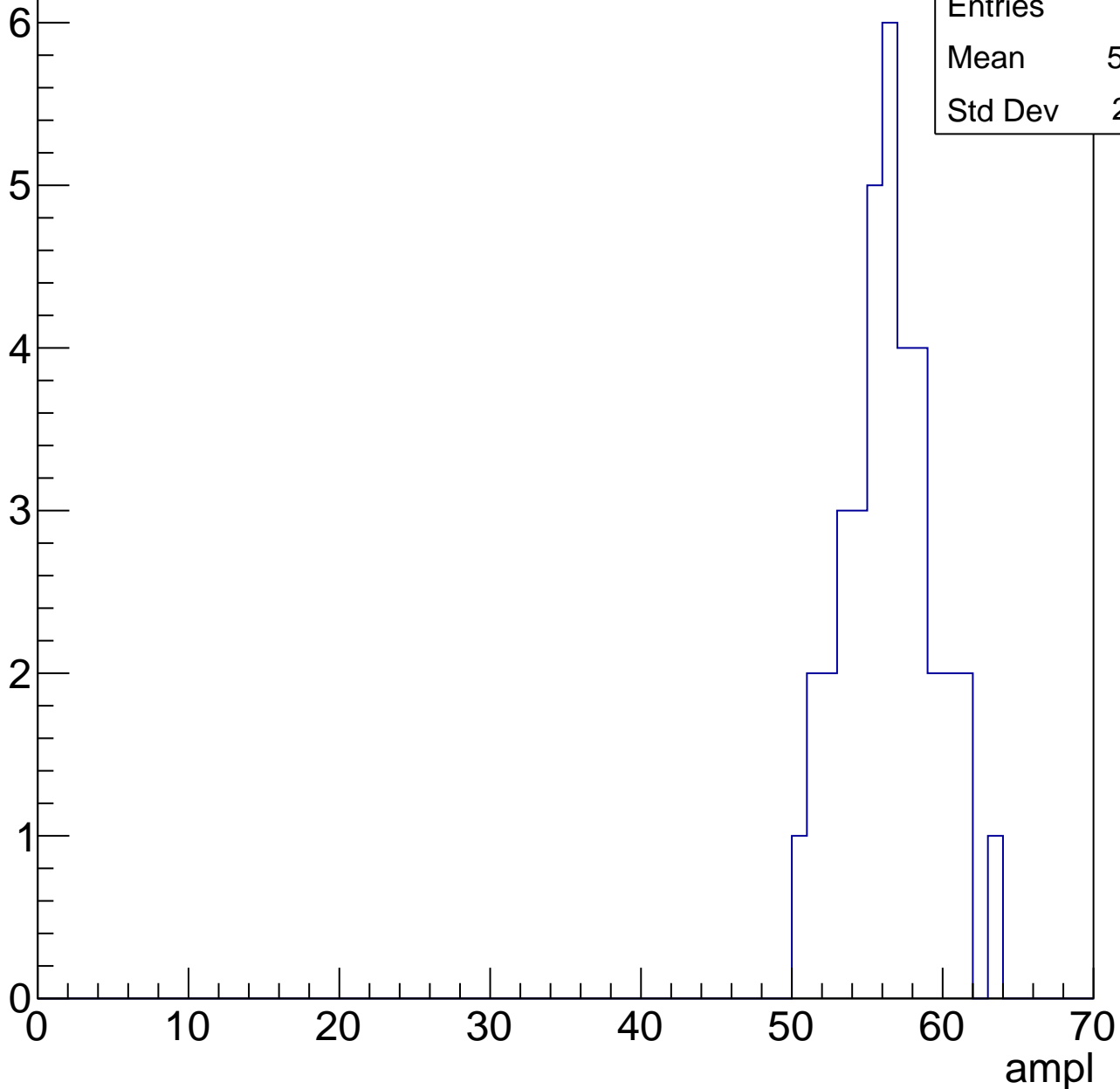


B1L100S, U9-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 55.97 |
| Std Dev | 2.991 |

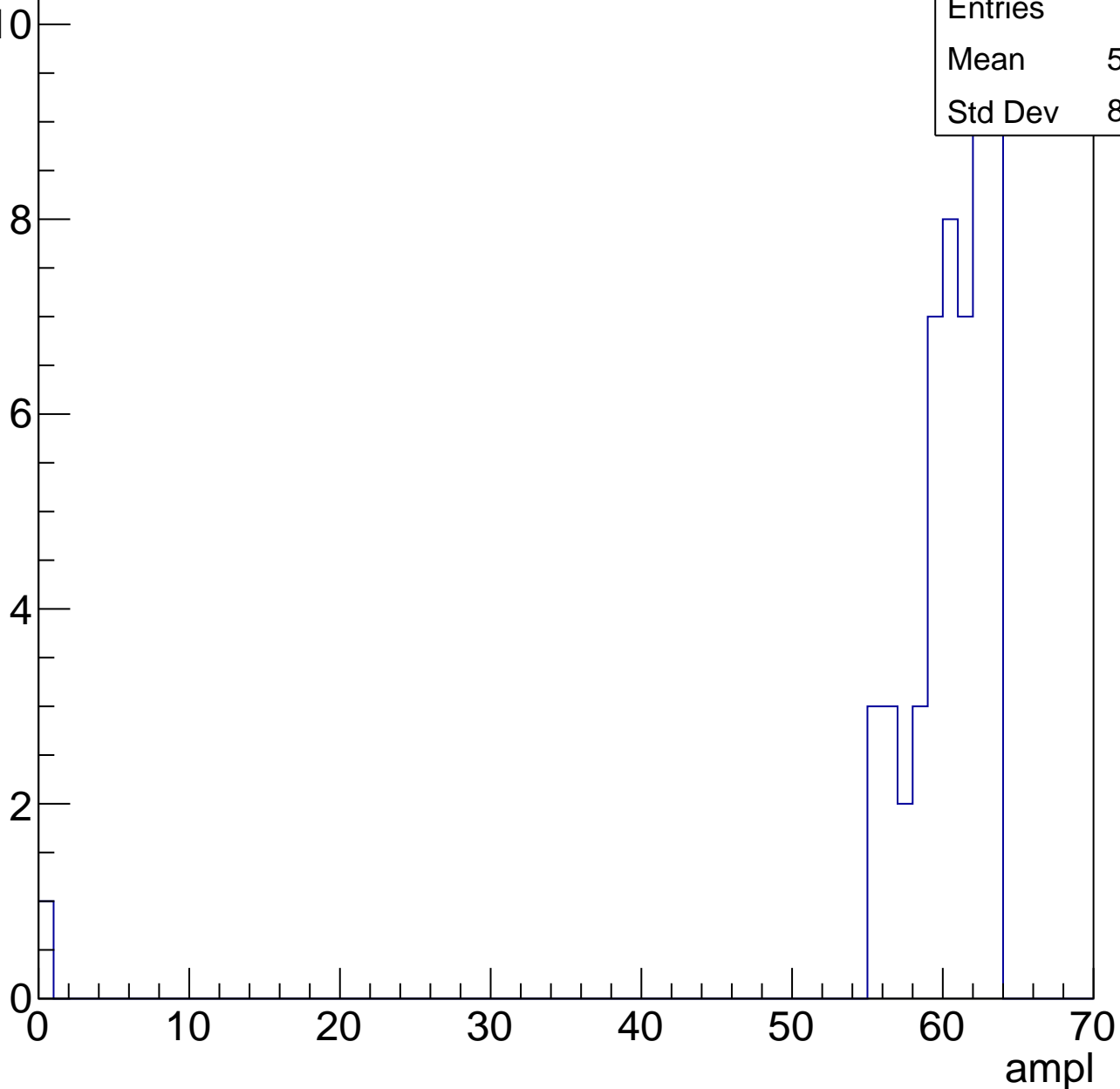


B1L100S, U9-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.02 |
| Std Dev | 8.509 |



B1L100S, U9-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch117, adc0

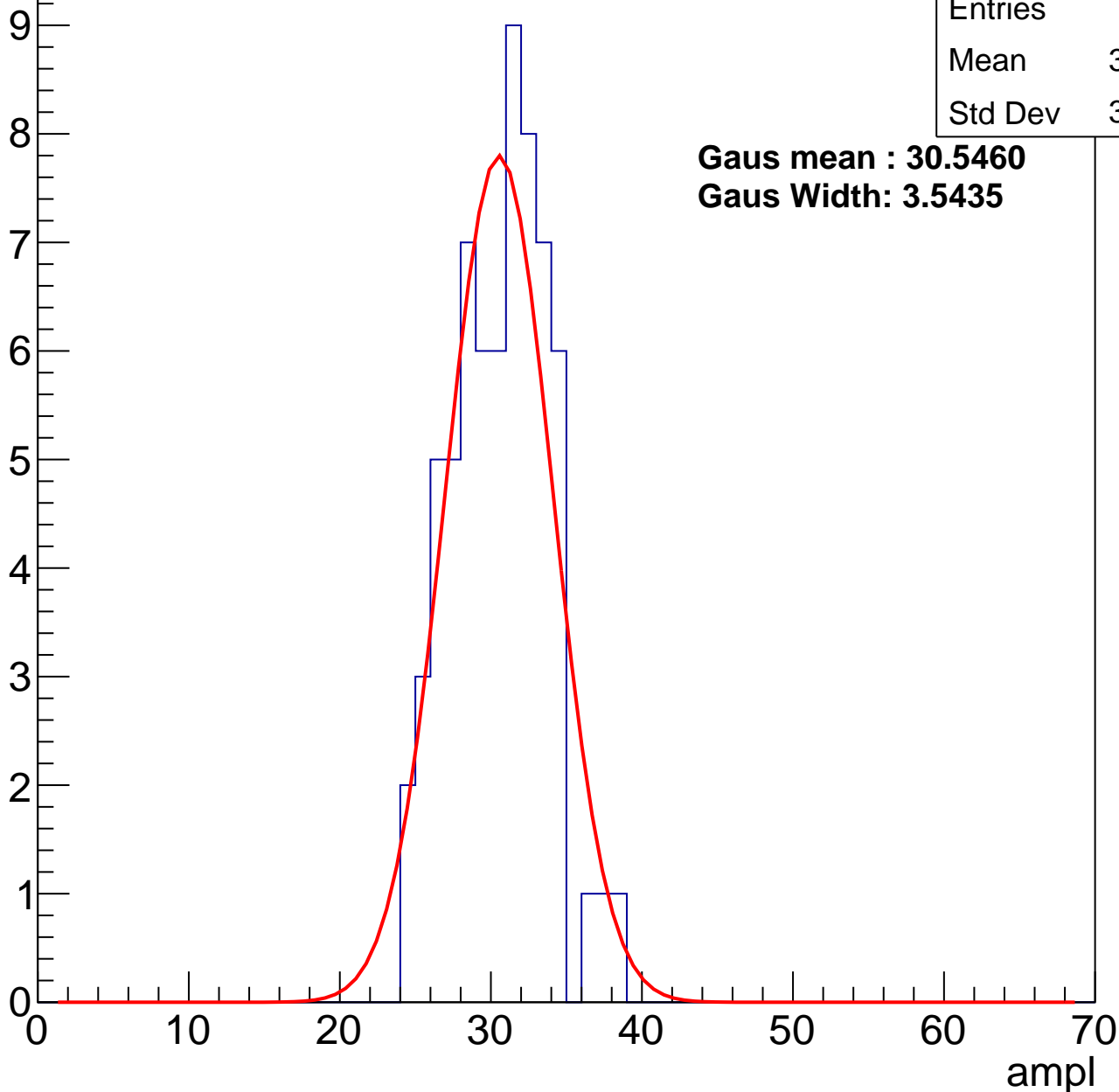
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.13 |
| Std Dev | 3.124 |

Gaus mean : 30.5460

Gaus Width: 3.5435

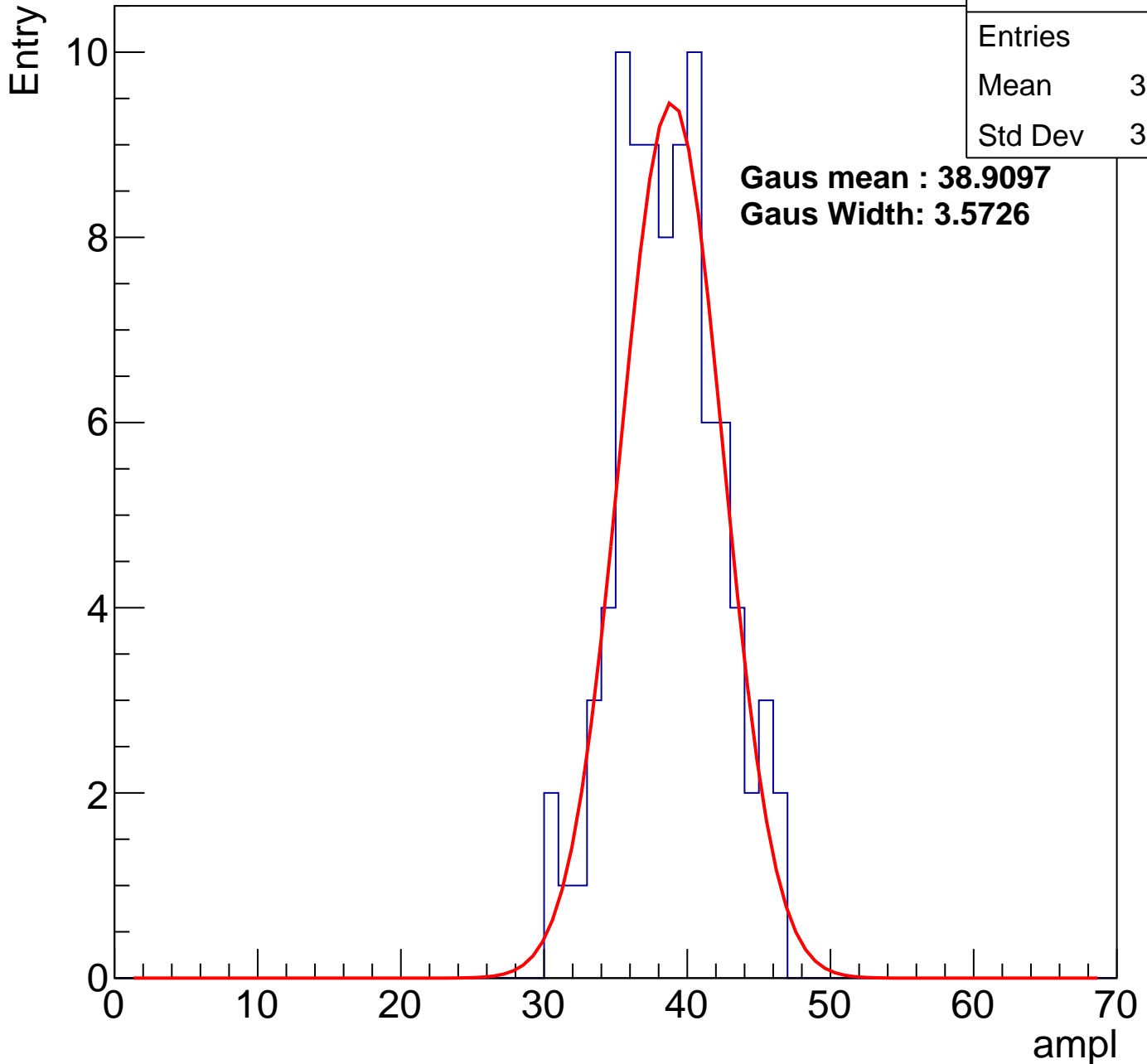


B1L100S, U9-ch117, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 89 |
| Mean | 38.26 |
| Std Dev | 3.559 |

Gaus mean : 38.9097
Gaus Width: 3.5726



B1L100S, U9-ch117, adc2

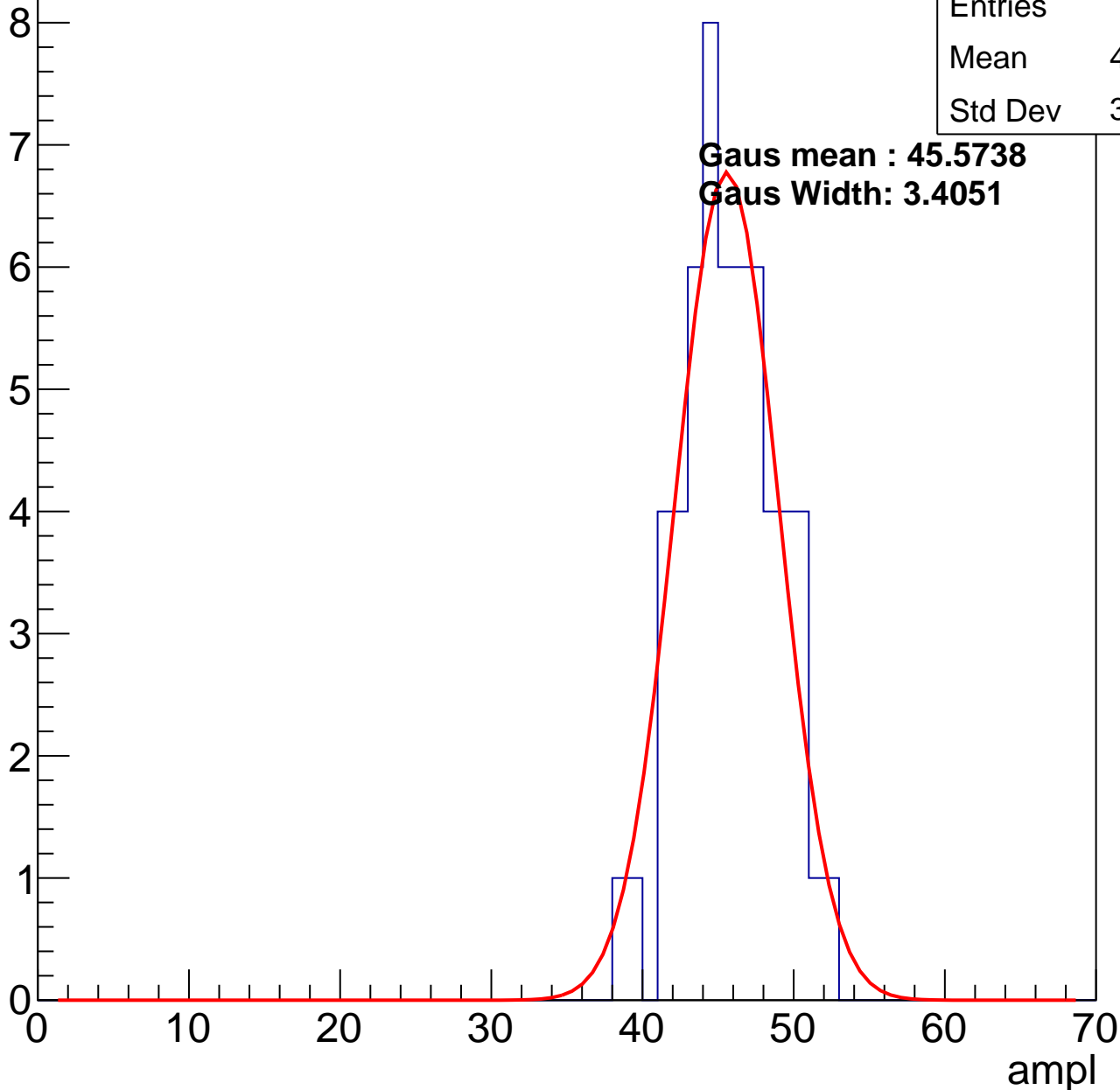
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 45.32 |
| Std Dev | 3.065 |

Gaus mean : 45.5738

Gaus Width: 3.4051

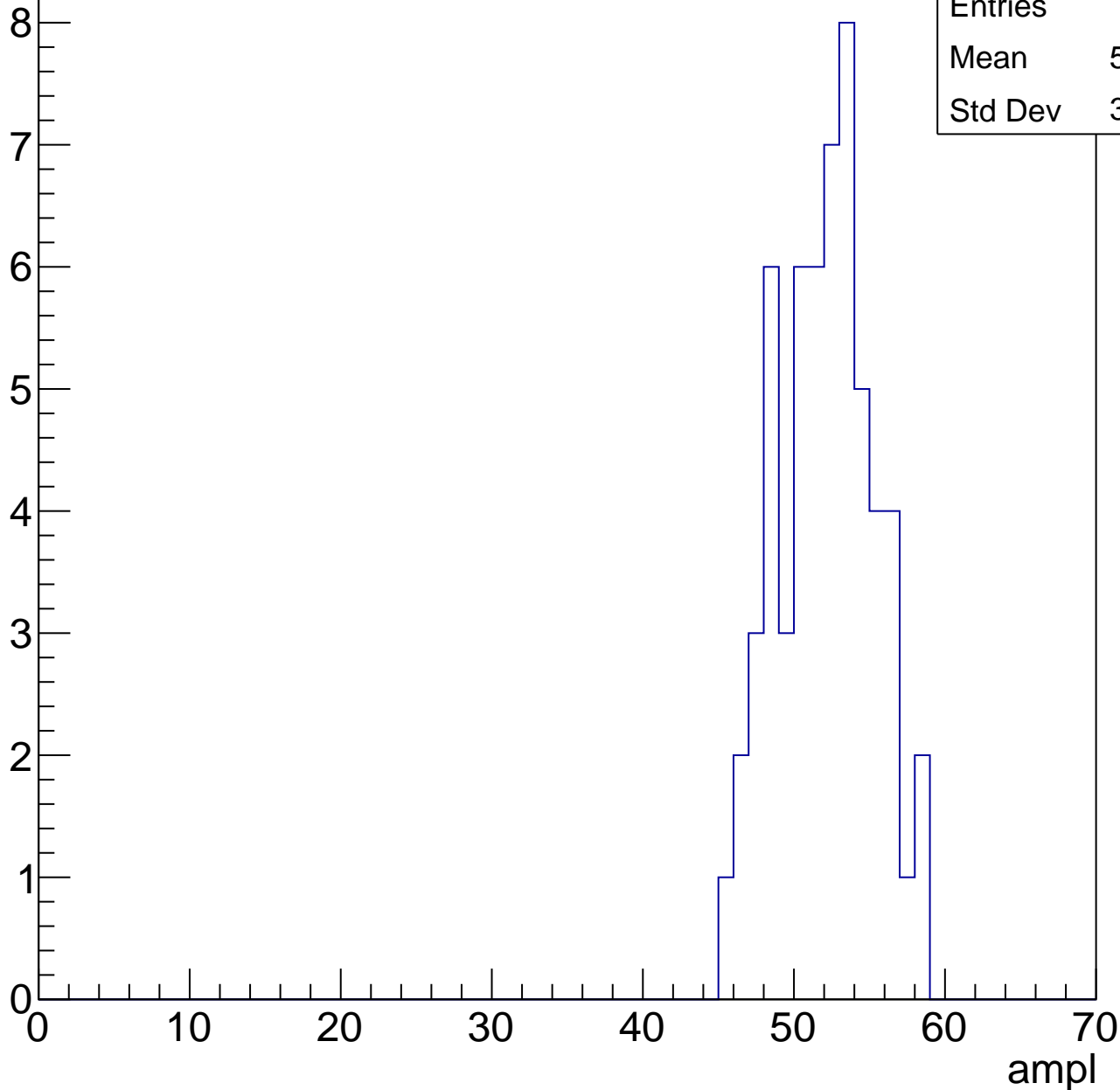


B1L100S, U9-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 51.62 |
| Std Dev | 3.123 |

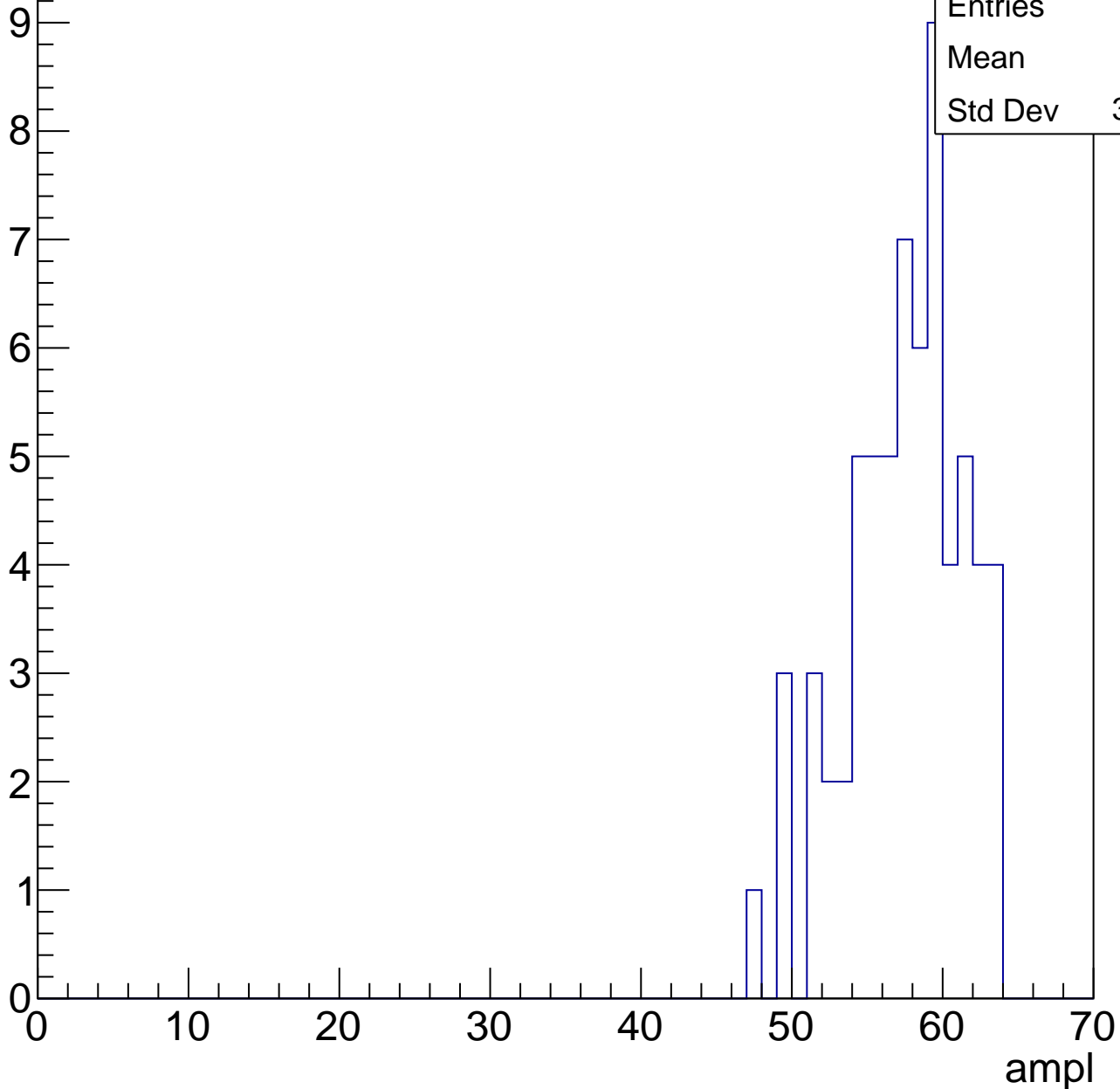


B1L100S, U9-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

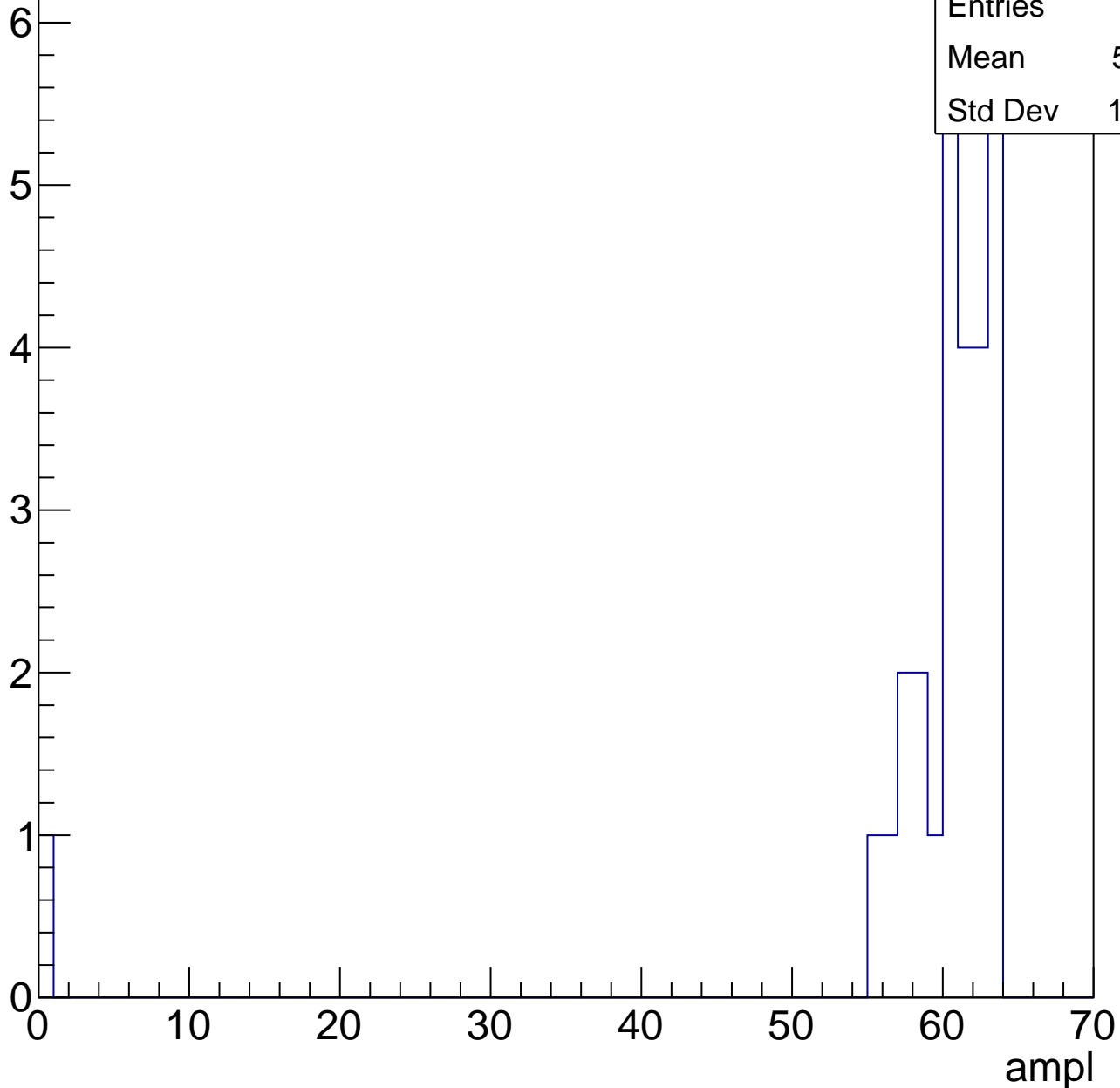
| | |
|---------|-------|
| Entries | 65 |
| Mean | 57 |
| Std Dev | 3.831 |



B1L100S, U9-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

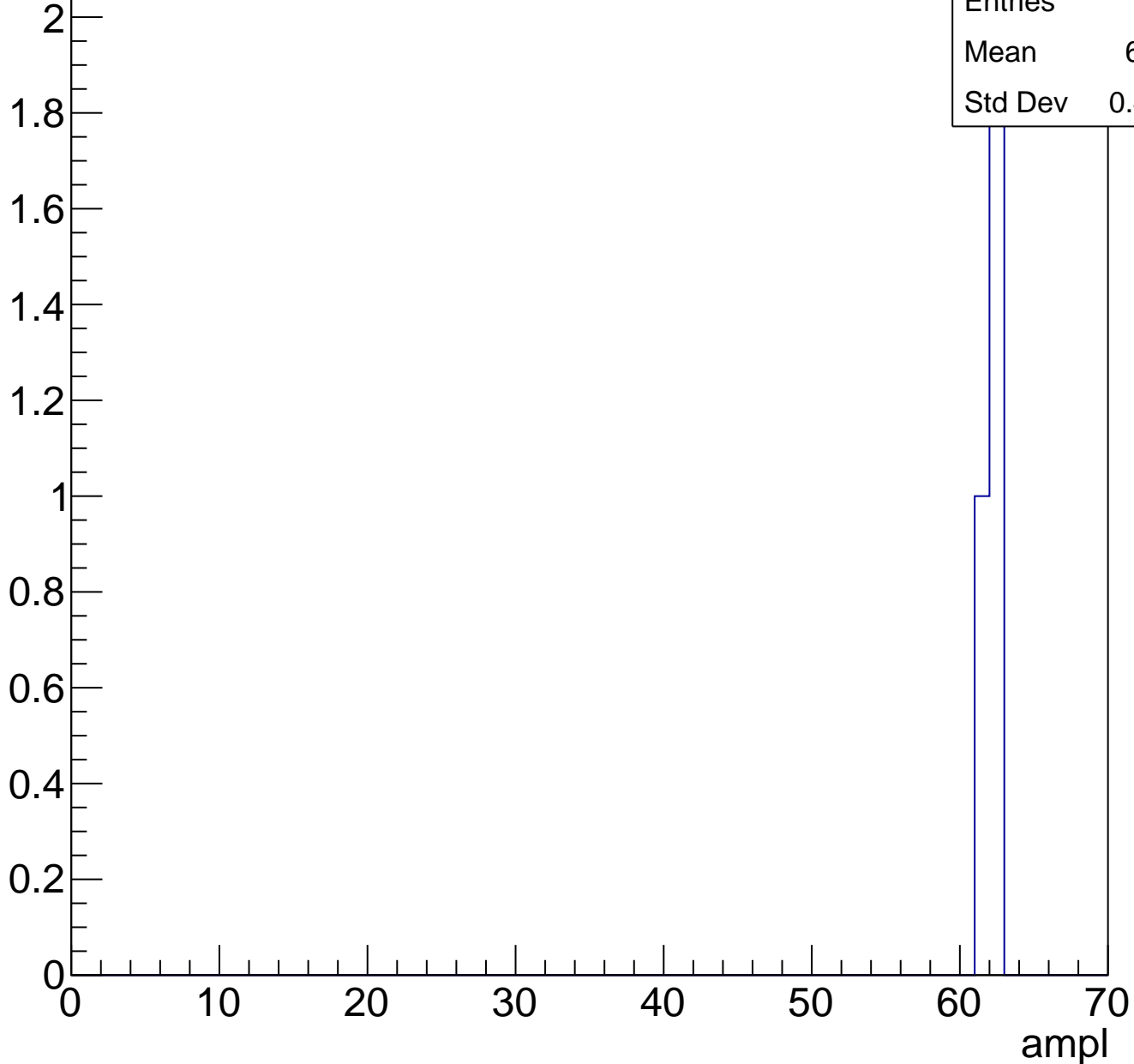
Entry



B1L100S, U9-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B1L100S, U9-ch118, adc0

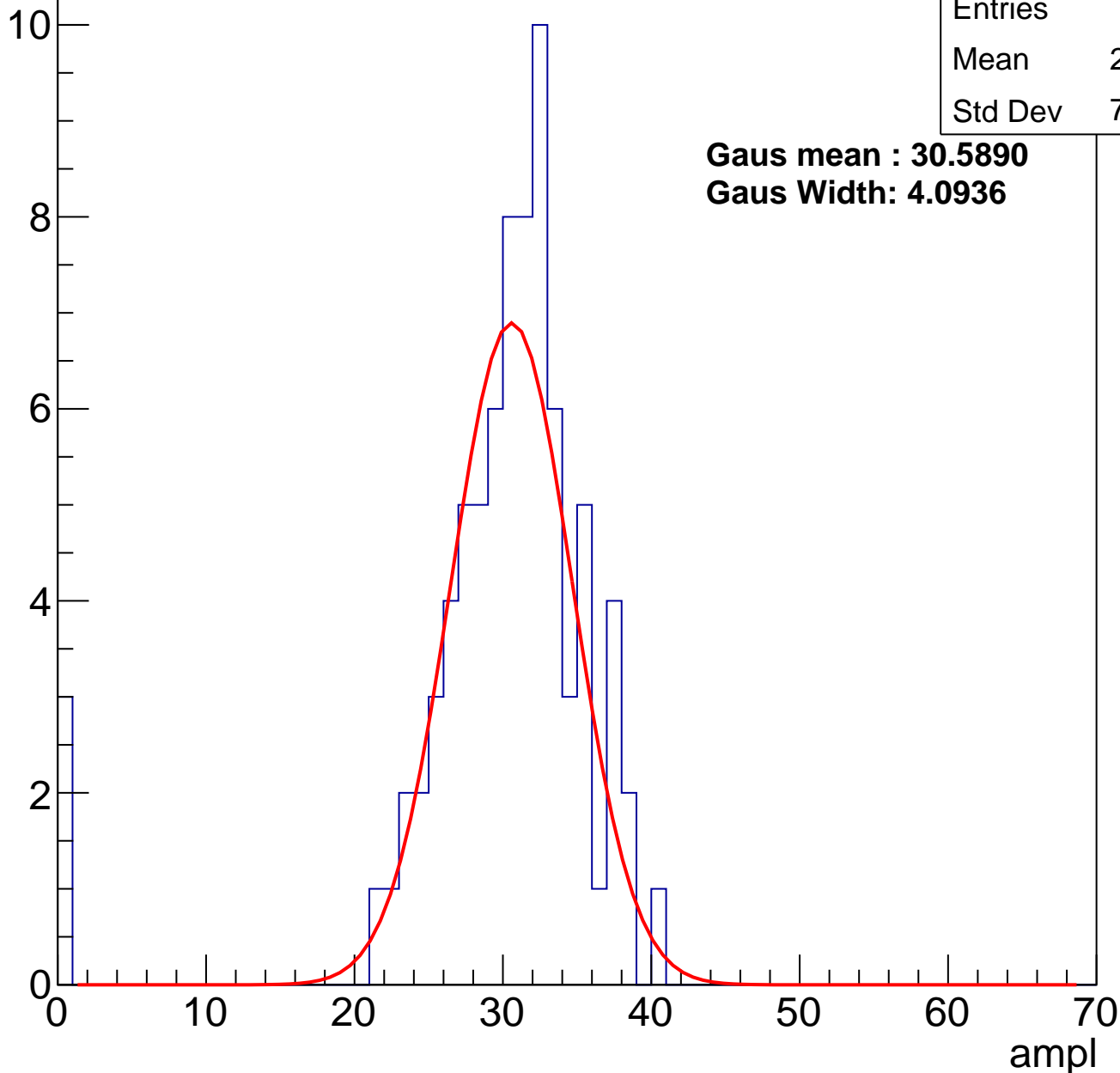
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 29.35 |
| Std Dev | 7.006 |

Gaus mean : 30.5890

Gaus Width: 4.0936

Entry



B1L100S, U9-ch118, adc1

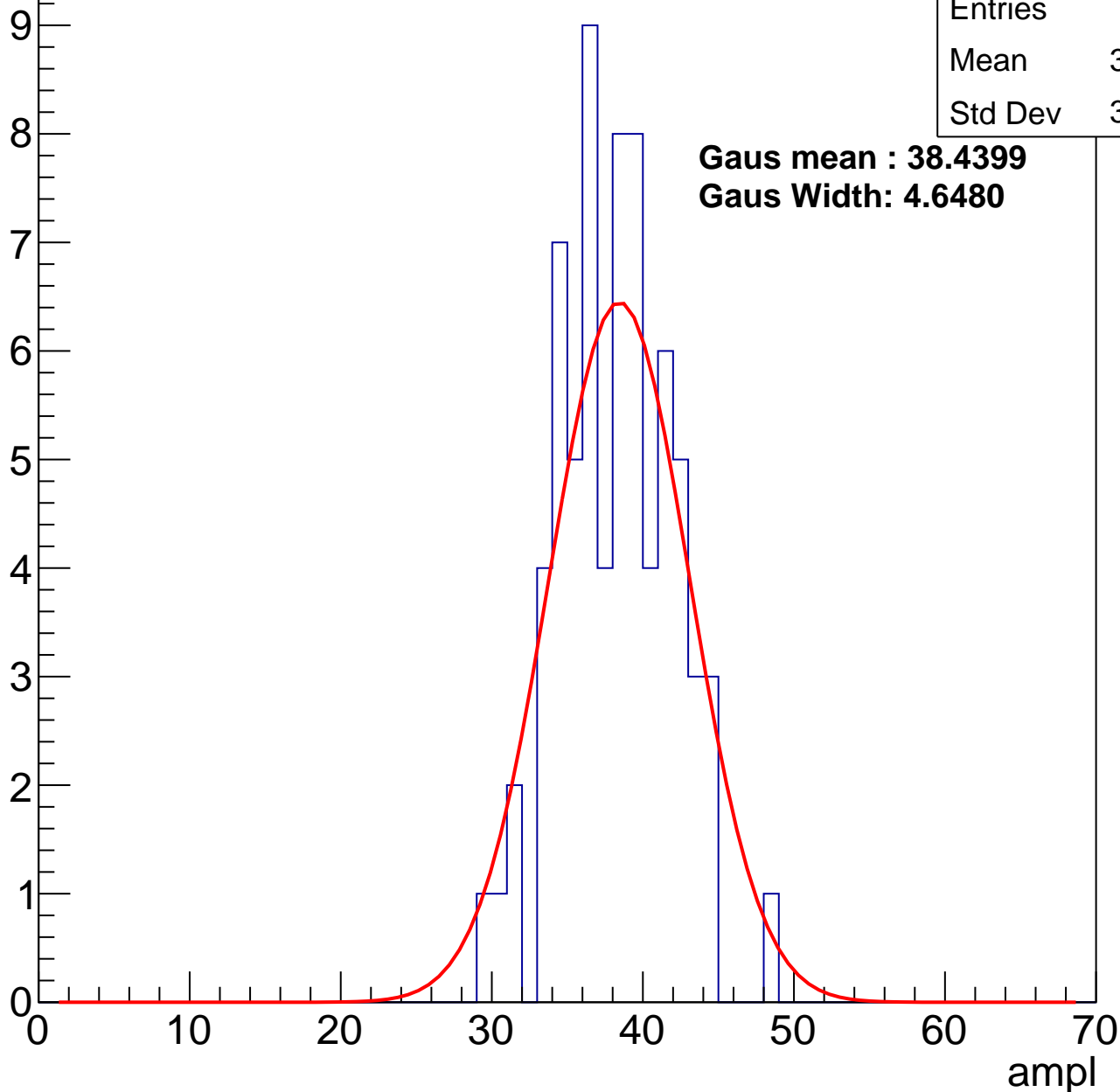
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.73 |
| Std Dev | 3.707 |

Gaus mean : 38.4399

Gaus Width: 4.6480



B1L100S, U9-ch118, adc2

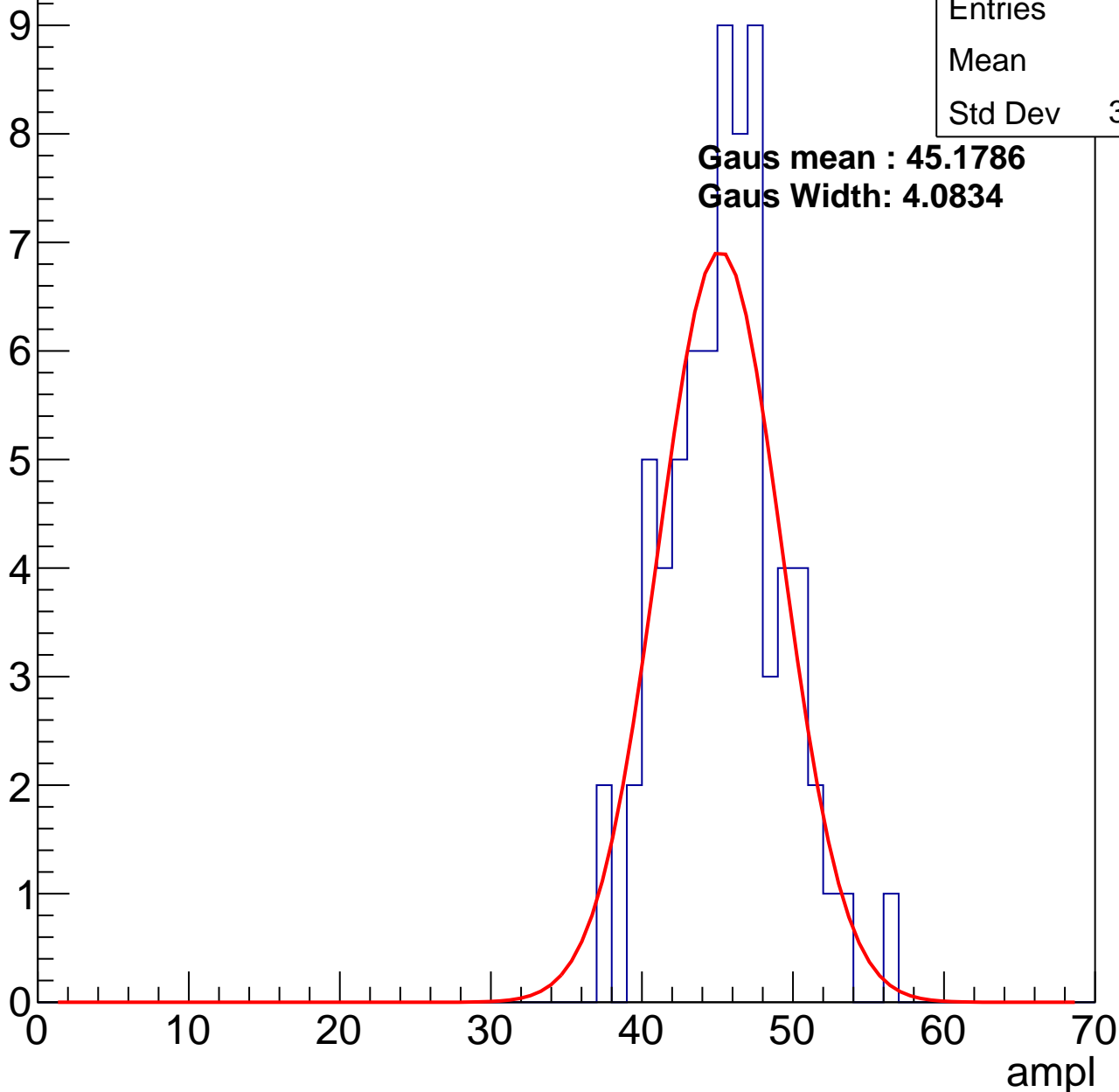
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 45.1 |
| Std Dev | 3.735 |

Gaus mean : 45.1786

Gaus Width: 4.0834

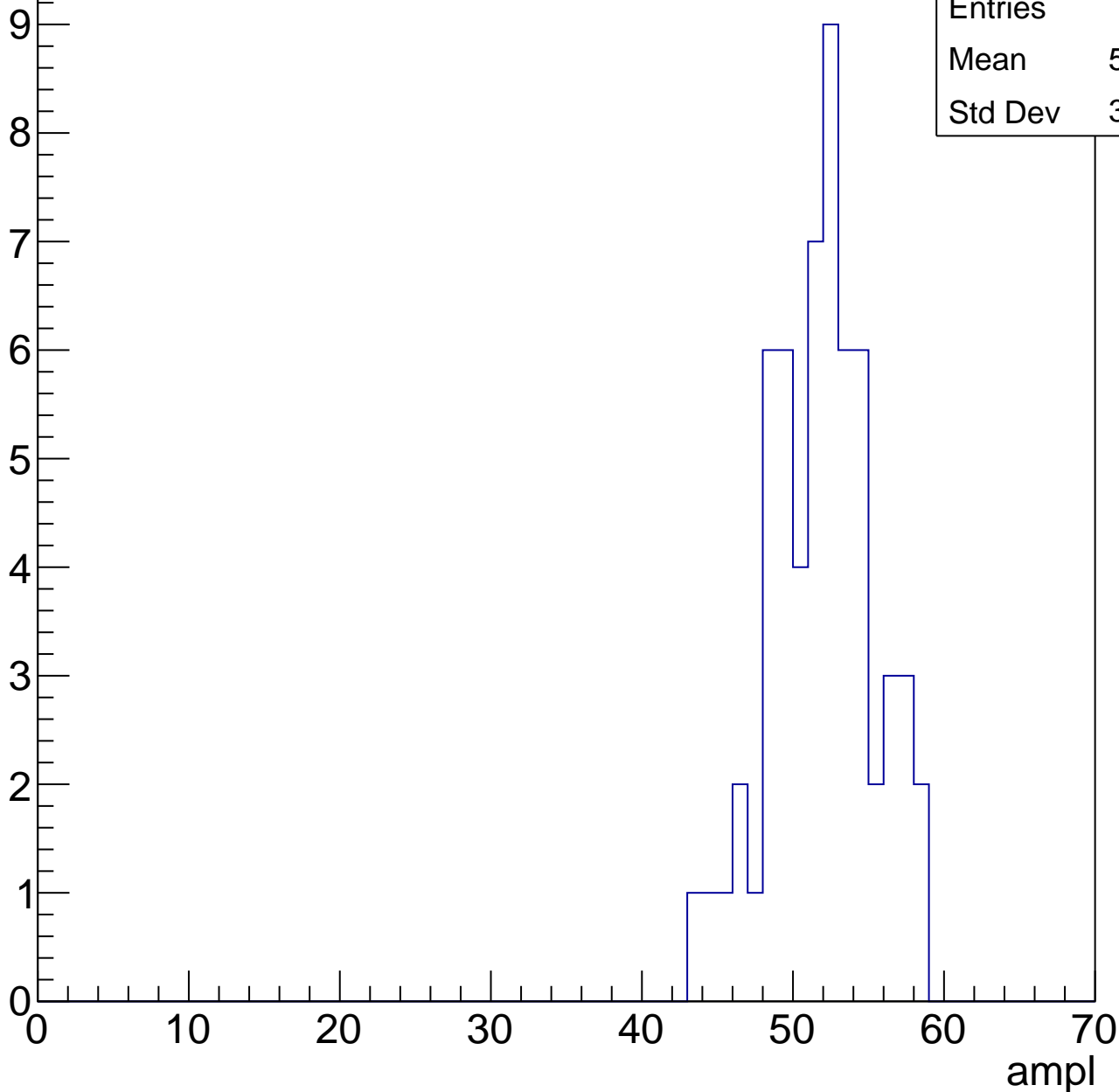


B1L100S, U9-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 51.42 |
| Std Dev | 3.383 |

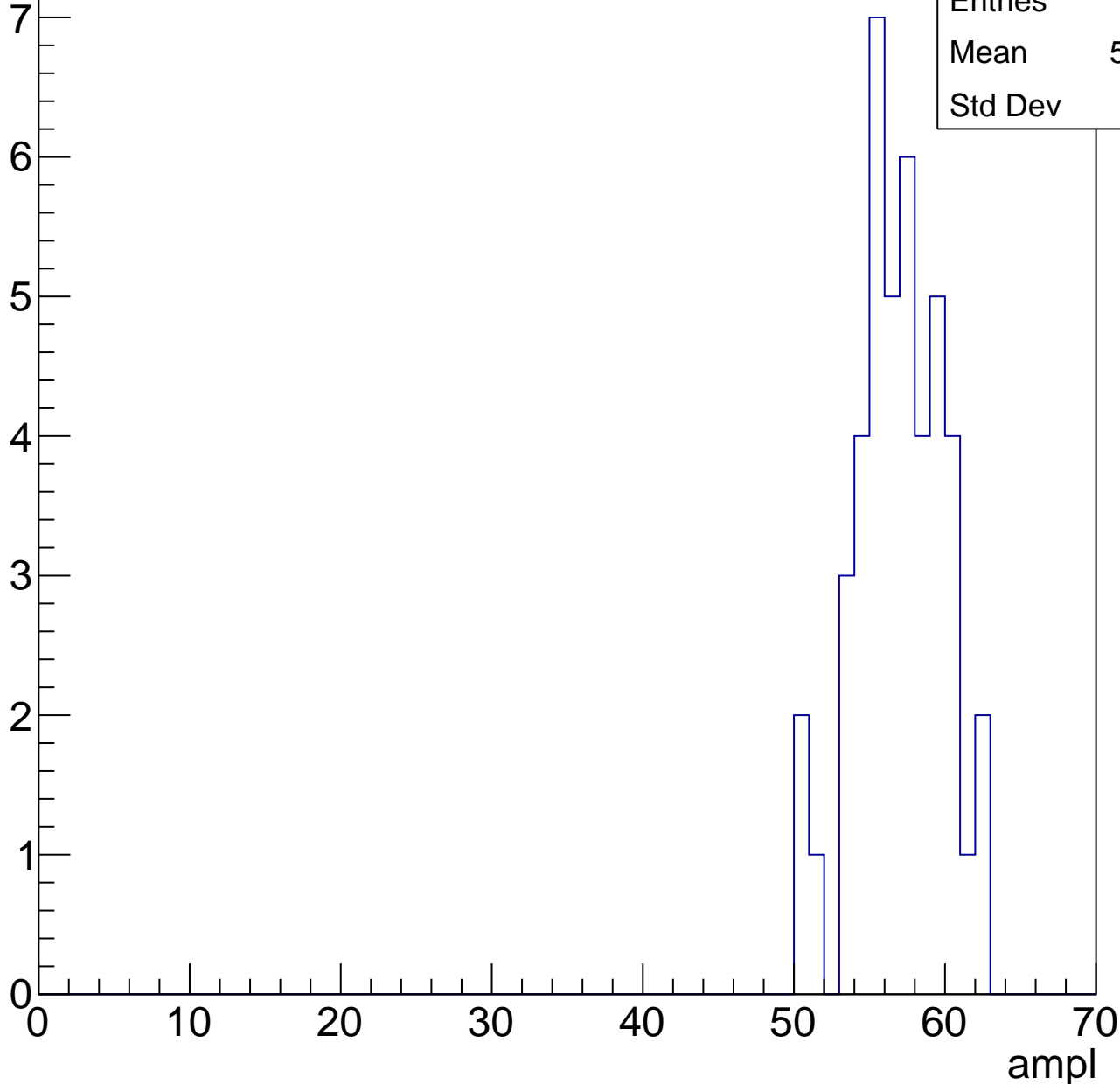


B1L100S, U9-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 56.48 |
| Std Dev | 2.88 |



B1L100S, U9-ch118, adc5

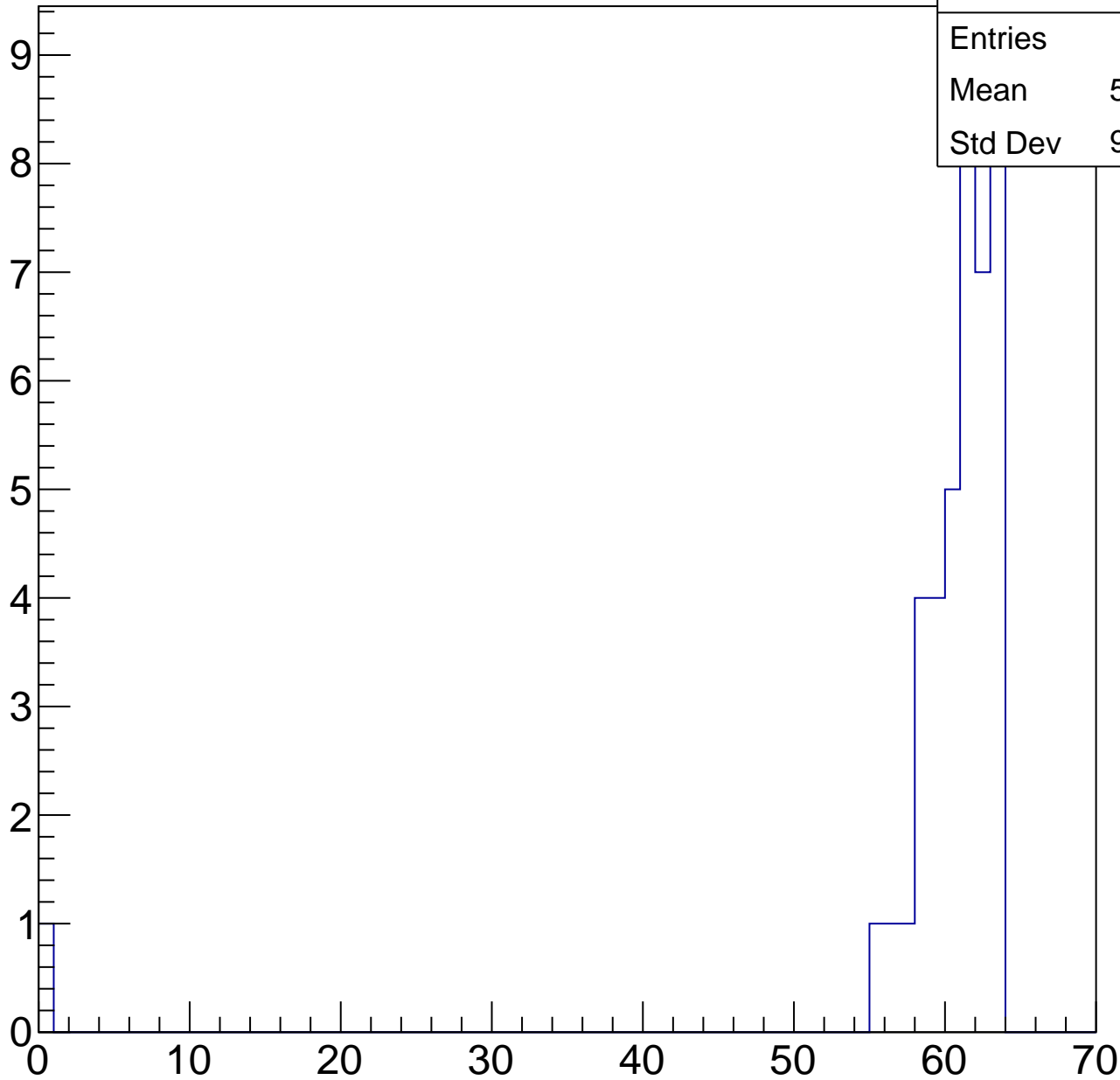
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 41 |
| Mean | 59.15 |
| Std Dev | 9.573 |

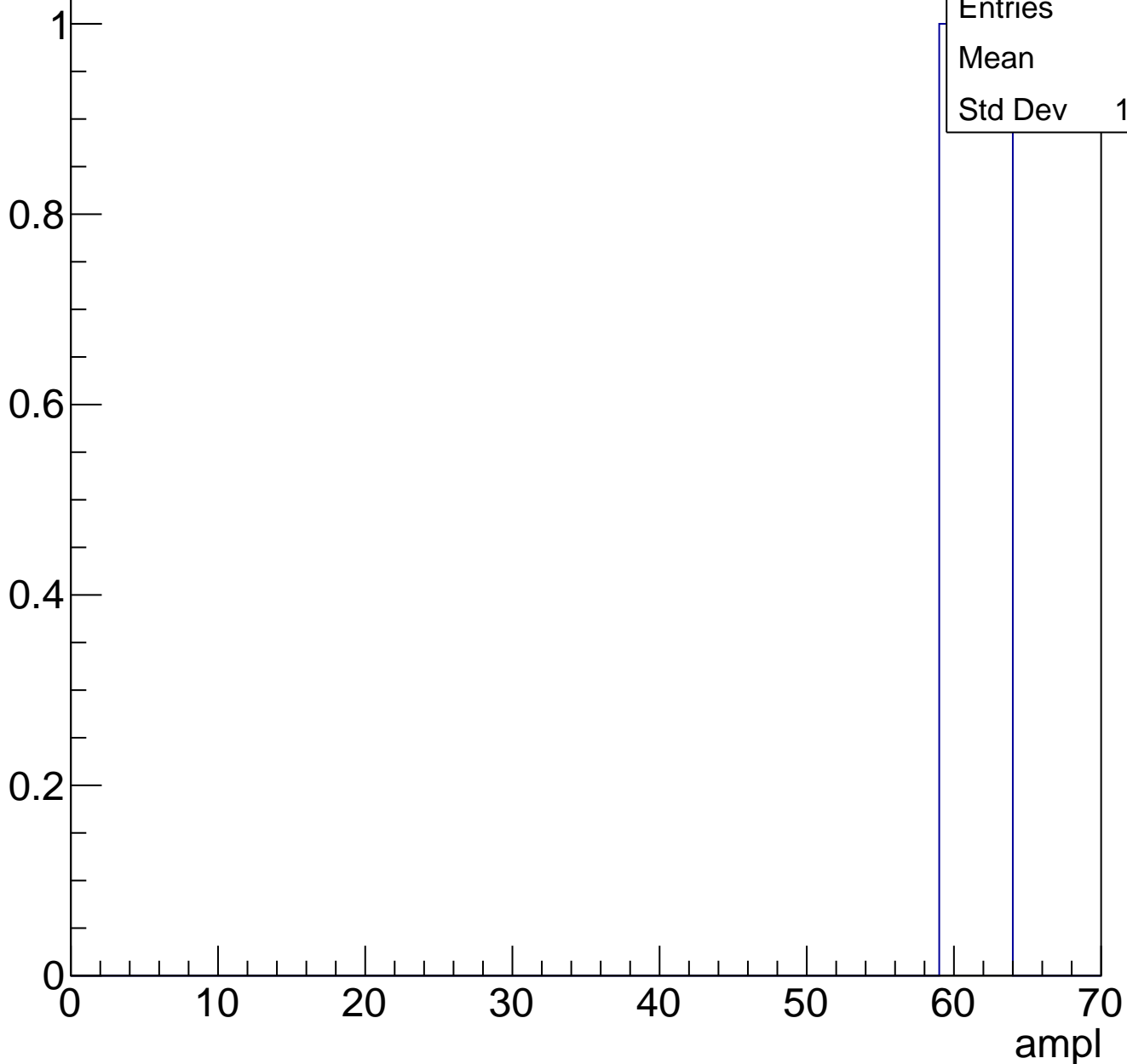
ampl



B1L100S, U9-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



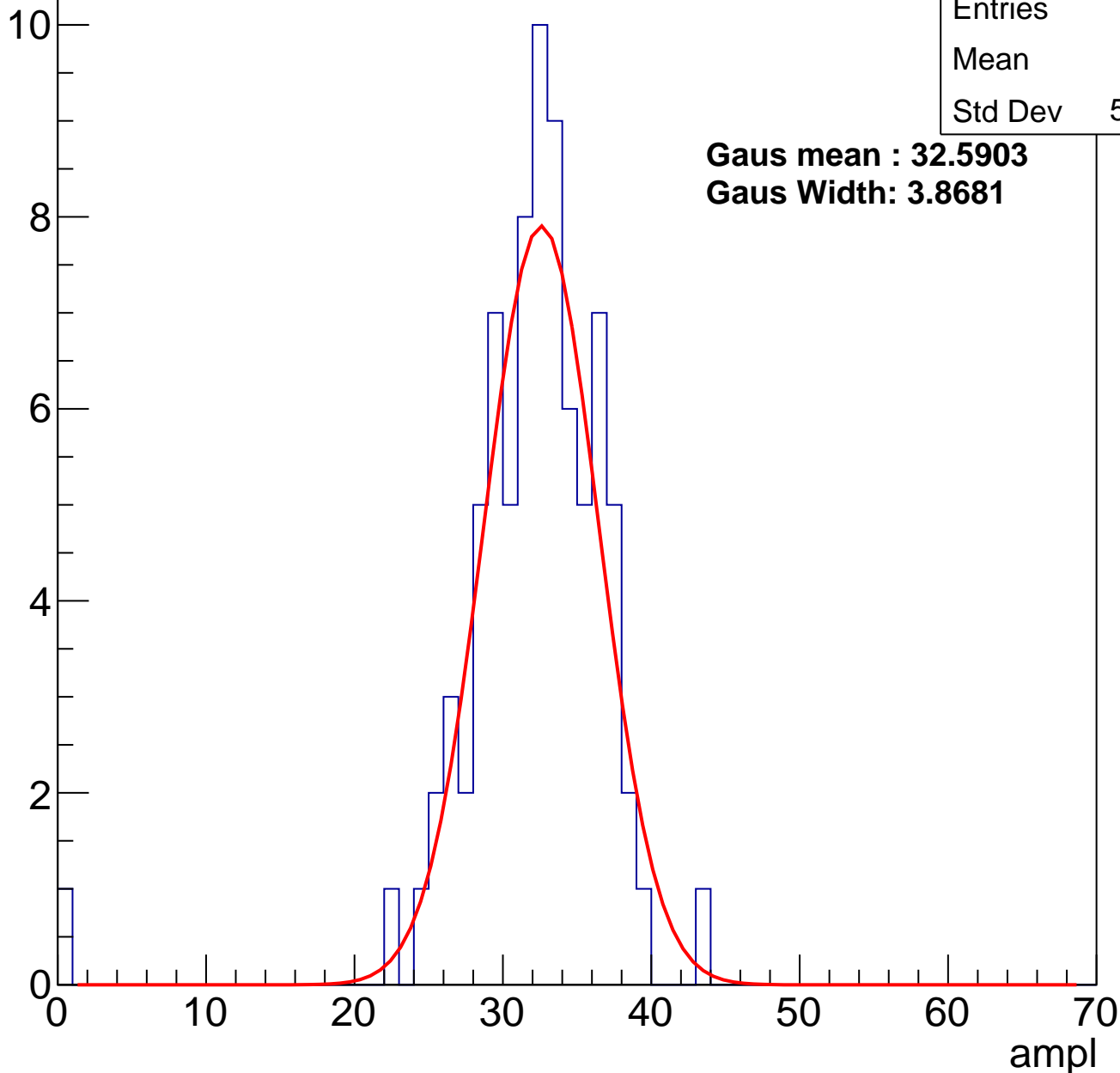
B1L100S, U9-ch119, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 31.6 |
| Std Dev | 5.163 |

Gaus mean : 32.5903
Gaus Width: 3.8681

Entry



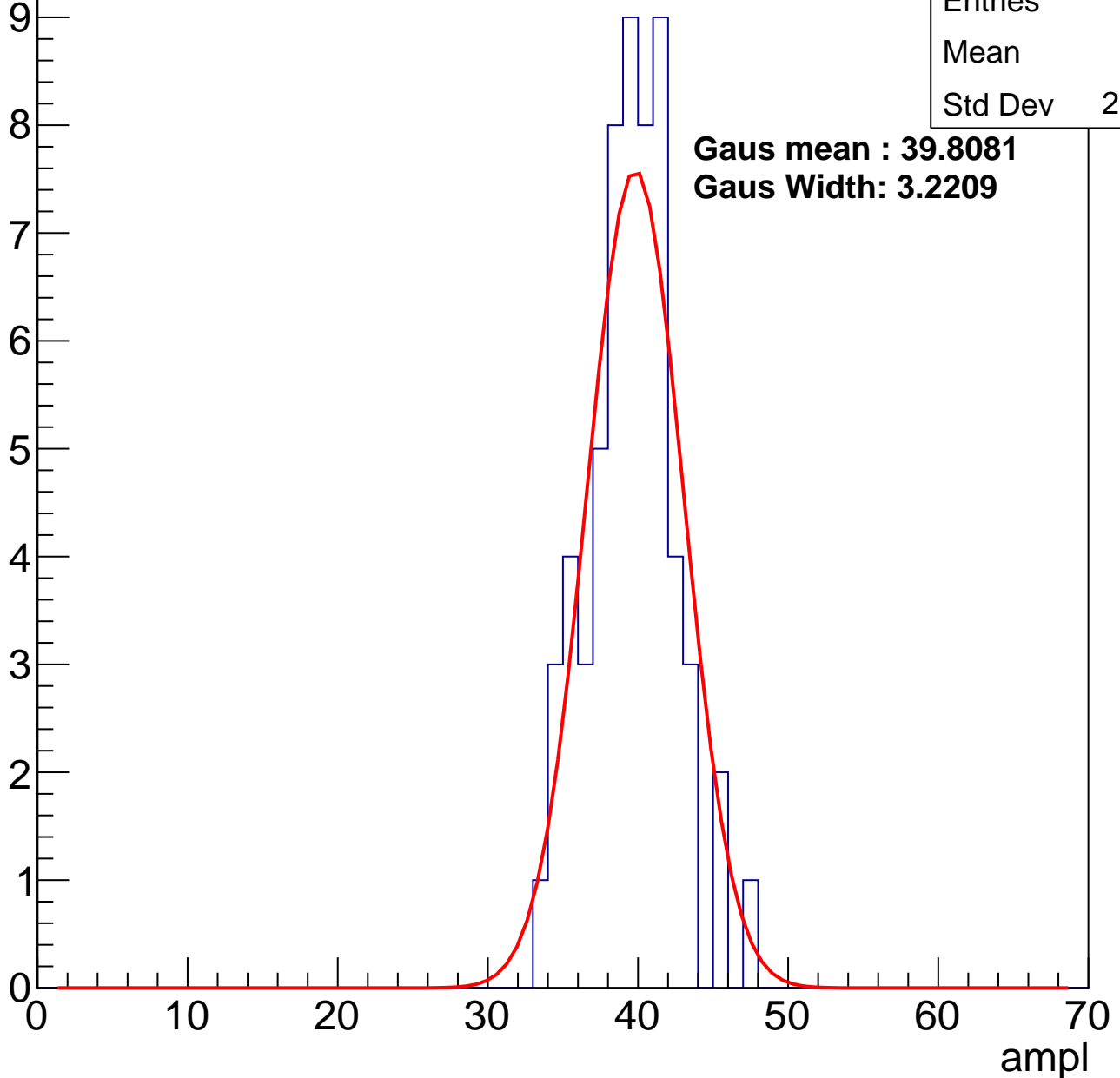
B1L100S, U9-ch119, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 39.1 |
| Std Dev | 2.868 |

Gaus mean : 39.8081
Gaus Width: 3.2209



B1L100S, U9-ch119, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 46 |
| Std Dev | 3.012 |

Entry

Gaus mean : 46.2861

Gaus Width: 2.7909

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

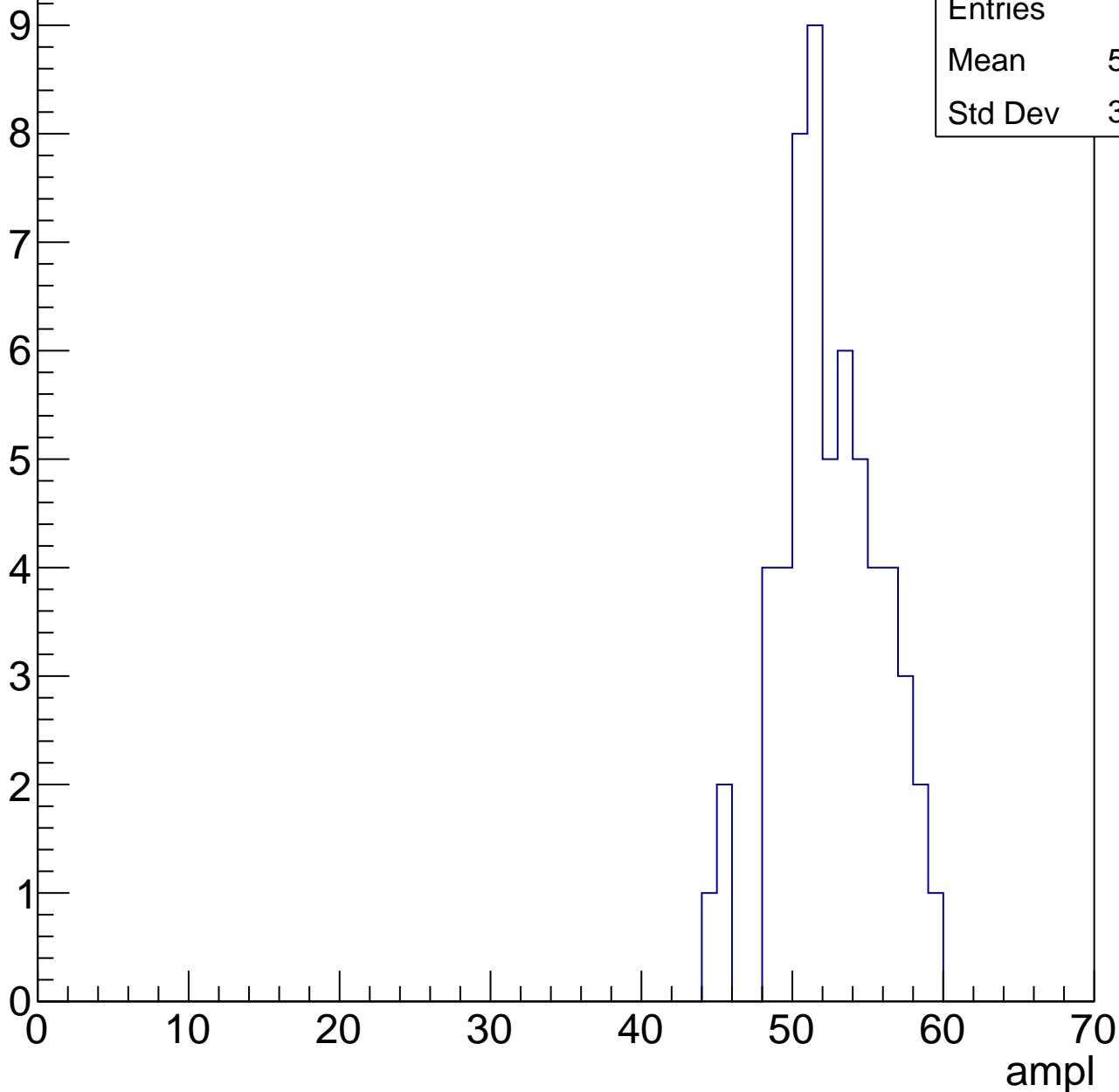
10

B1L100S, U9-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

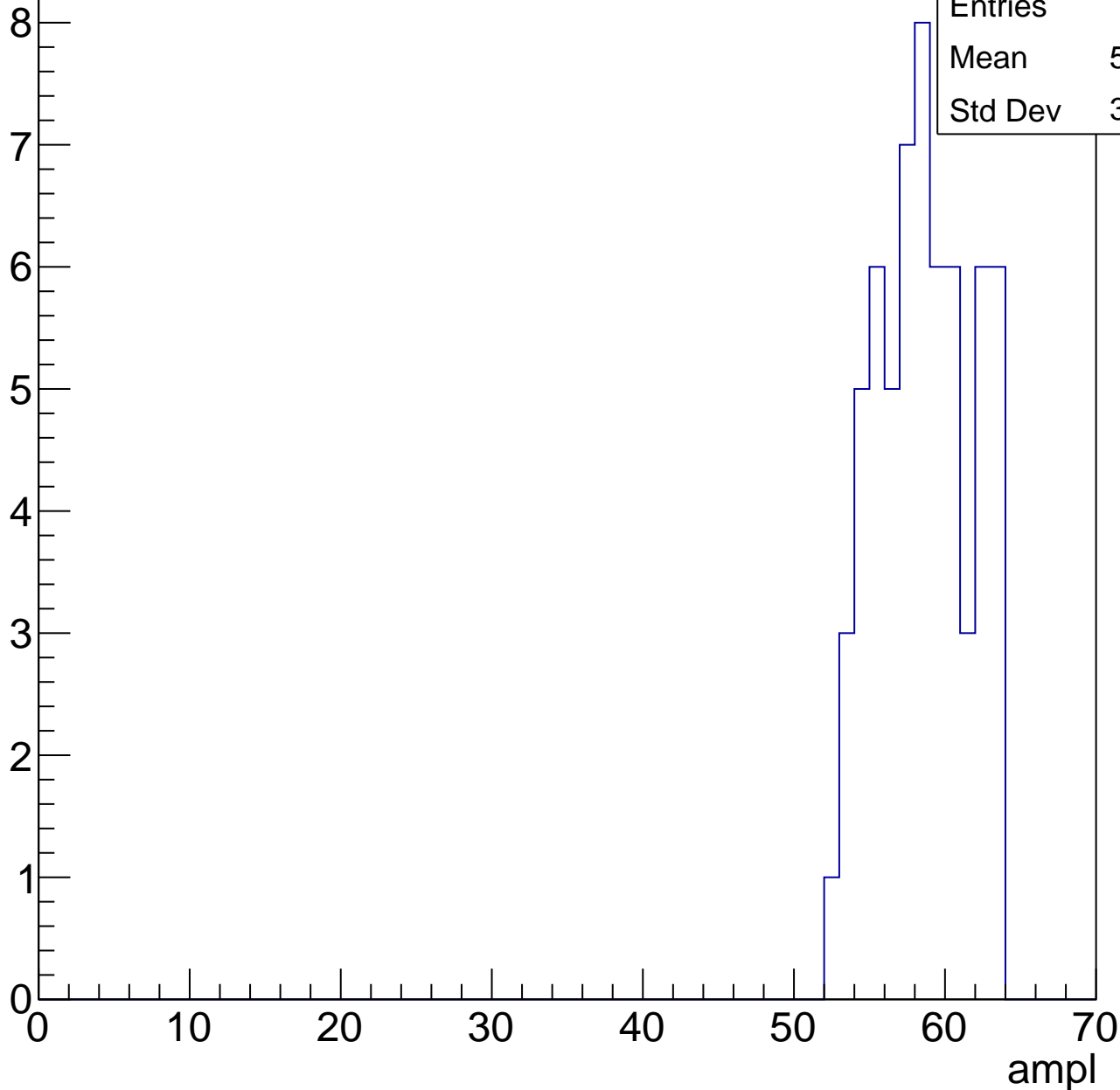
| | |
|---------|-------|
| Entries | 58 |
| Mean | 52.05 |
| Std Dev | 3.282 |



B1L100S, U9-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

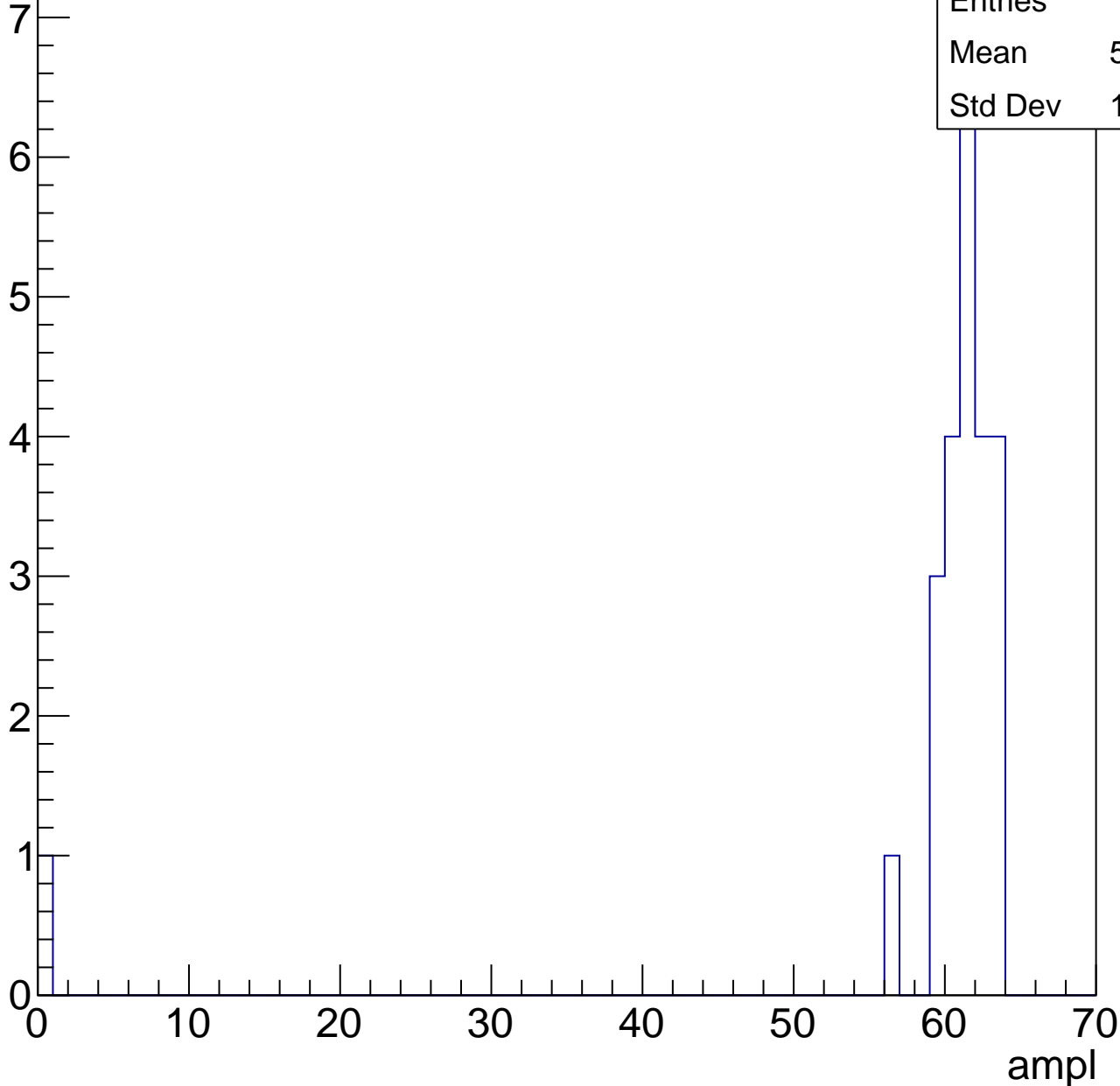


B1L100S, U9-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.33 |
| Std Dev | 12.27 |



B1L100S, U9-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch120, adc0

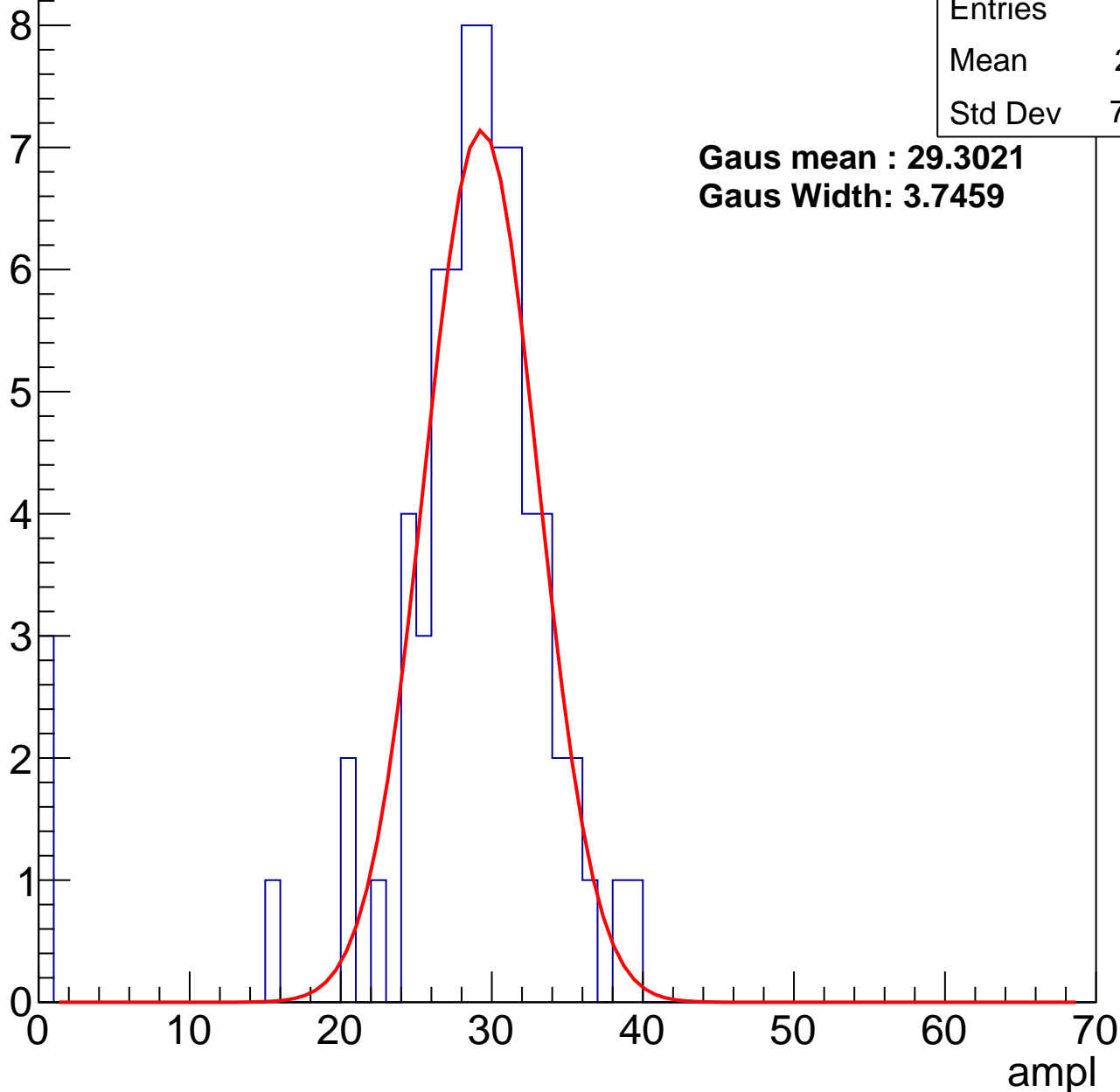
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 27.61 |
| Std Dev | 7.034 |

Gaus mean : 29.3021

Gaus Width: 3.7459



B1L100S, U9-ch120, adc1

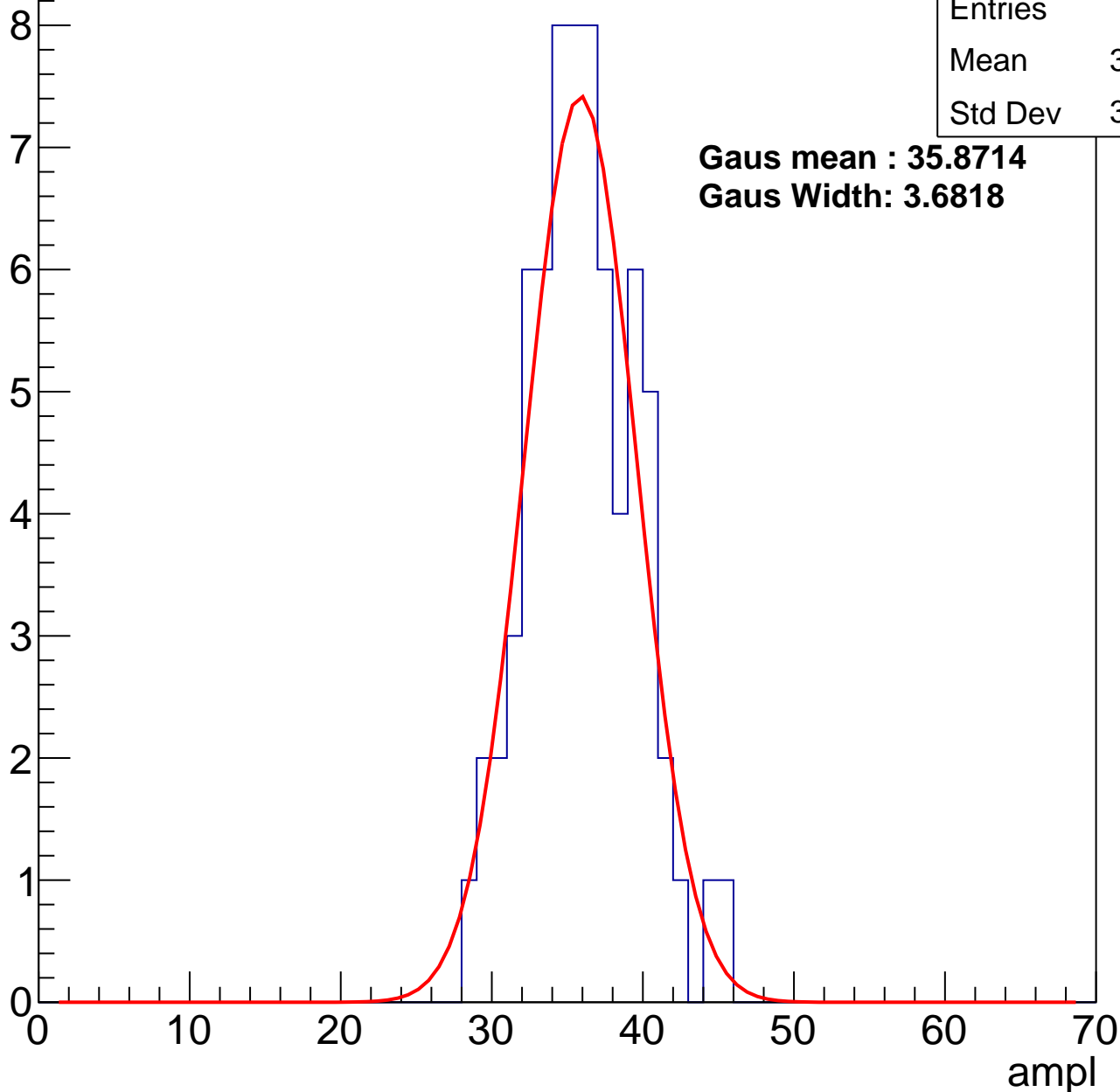
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.57 |
| Std Dev | 3.532 |

Gaus mean : 35.8714

Gaus Width: 3.6818



B1L100S, U9-ch120, adc2

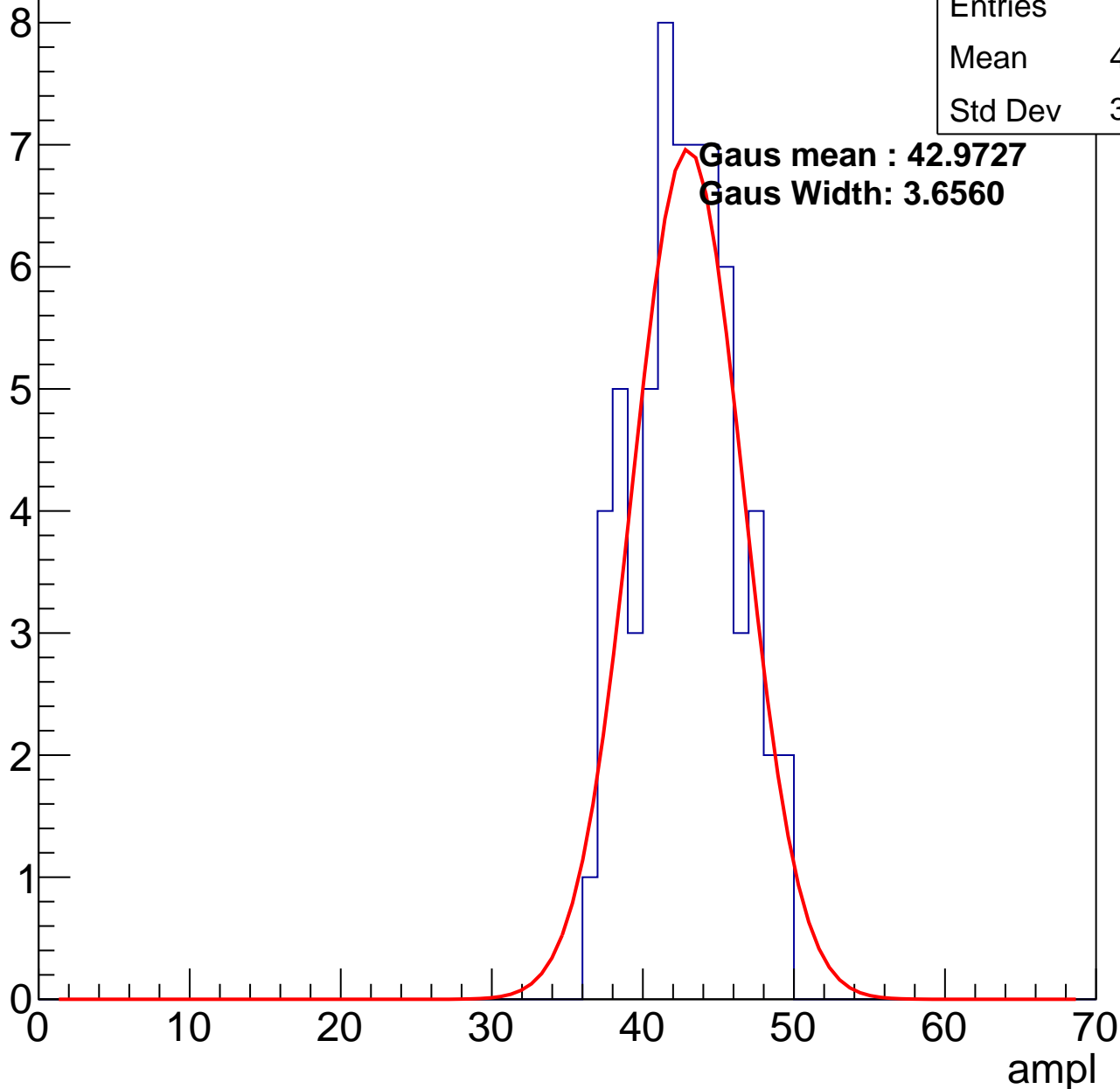
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.38 |
| Std Dev | 3.233 |

Gaus mean : 42.9727

Gaus Width: 3.6560

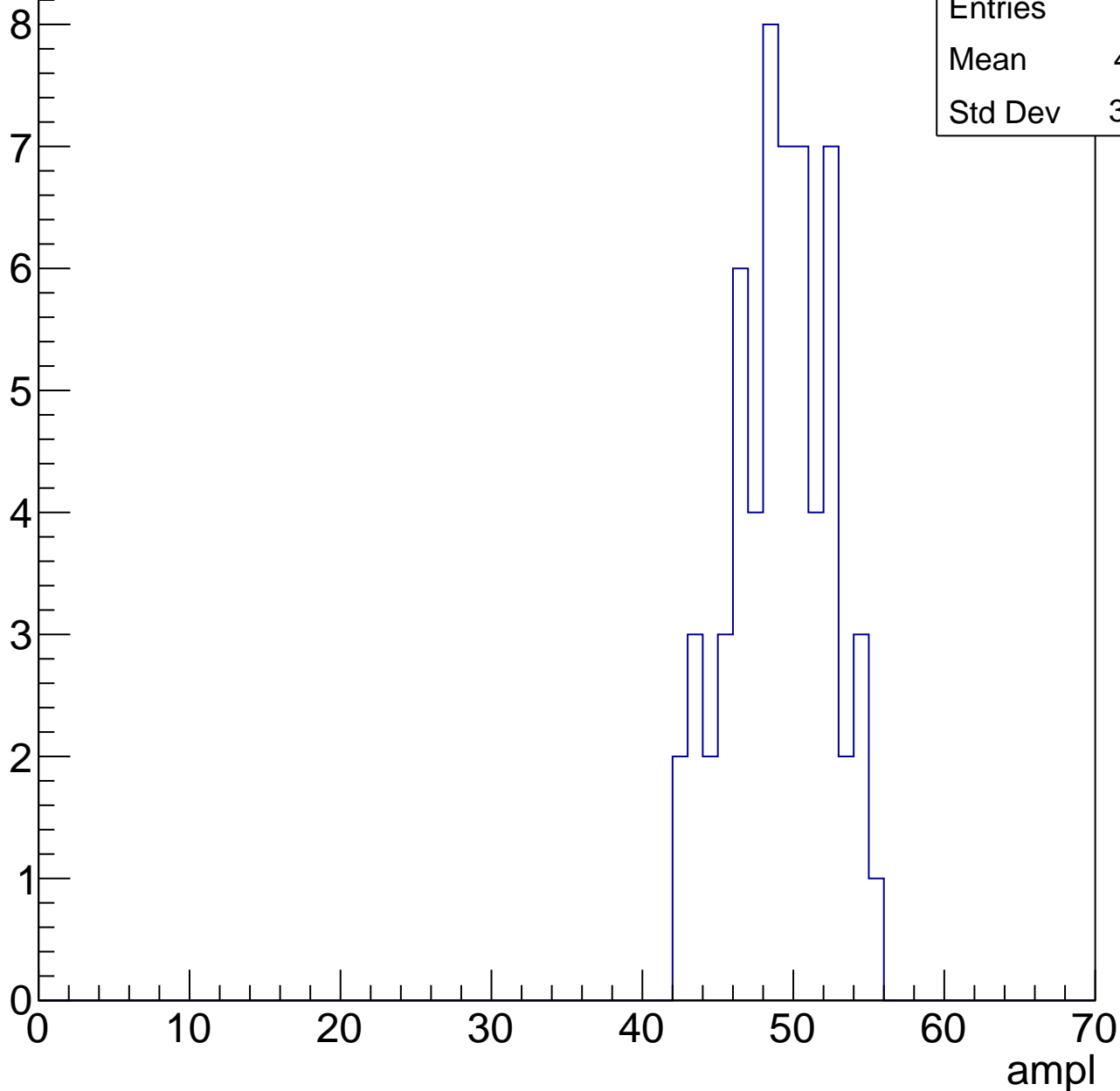


B1L100S, U9-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 48.61 |
| Std Dev | 3.194 |

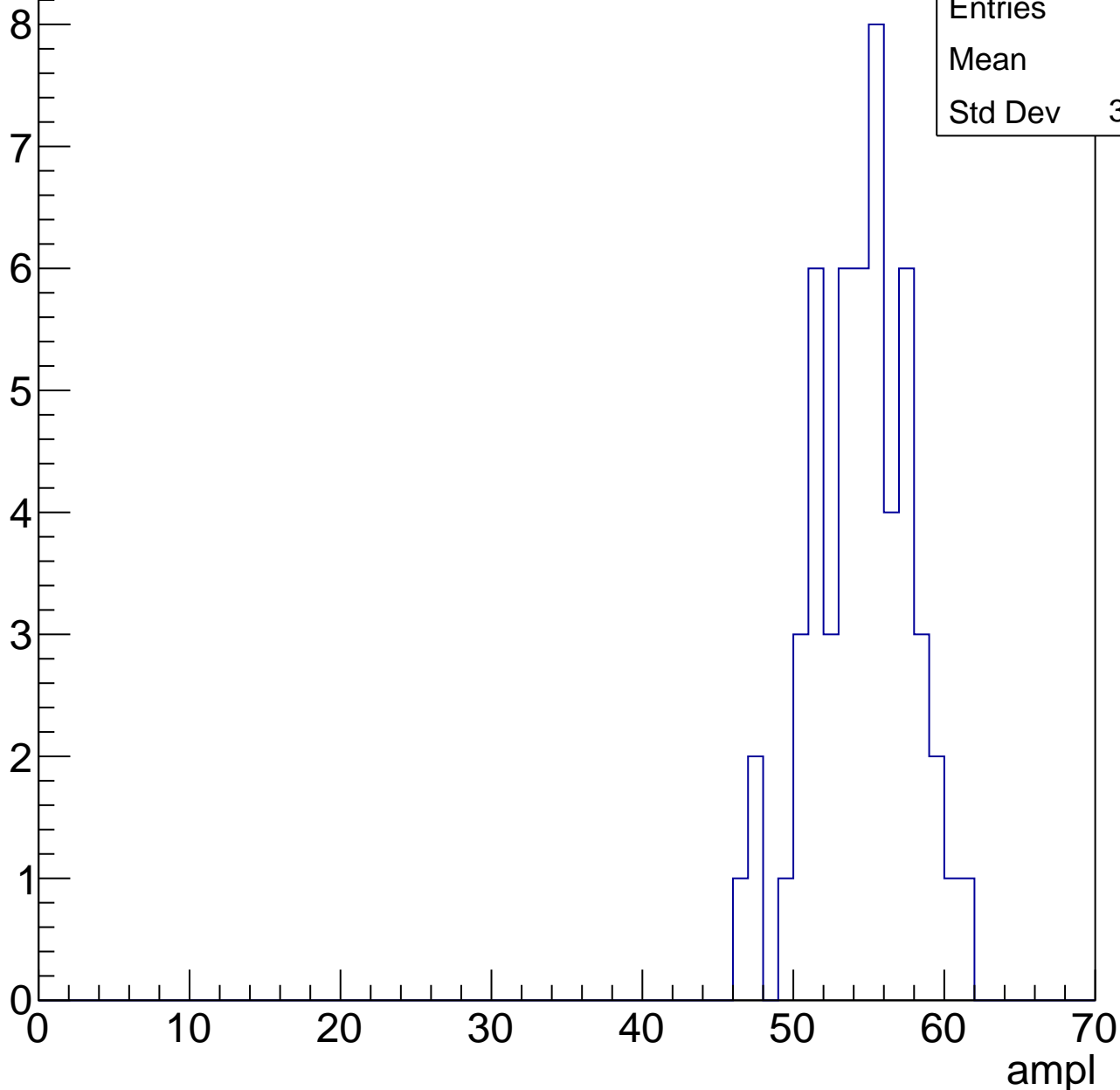


B1L100S, U9-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

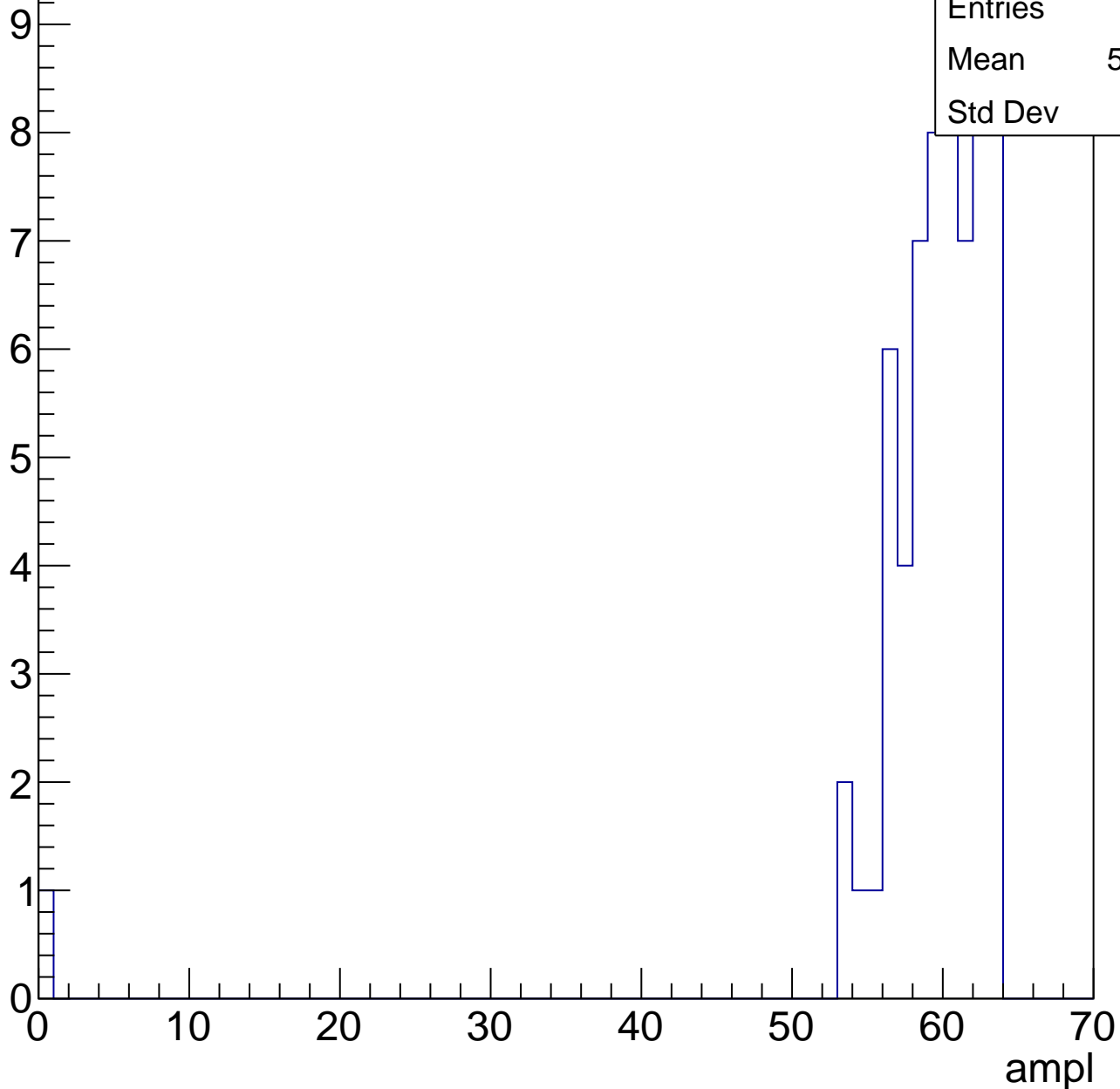
| | |
|---------|-------|
| Entries | 53 |
| Mean | 54 |
| Std Dev | 3.274 |



B1L100S, U9-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 6 |
| Mean | 62 |
| Std Dev | 0.8165 |

ampl

B1L100S, U9-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch121, adc0

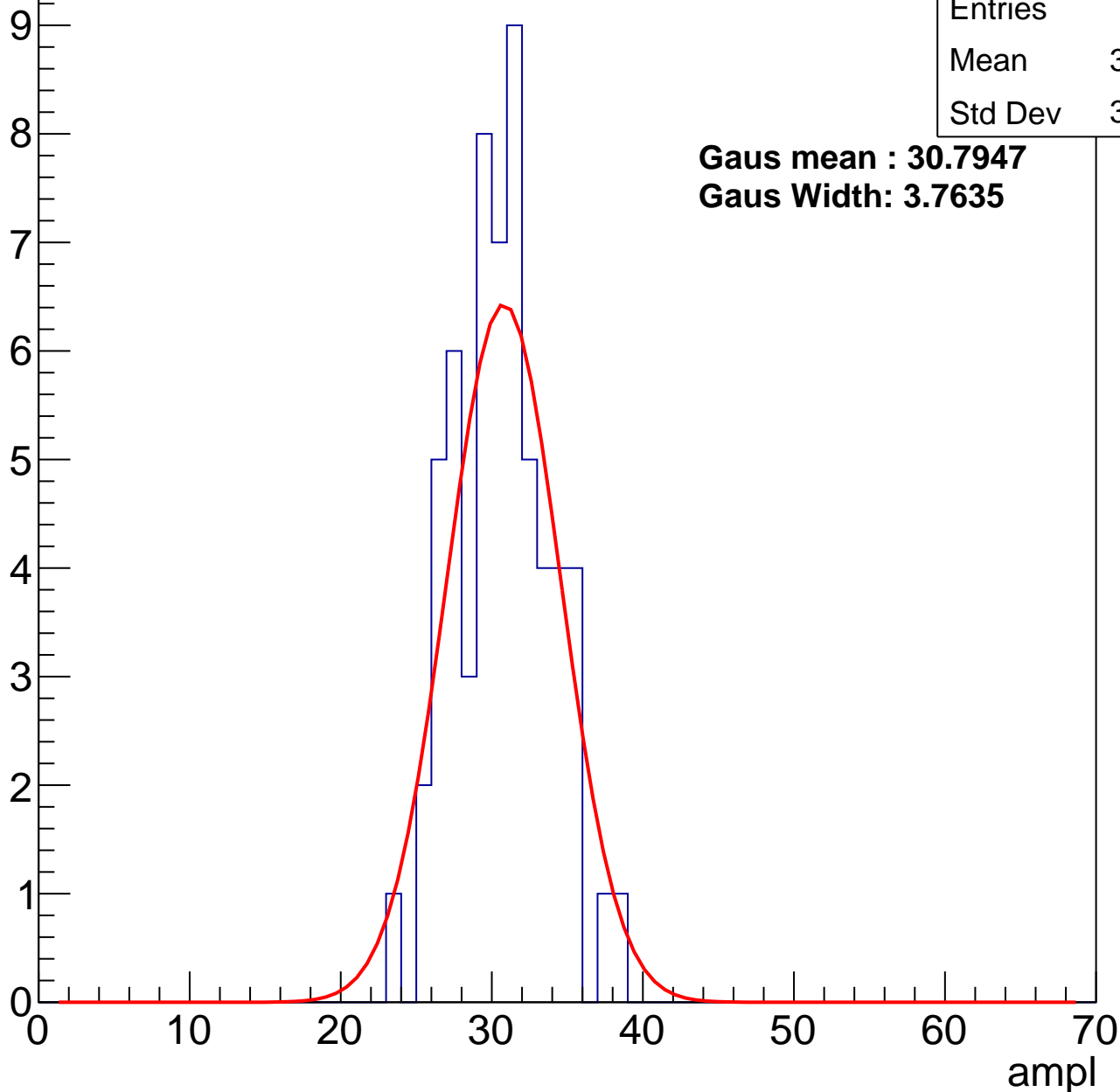
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 30.22 |
| Std Dev | 3.142 |

Gaus mean : 30.7947

Gaus Width: 3.7635



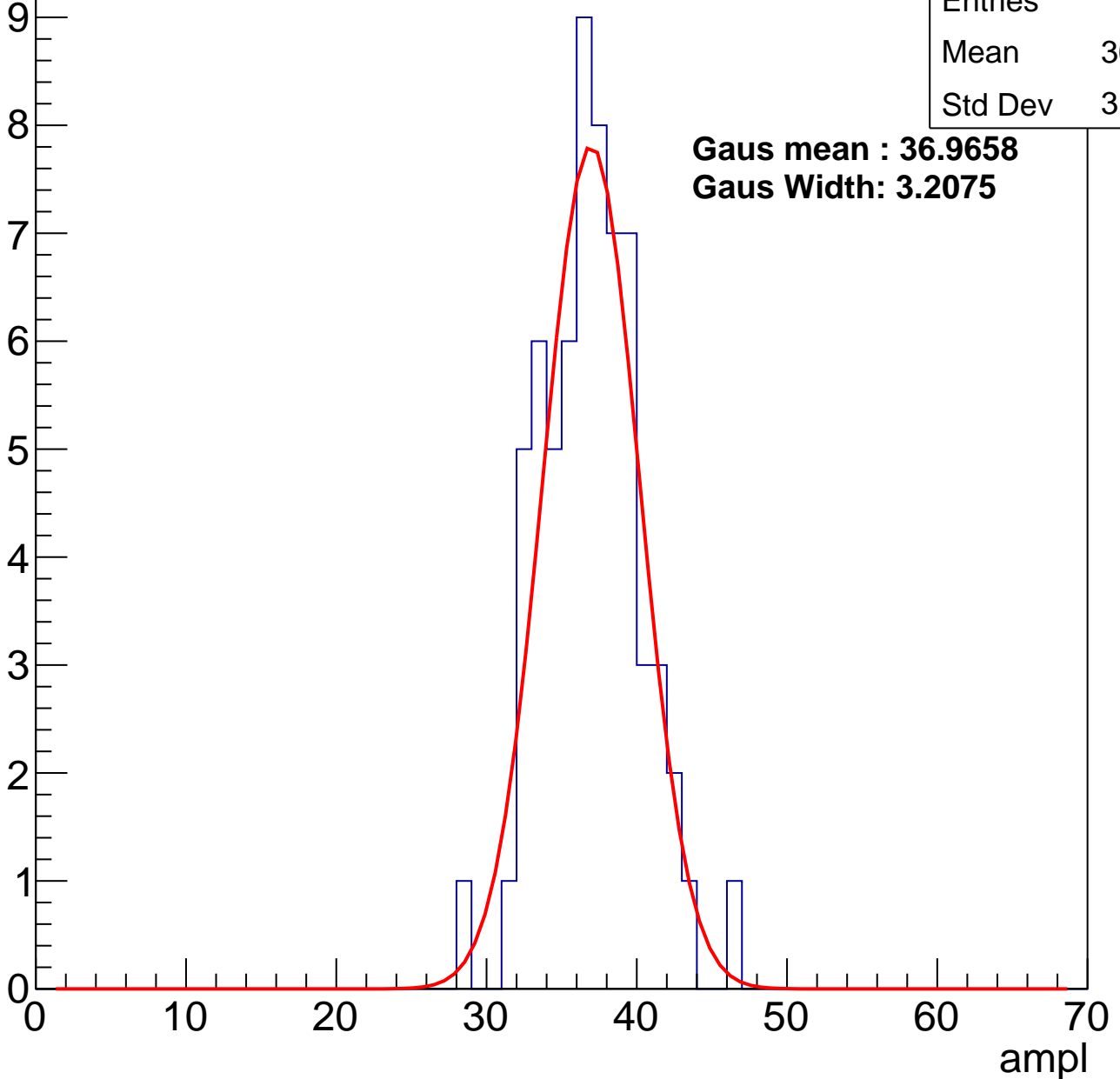
B1L100S, U9-ch121, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.49 |
| Std Dev | 3.225 |

Gaus mean : 36.9658
Gaus Width: 3.2075



B1L100S, U9-ch121, adc2

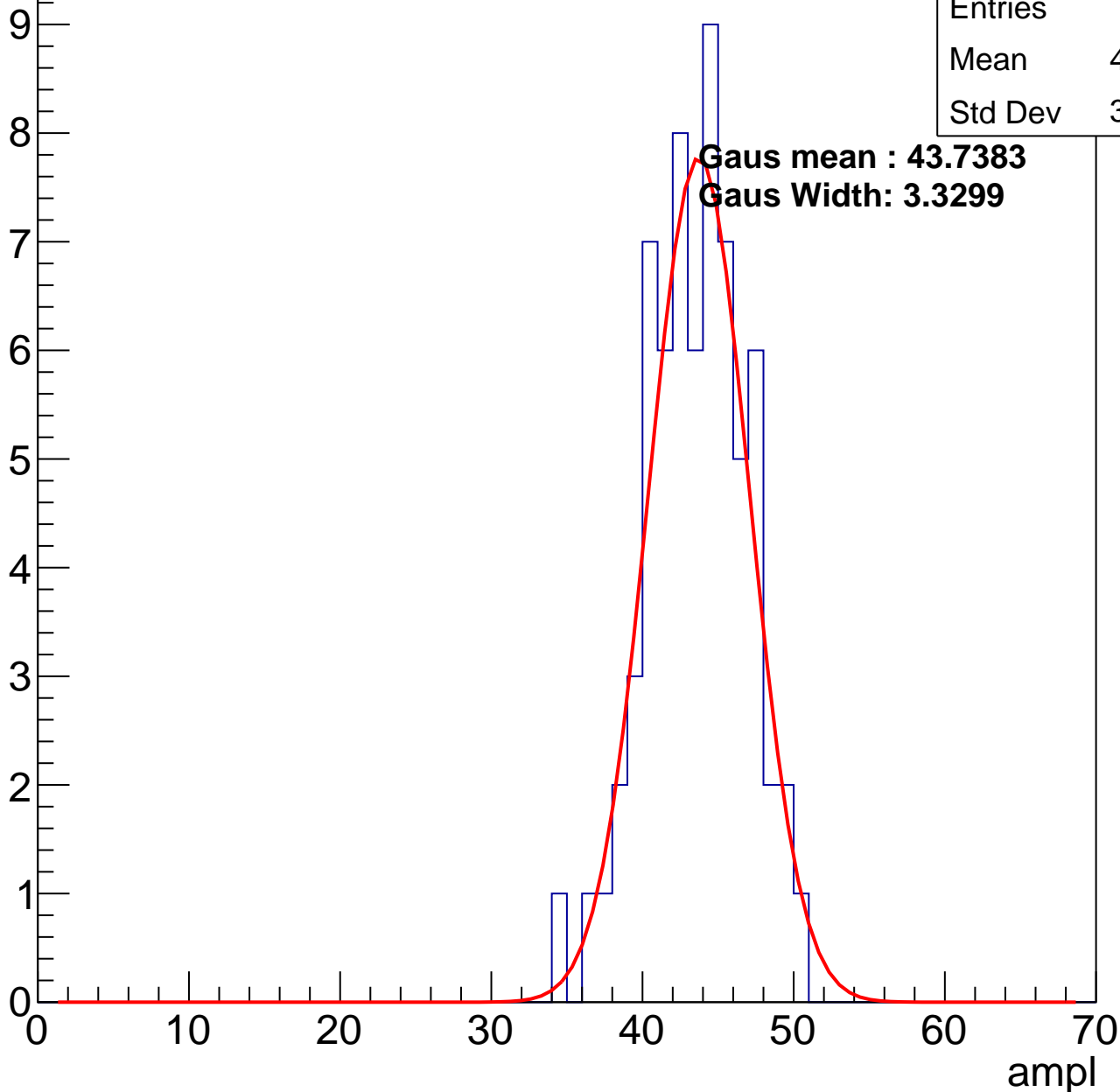
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.09 |
| Std Dev | 3.249 |

Gaus mean : 43.7383

Gaus Width: 3.3299

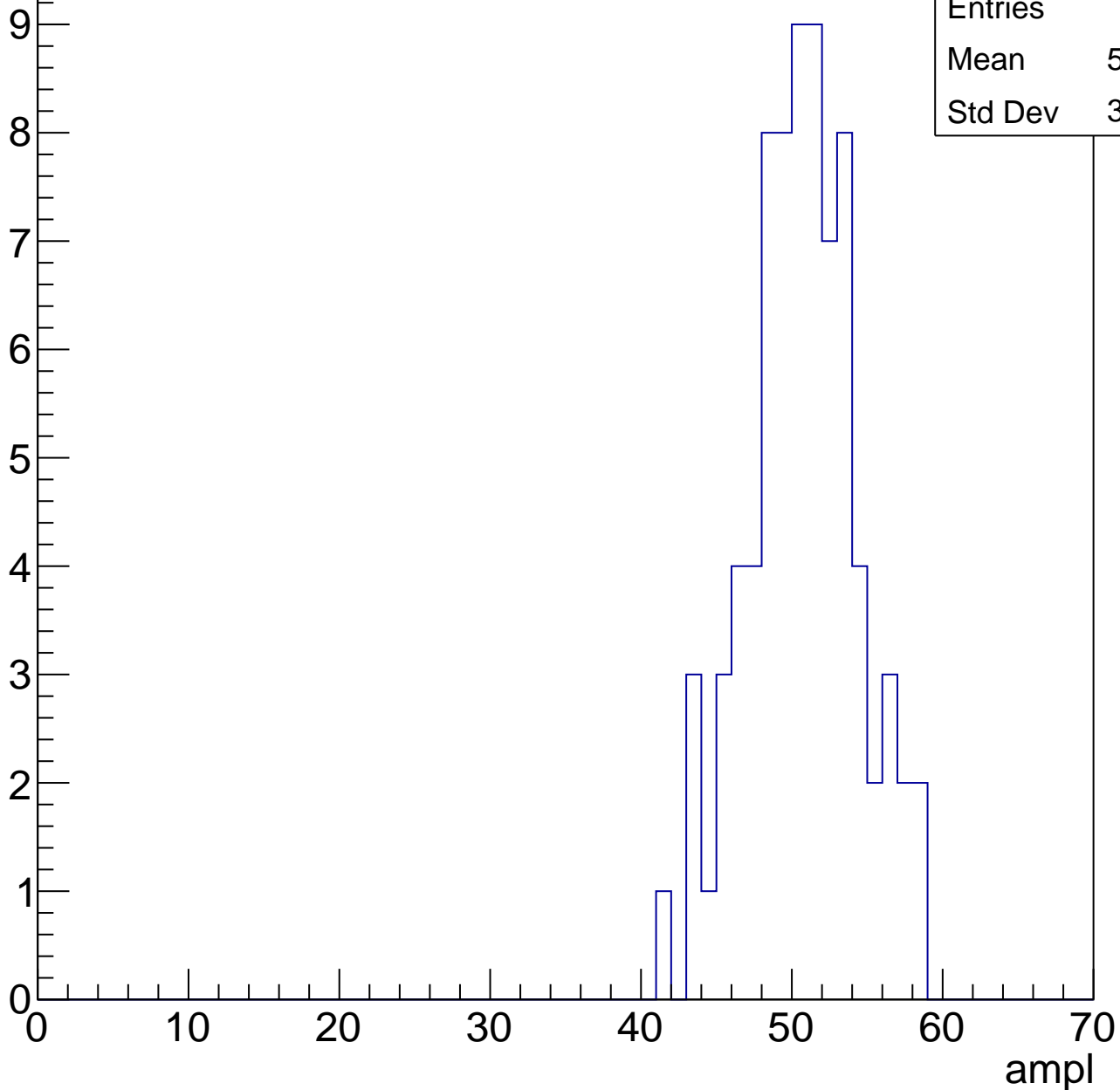


B1L100S, U9-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 50.23 |
| Std Dev | 3.637 |

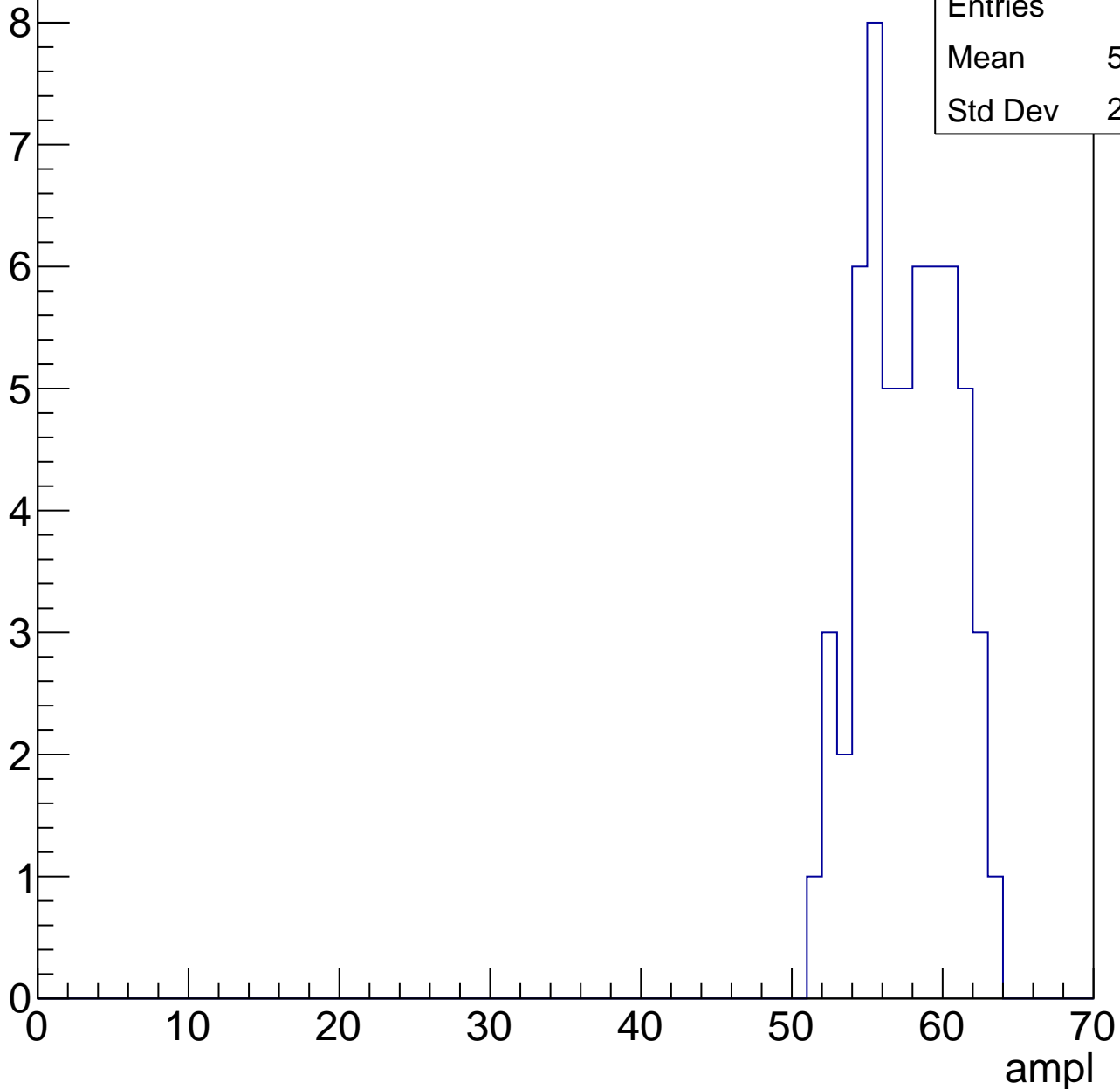


B1L100S, U9-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.16 |
| Std Dev | 2.984 |

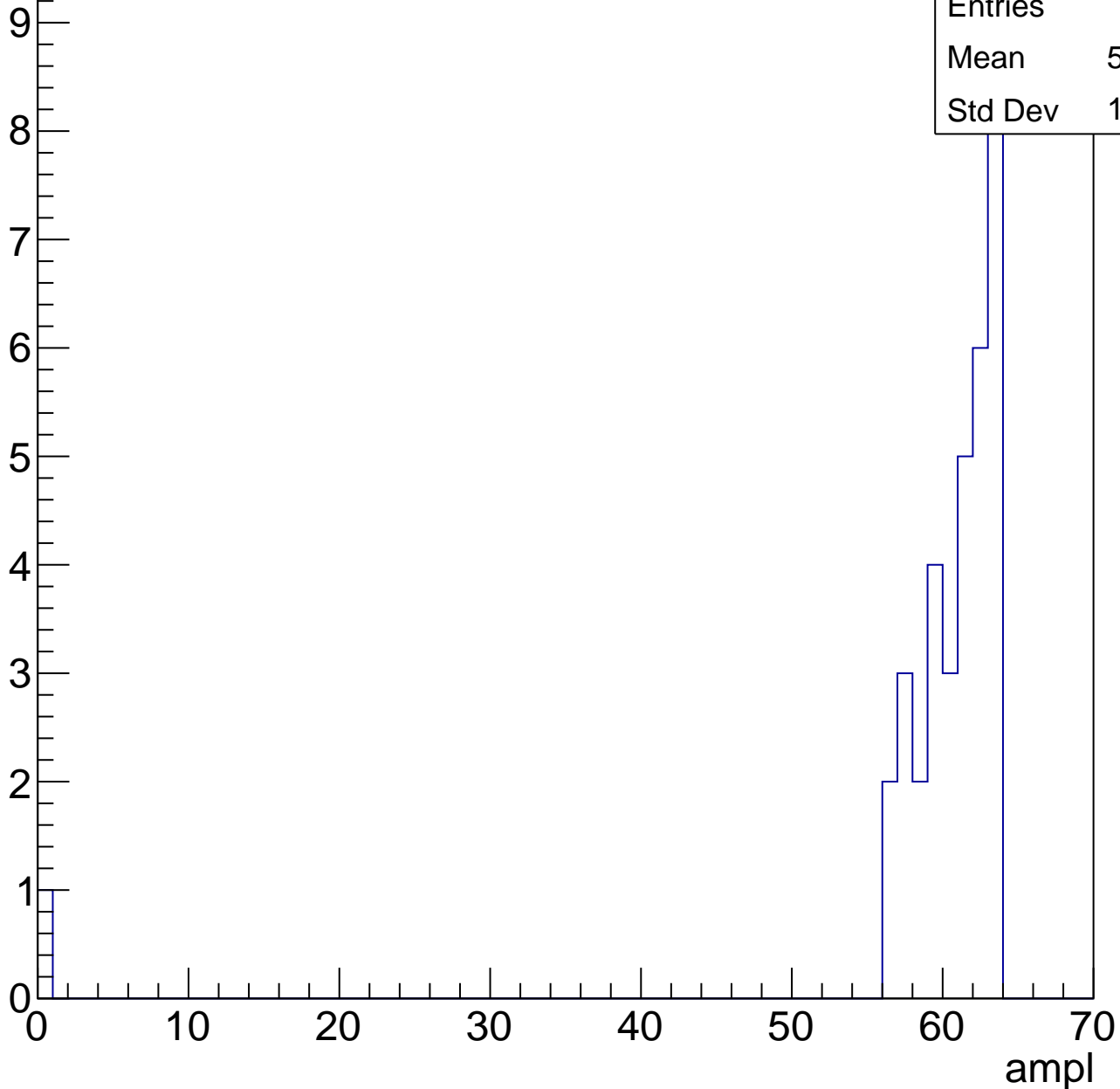


B1L100S, U9-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

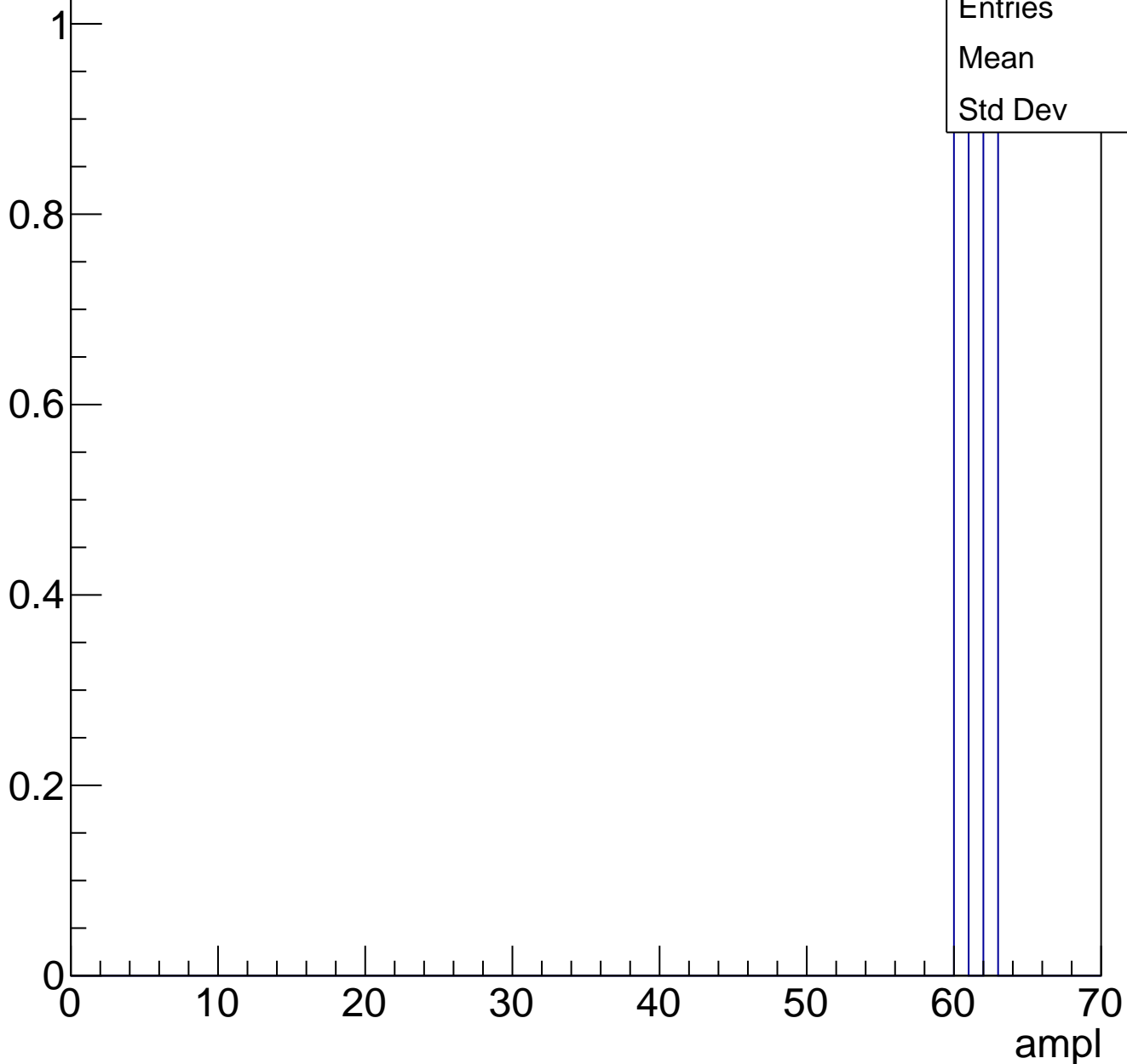
| | |
|---------|-------|
| Entries | 35 |
| Mean | 58.83 |
| Std Dev | 10.33 |



B1L100S, U9-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch122, adc0

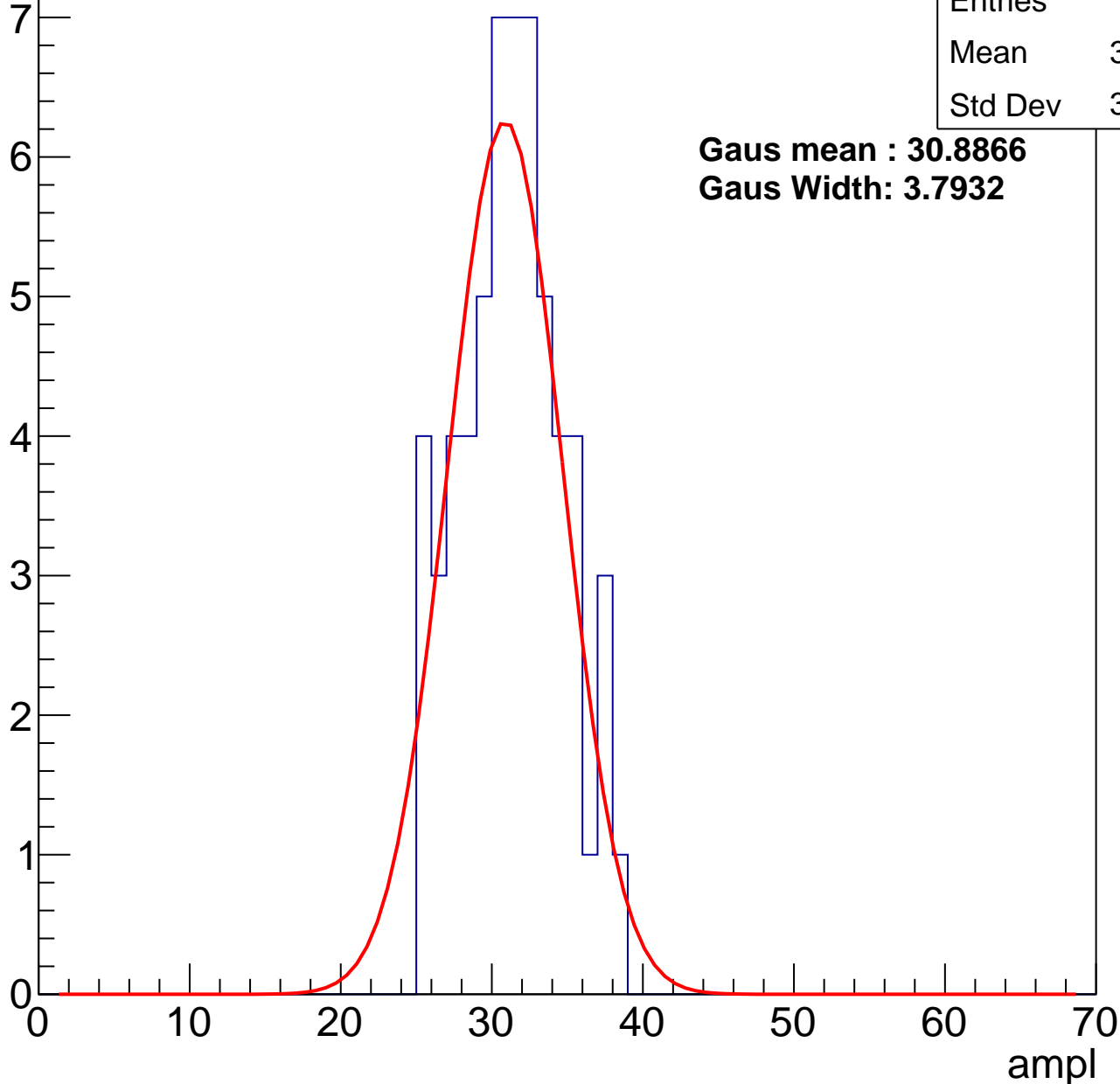
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 30.85 |
| Std Dev | 3.328 |

Gaus mean : 30.8866

Gaus Width: 3.7932



B1L100S, U9-ch122, adc1

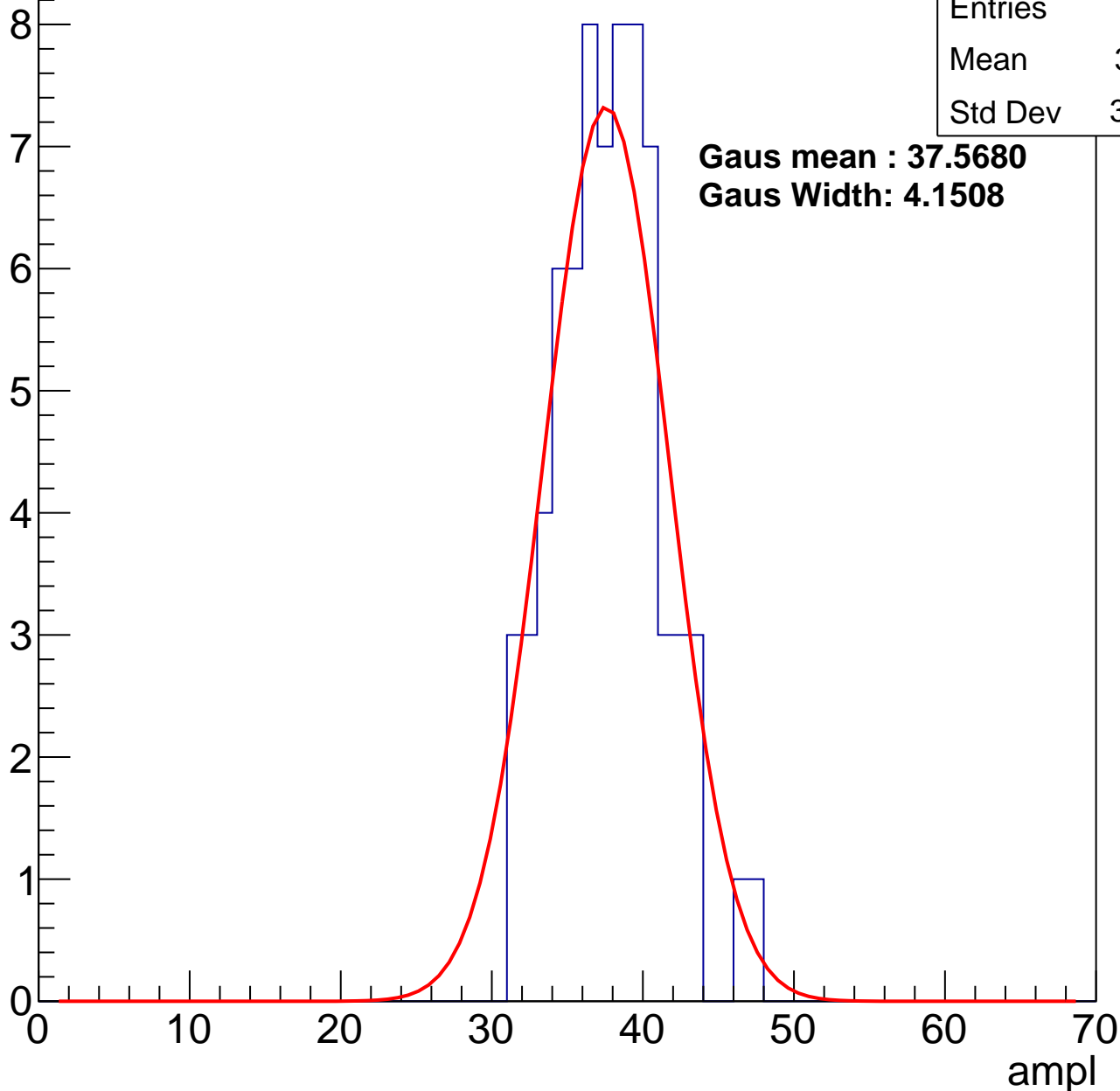
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.31 |
| Std Dev | 3.442 |

Gaus mean : 37.5680

Gaus Width: 4.1508



B1L100S, U9-ch122, adc2

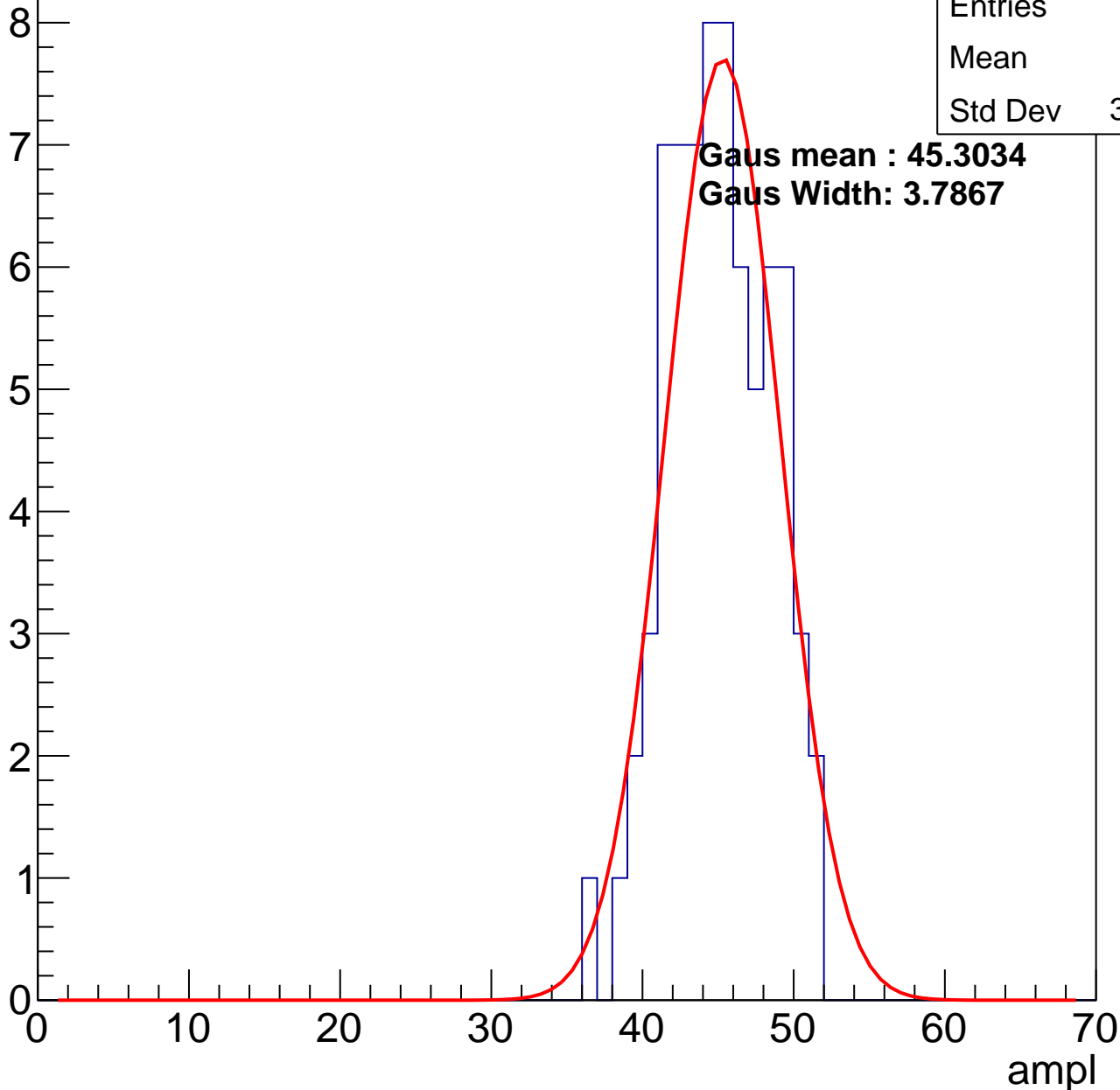
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 44.6 |
| Std Dev | 3.332 |

Gaus mean : 45.3034

Gaus Width: 3.7867

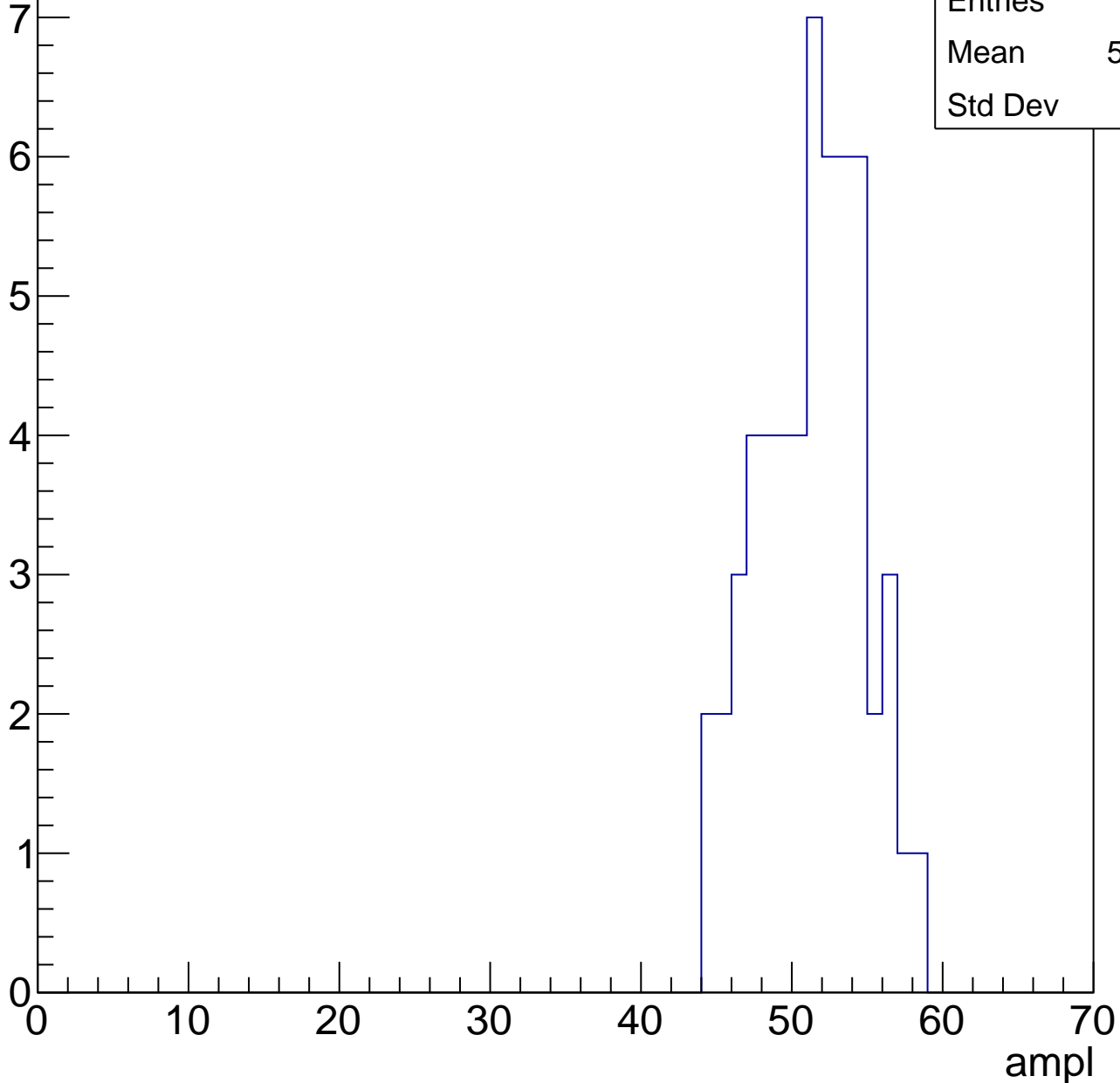


B1L100S, U9-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

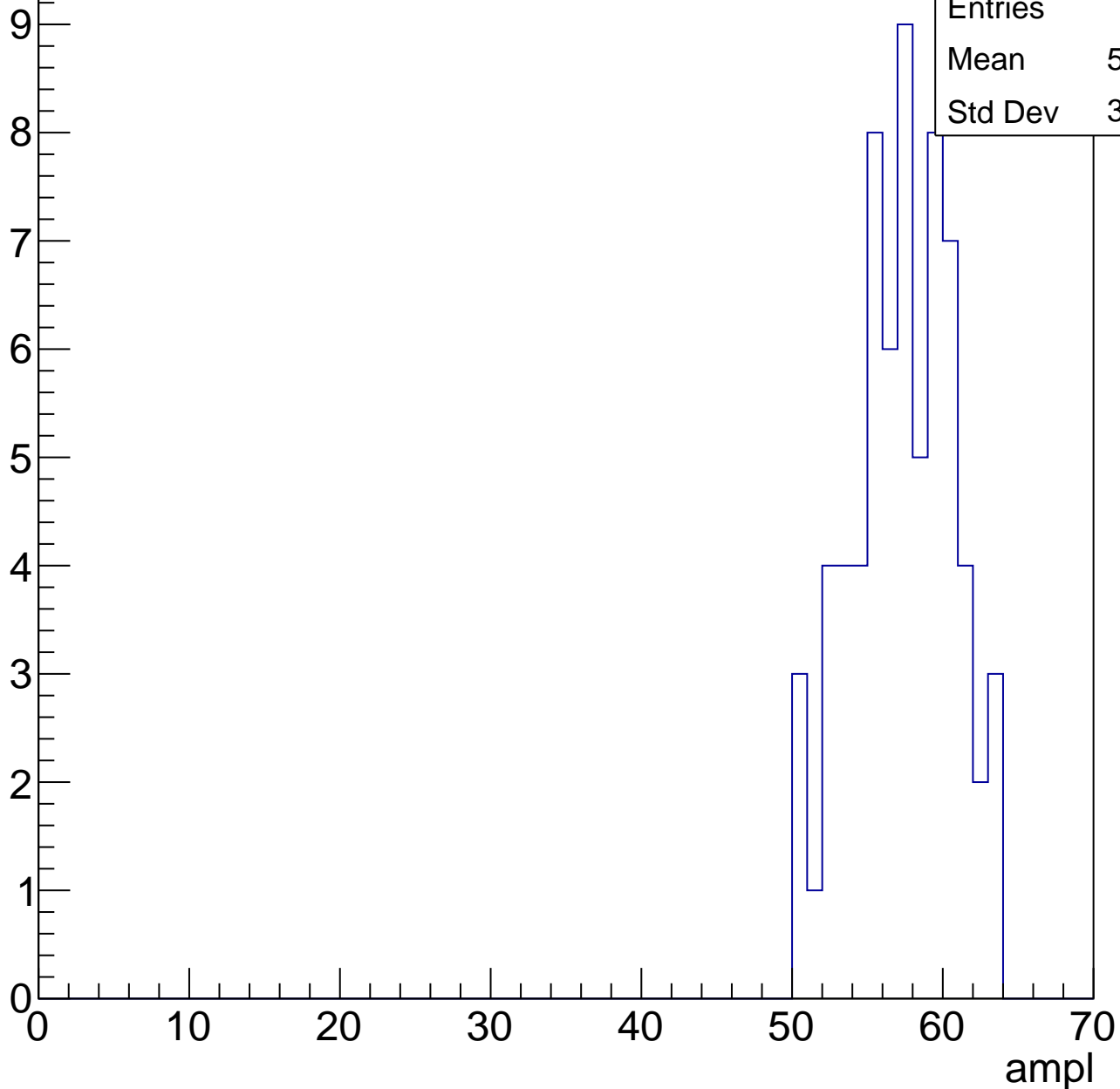
| | |
|---------|-------|
| Entries | 55 |
| Mean | 50.84 |
| Std Dev | 3.41 |



B1L100S, U9-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

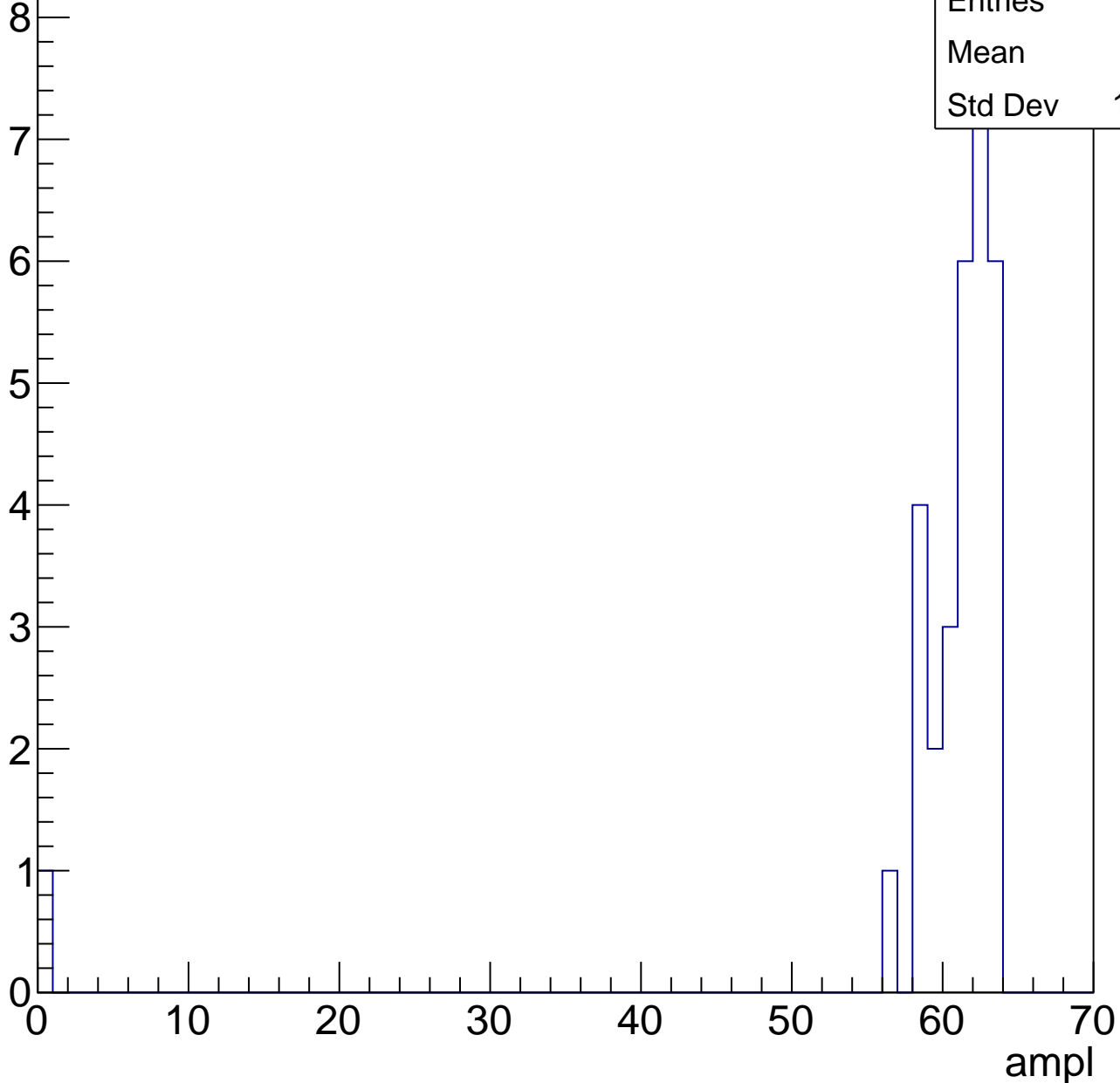


B1L100S, U9-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 31 |
| Mean | 58.9 |
| Std Dev | 10.91 |



B1L100S, U9-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B1L100S, U9-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U9-ch123, adc0

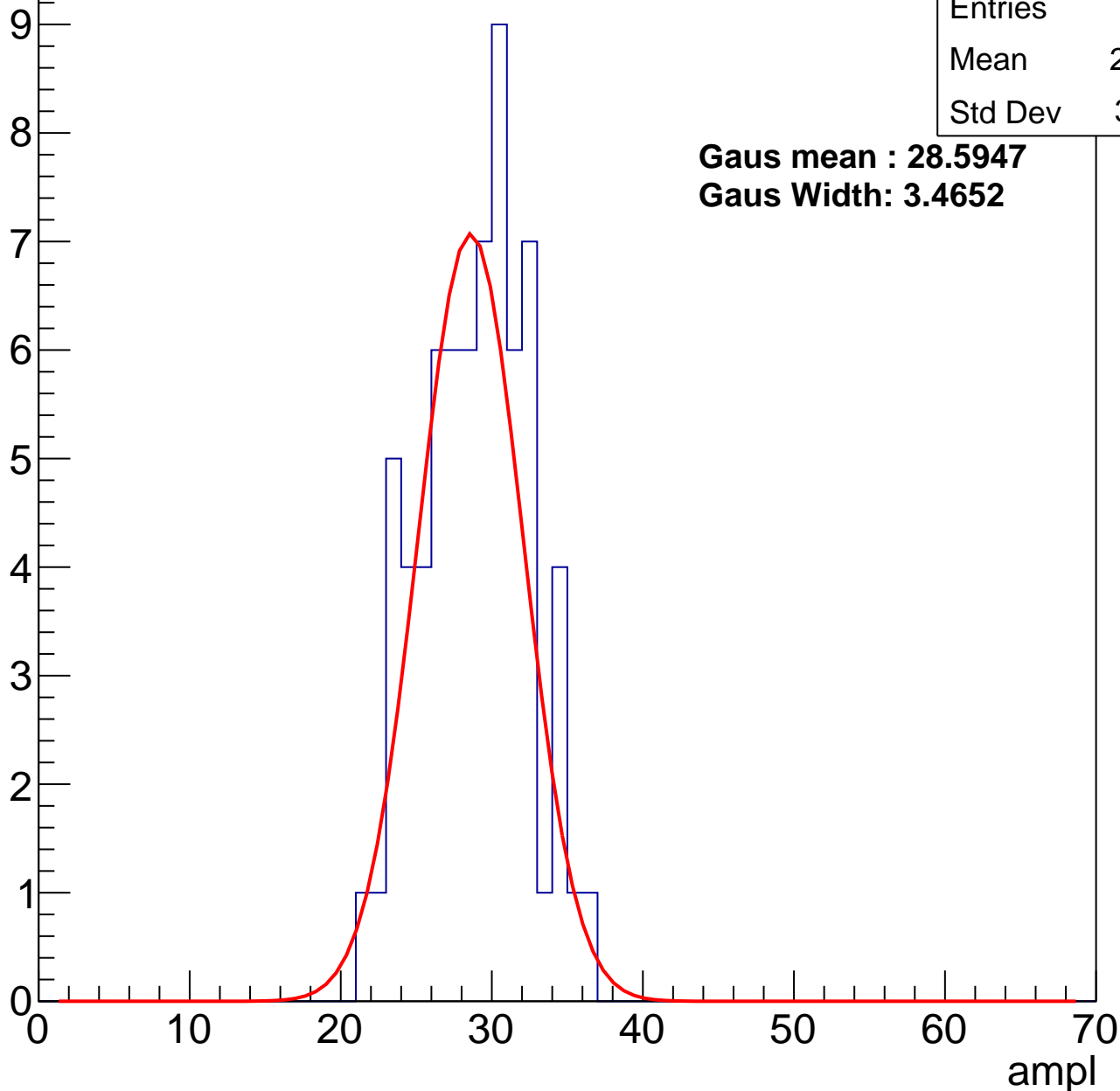
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.45 |
| Std Dev | 3.441 |

Gaus mean : 28.5947

Gaus Width: 3.4652



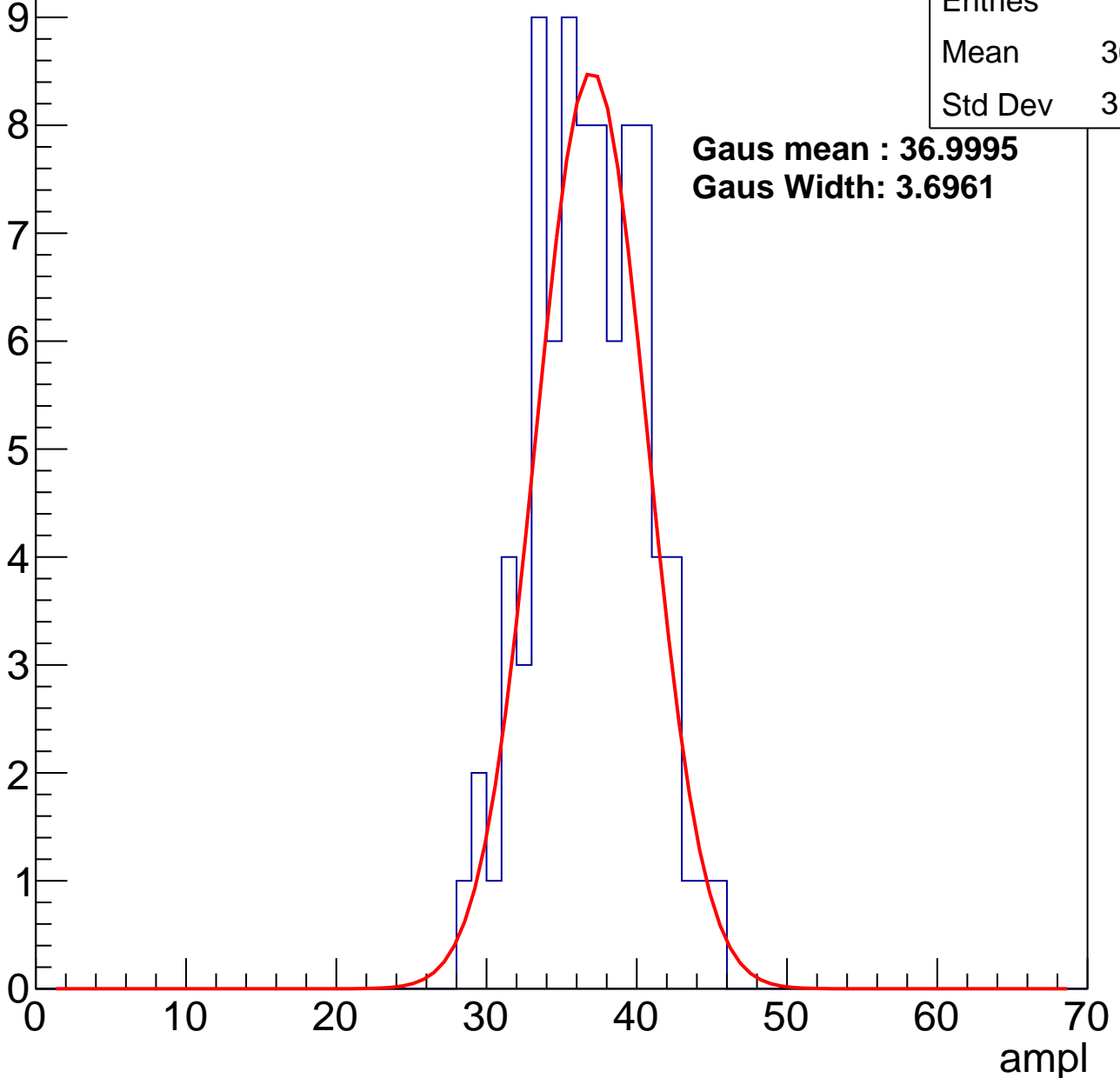
B1L100S, U9-ch123, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 36.43 |
| Std Dev | 3.643 |

Gaus mean : 36.9995
Gaus Width: 3.6961



B1L100S, U9-ch123, adc2

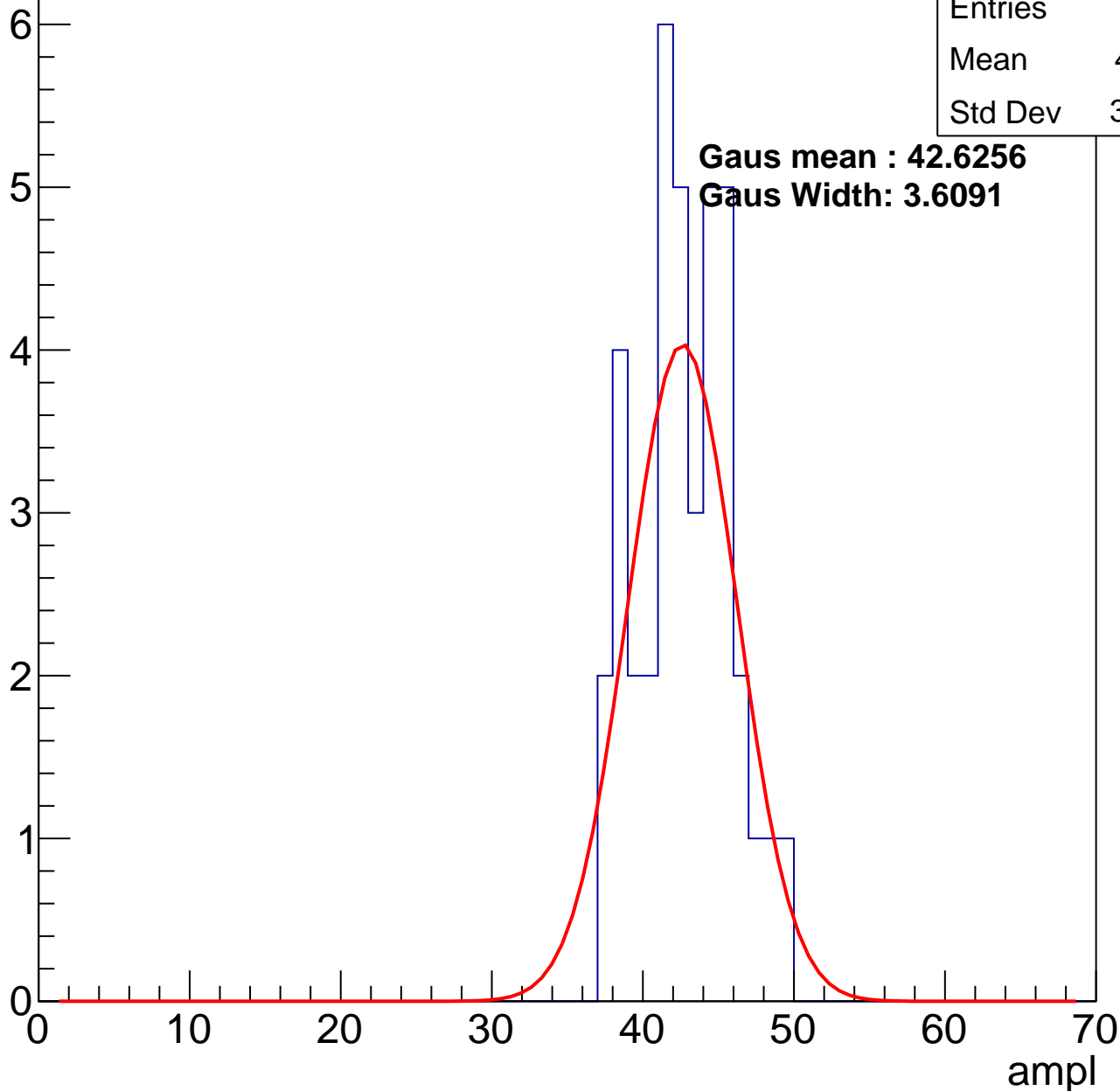
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 42.31 |
| Std Dev | 3.006 |

Gaus mean : 42.6256

Gaus Width: 3.6091

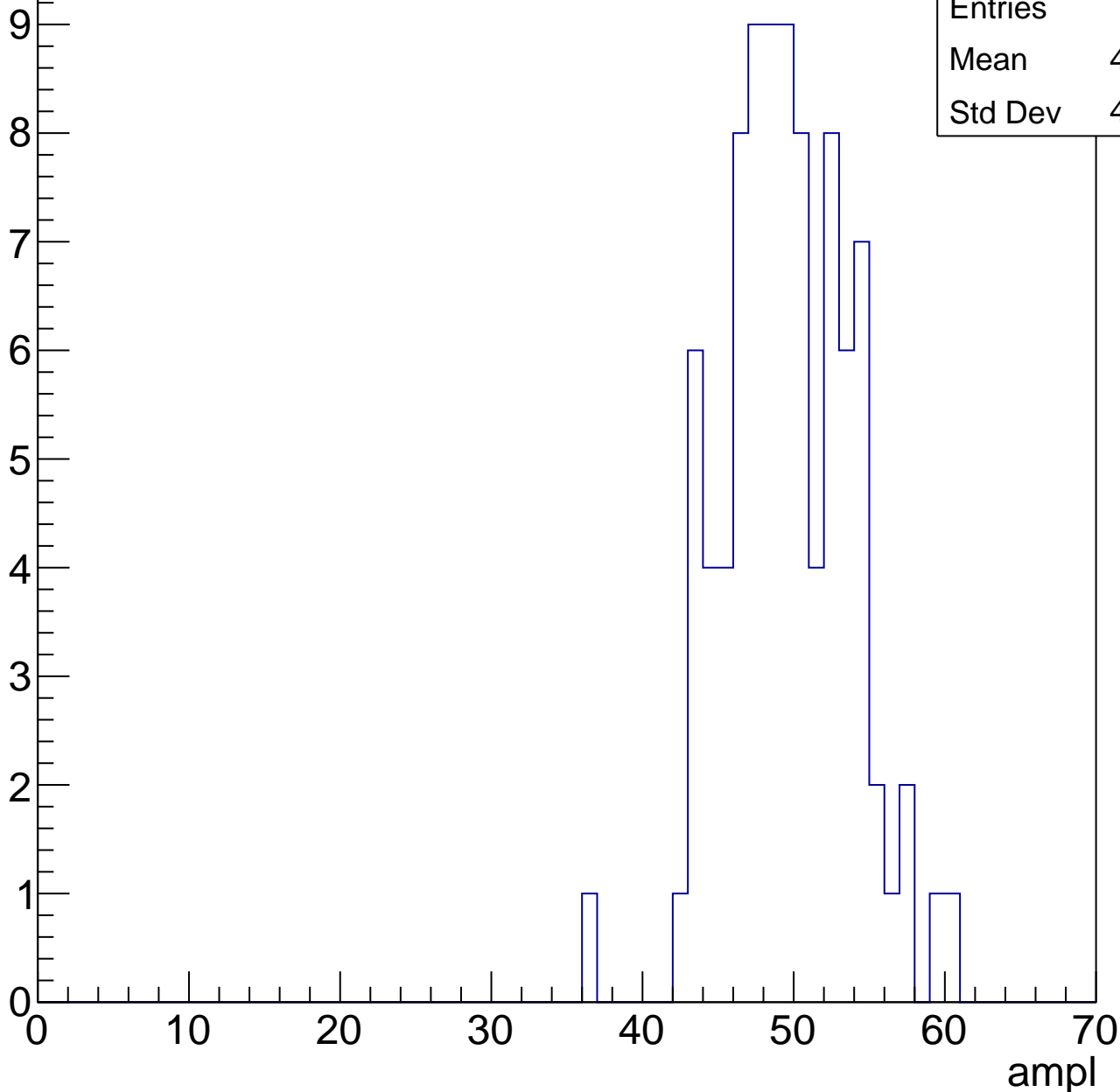


B1L100S, U9-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 91 |
| Mean | 49.13 |
| Std Dev | 4.128 |

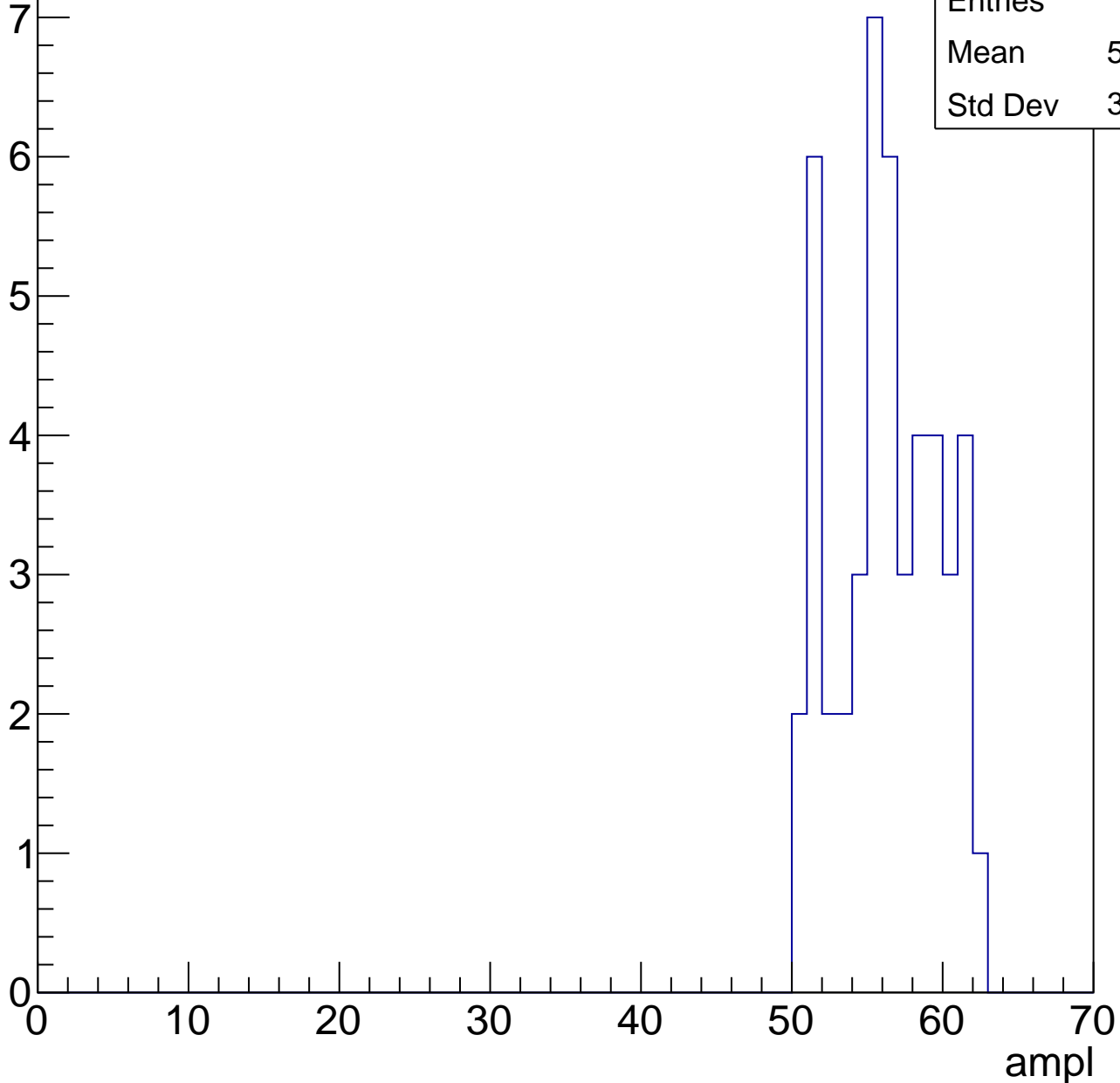


B1L100S, U9-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.83 |
| Std Dev | 3.354 |

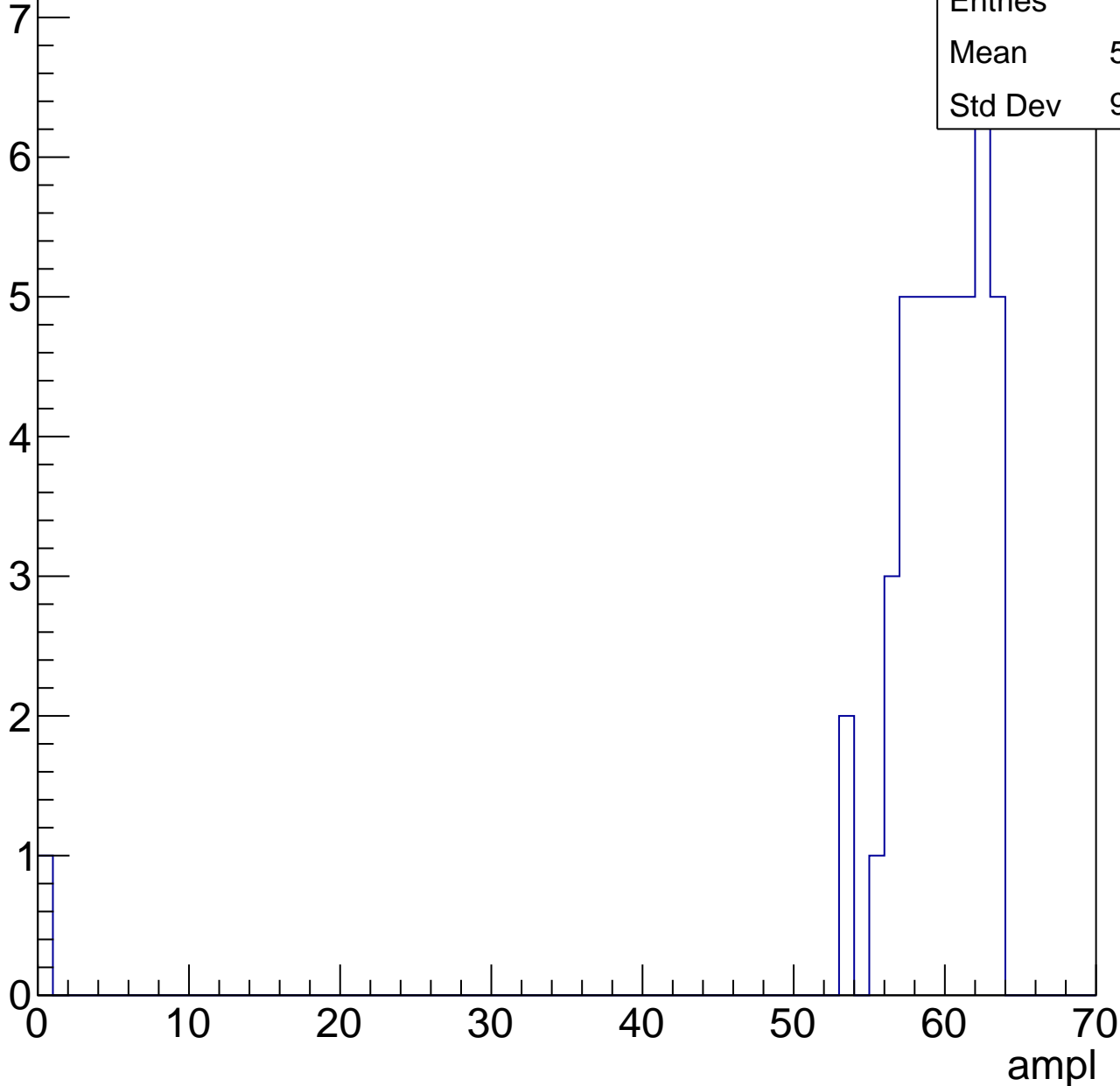


B1L100S, U9-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.02 |
| Std Dev | 9.228 |

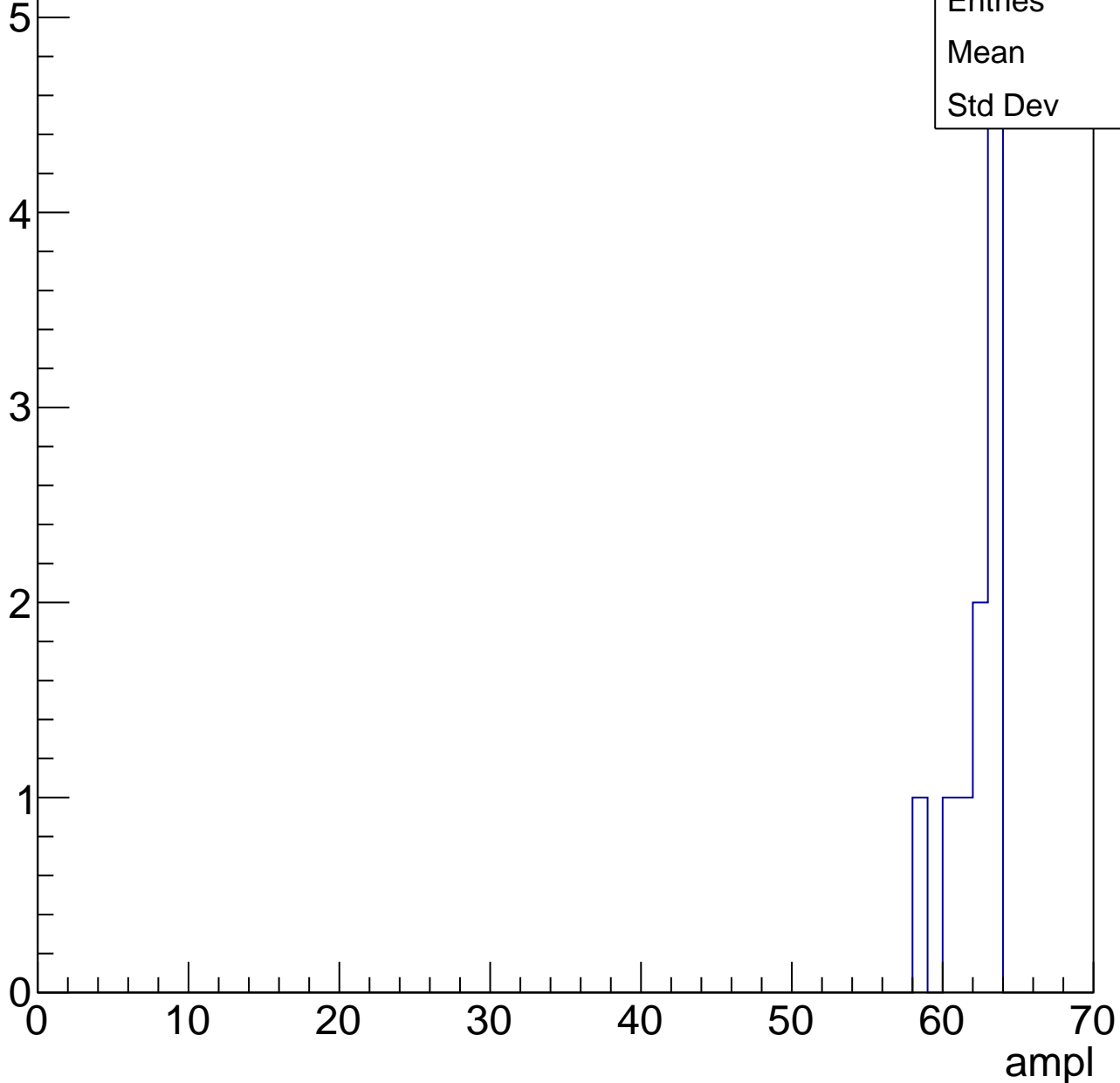


B1L100S, U9-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 10 |
| Mean | 61.8 |
| Std Dev | 1.6 |



B1L100S, U9-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch124, adc0

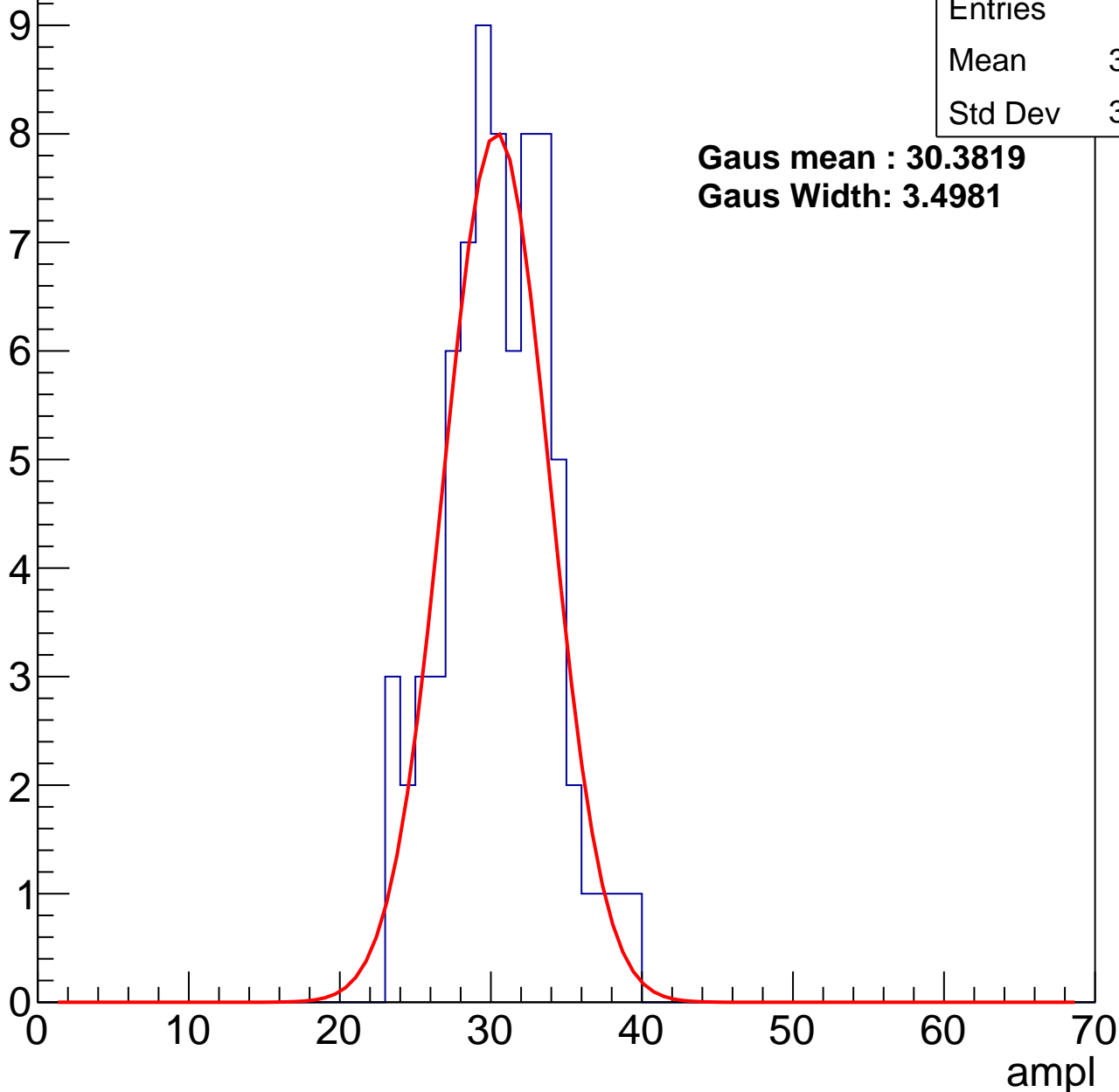
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 30.07 |
| Std Dev | 3.493 |

Gaus mean : 30.3819

Gaus Width: 3.4981



B1L100S, U9-ch124, adc1

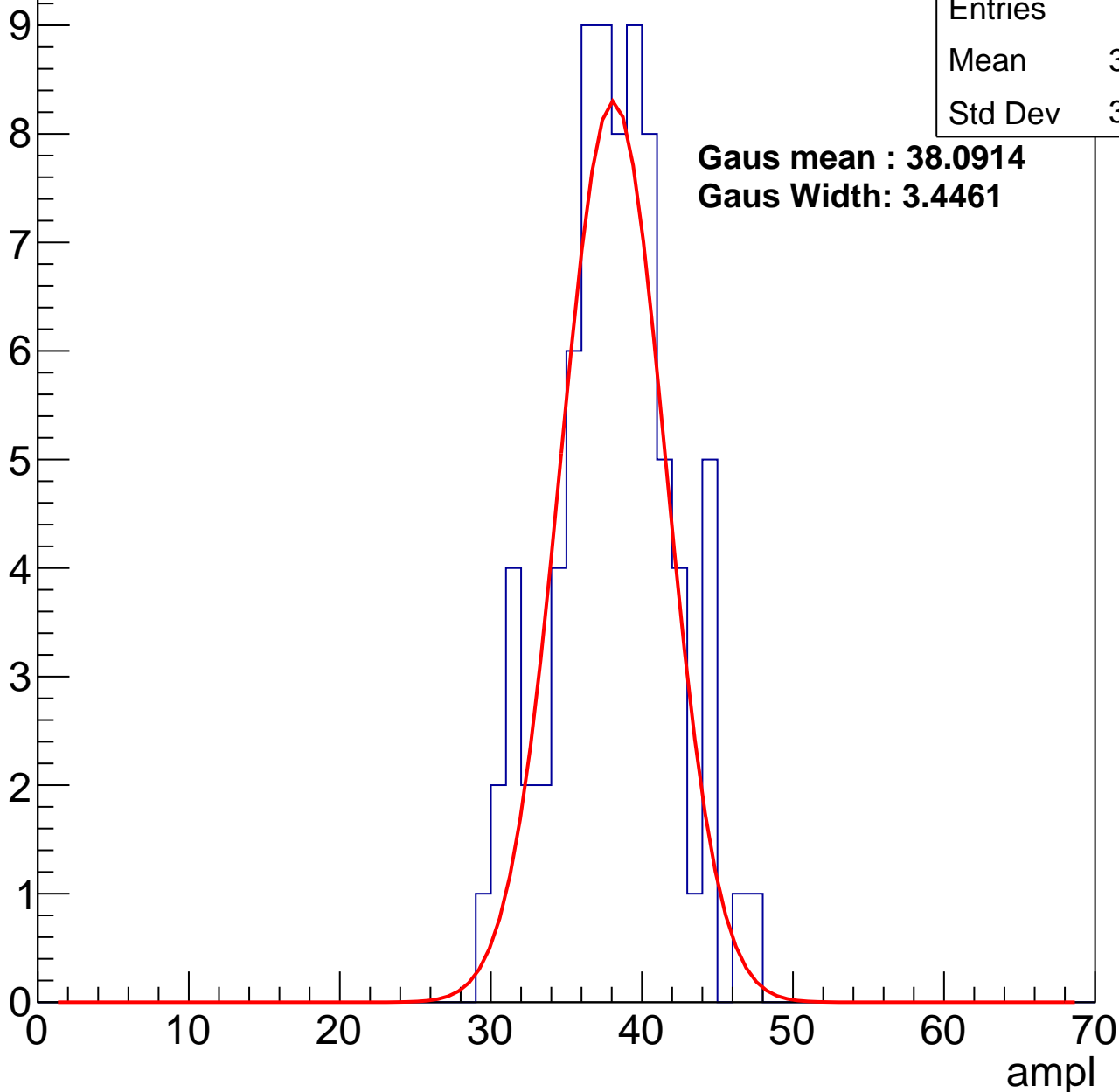
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 37.65 |
| Std Dev | 3.817 |

Gaus mean : 38.0914

Gaus Width: 3.4461



B1L100S, U9-ch124, adc2

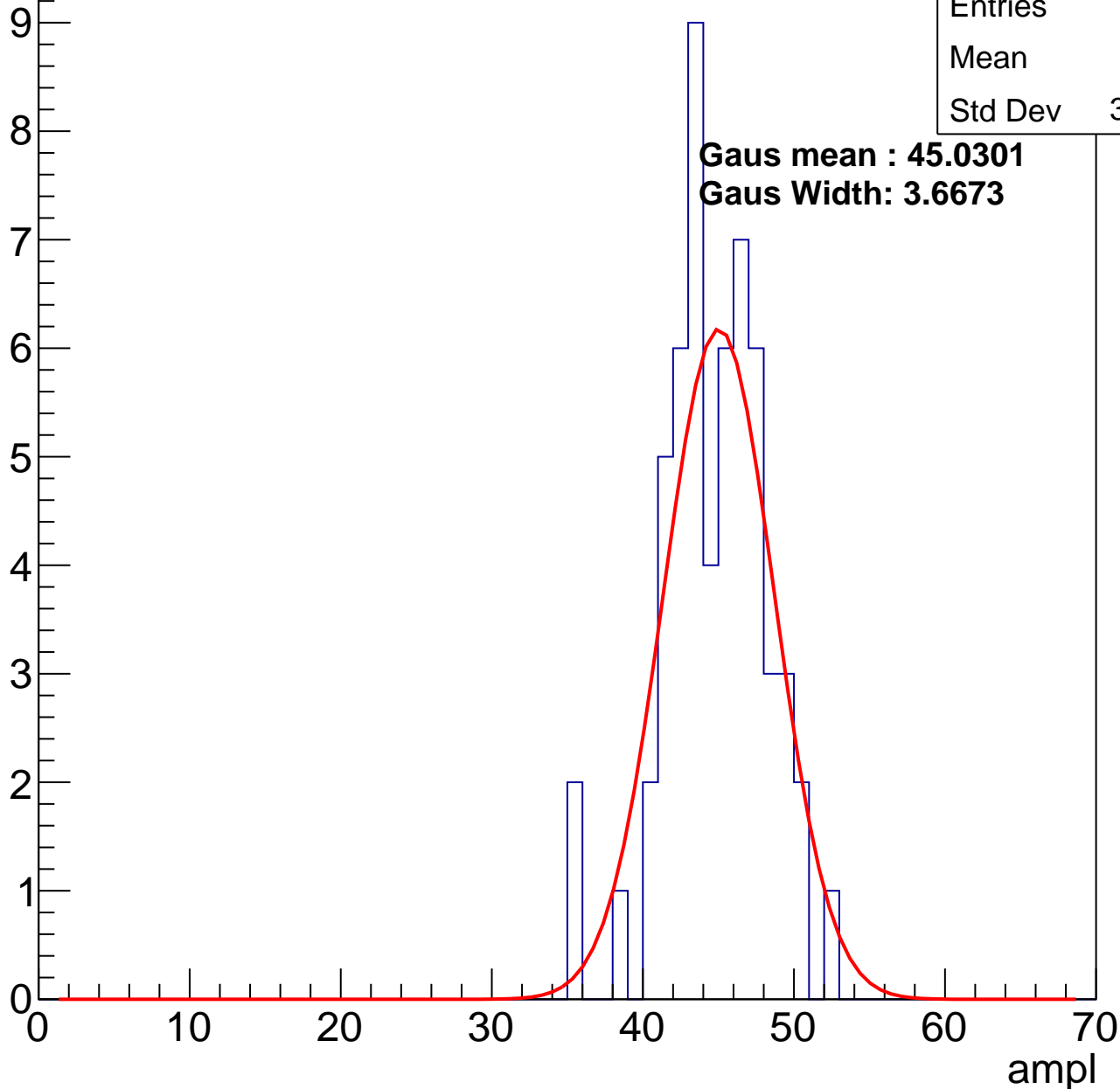
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 44.3 |
| Std Dev | 3.377 |

Gaus mean : 45.0301

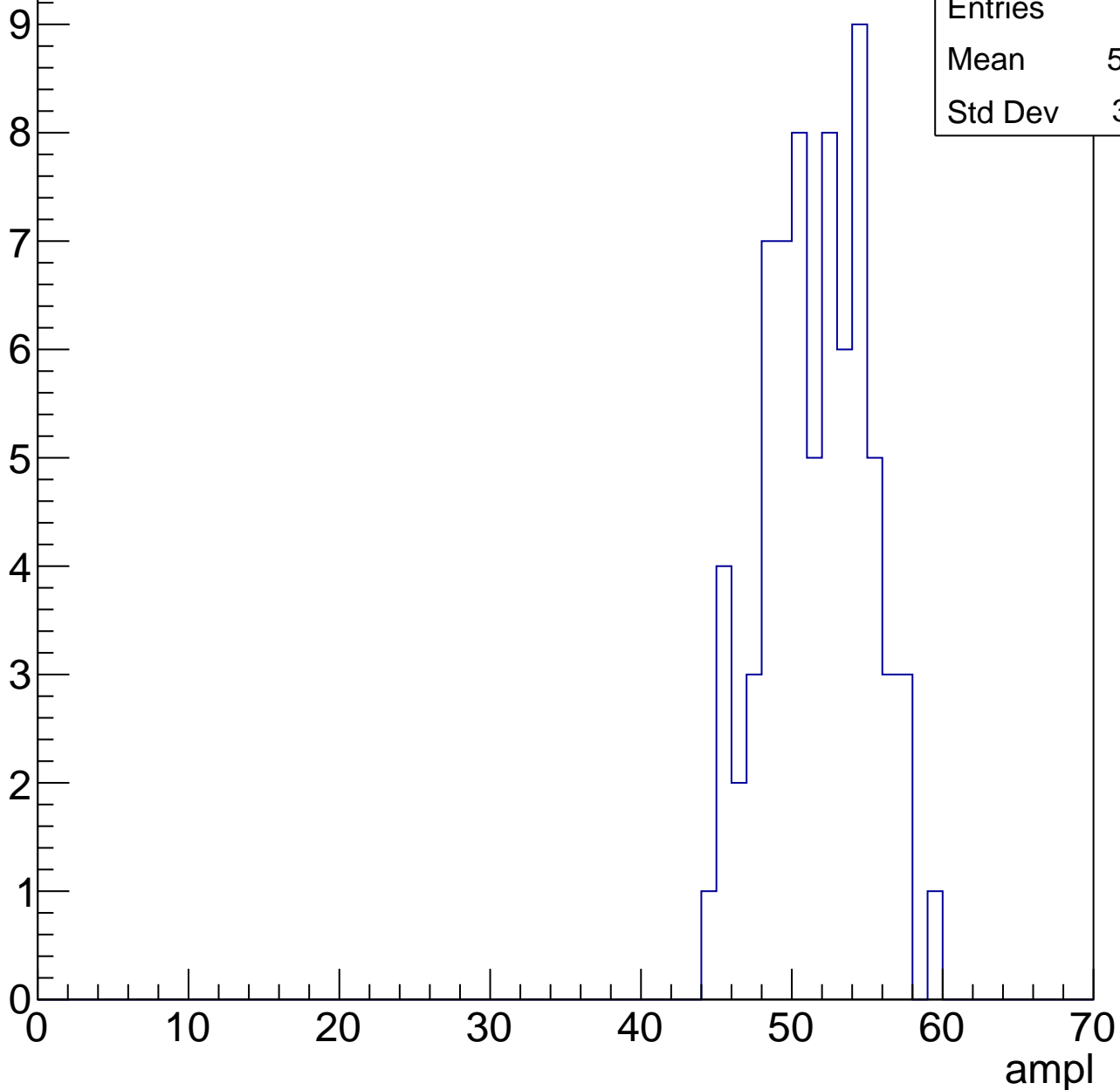
Gaus Width: 3.6673



B1L100S, U9-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

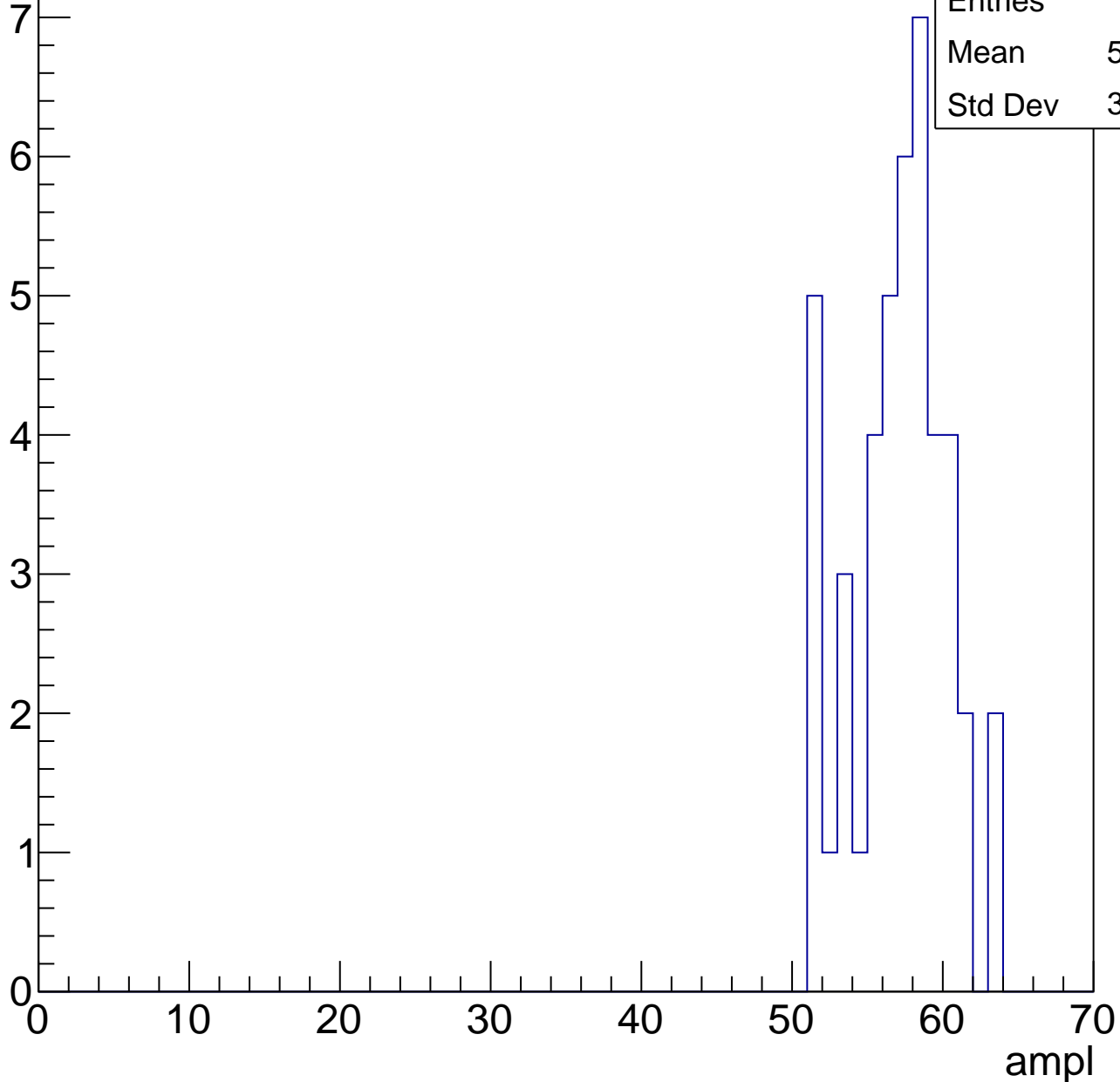
Entry



B1L100S, U9-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

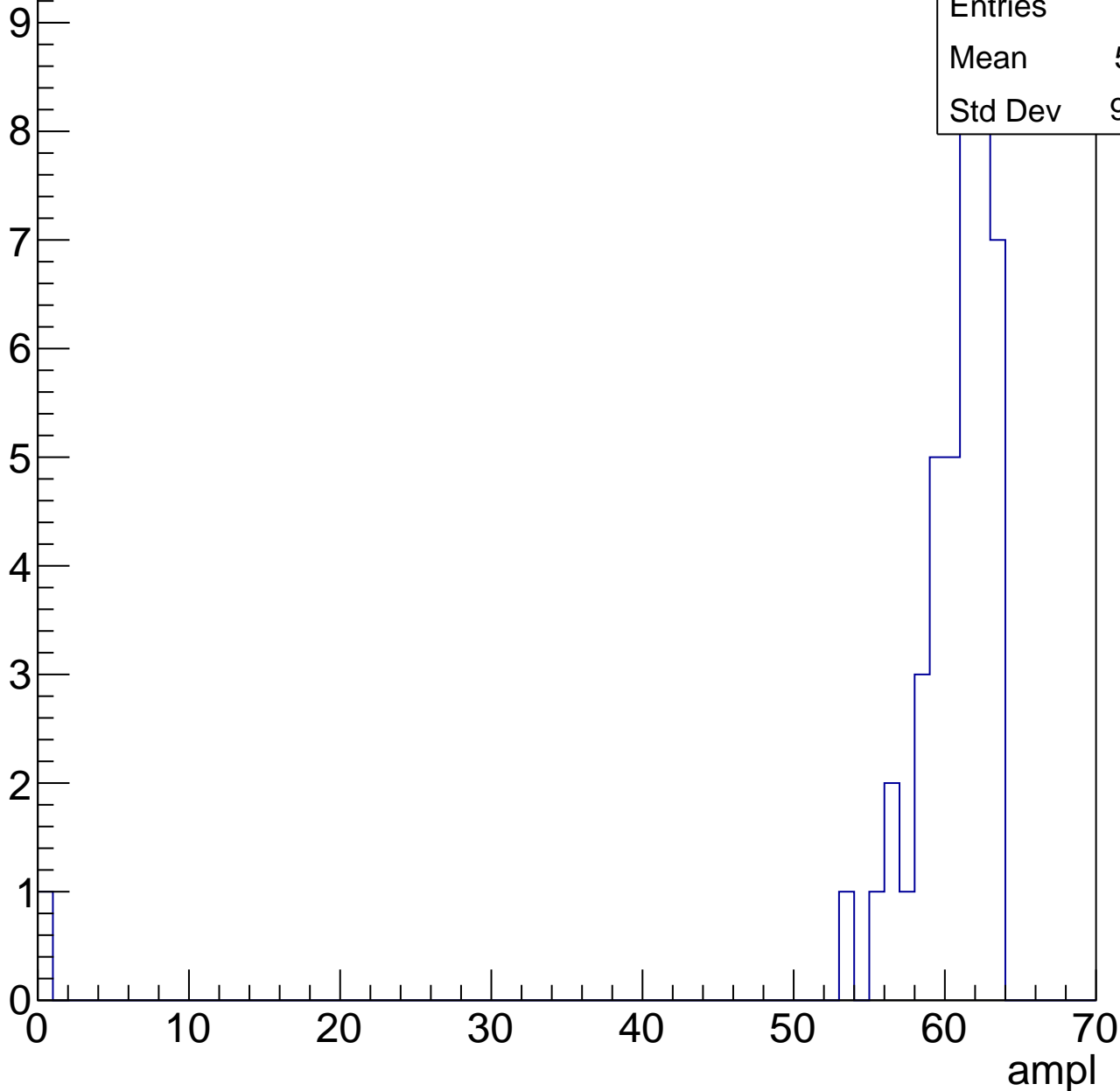


B1L100S, U9-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

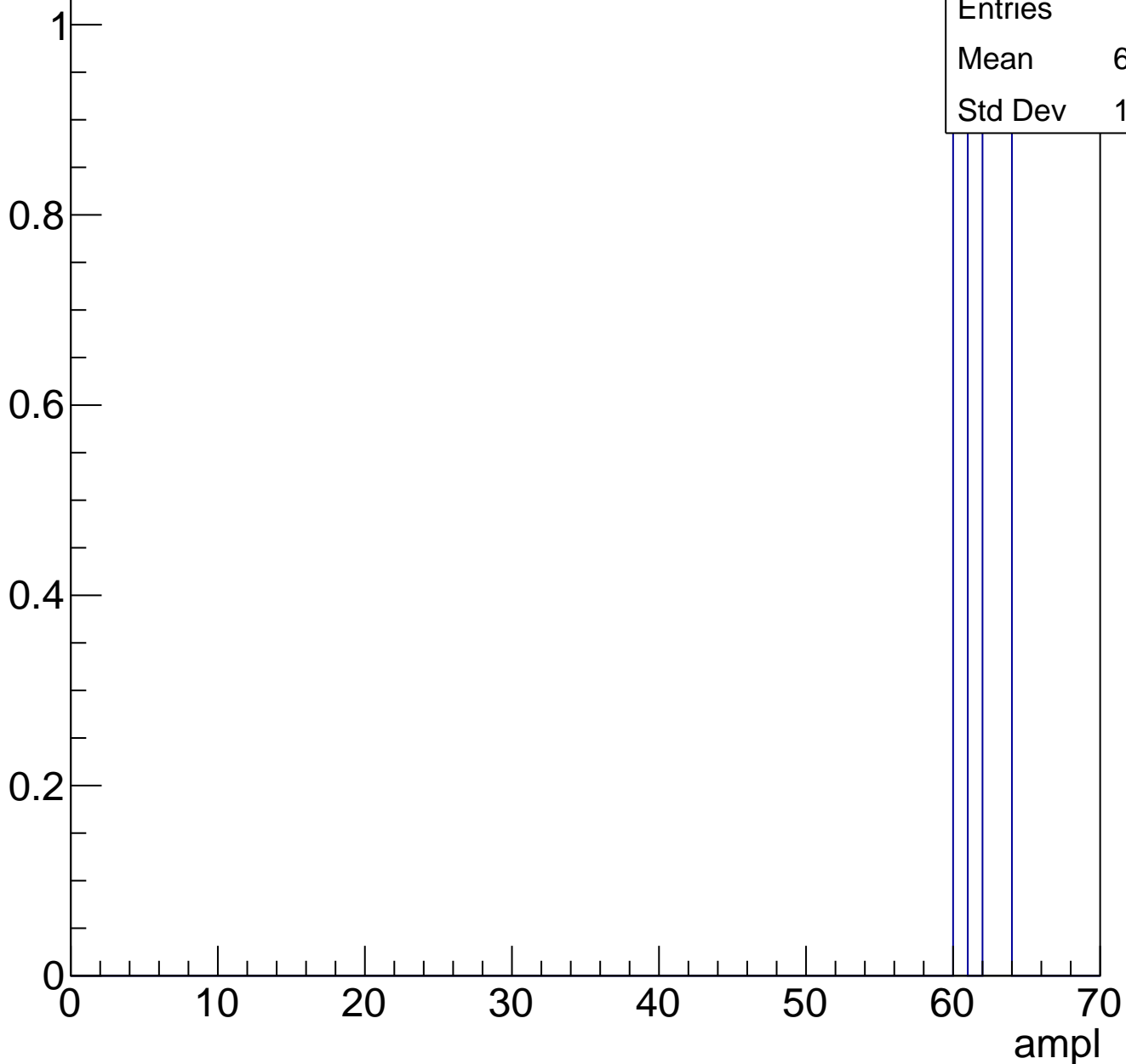
| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.91 |
| Std Dev | 9.385 |



B1L100S, U9-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch125, adc0

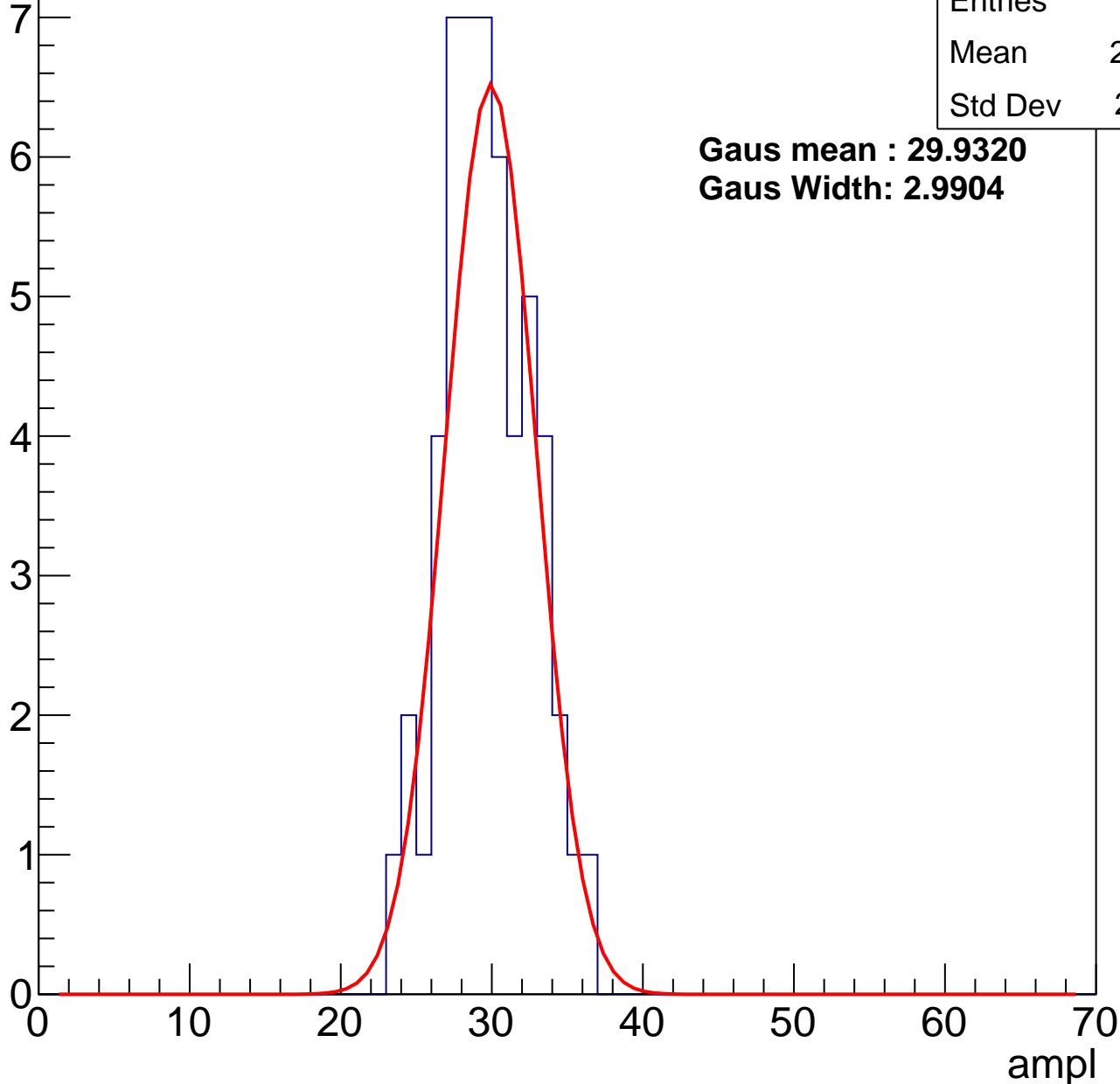
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 29.29 |
| Std Dev | 2.891 |

Gaus mean : 29.9320

Gaus Width: 2.9904



B1L100S, U9-ch125, adc1

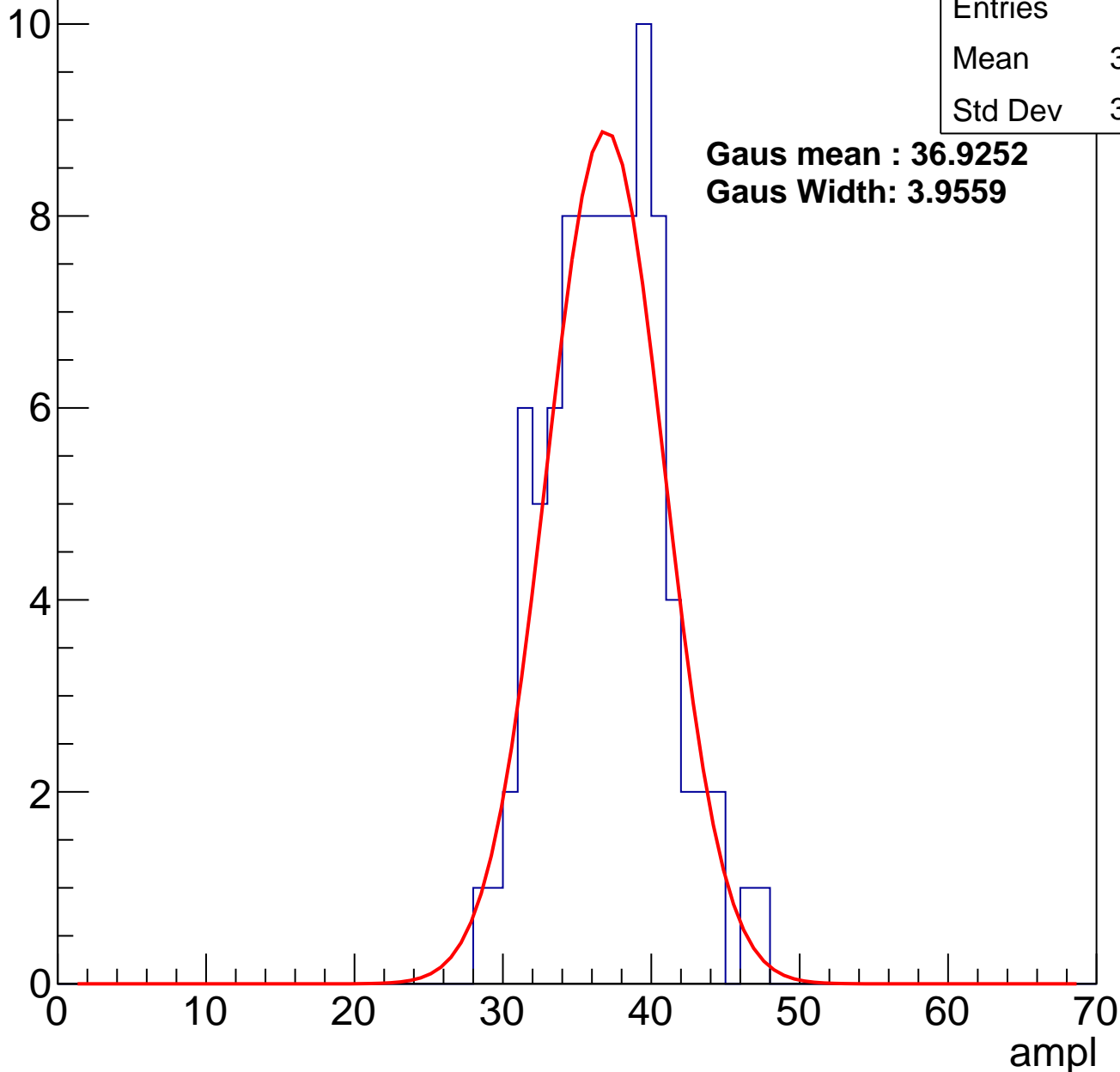
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 36.55 |
| Std Dev | 3.864 |

Gaus mean : 36.9252

Gaus Width: 3.9559

Entry



B1L100S, U9-ch125, adc2

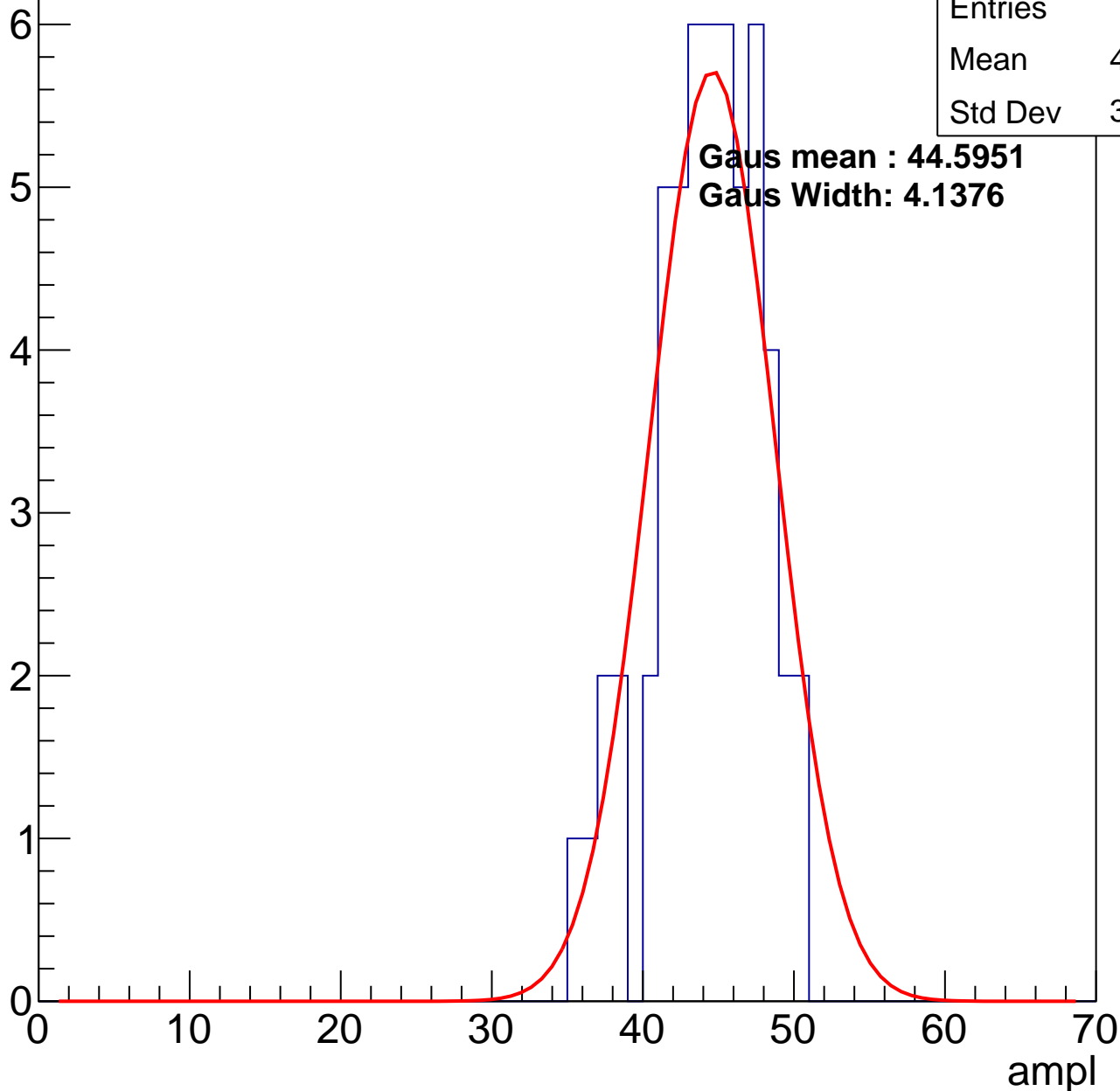
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.82 |
| Std Dev | 3.522 |

Gaus mean : 44.5951

Gaus Width: 4.1376

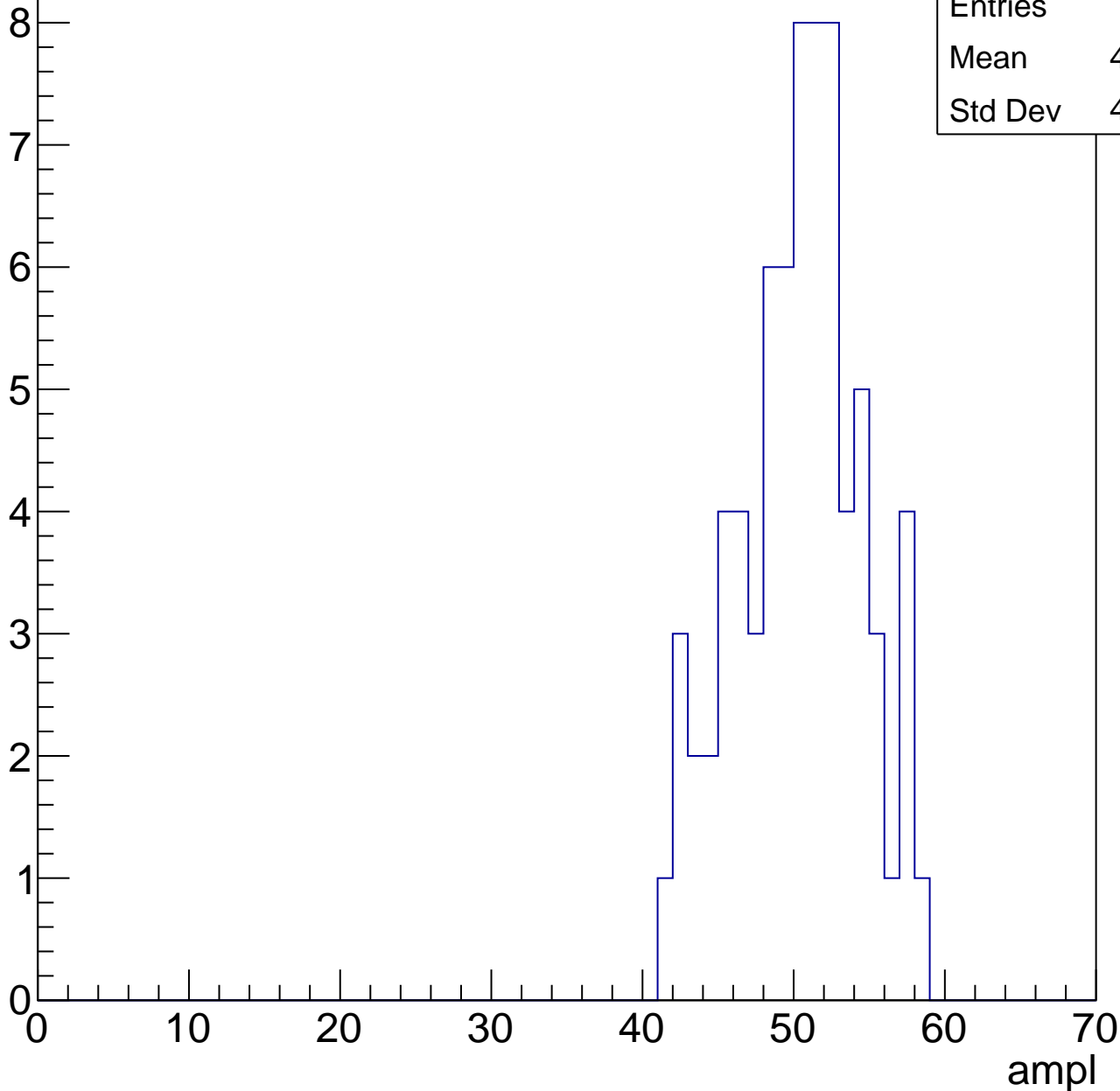


B1L100S, U9-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 49.88 |
| Std Dev | 4.037 |

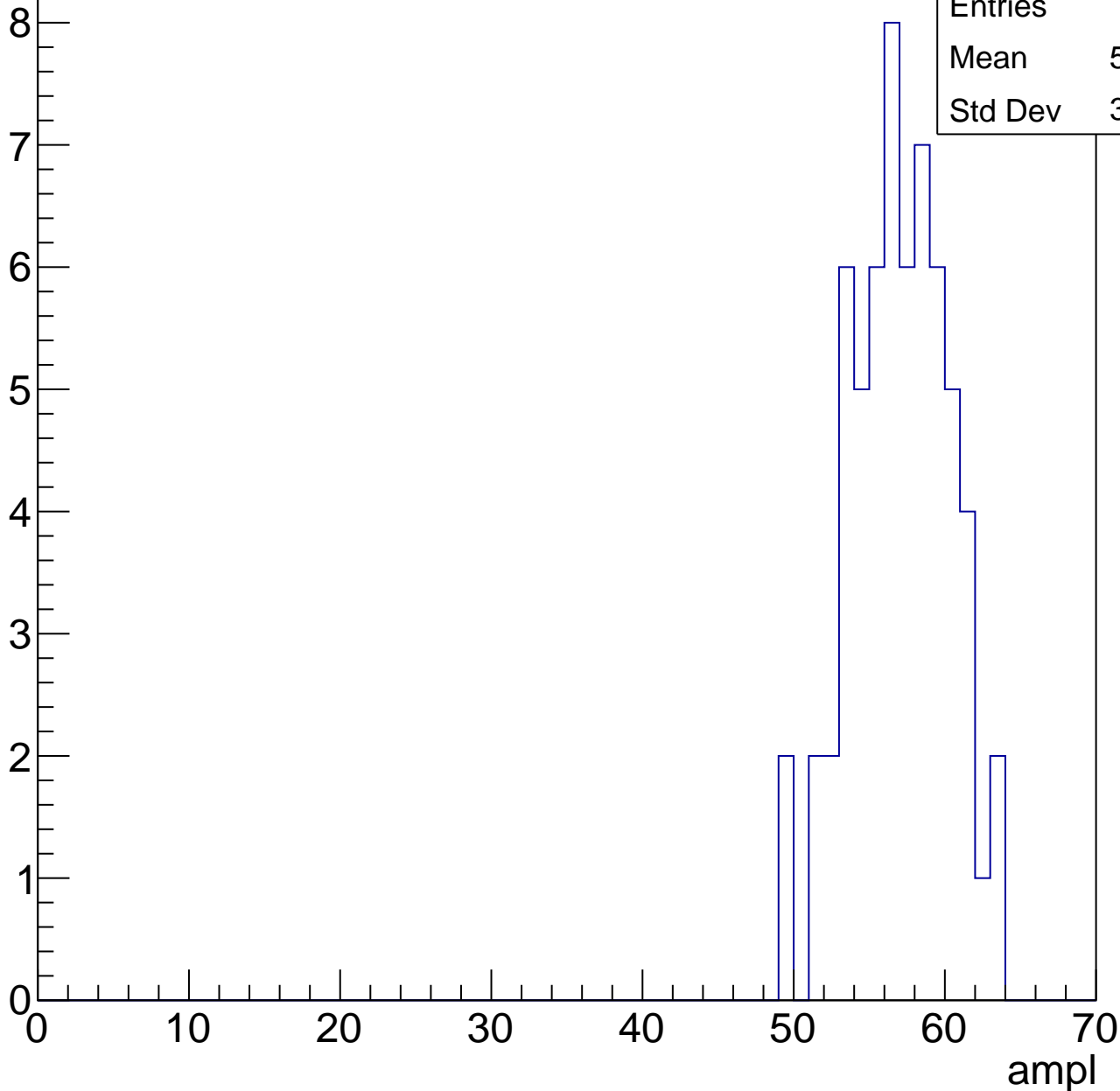


B1L100S, U9-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 56.52 |
| Std Dev | 3.227 |

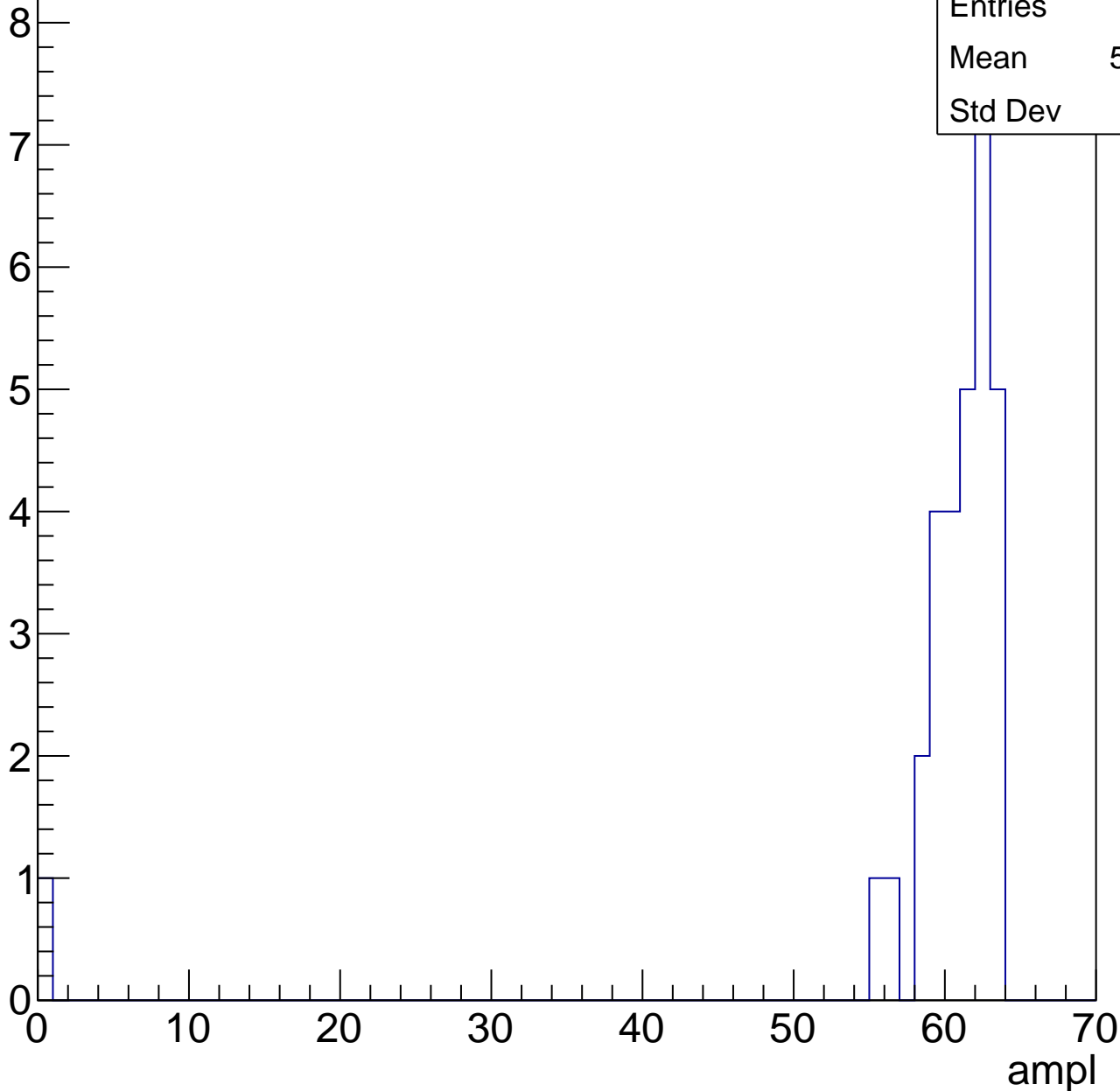


B1L100S, U9-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 31 |
| Mean | 58.68 |
| Std Dev | 10.9 |



B1L100S, U9-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U9-ch126, adc0

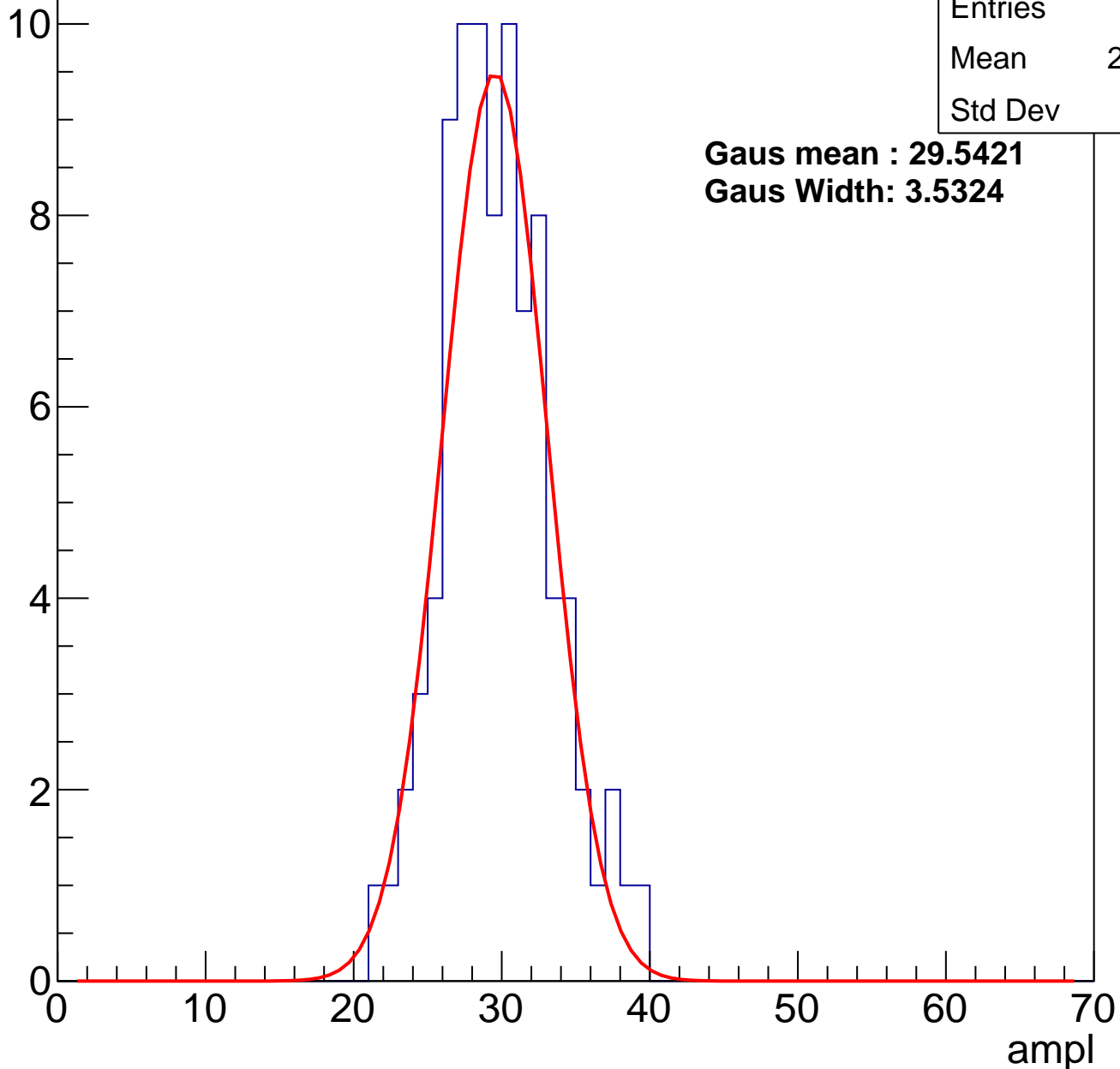
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 88 |
| Mean | 29.26 |
| Std Dev | 3.62 |

Gaus mean : 29.5421

Gaus Width: 3.5324

Entry



B1L100S, U9-ch126, adc1

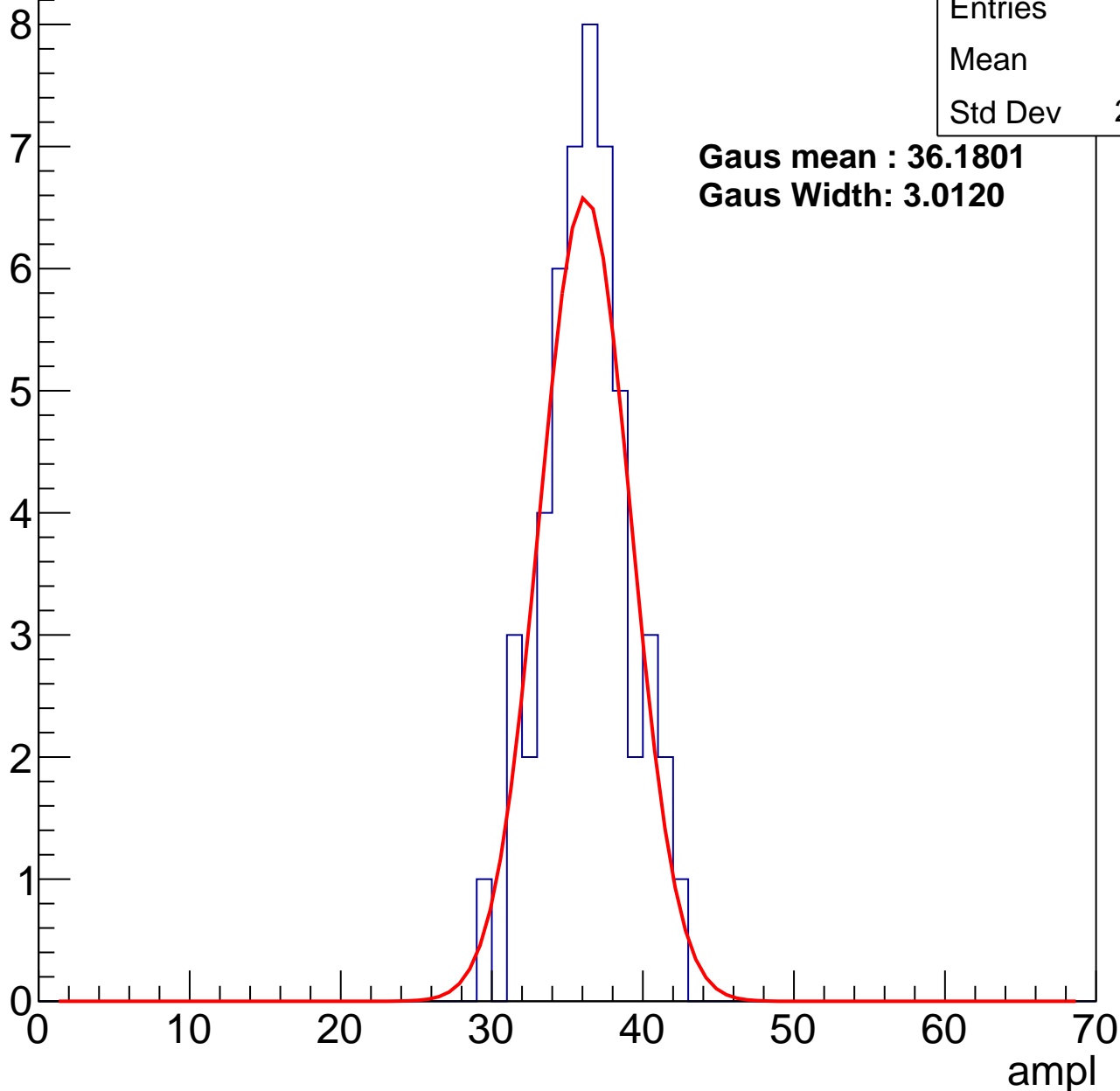
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 35.8 |
| Std Dev | 2.801 |

Gaus mean : 36.1801

Gaus Width: 3.0120

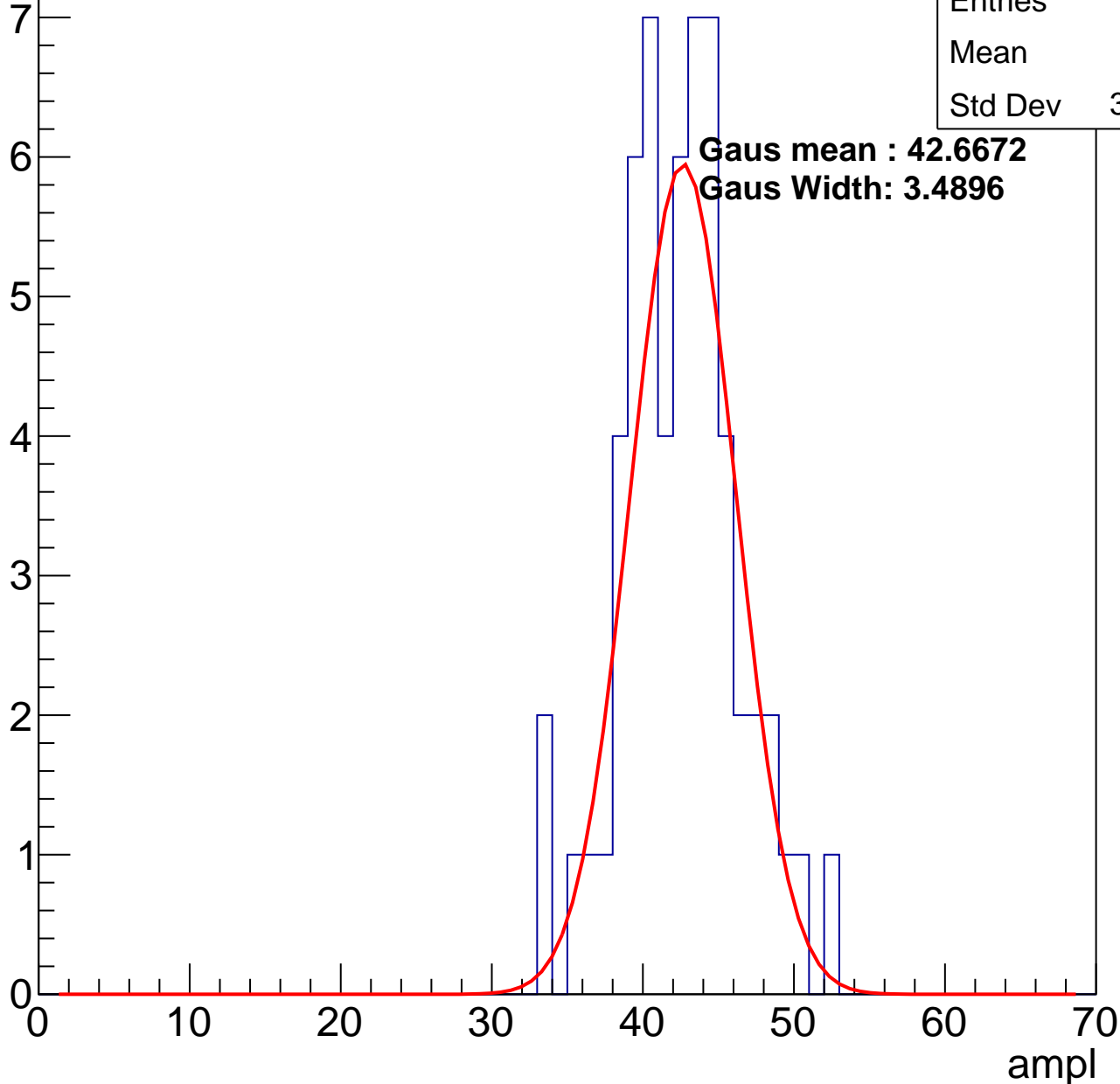


B1L100S, U9-ch126, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

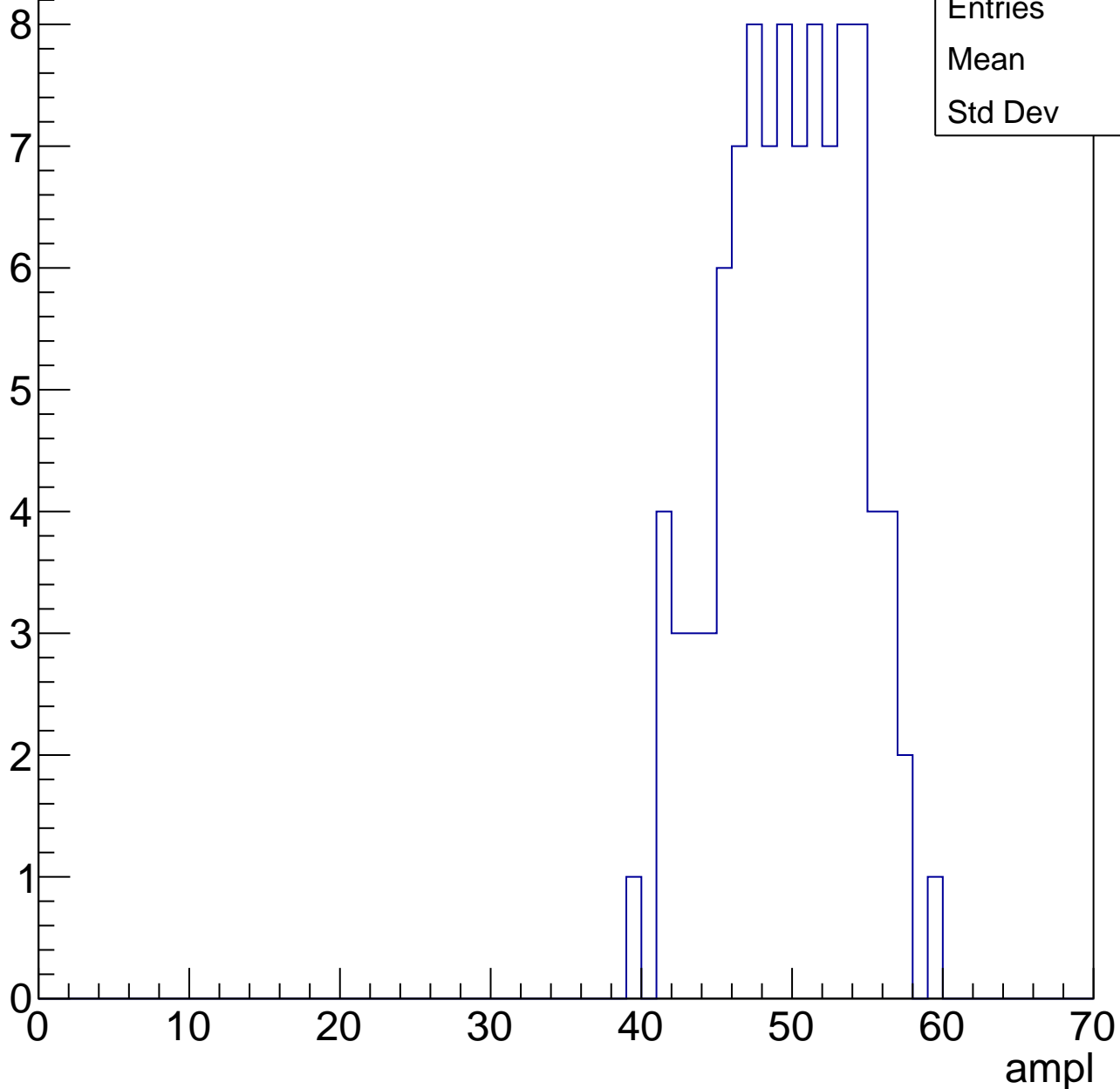
| | |
|---------|-------|
| Entries | 59 |
| Mean | 42 |
| Std Dev | 3.818 |



B1L100S, U9-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



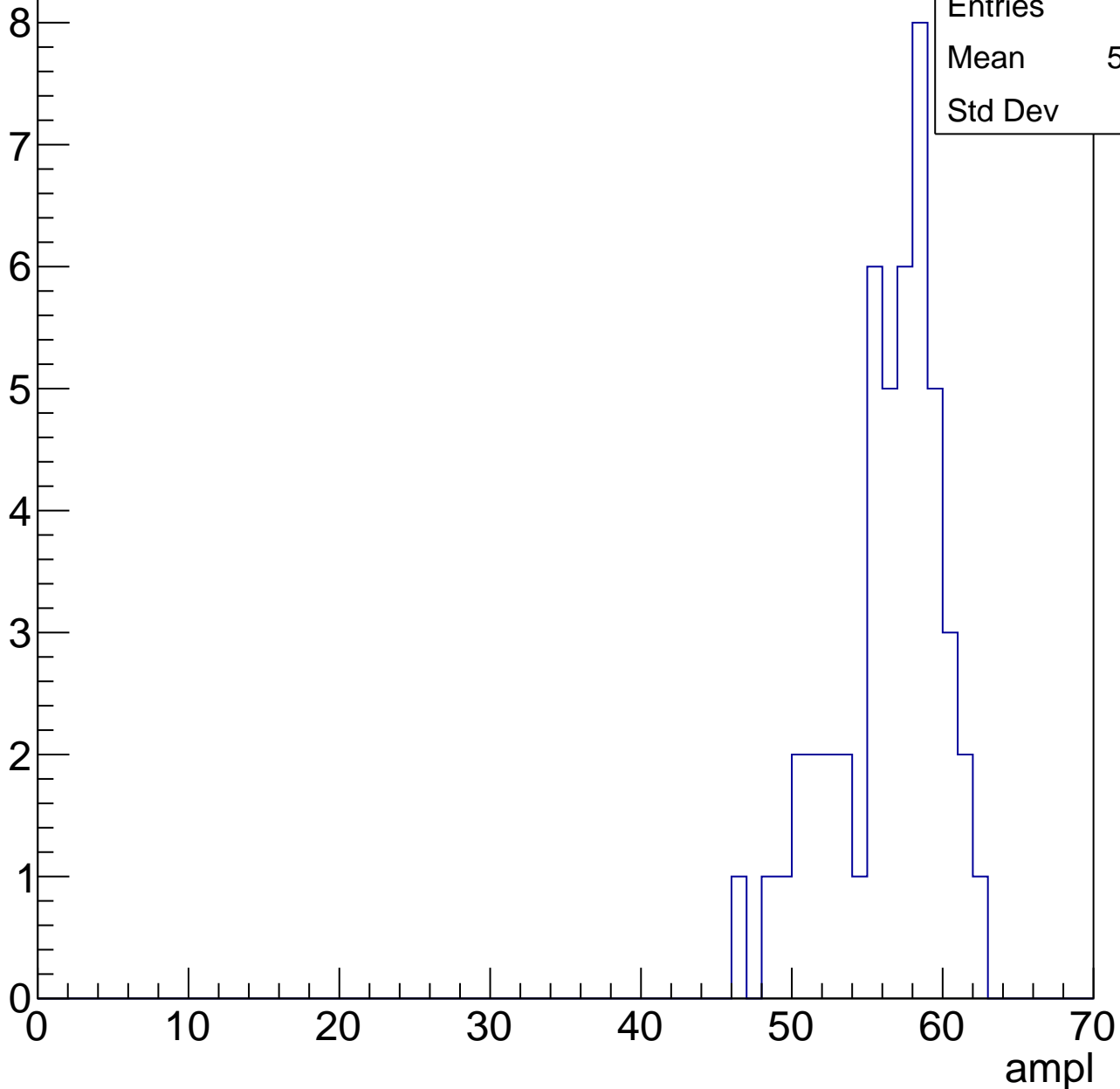
| | |
|---------|------|
| Entries | 99 |
| Mean | 49.3 |
| Std Dev | 4.35 |

B1L100S, U9-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

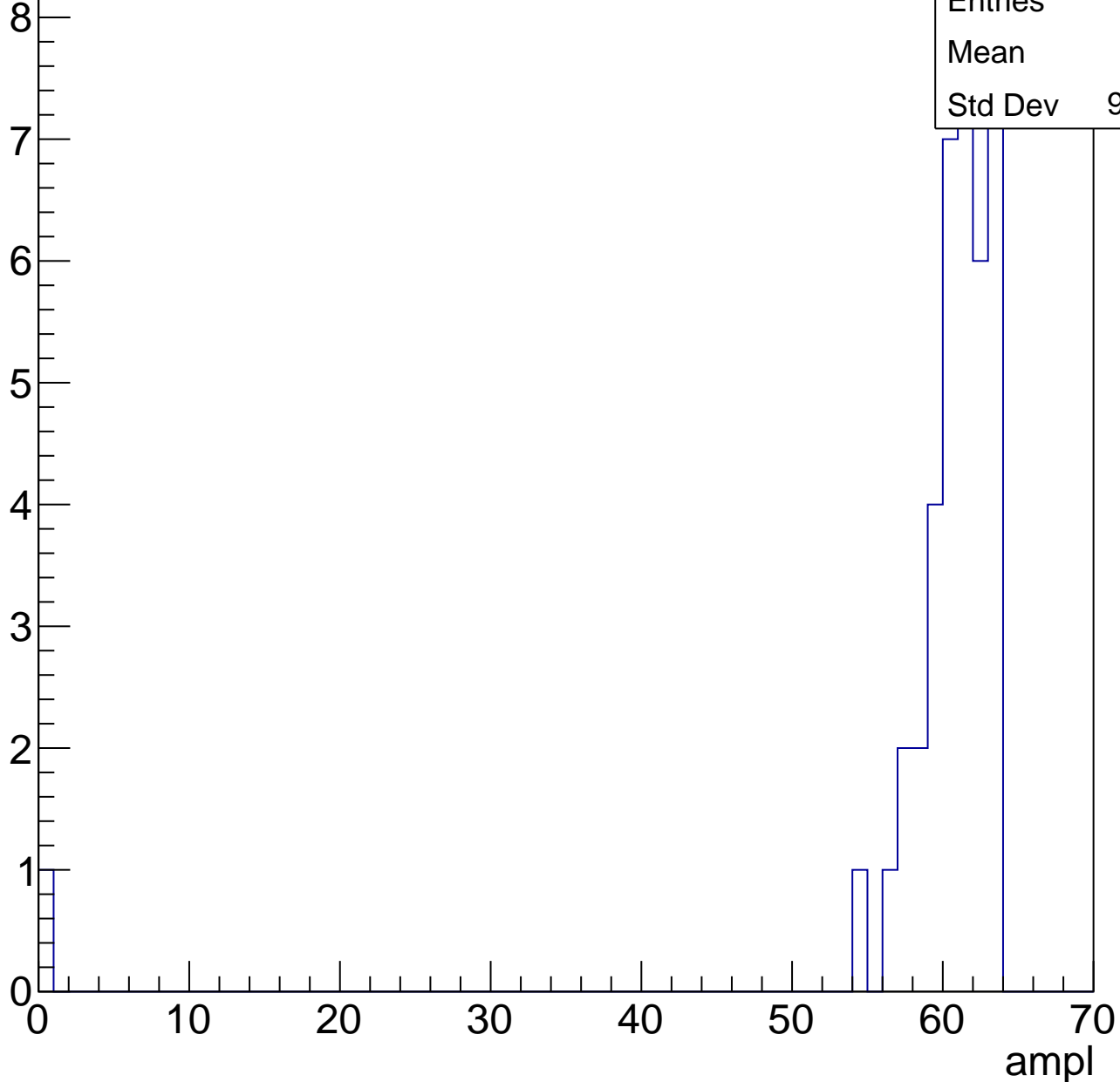
| | |
|---------|-------|
| Entries | 48 |
| Mean | 55.92 |
| Std Dev | 3.57 |



B1L100S, U9-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

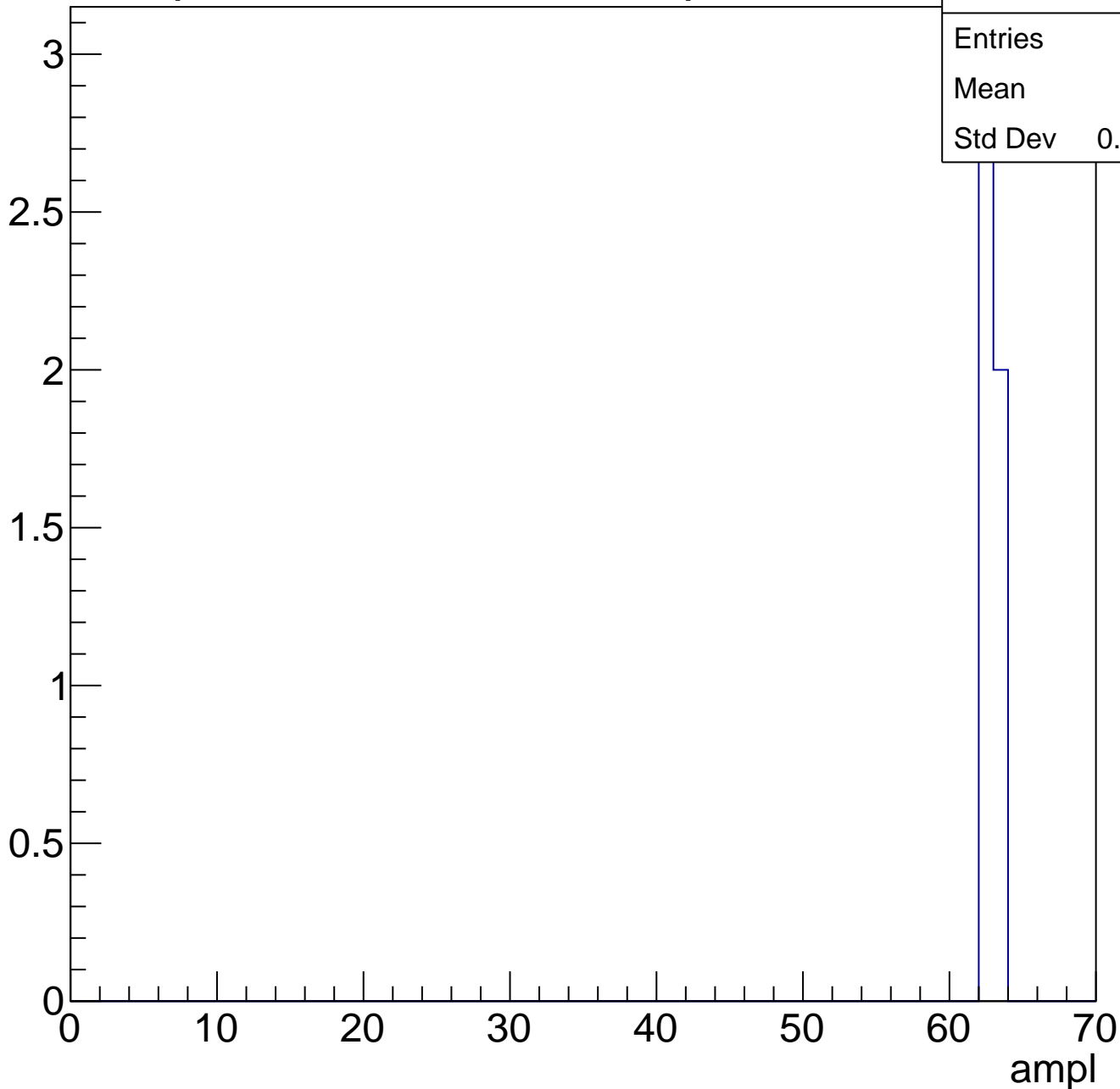
Entry



B1L100S, U9-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch127, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 29.25 |
| Std Dev | 5.029 |

Gaus mean : 30.0494

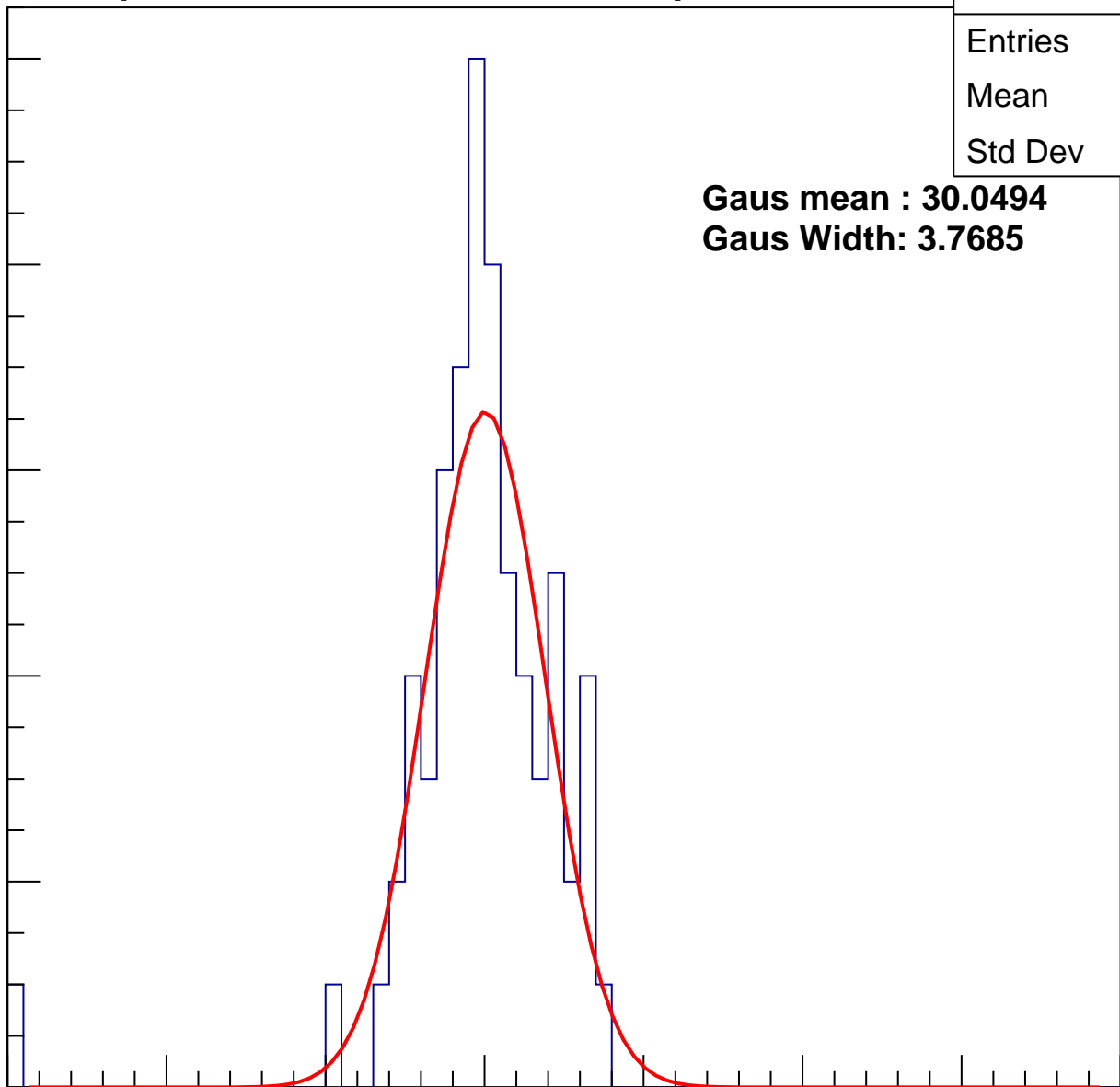
Gaus Width: 3.7685

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



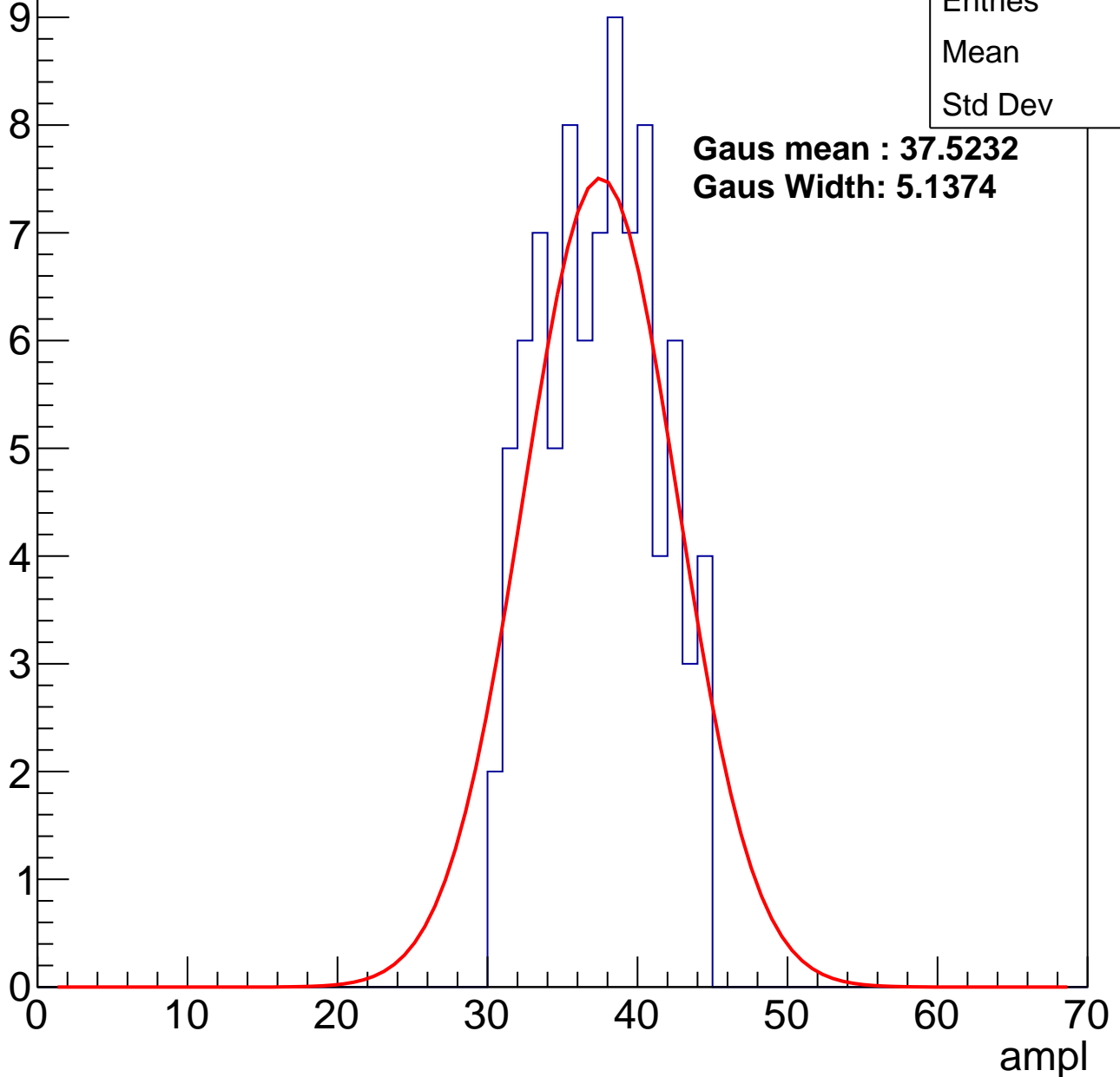
B1L100S, U9-ch127, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 87 |
| Mean | 37 |
| Std Dev | 3.79 |

Gaus mean : 37.5232
Gaus Width: 5.1374



B1L100S, U9-ch127, adc2

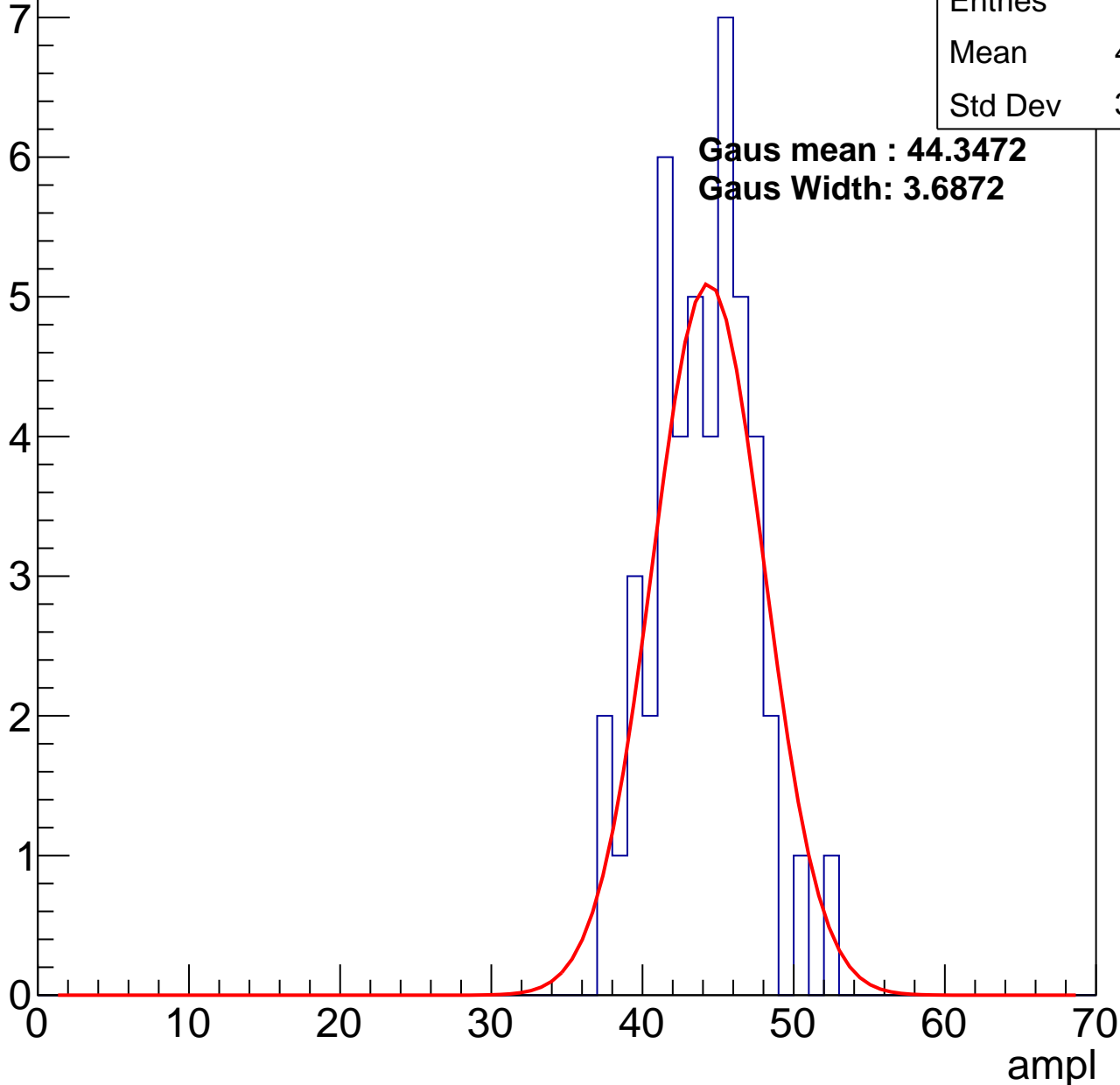
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 43.51 |
| Std Dev | 3.261 |

Gaus mean : 44.3472

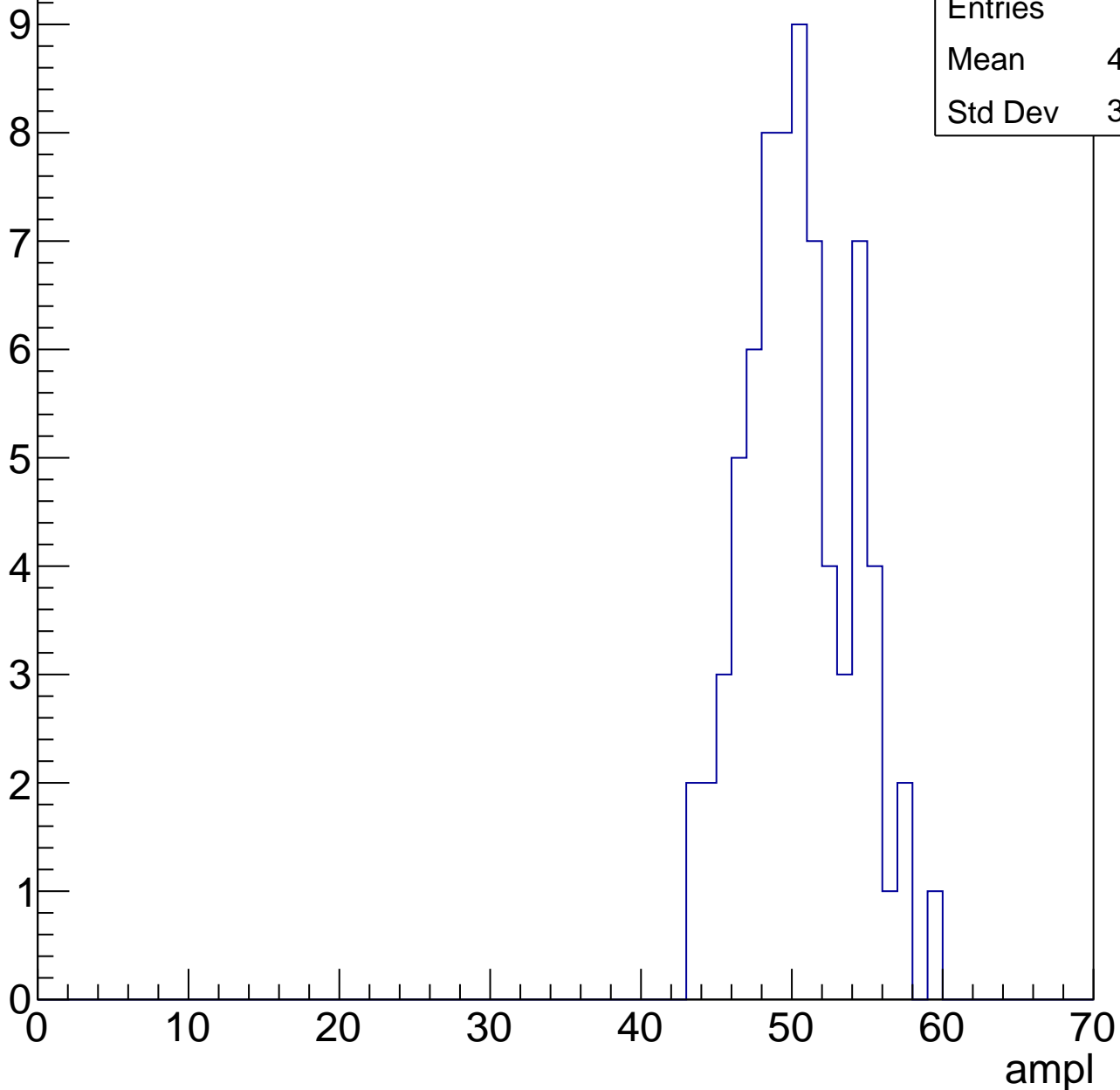
Gaus Width: 3.6872



B1L100S, U9-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

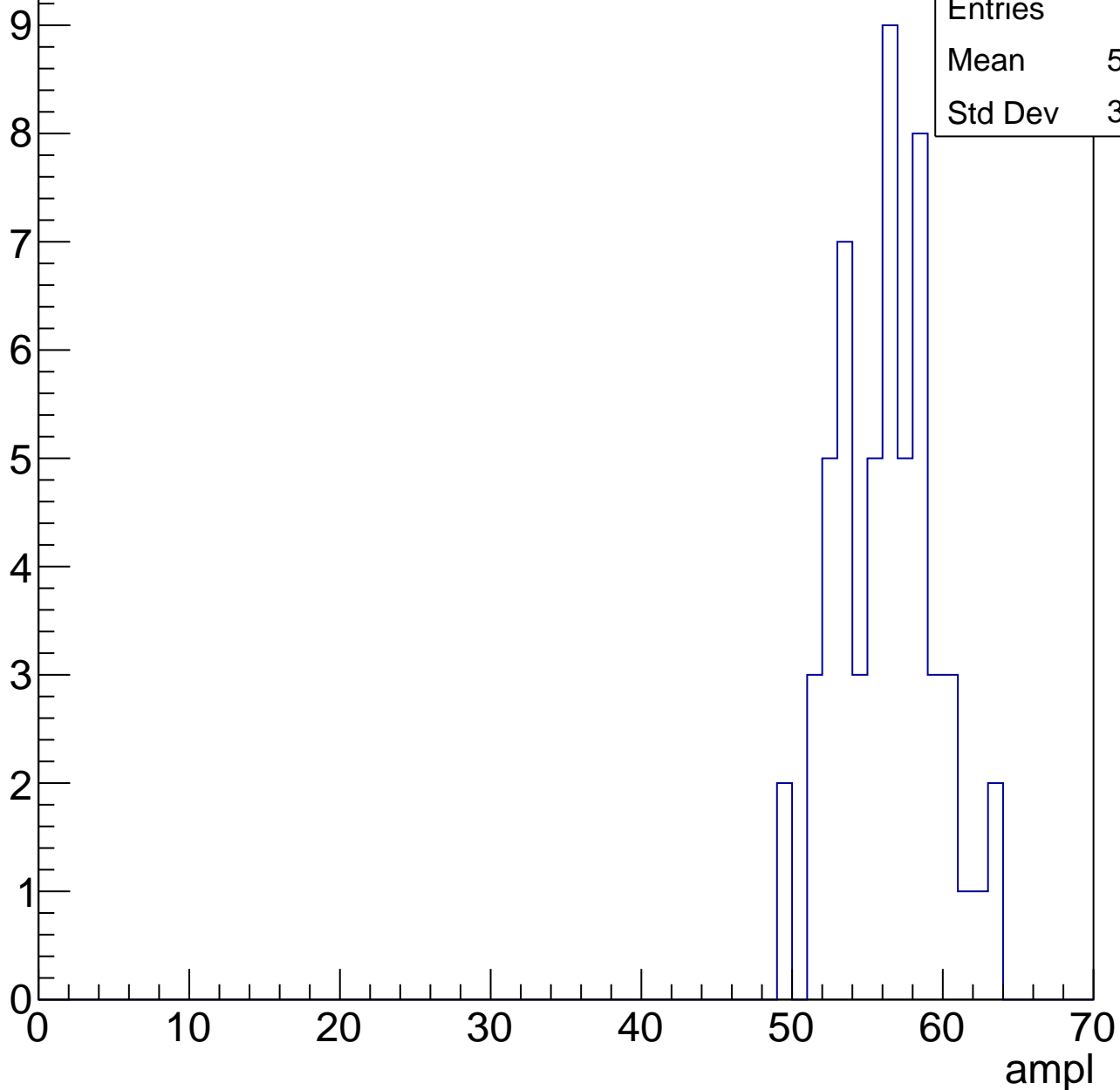


| | |
|---------|-------|
| Entries | 72 |
| Mean | 49.97 |
| Std Dev | 3.528 |

B1L100S, U9-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

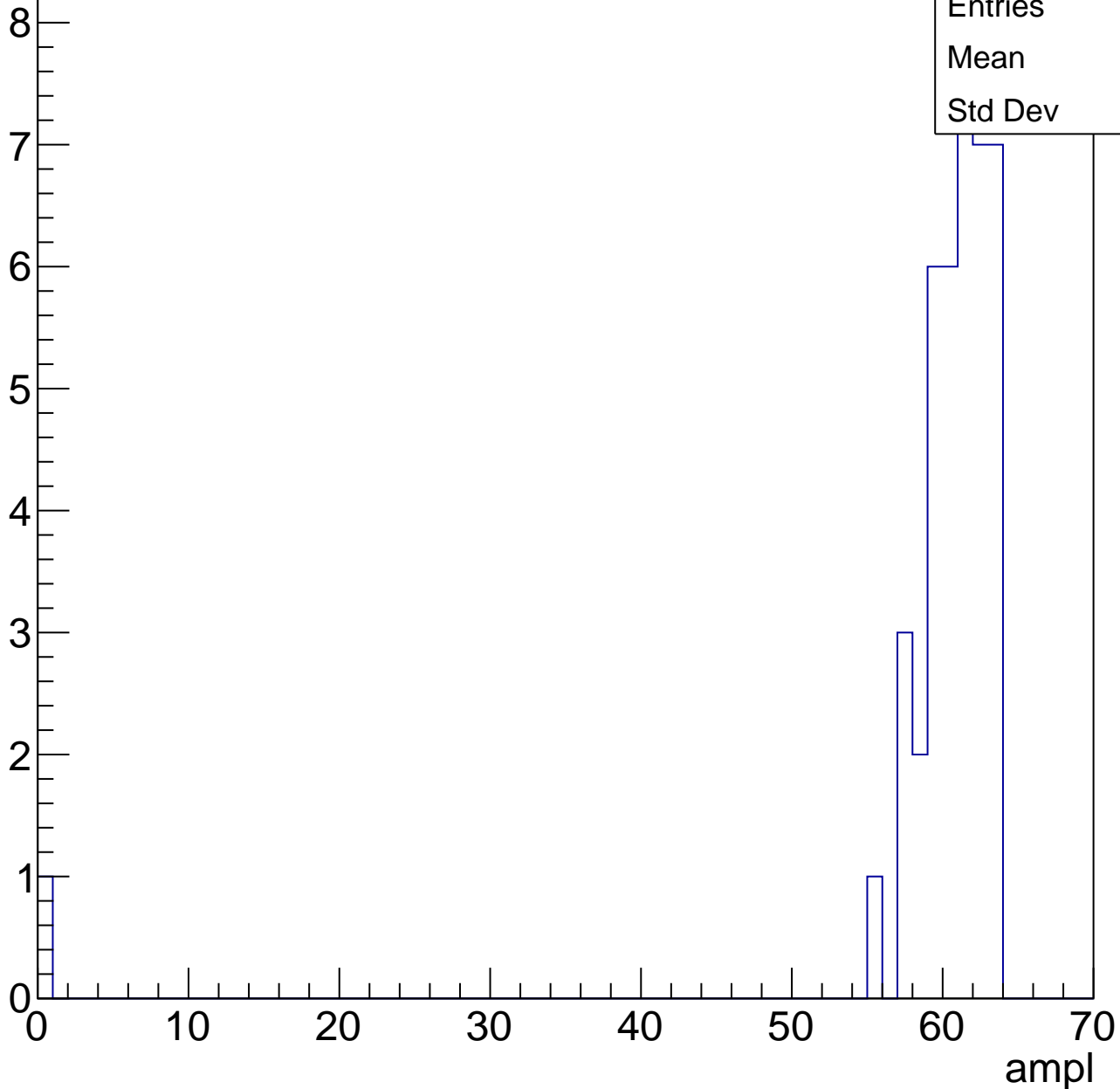


B1L100S, U9-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 41 |
| Mean | 59 |
| Std Dev | 9.53 |



B1L100S, U9-ch127, adc6

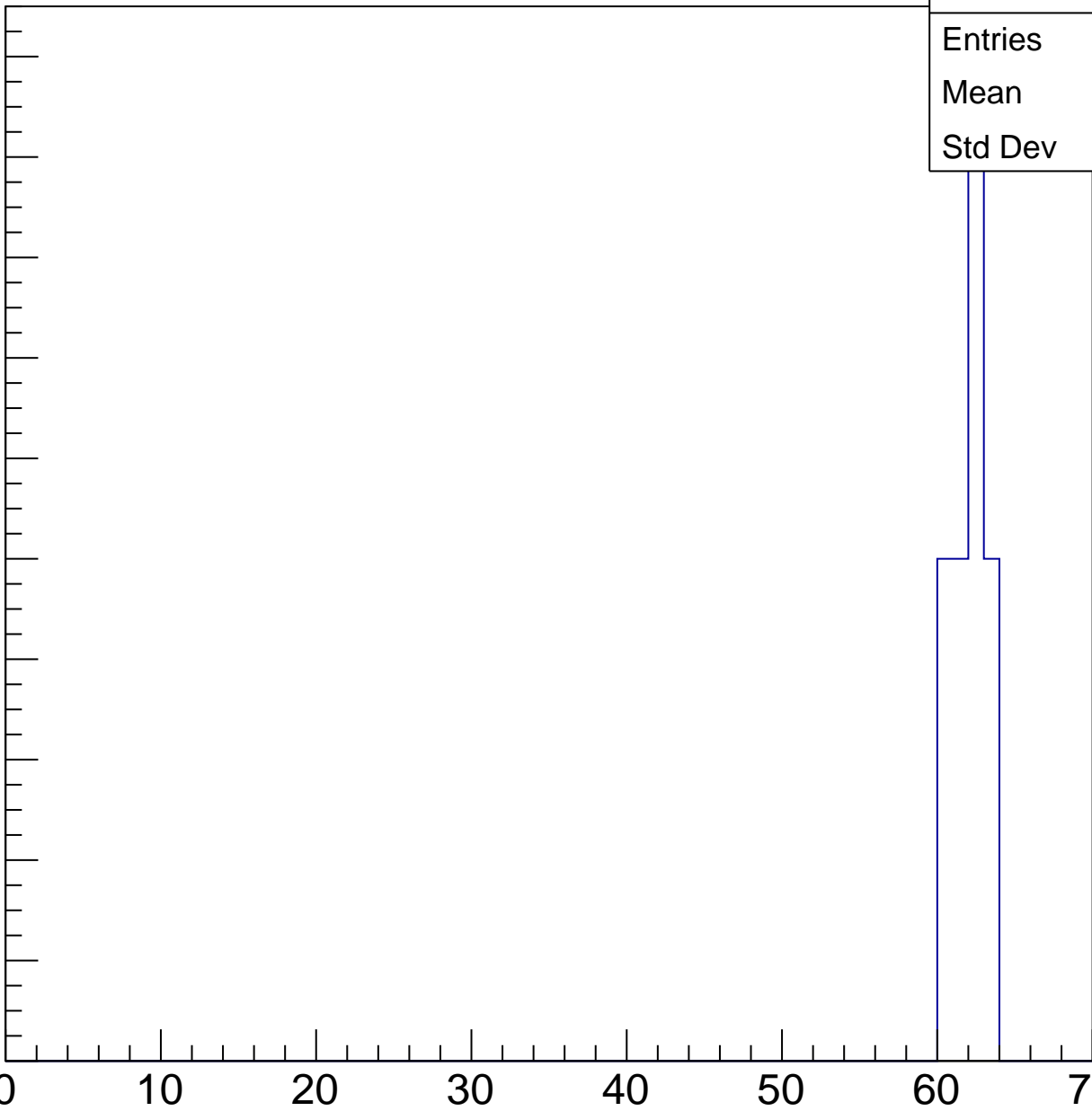
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 5 |
| Mean | 61.6 |
| Std Dev | 1.02 |

ampl



B1L100S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |