

B1L002S, U25-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.14
Std Dev	11.58

Turn on : 26.1320

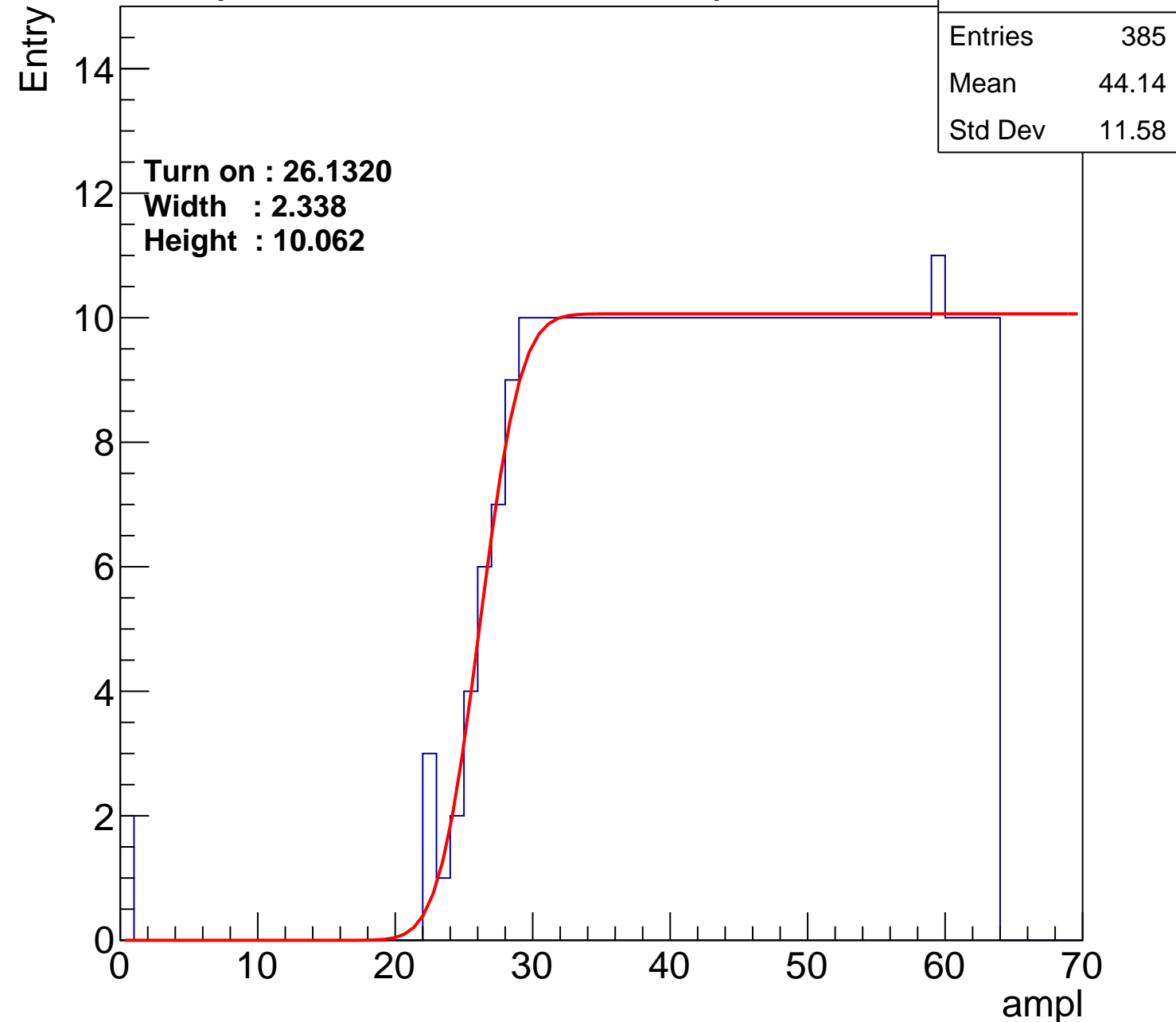
Width : 2.338

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.67
Std Dev	11.65

Turn on : 24.7112

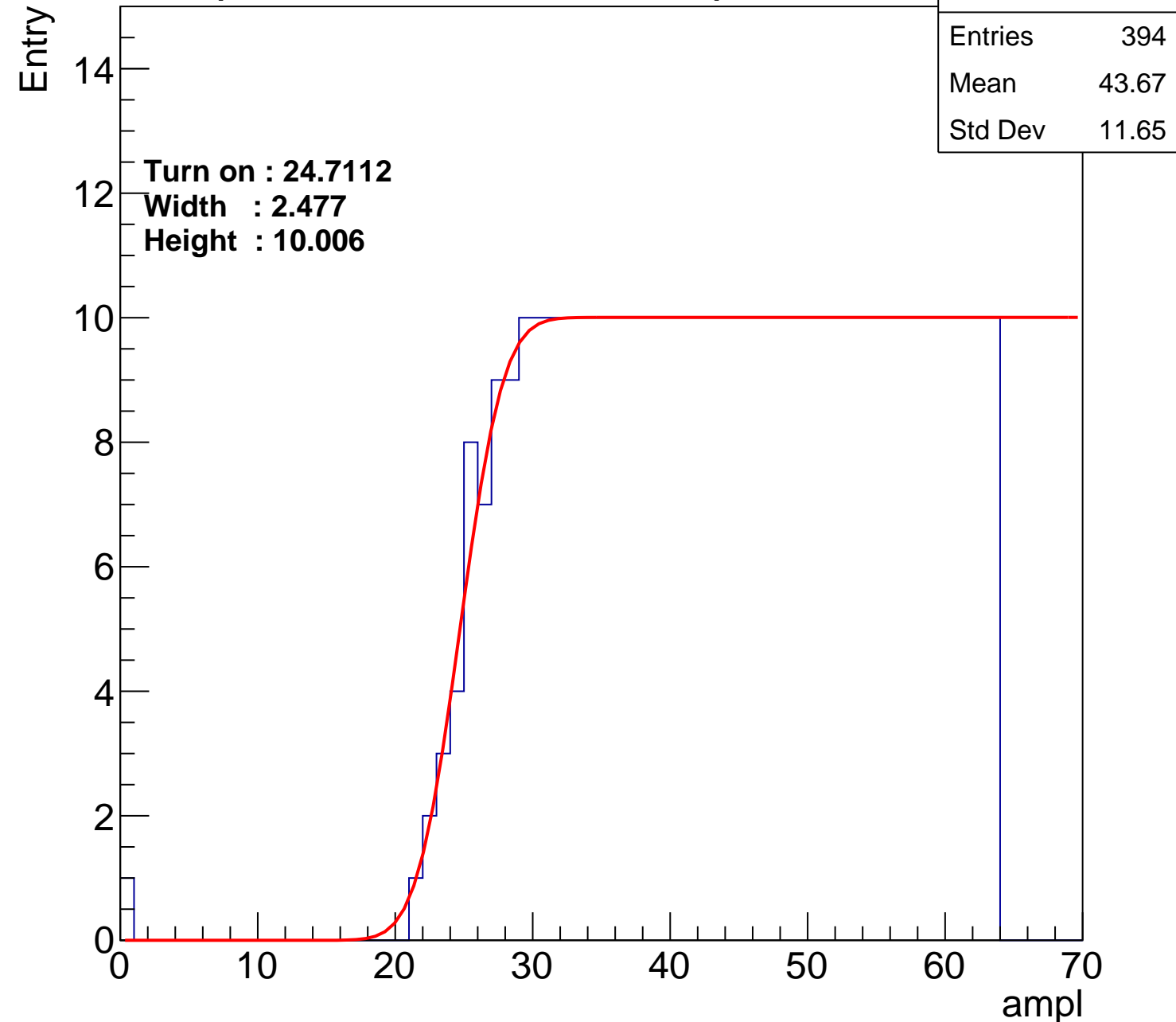
Width : 2.477

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.6905

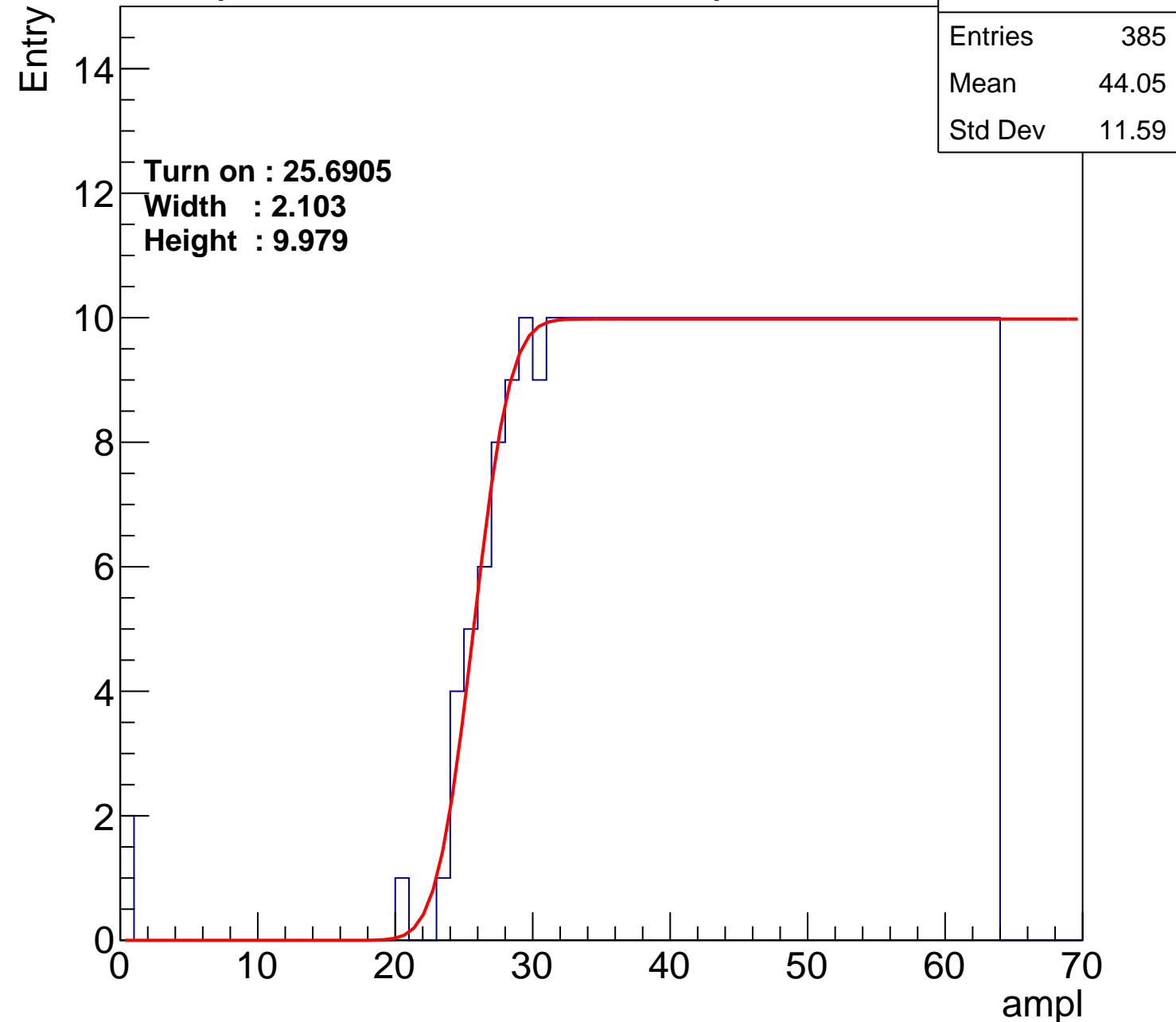
Width : 2.103

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch3

calib_packv5_042523_0143.root, FC#10, port B3

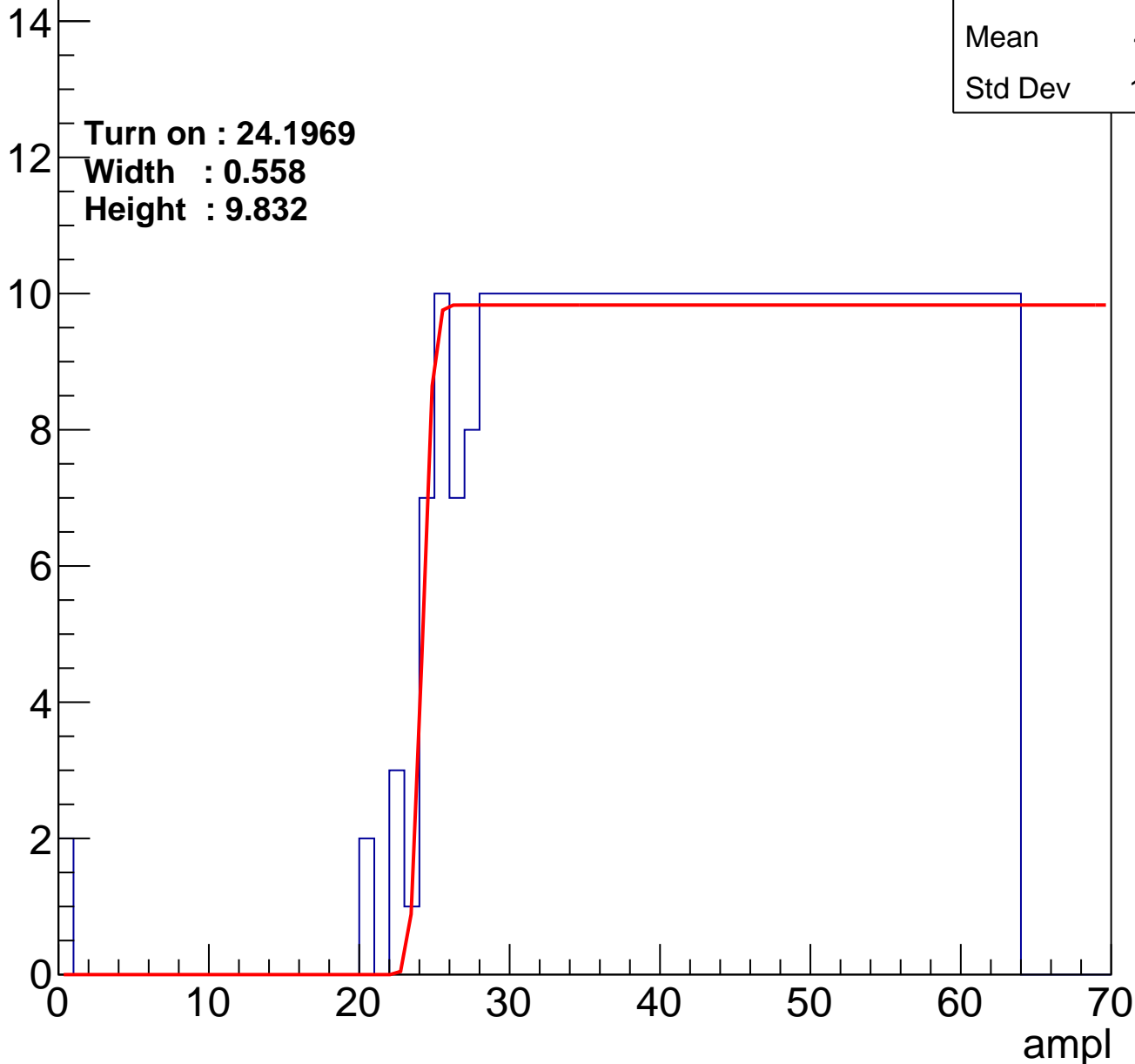
Entry

Entries	400
Mean	43.31
Std Dev	11.98

Turn on : 24.1969

Width : 0.558

Height : 9.832



B1L002S, U25-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.9462

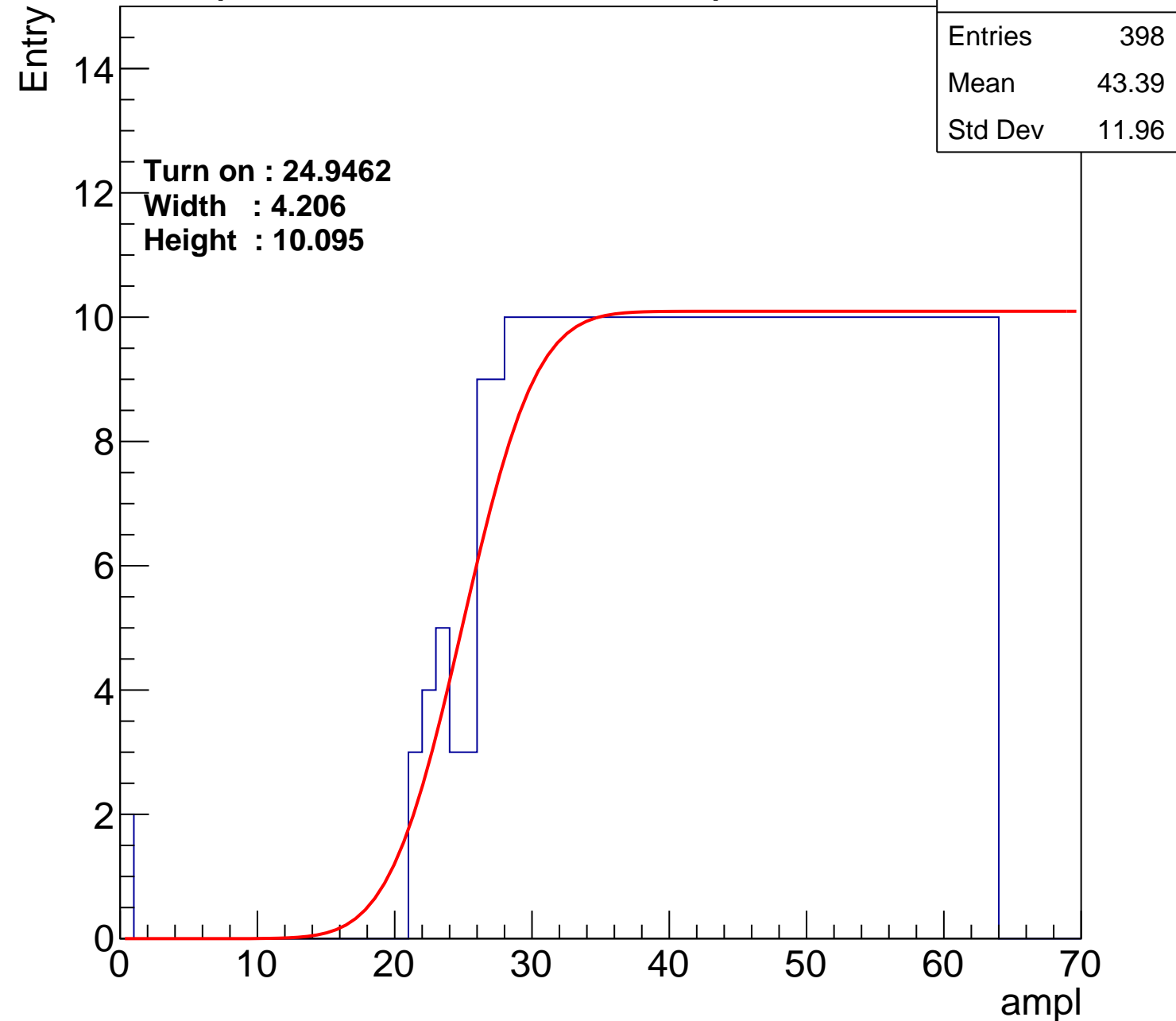
Width : 4.206

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.47
Std Dev	11.76

Turn on : 24.6316

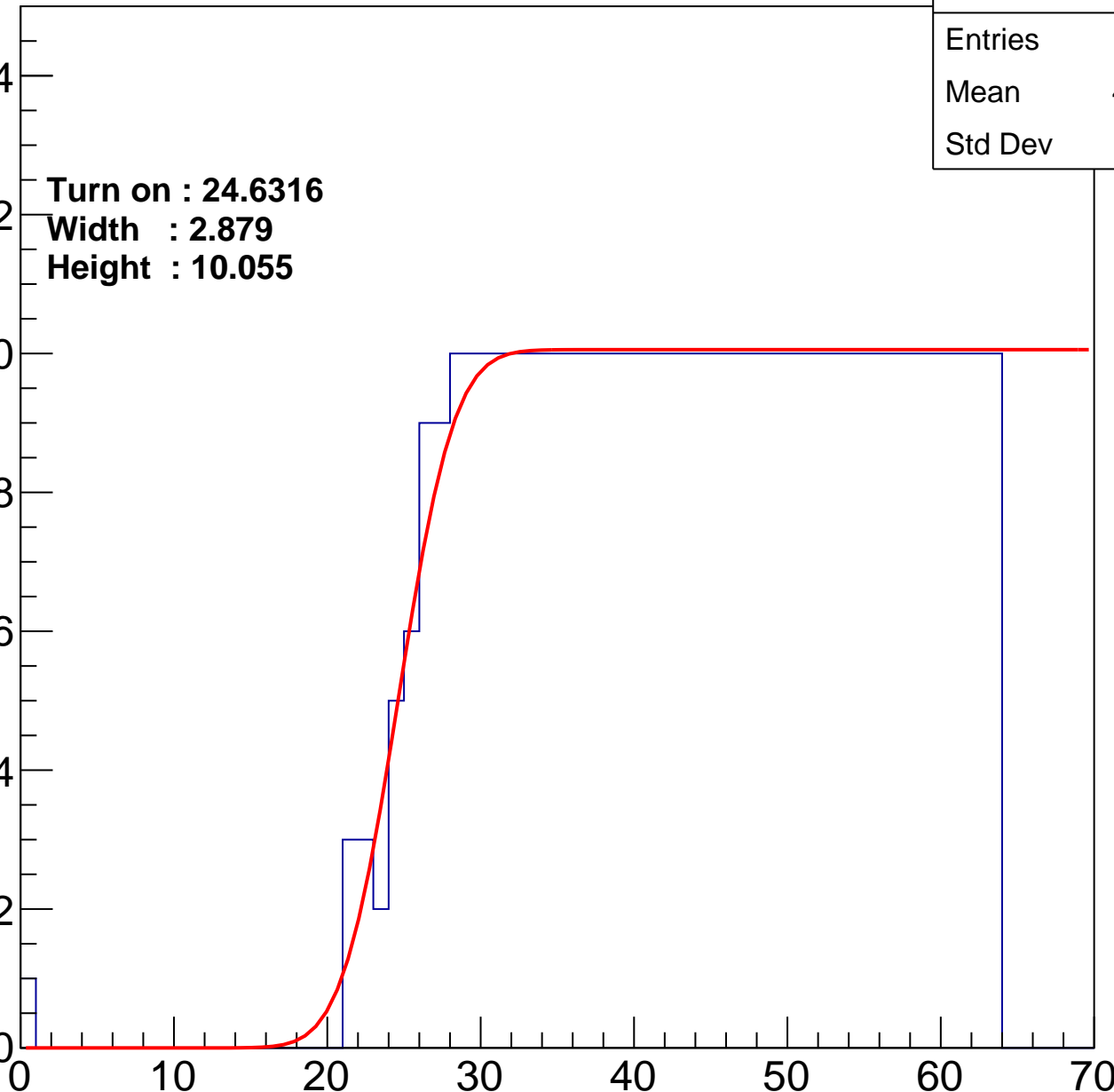
Width : 2.879

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.11

Turn on : 26.9305

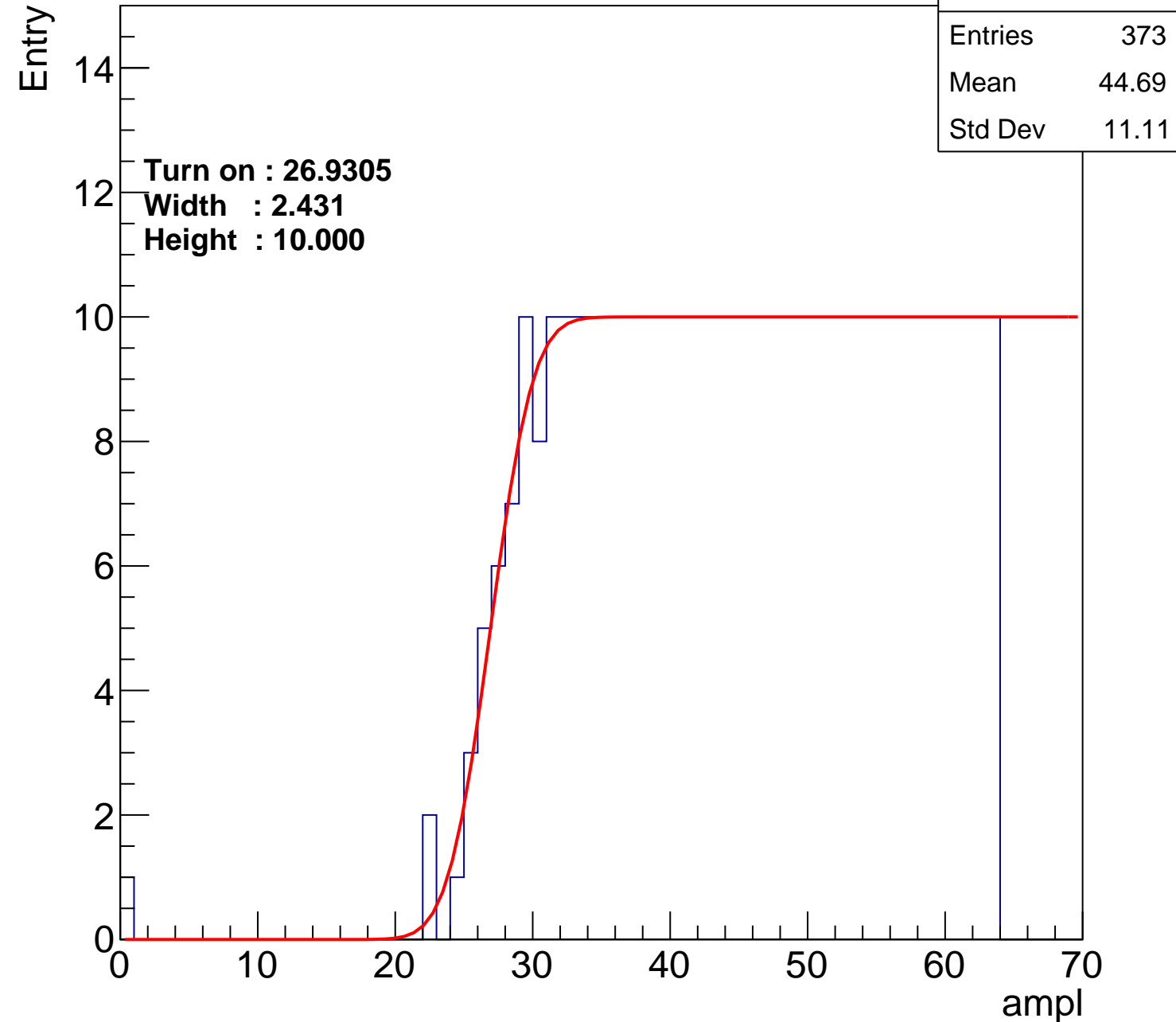
Width : 2.431

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.7
Std Dev	11.11

Turn on : 27.0997

Width : 2.647

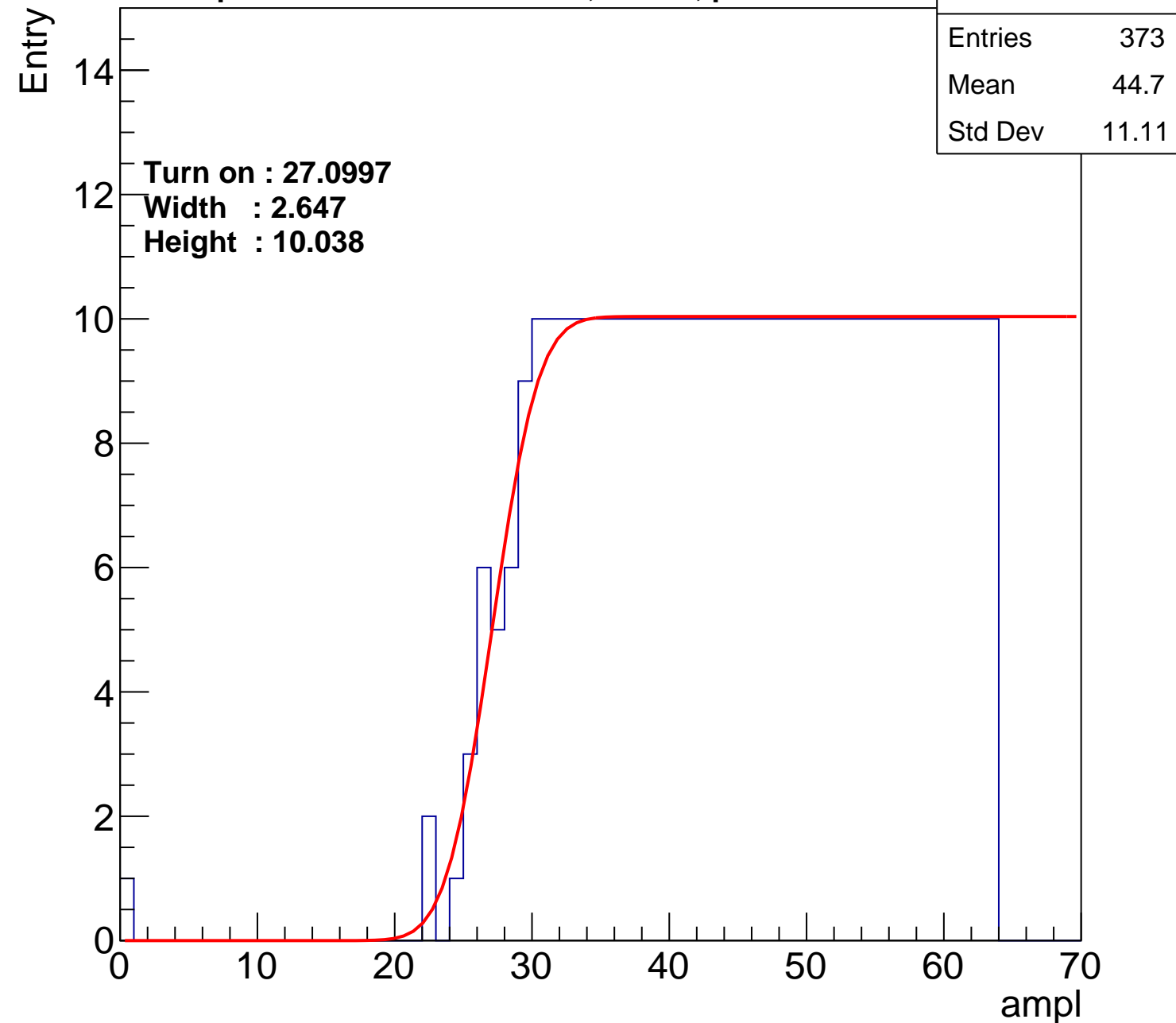
Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U25-ch8

calib_packv5_042523_0143.root, FC#10, port B3

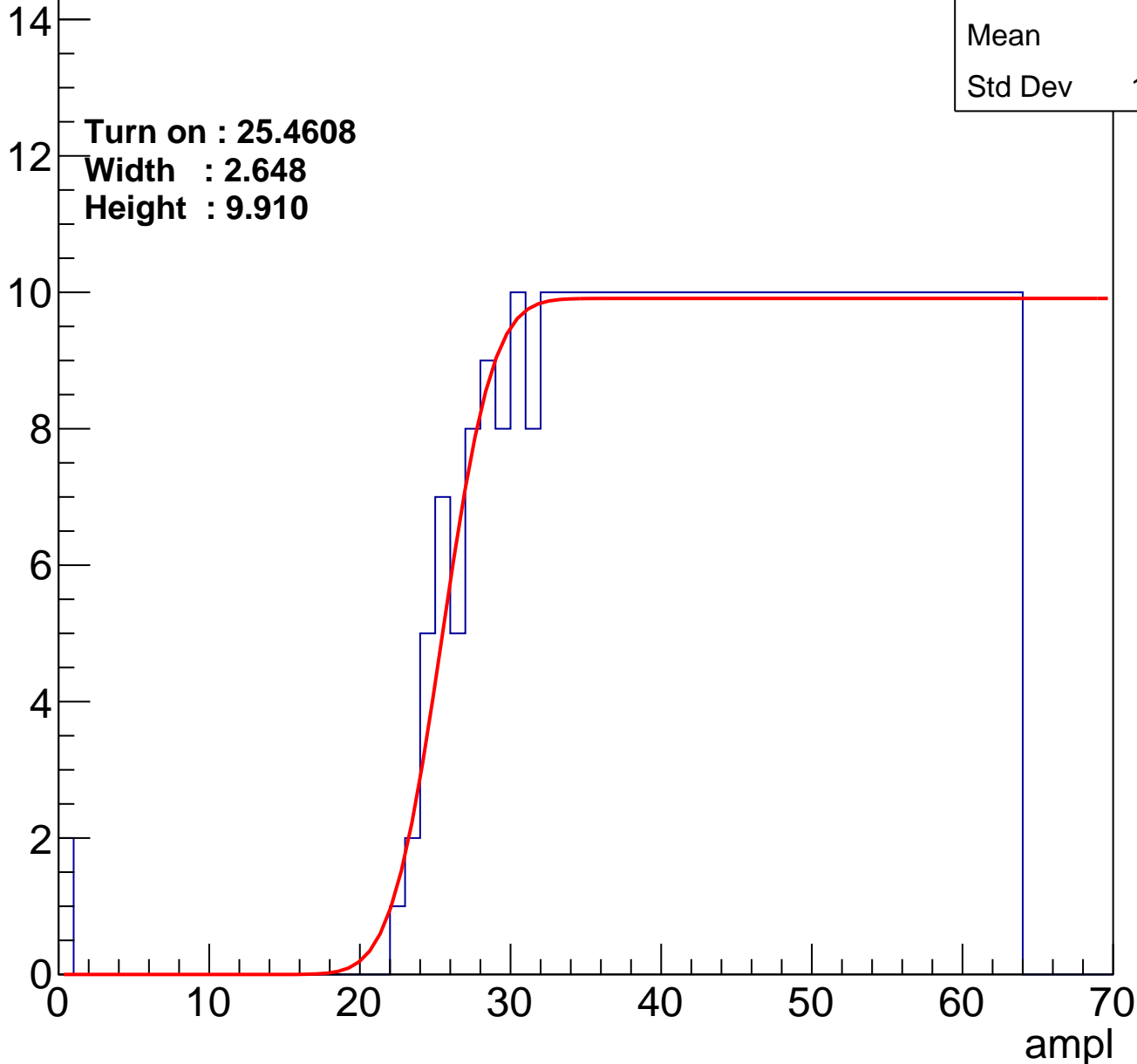
Entry

Entries	385
Mean	44
Std Dev	11.66

Turn on : 25.4608

Width : 2.648

Height : 9.910



B1L002S, U25-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.74
Std Dev	11.22

Turn on : 26.9872

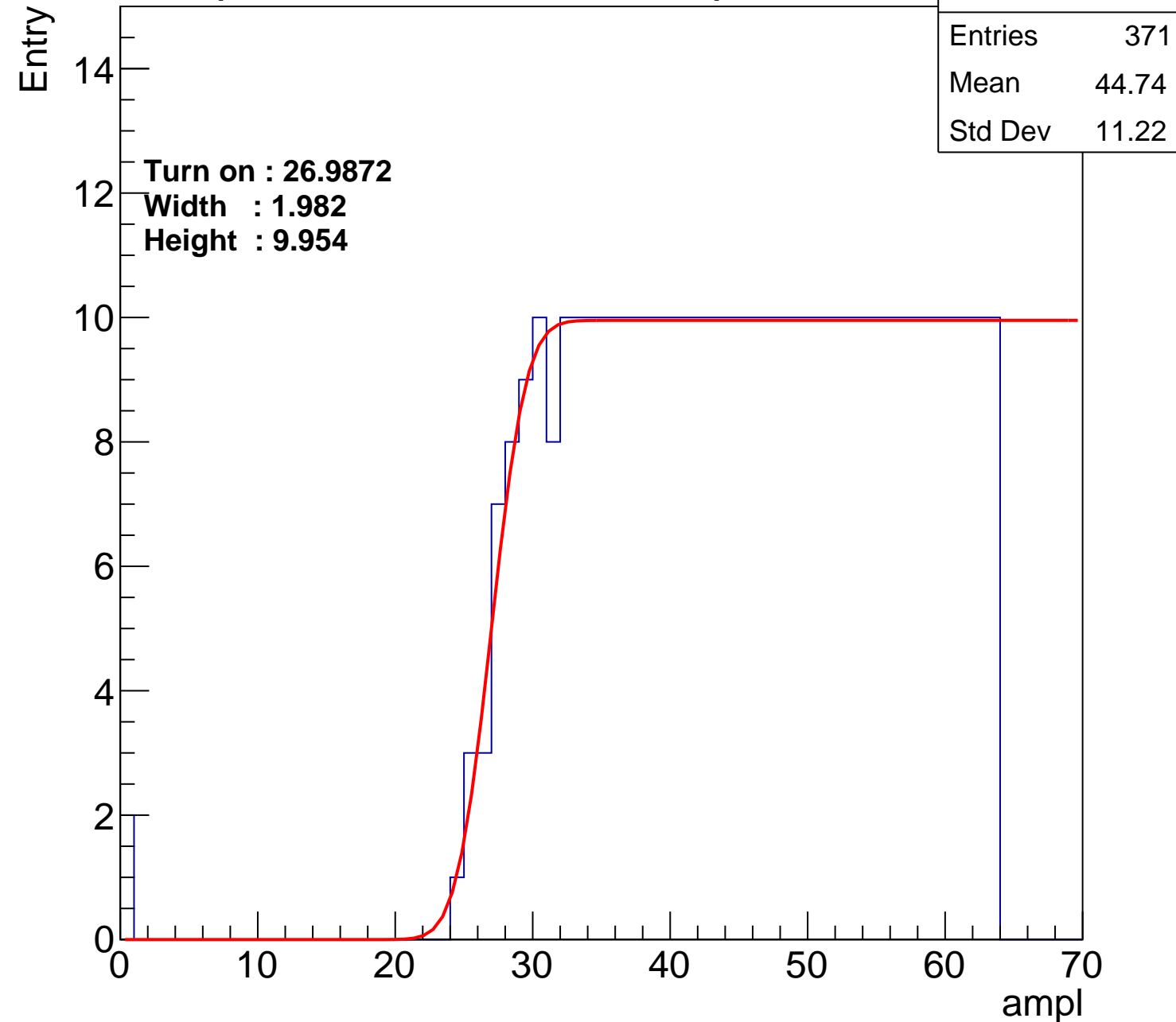
Width : 1.982

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch10

calib_packv5_042523_0143.root, FC#10, port B3

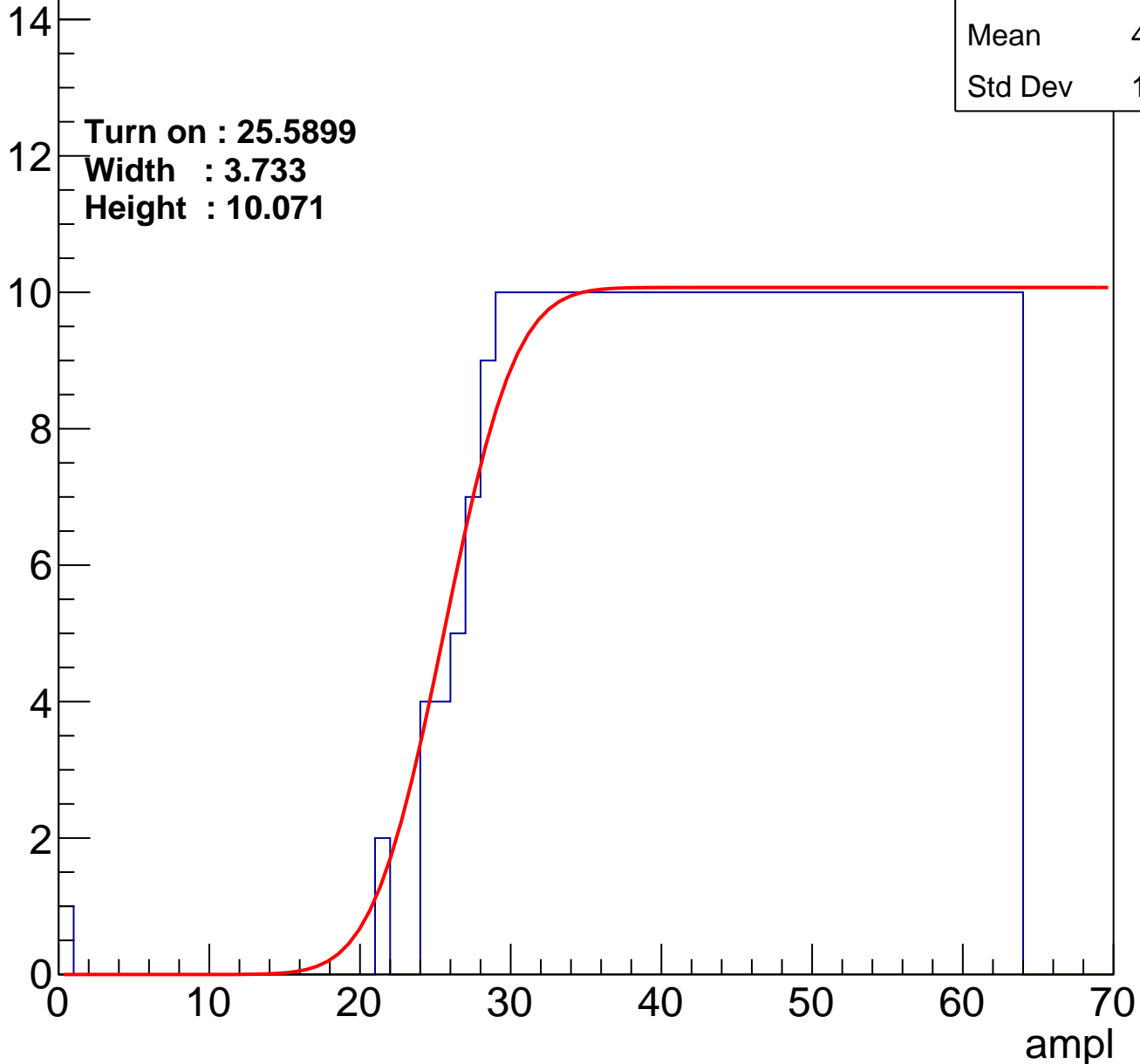
Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.5899

Width : 3.733

Height : 10.071

Entry



B1L002S, U25-ch11

calib_packv5_042523_0143.root, FC#10, port B3

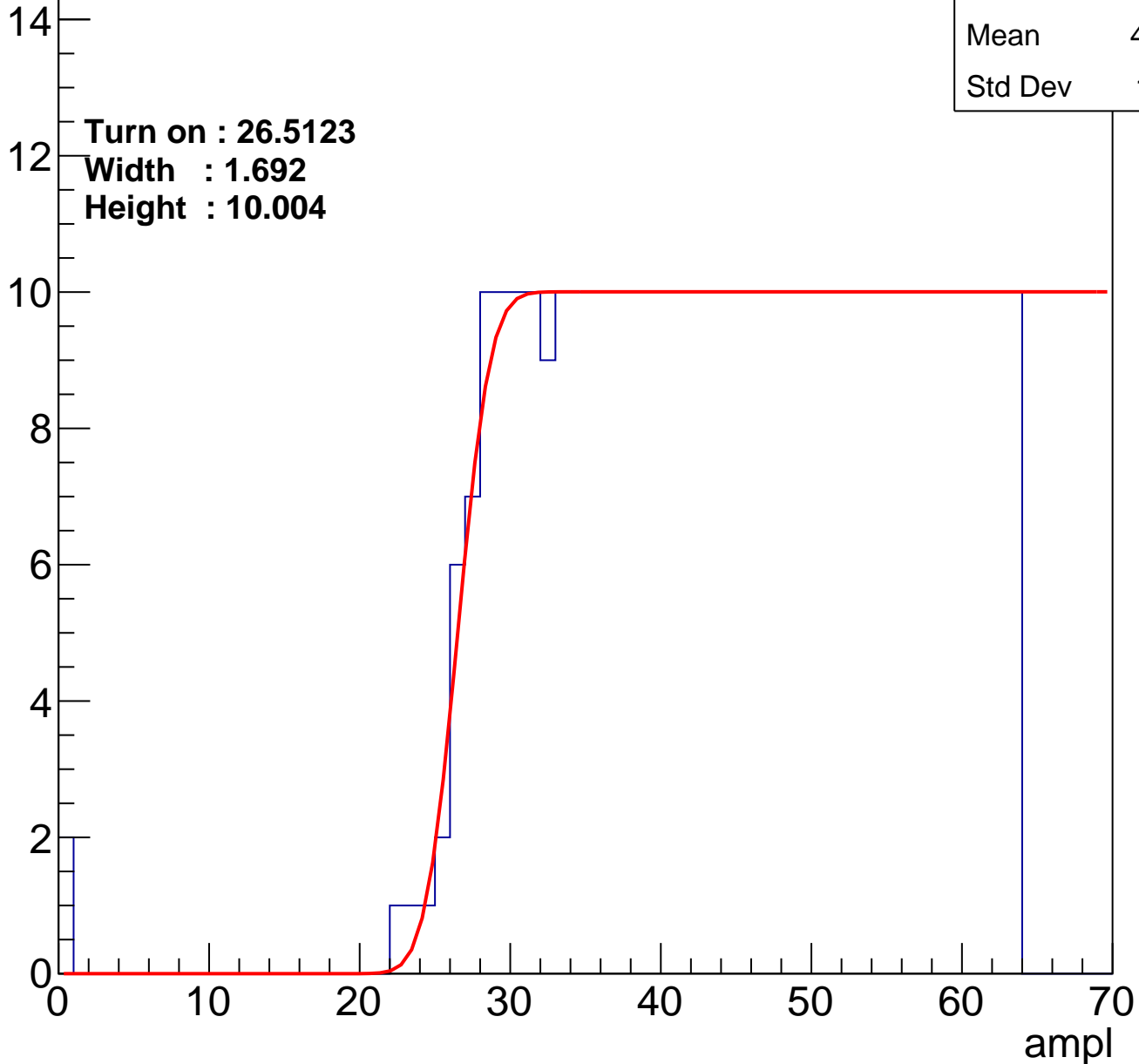
Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 26.5123

Width : 1.692

Height : 10.004

Entry



B1L002S, U25-ch12

calib_packv5_042523_0143.root, FC#10, port B3

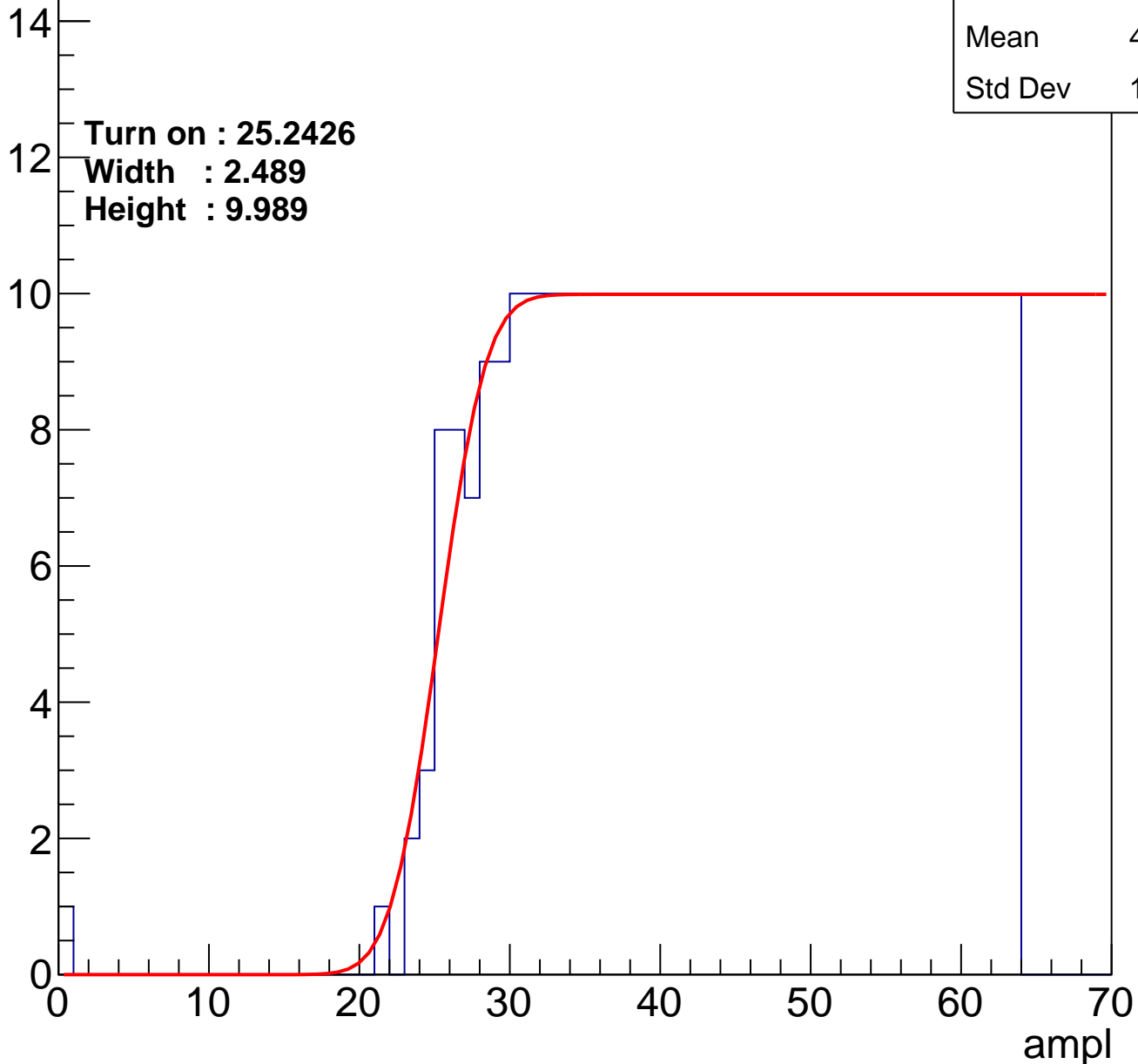
Entries	388
Mean	43.97
Std Dev	11.49

Turn on : 25.2426

Width : 2.489

Height : 9.989

Entry



B1L002S, U25-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.67
Std Dev	12.18

Turn on : 25.6895

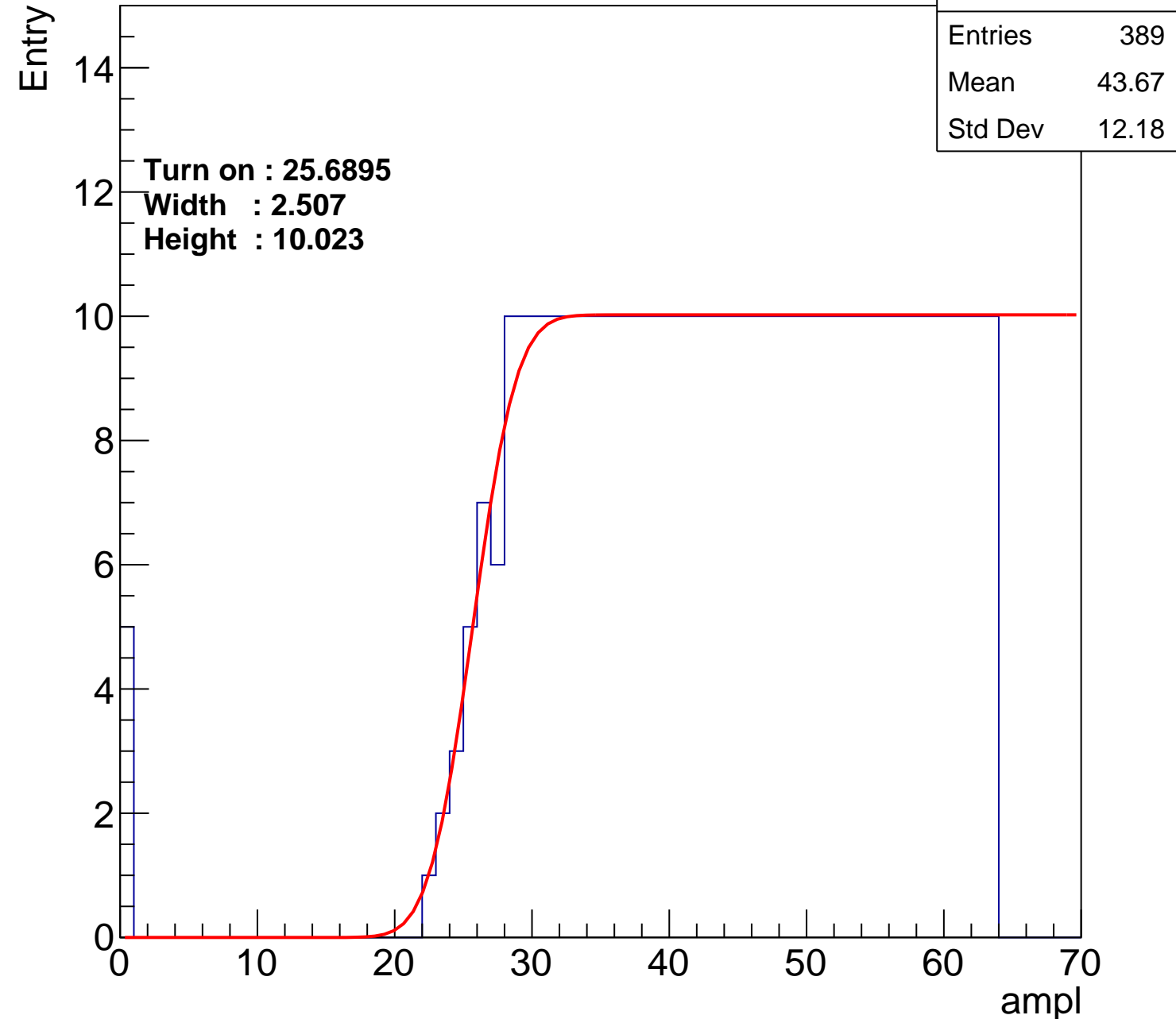
Width : 2.507

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.6658

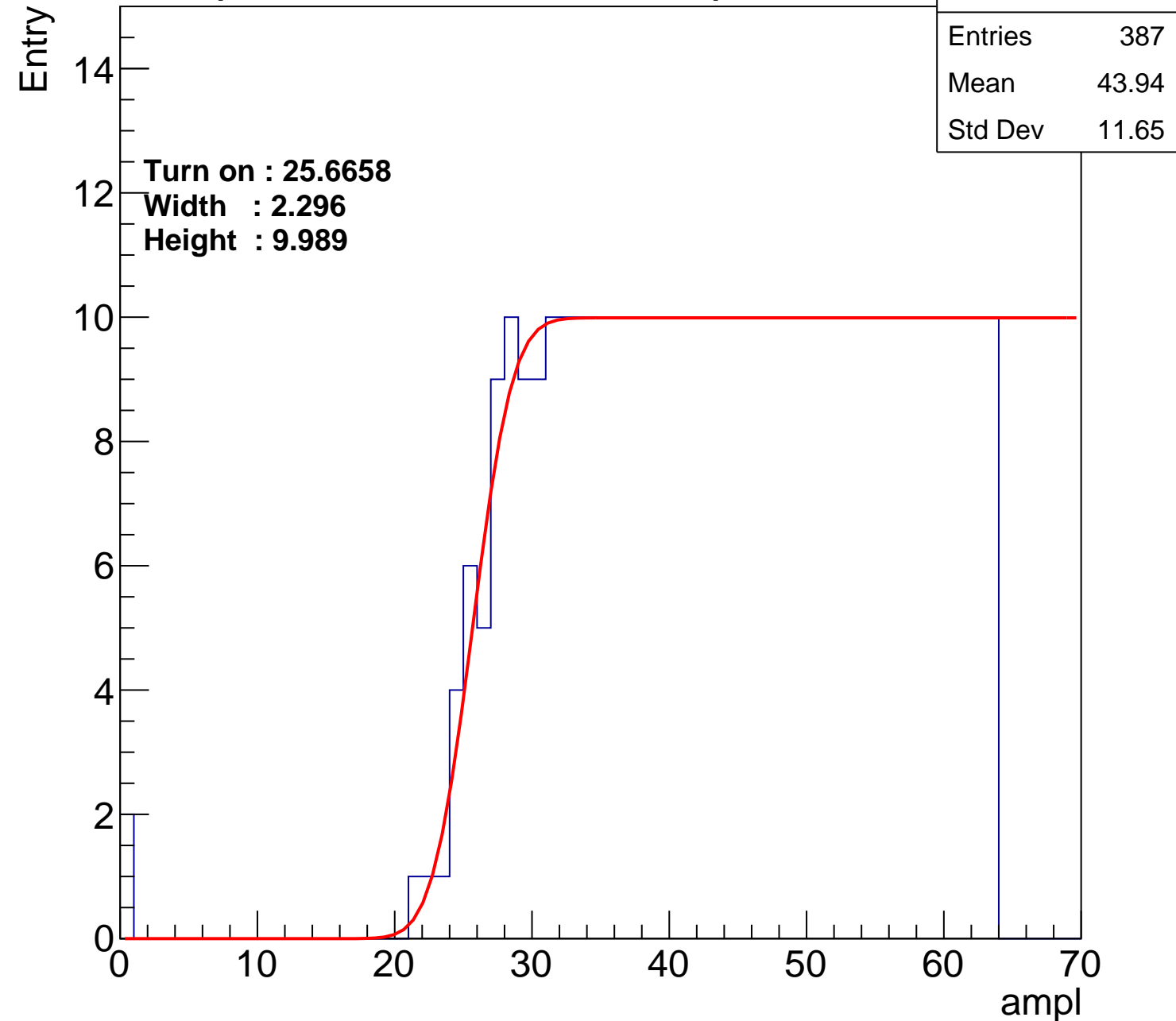
Width : 2.296

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch15

calib_packv5_042523_0143.root, FC#10, port B3

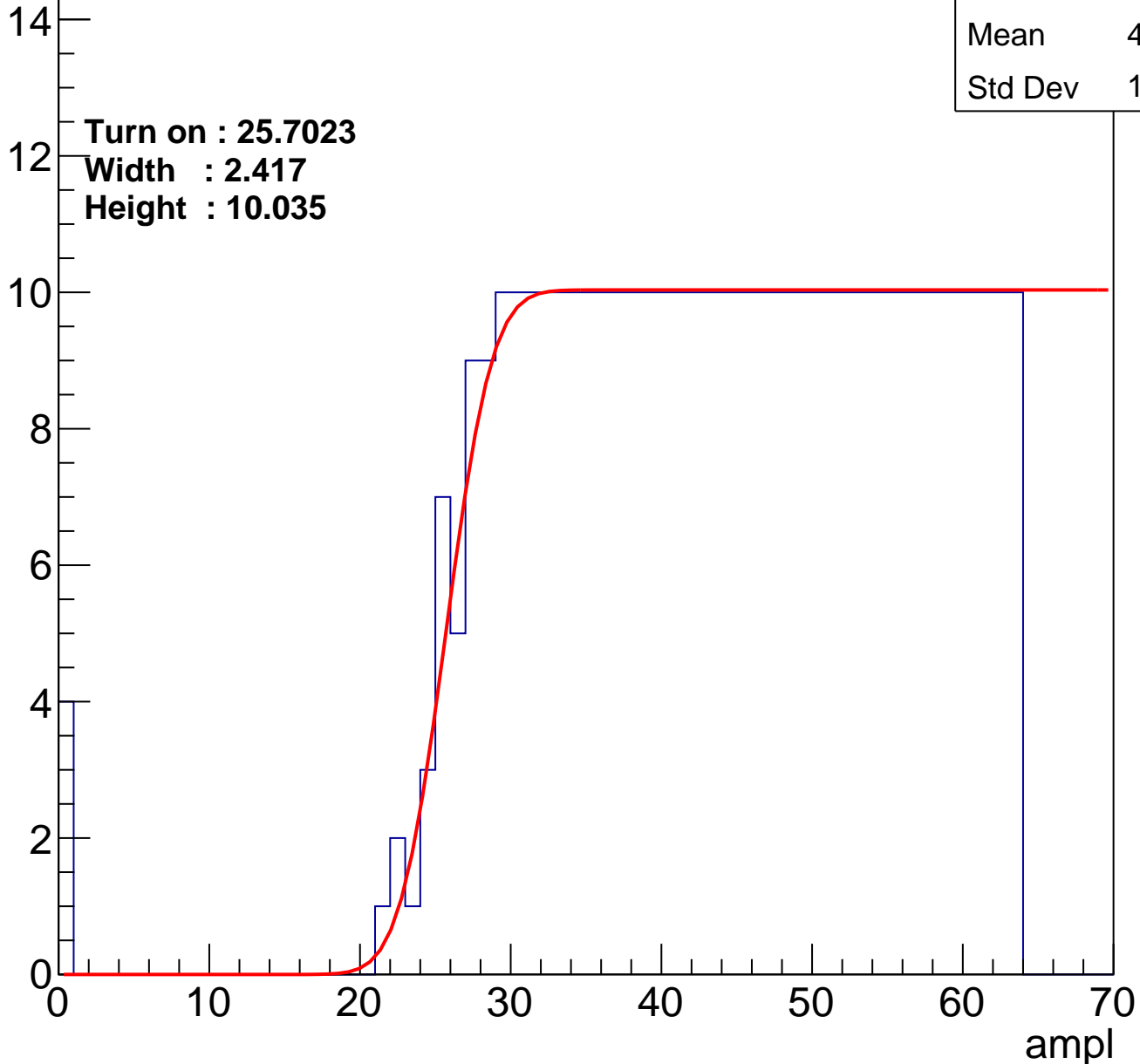
Entries	391
Mean	43.63
Std Dev	12.07

Turn on : 25.7023

Width : 2.417

Height : 10.035

Entry



B1L002S, U25-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.4098

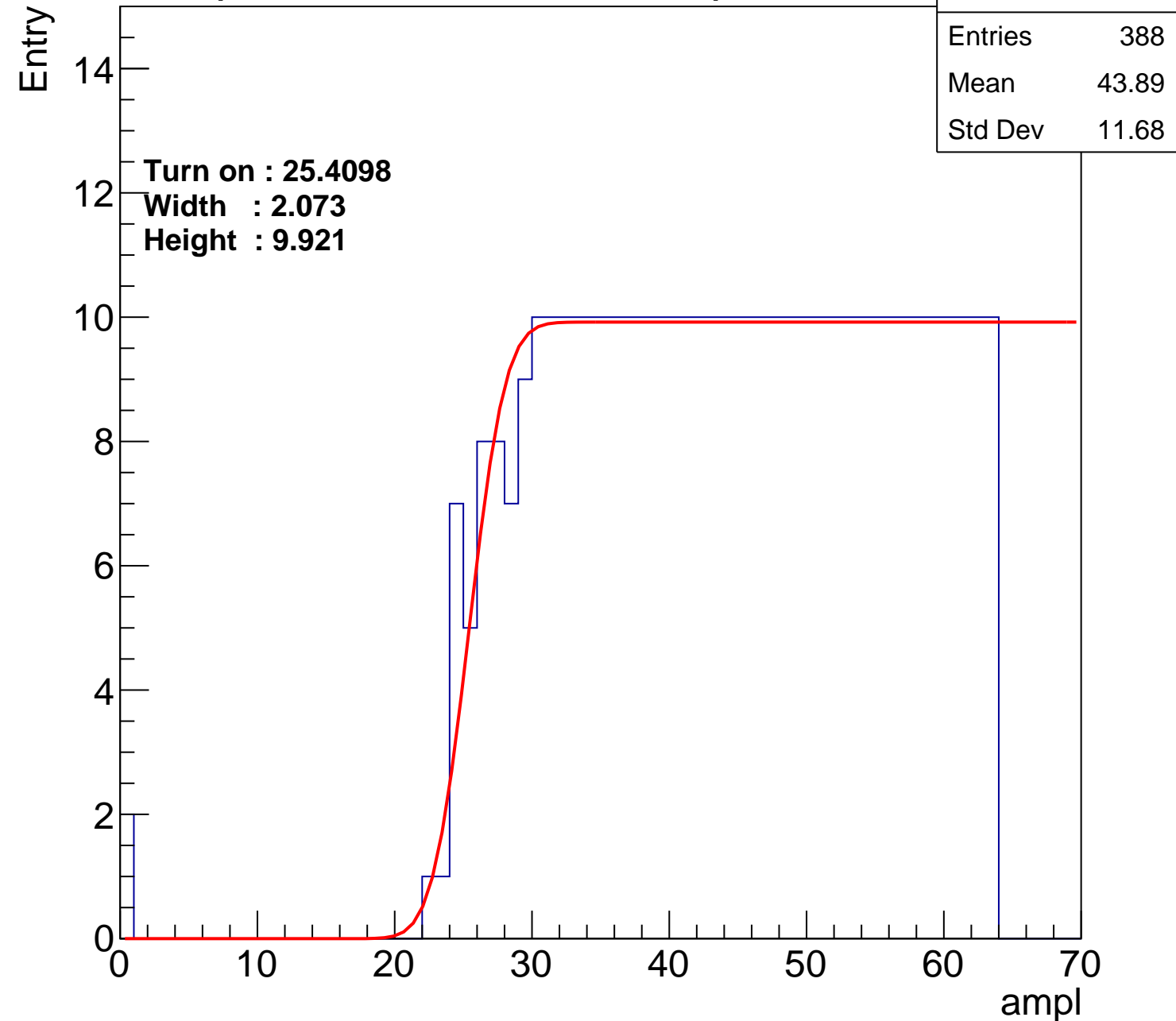
Width : 2.073

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	366
Mean	44.97
Std Dev	11.13

Turn on : 28.3324

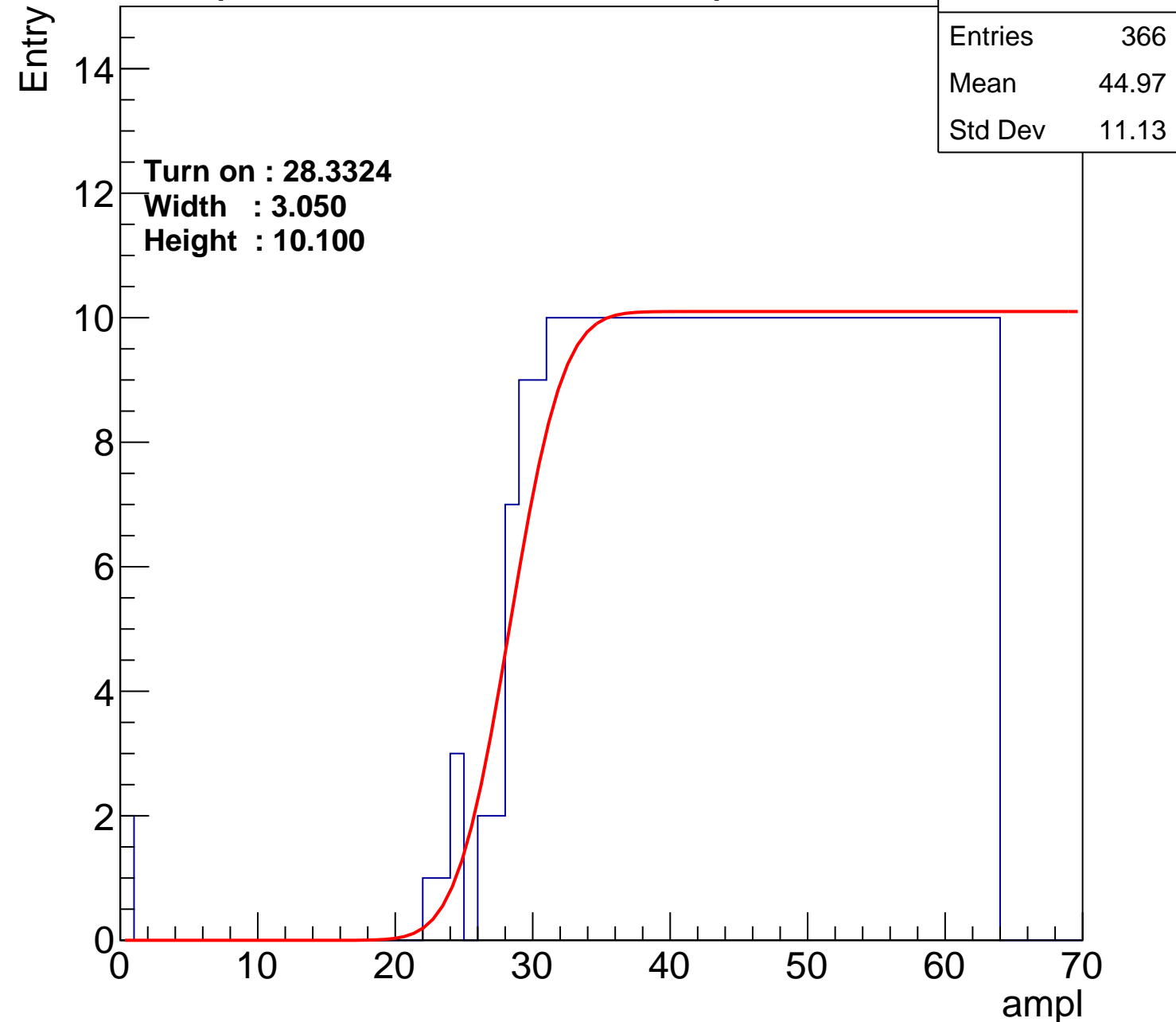
Width : 3.050

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.09
Std Dev	12.33

Turn on : 23.9288

Width : 2.126

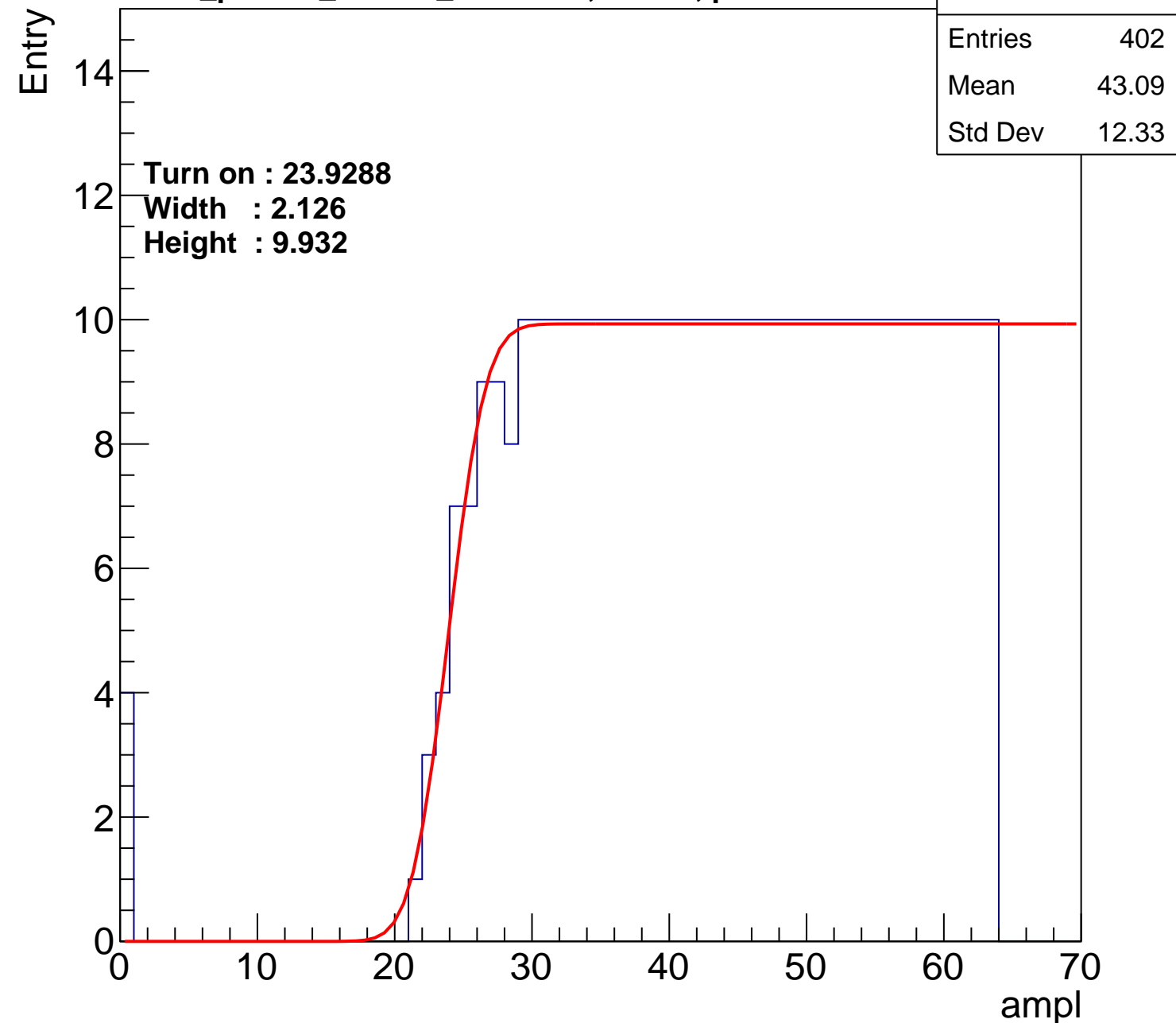
Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U25-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.3124

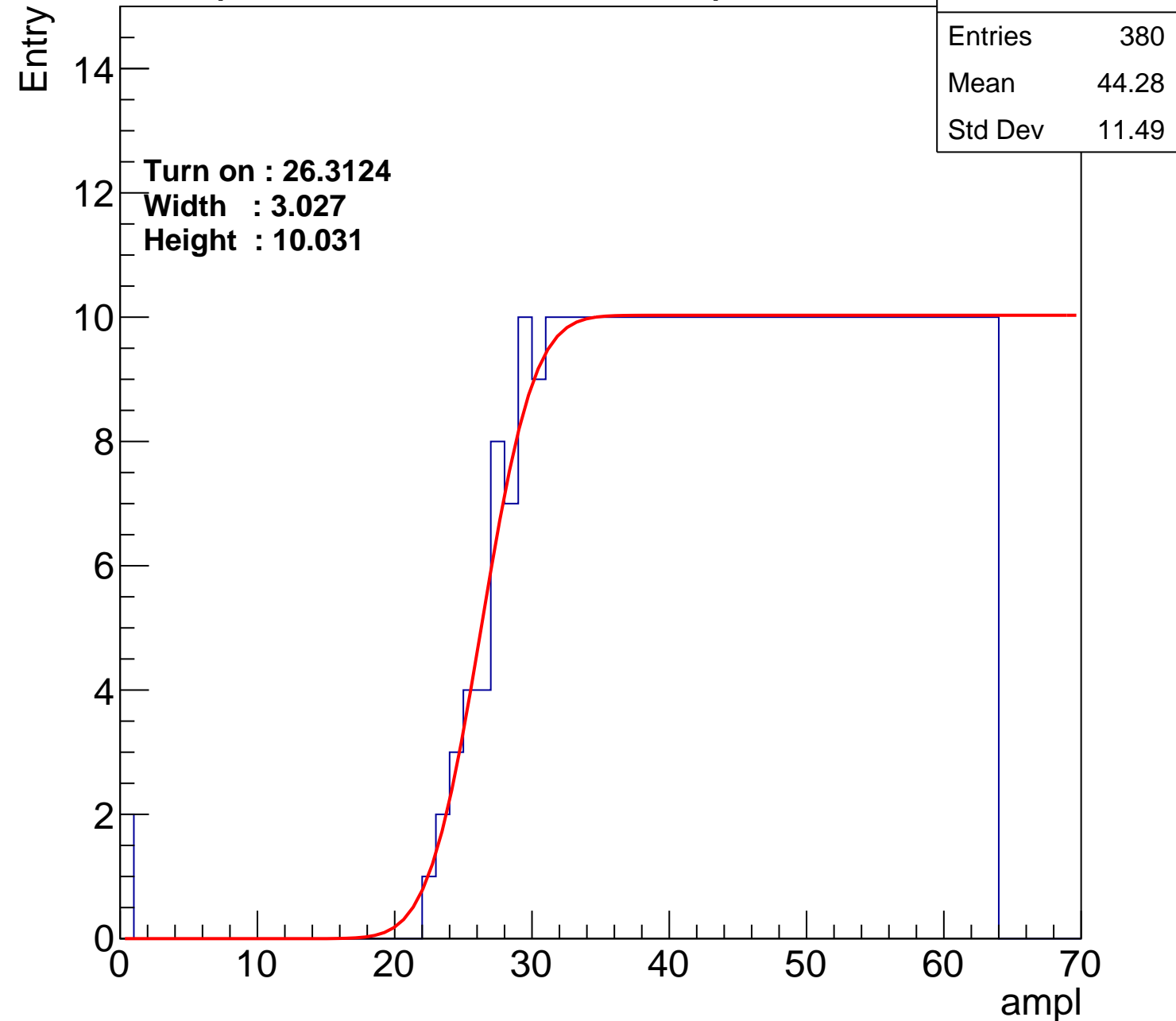
Width : 3.027

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.48
Std Dev	12.14

Turn on : 24.6988

Width : 1.905

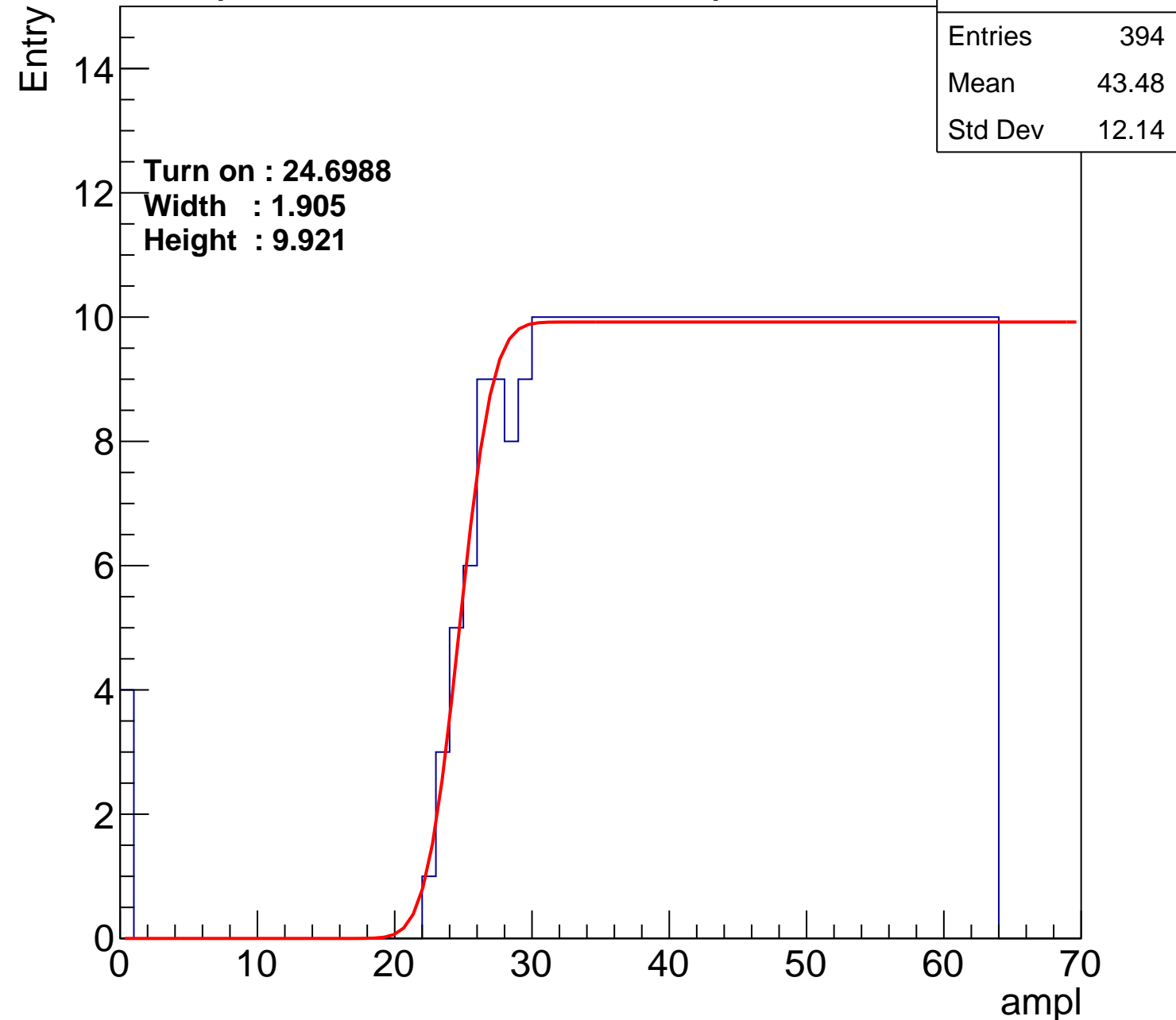
Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U25-ch21

calib_packv5_042523_0143.root, FC#10, port B3

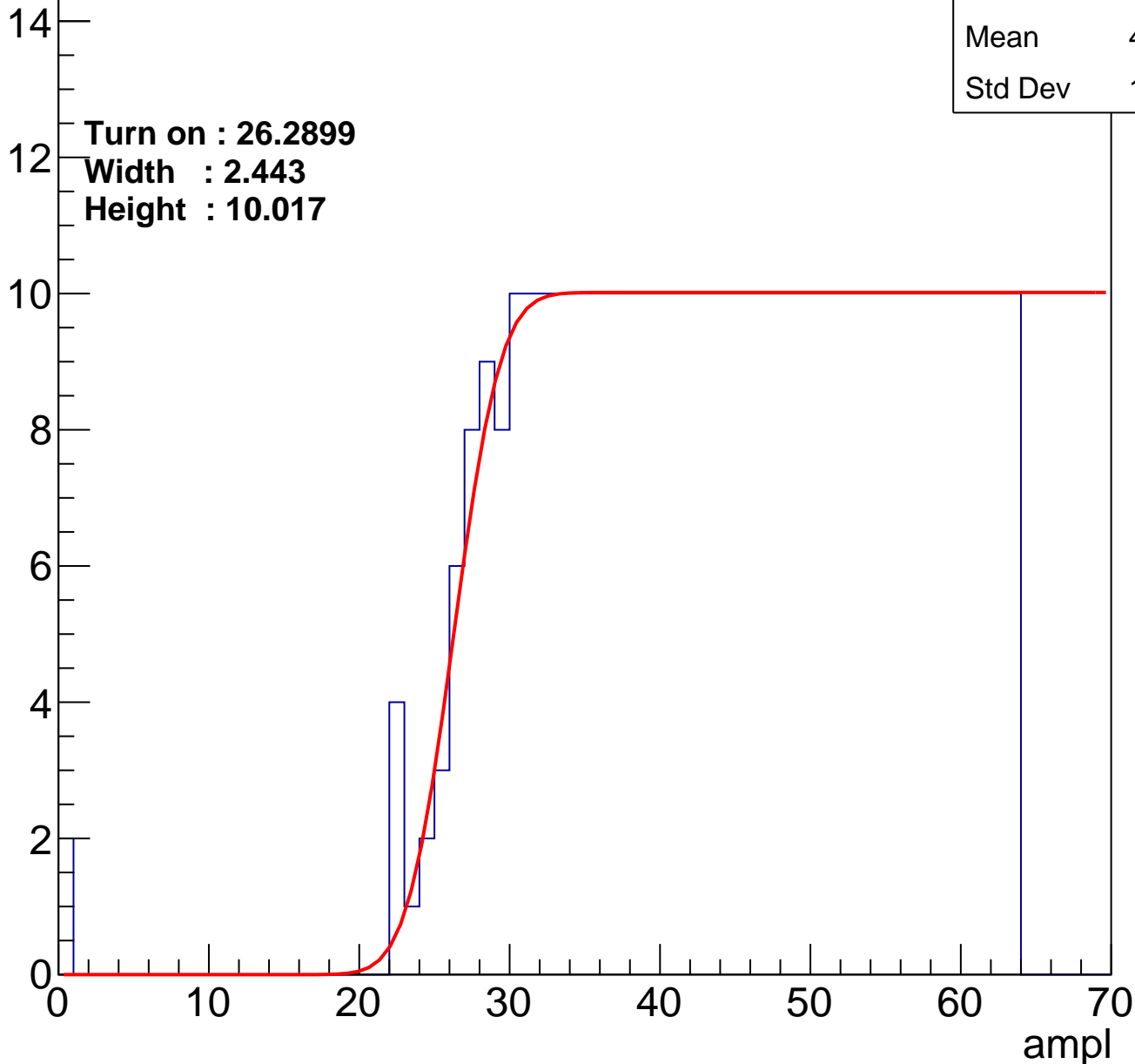
Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 26.2899

Width : 2.443

Height : 10.017

Entry



B1L002S, U25-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.53
Std Dev	11.23

Turn on : 26.6979

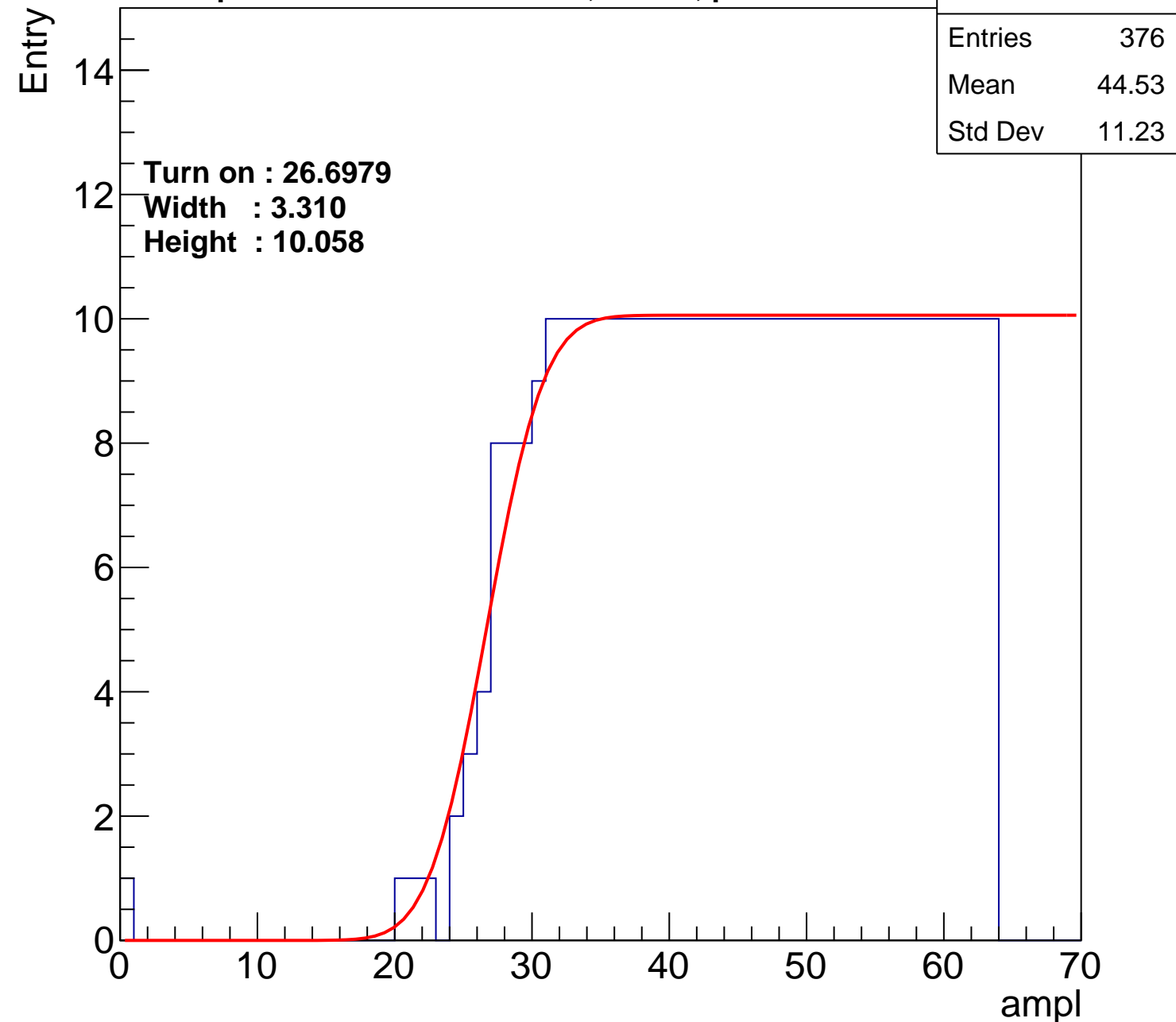
Width : 3.310

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.81
Std Dev	12.01

Turn on : 26.2424

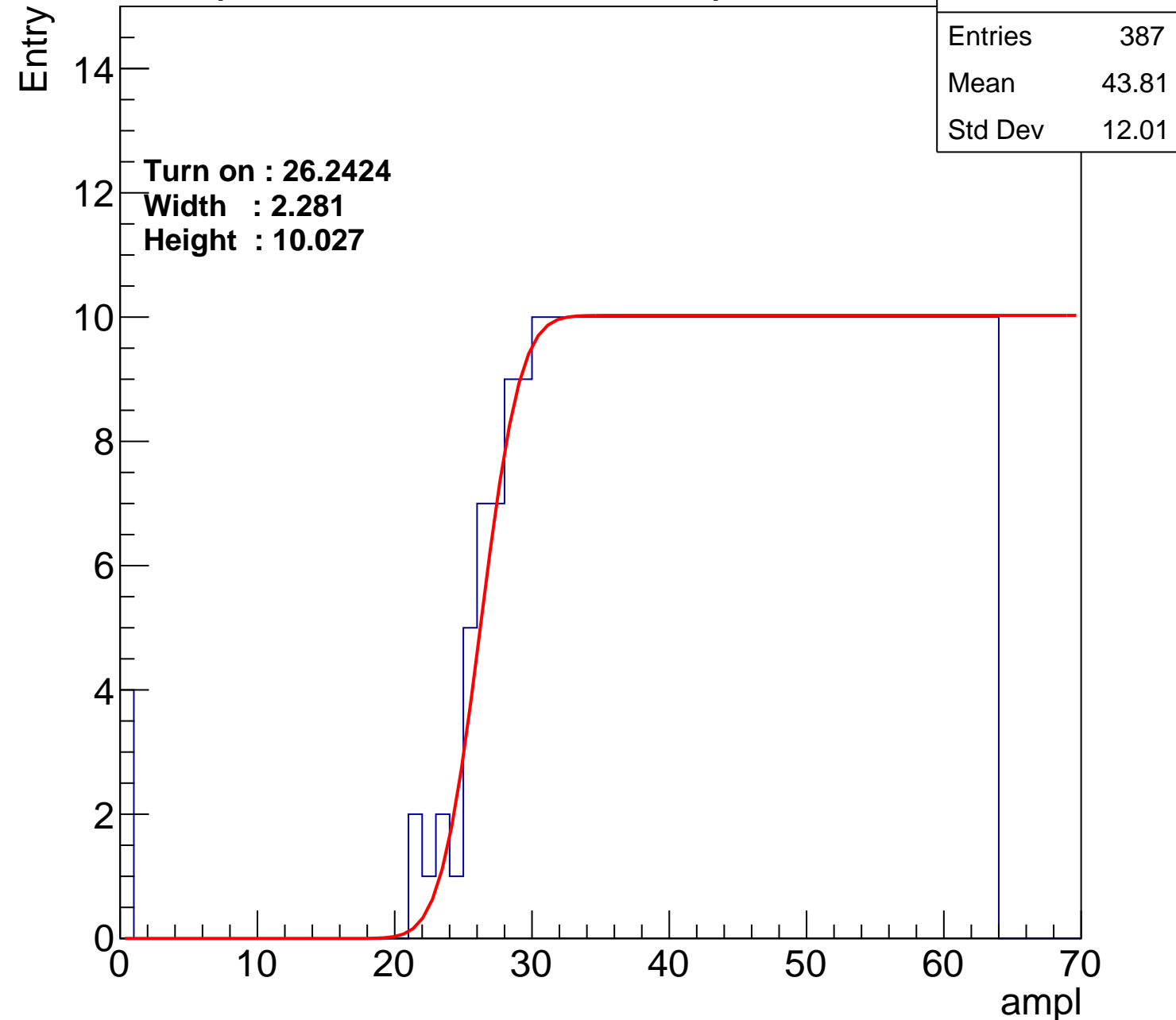
Width : 2.281

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.39
Std Dev	12.08

Turn on : 24.8699

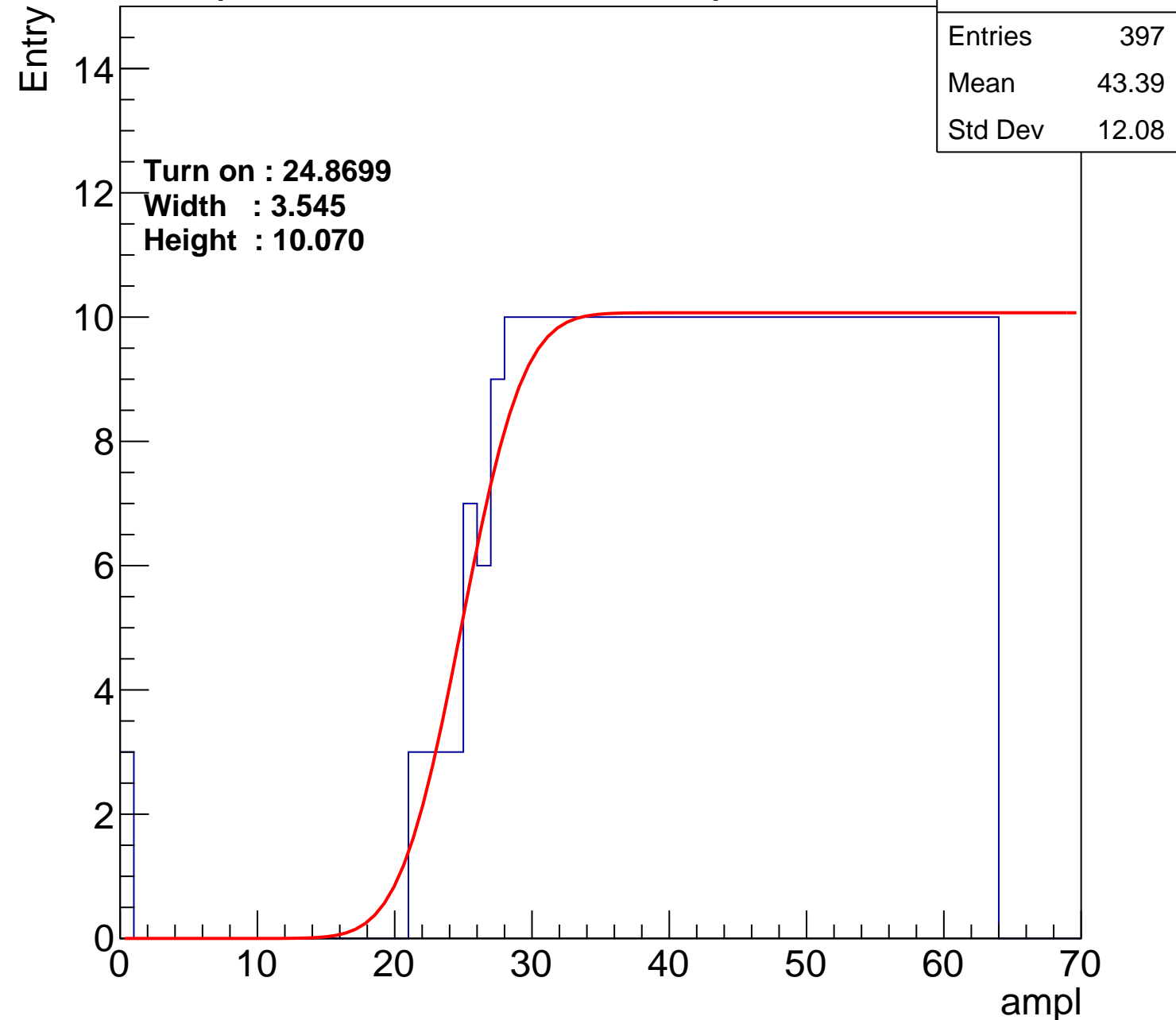
Width : 3.545

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch25

calib_packv5_042523_0143.root, FC#10, port B3

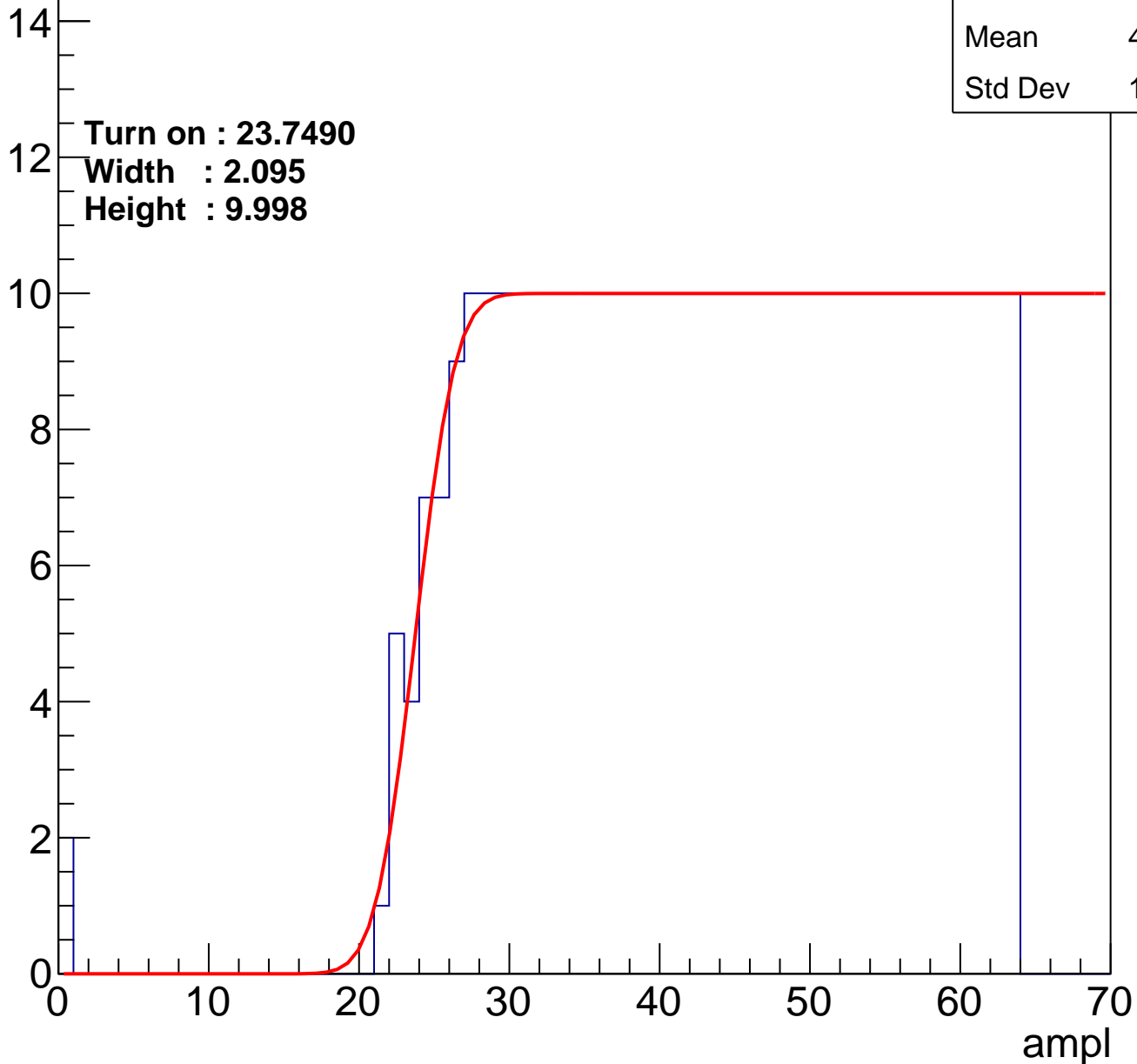
Entries	405
Mean	43.09
Std Dev	12.07

Turn on : 23.7490

Width : 2.095

Height : 9.998

Entry



B1L002S, U25-ch26

calib_packv5_042523_0143.root, FC#10, port B3

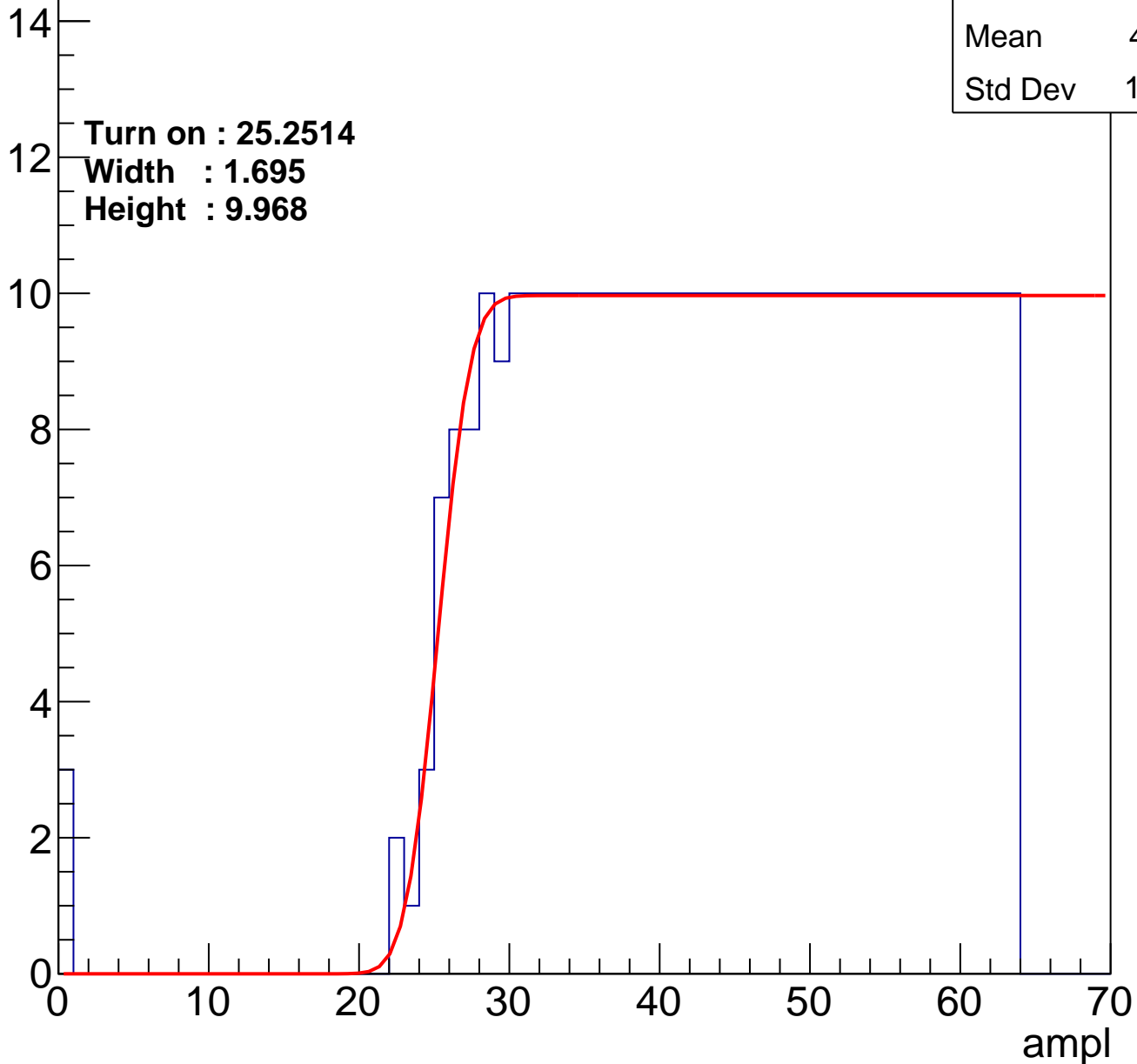
Entries	391
Mean	43.71
Std Dev	11.89

Turn on : 25.2514

Width : 1.695

Height : 9.968

Entry



B1L002S, U25-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	43.94
Std Dev	12.09

Turn on : 26.5146

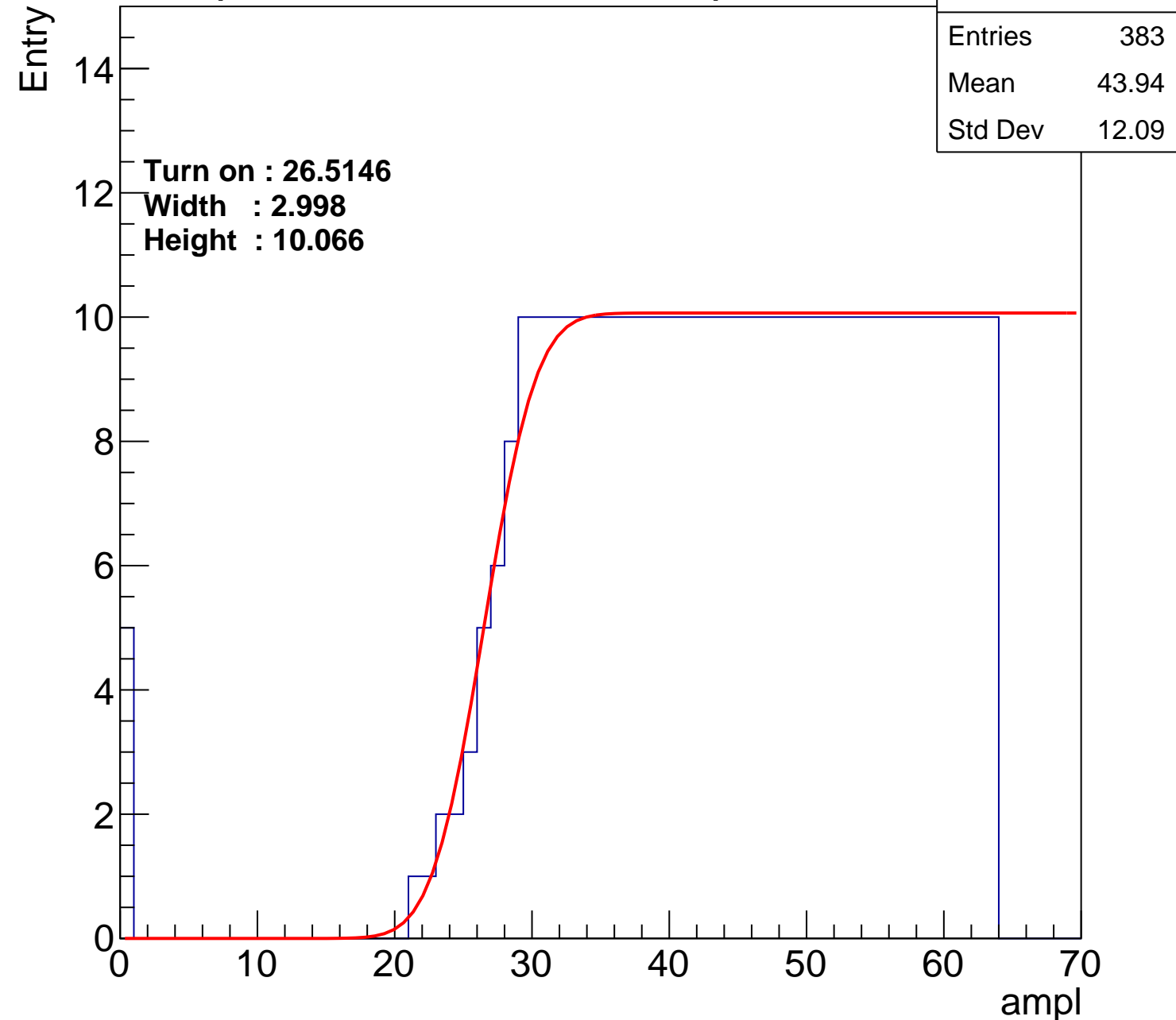
Width : 2.998

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.47
Std Dev	11.88

Turn on : 24.9138

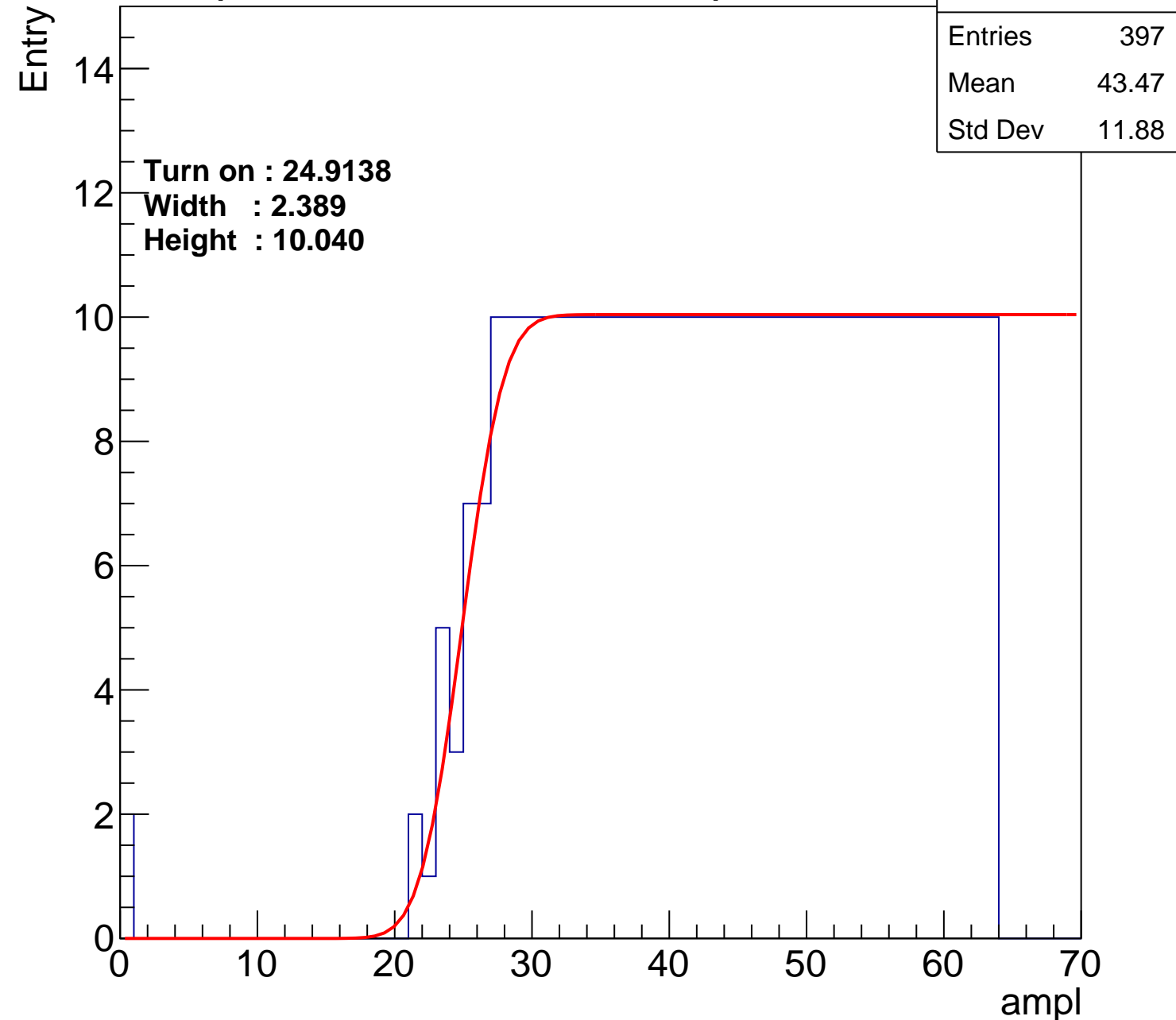
Width : 2.389

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.8530

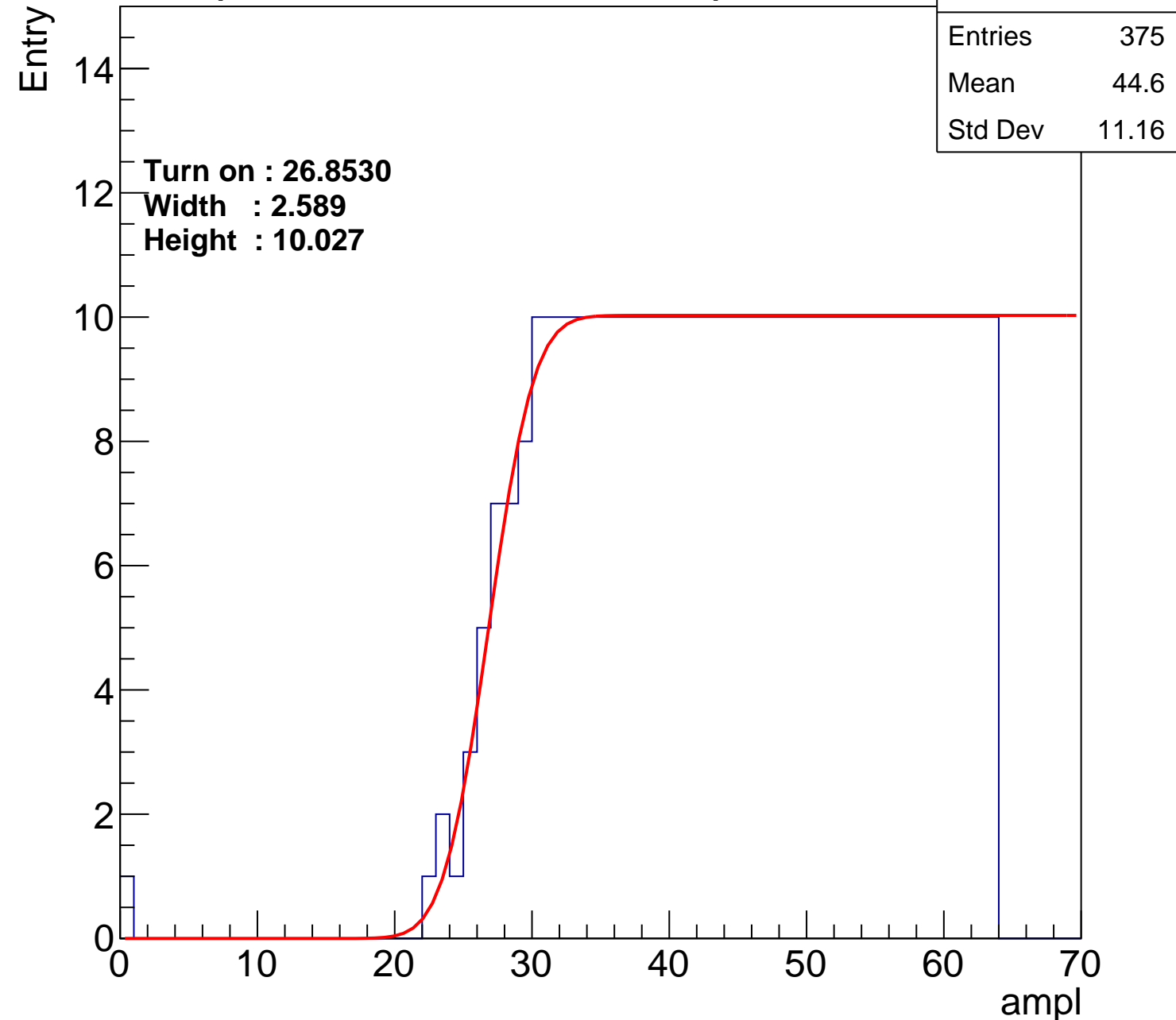
Width : 2.589

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch30

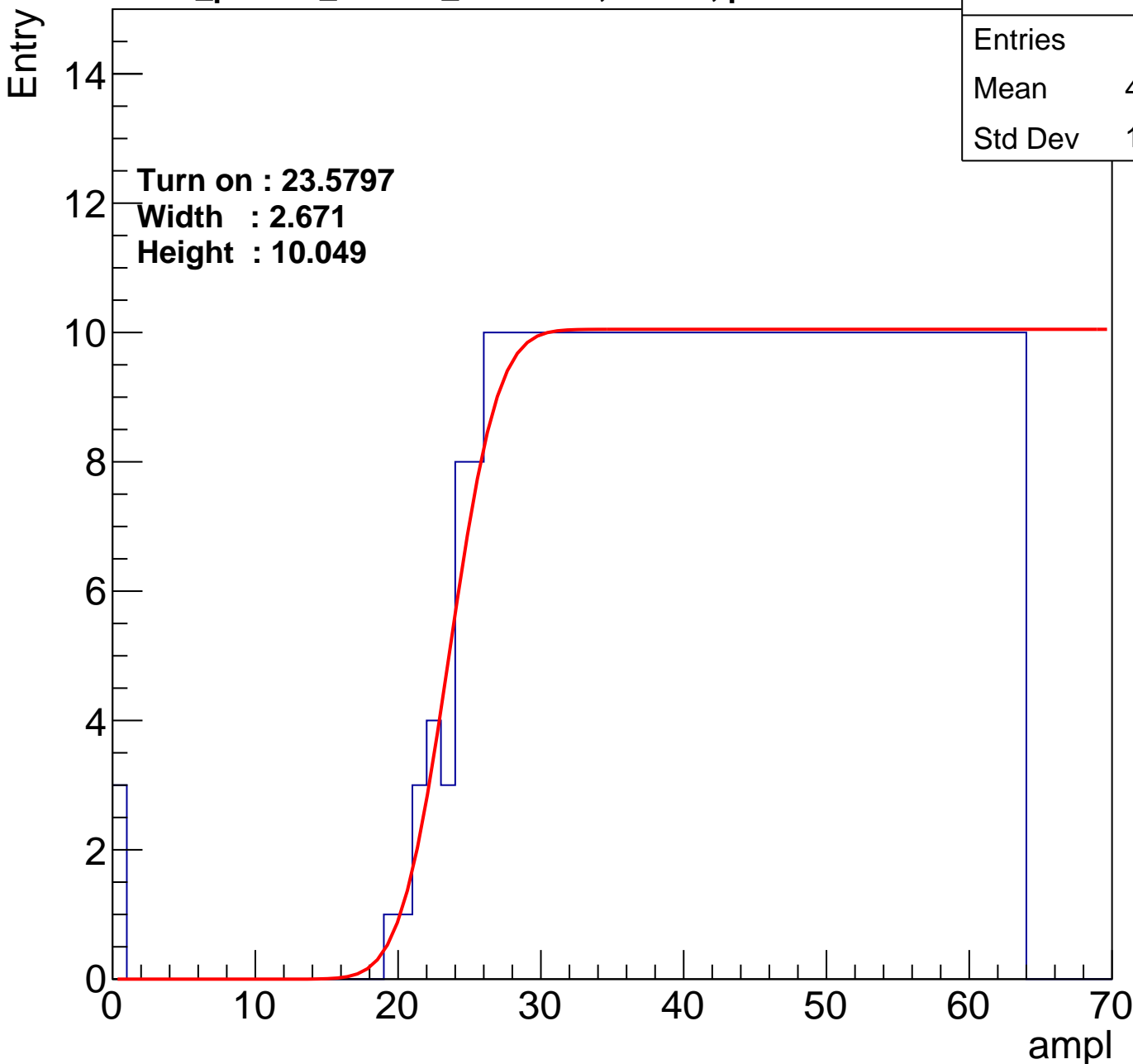
calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.73
Std Dev	12.39

Turn on : 23.5797

Width : 2.671

Height : 10.049



B1L002S, U25-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.95
Std Dev	12.41

Turn on : 24.3195

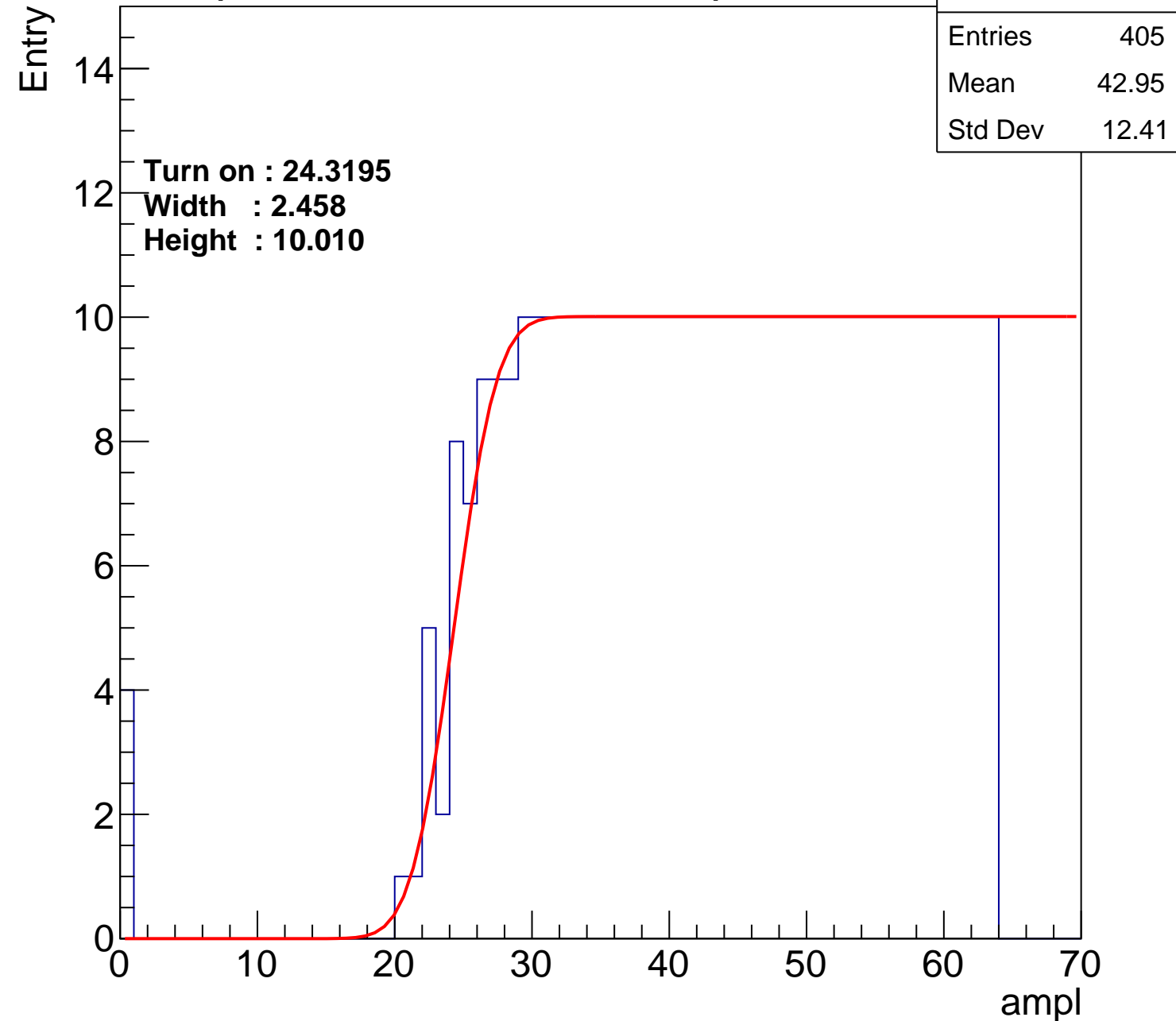
Width : 2.458

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch32

calib_packv5_042523_0143.root, FC#10, port B3

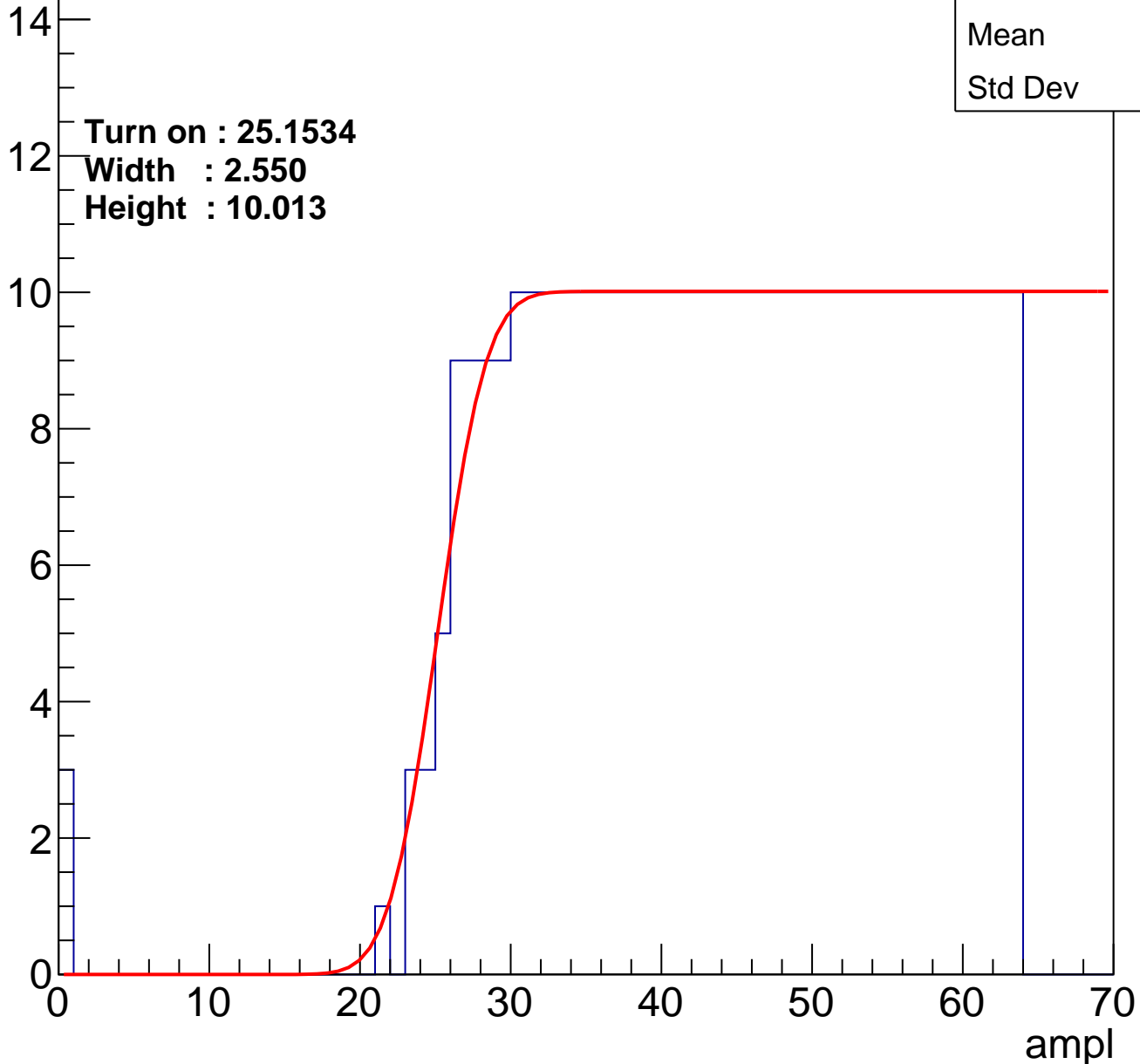
Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.1534

Width : 2.550

Height : 10.013

Entry



B1L002S, U25-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 25.8925

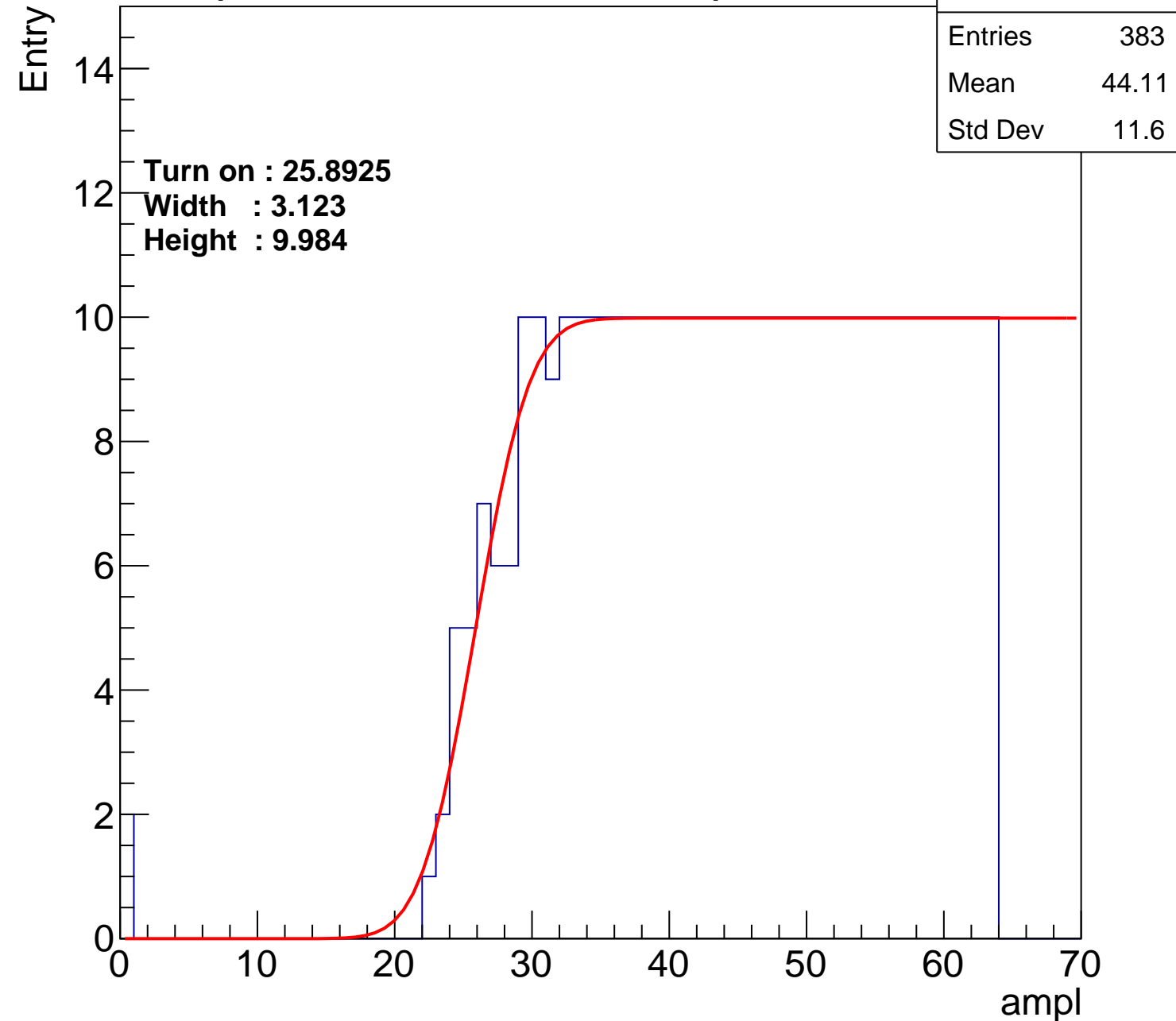
Width : 3.123

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch34

calib_packv5_042523_0143.root, FC#10, port B3

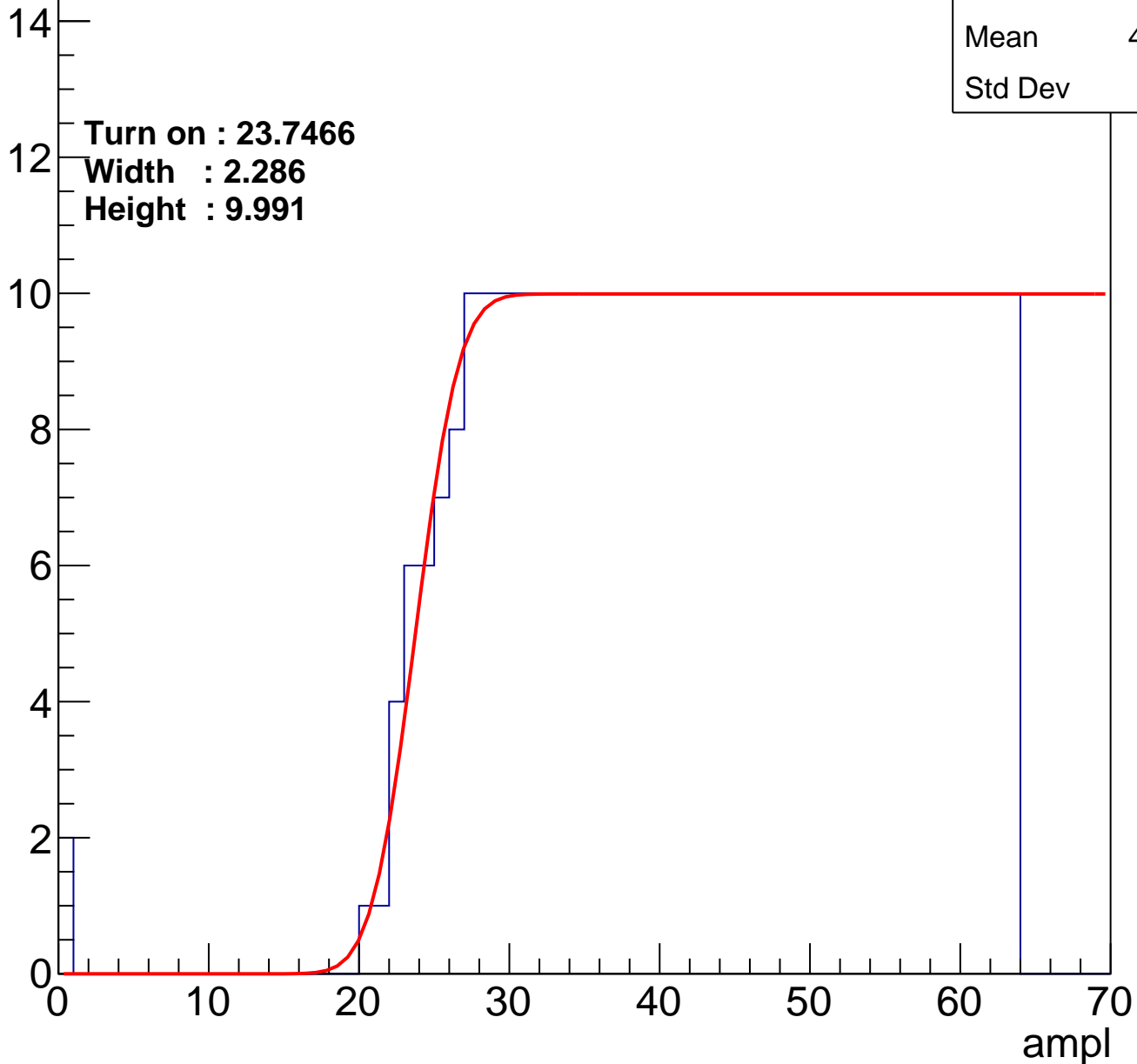
Entries	405
Mean	43.07
Std Dev	12.1

Turn on : 23.7466

Width : 2.286

Height : 9.991

Entry



B1L002S, U25-ch35

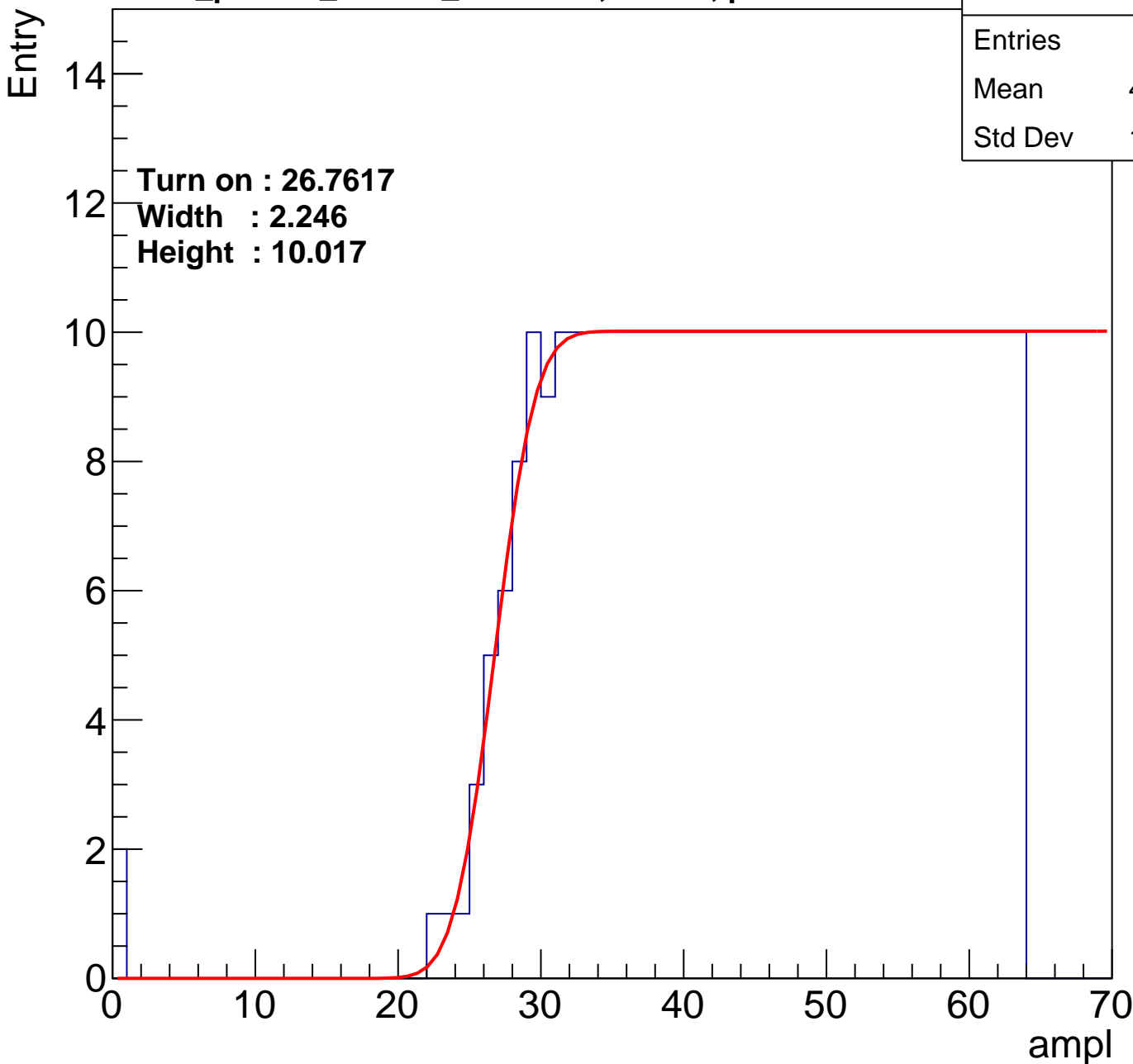
calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.7617

Width : 2.246

Height : 10.017



B1L002S, U25-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.7
Std Dev	11.25

Turn on : 27.2307

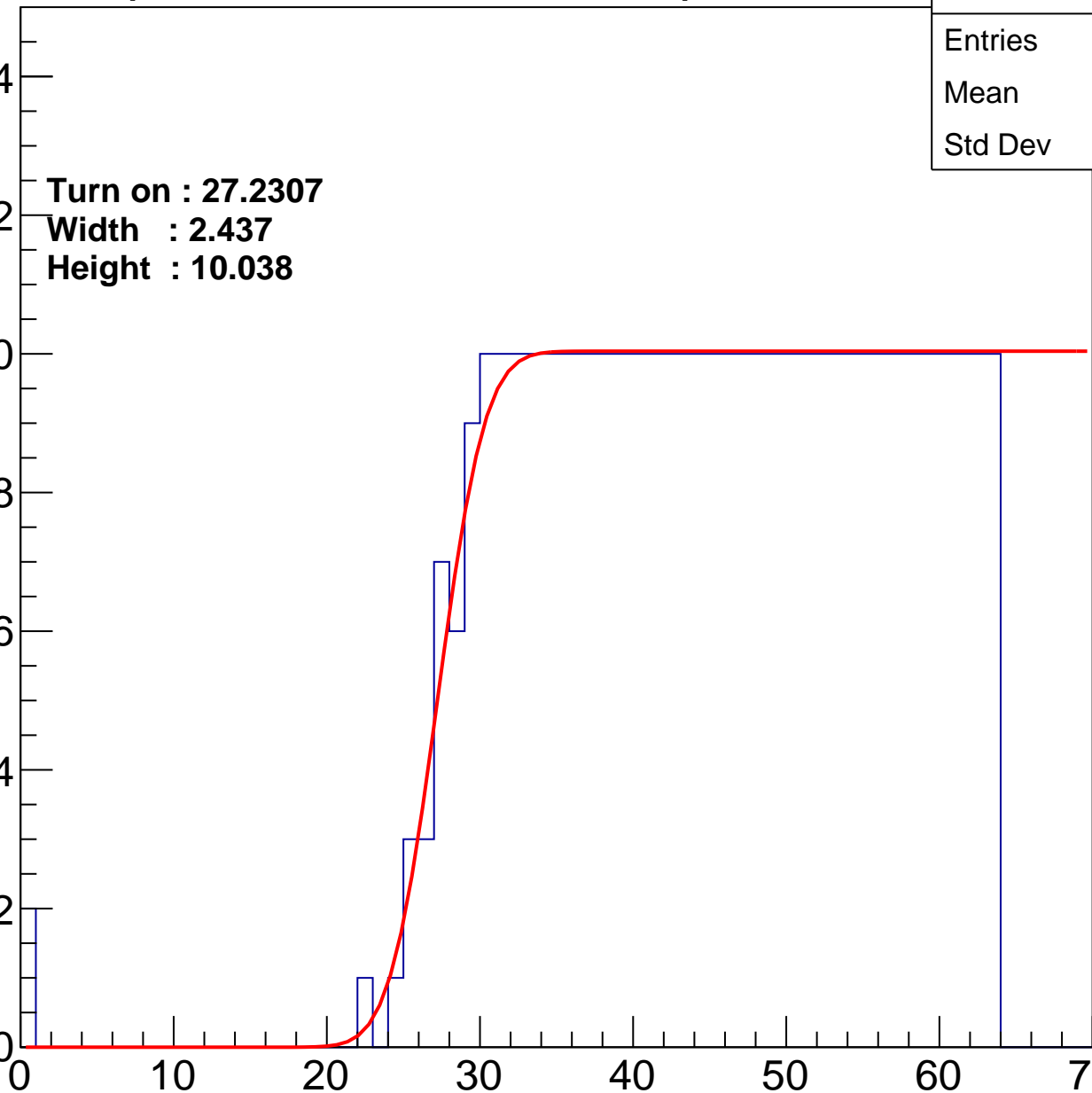
Width : 2.437

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.29

Turn on : 26.4158

Width : 2.951

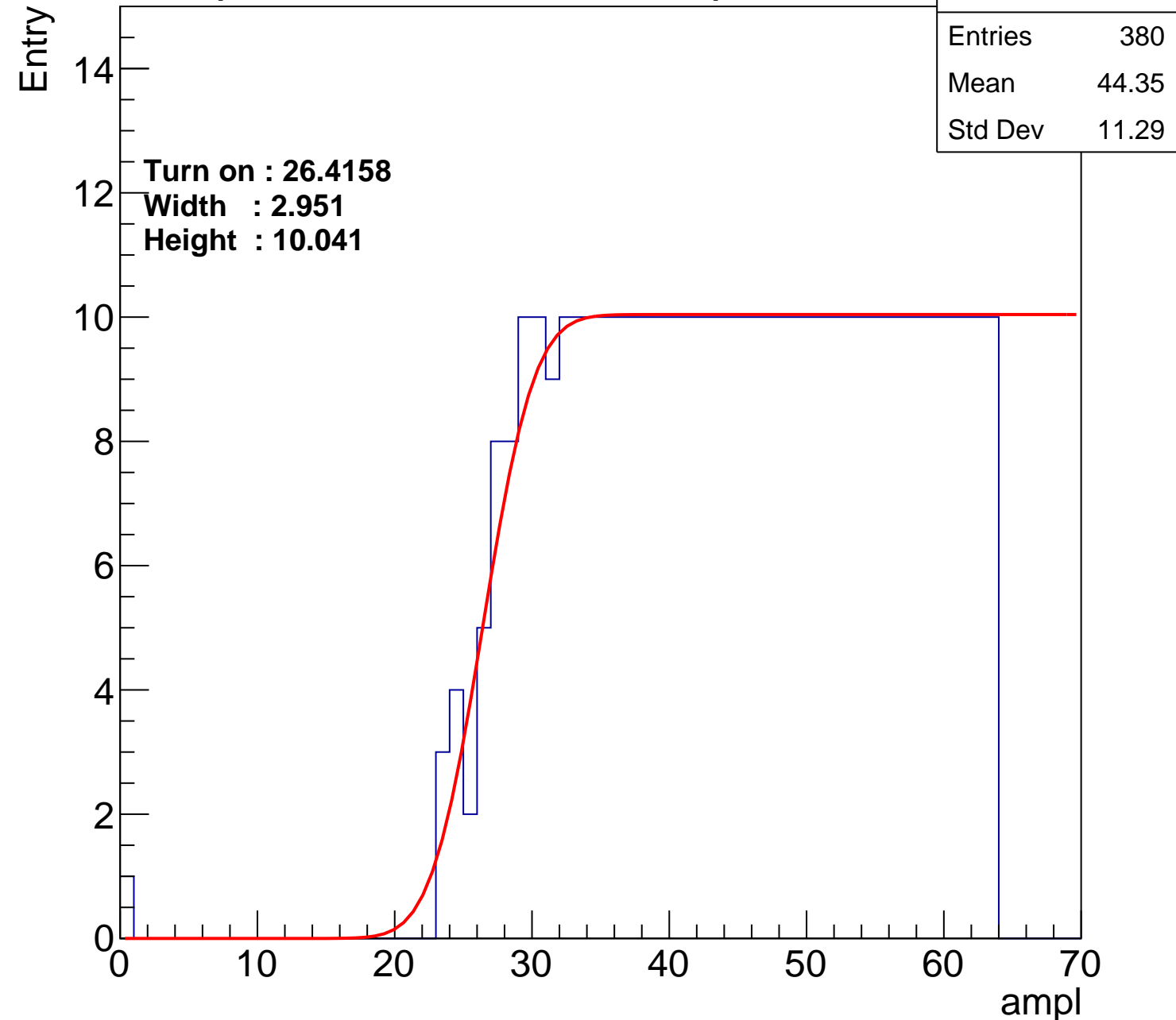
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U25-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.43
Std Dev	11.22

Turn on : 26.2183

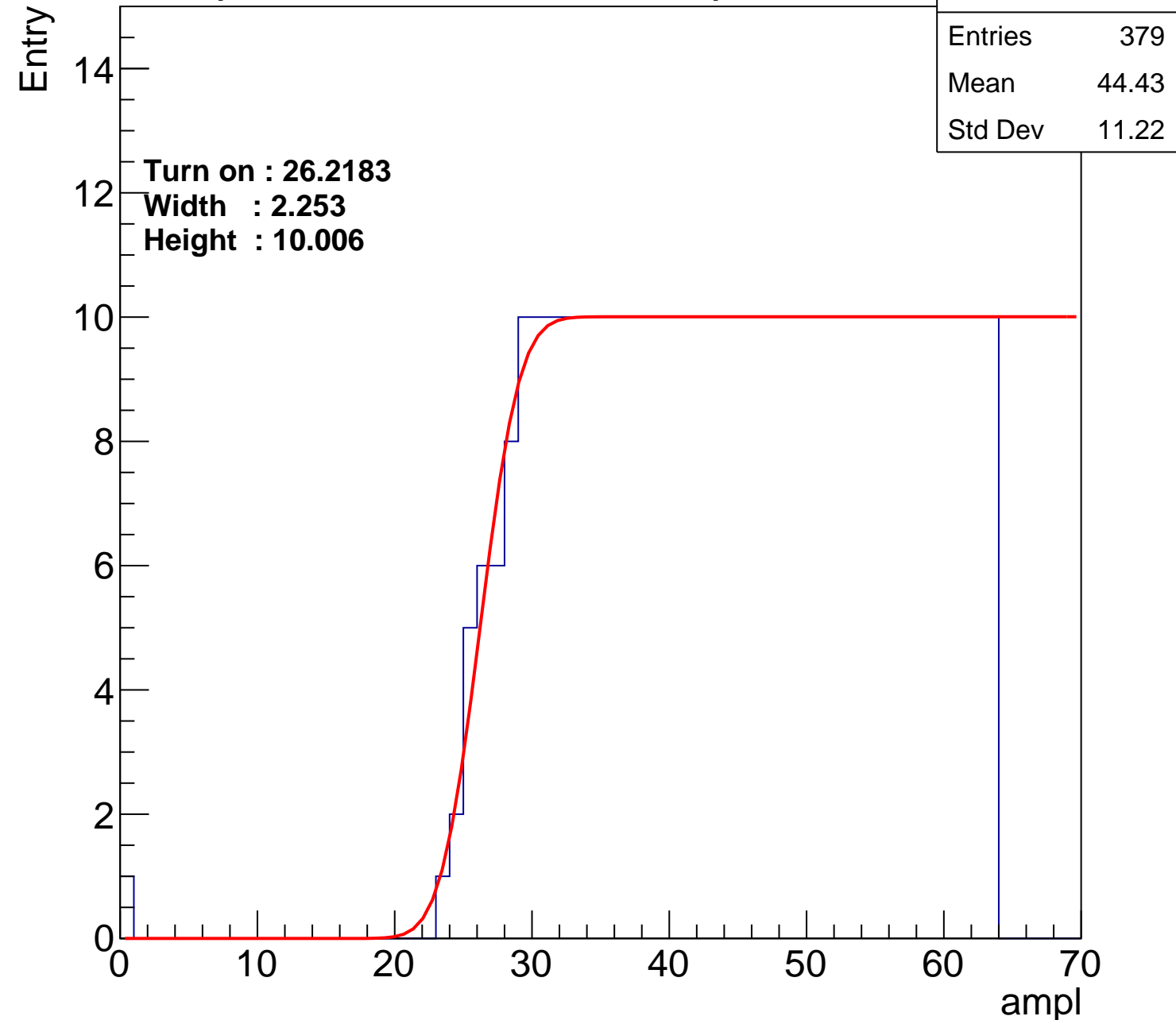
Width : 2.253

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.96
Std Dev	11.55

Turn on : 25.6288

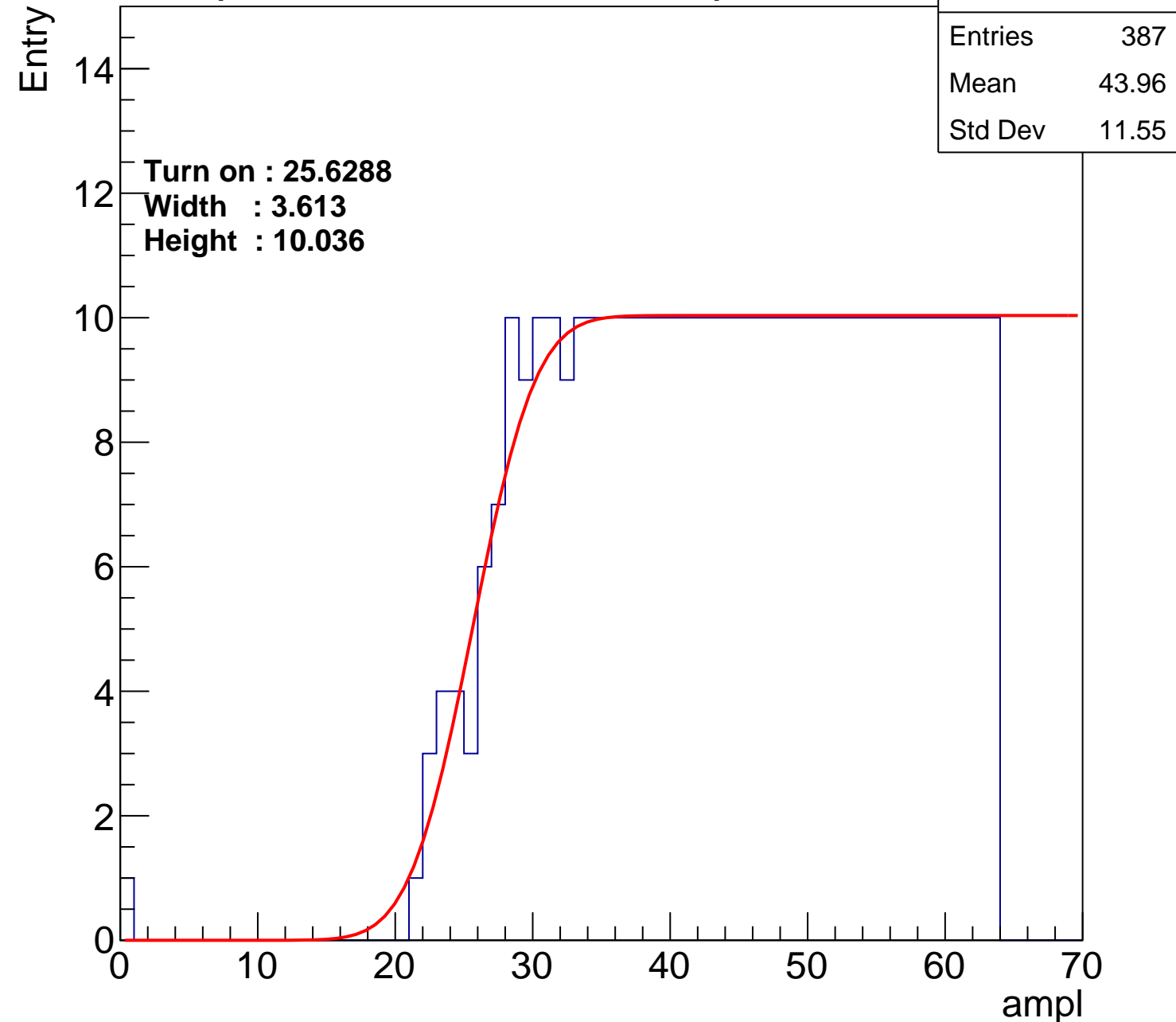
Width : 3.613

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 27.2779

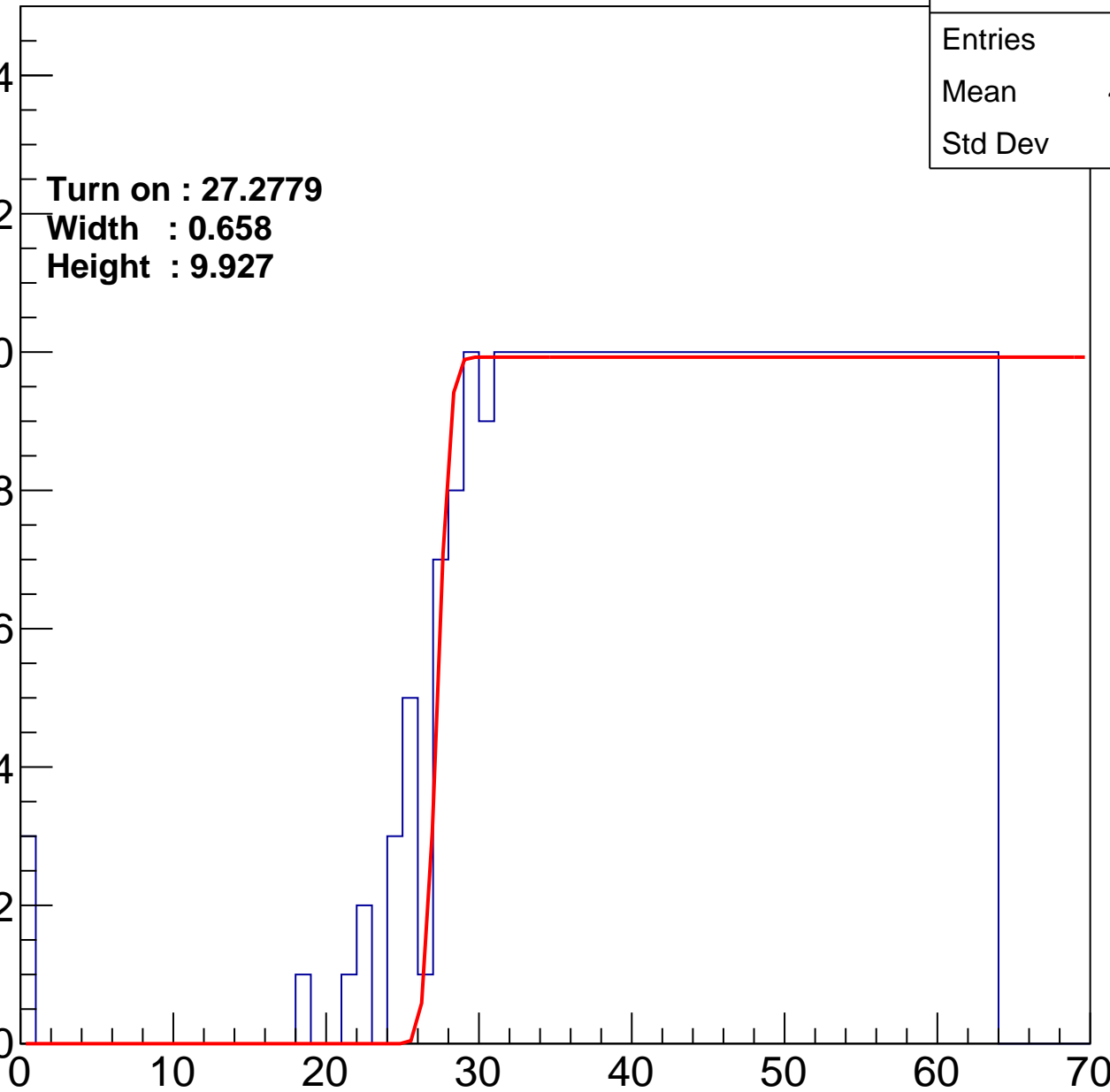
Width : 0.658

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.77
Std Dev	11.23

Turn on : 27.4340

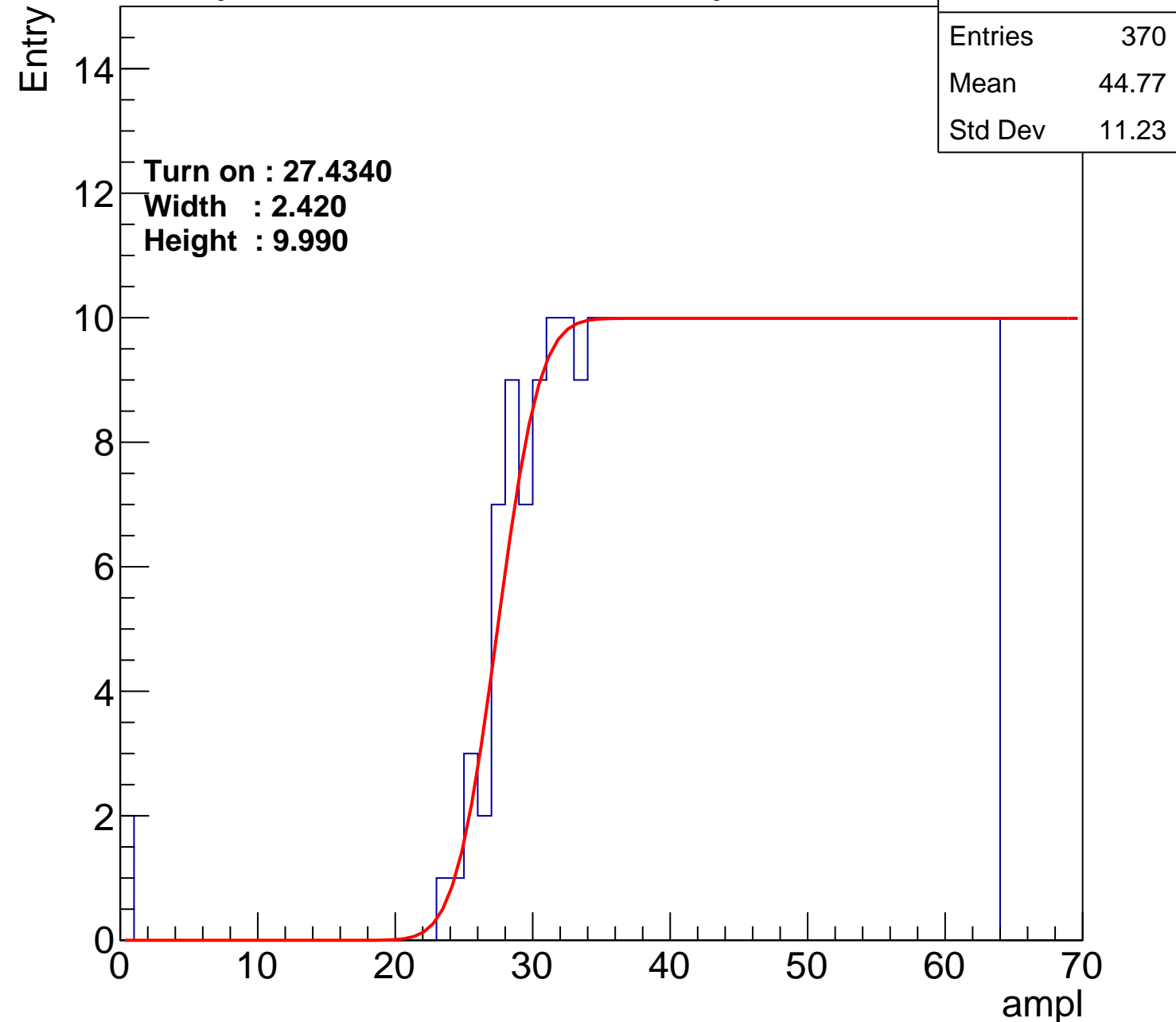
Width : 2.420

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.5
Std Dev	12.05

Turn on : 24.3296

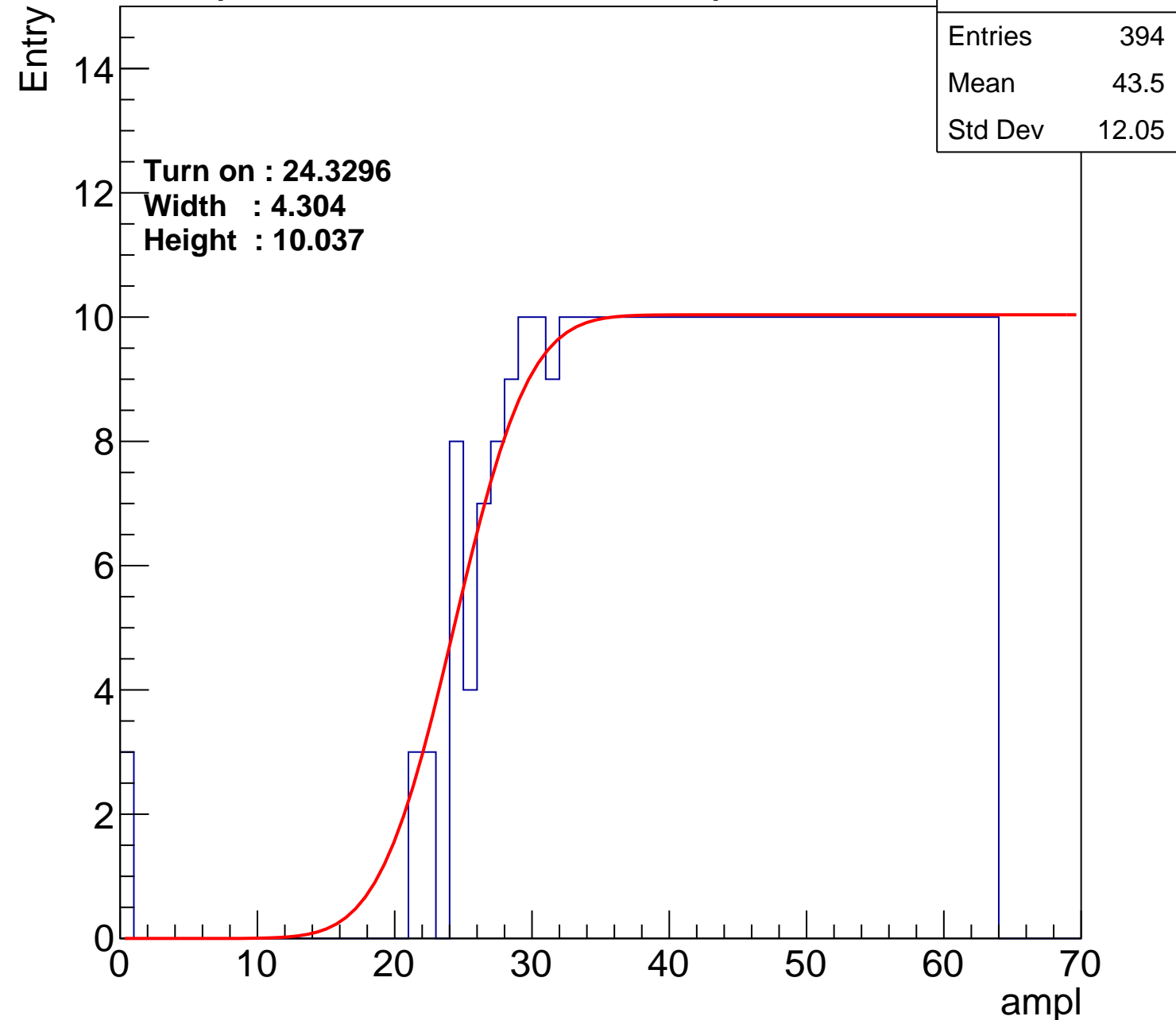
Width : 4.304

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.07
Std Dev	11.87

Turn on : 26.6242

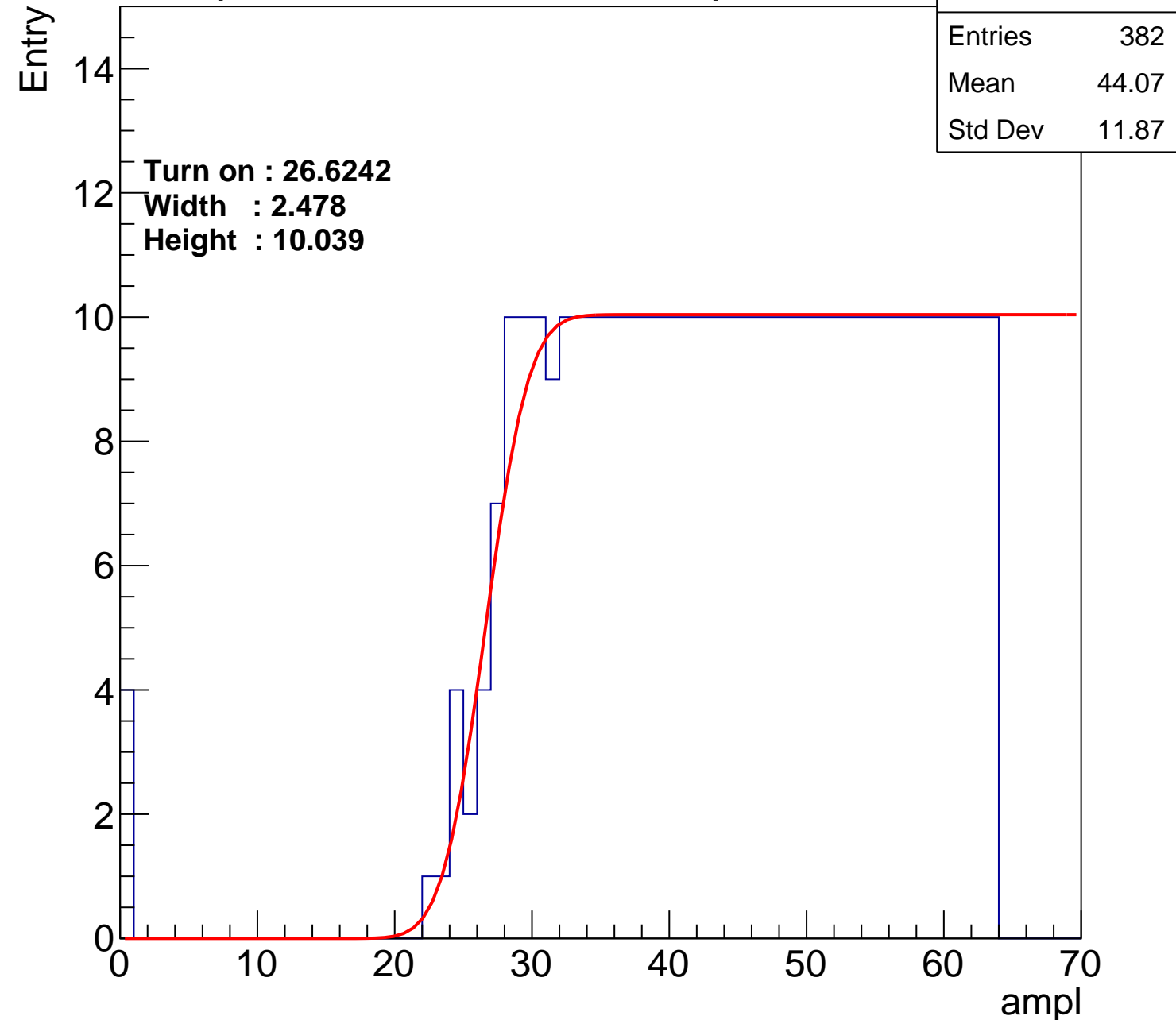
Width : 2.478

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.69
Std Dev	12.08

Turn on : 25.4403

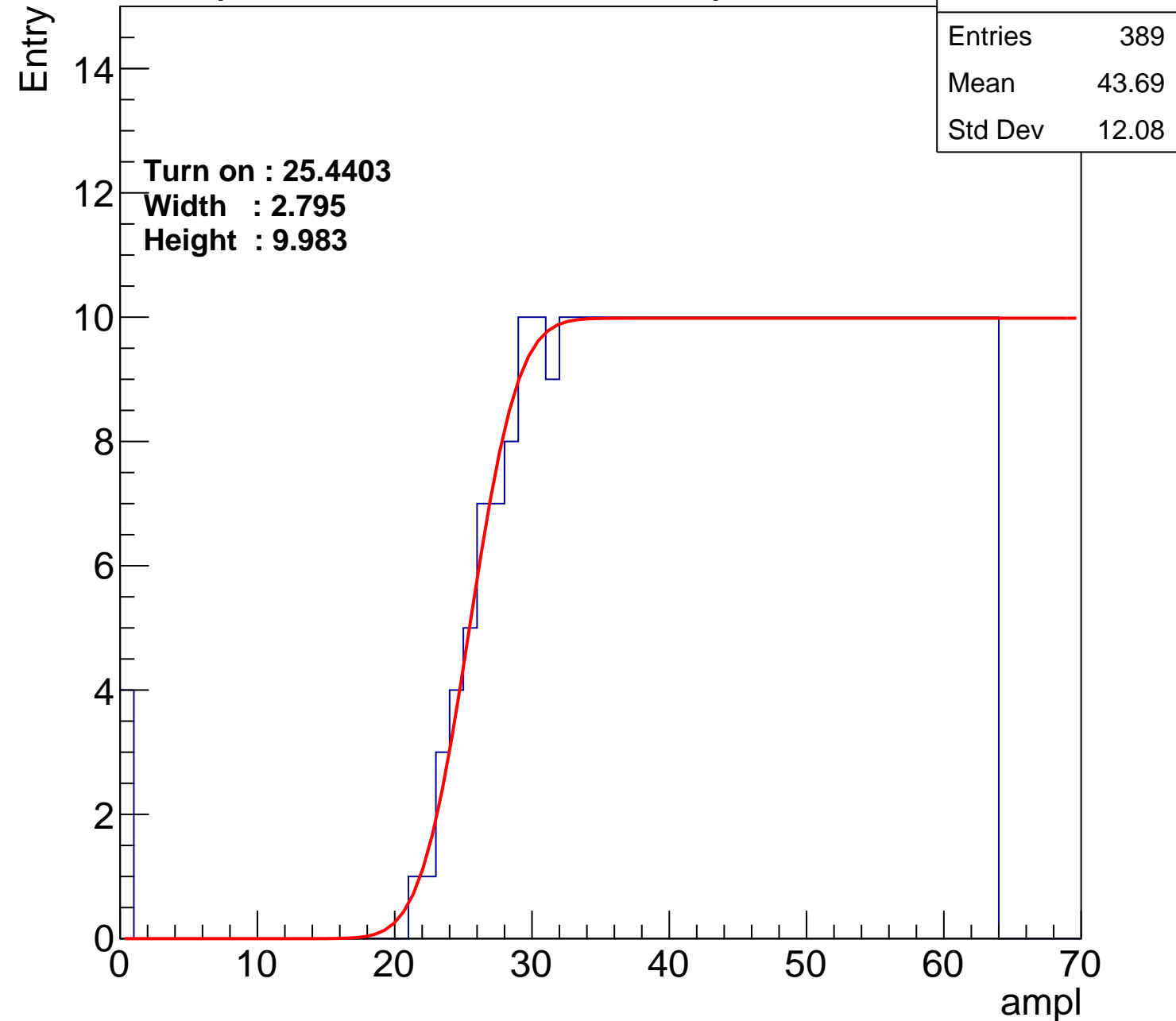
Width : 2.795

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch45

calib_packv5_042523_0143.root, FC#10, port B3

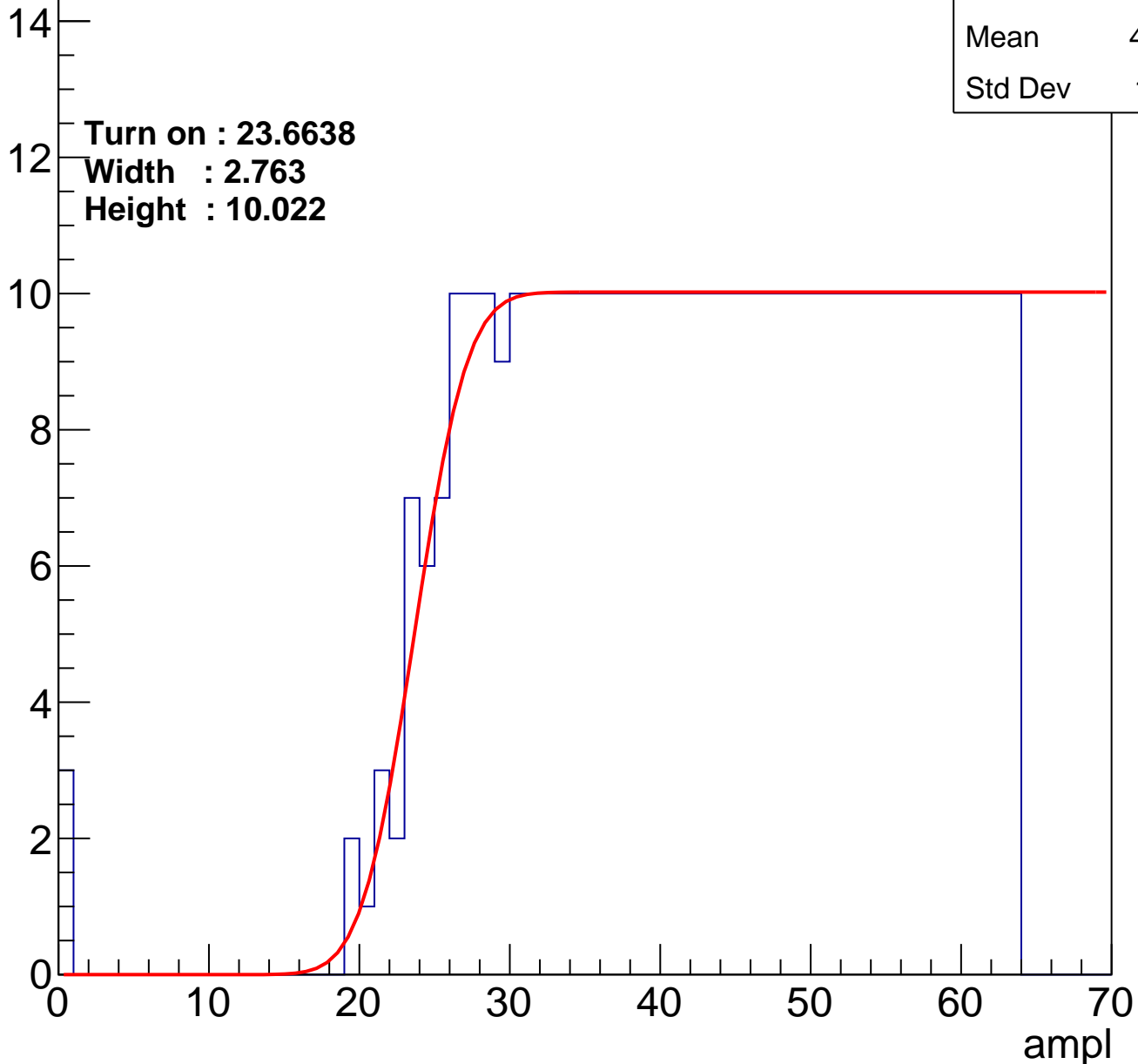
Entries	410
Mean	42.75
Std Dev	12.41

Turn on : 23.6638

Width : 2.763

Height : 10.022

Entry



B1L002S, U25-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.77

Turn on : 25.0749

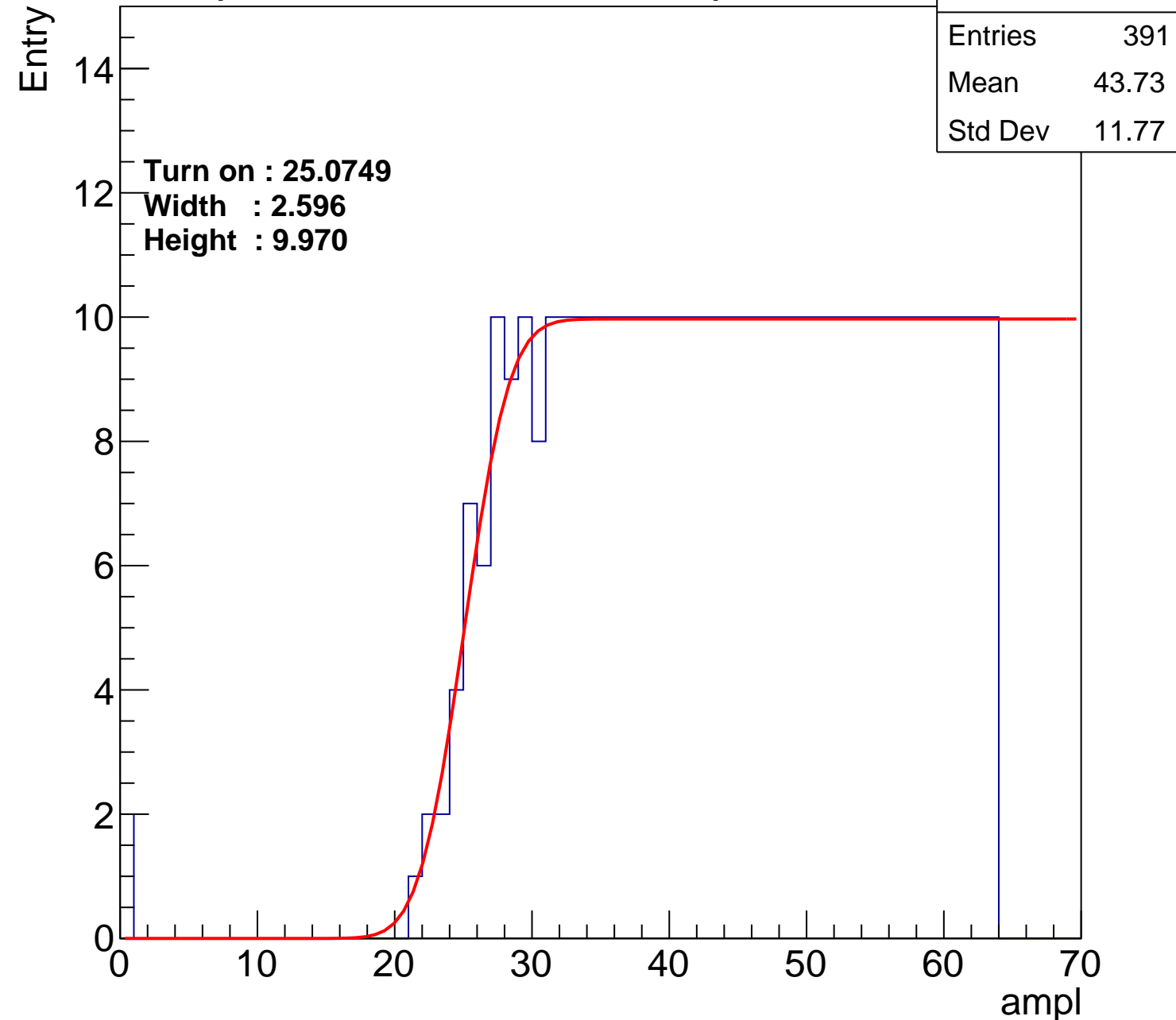
Width : 2.596

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch47

calib_packv5_042523_0143.root, FC#10, port B3

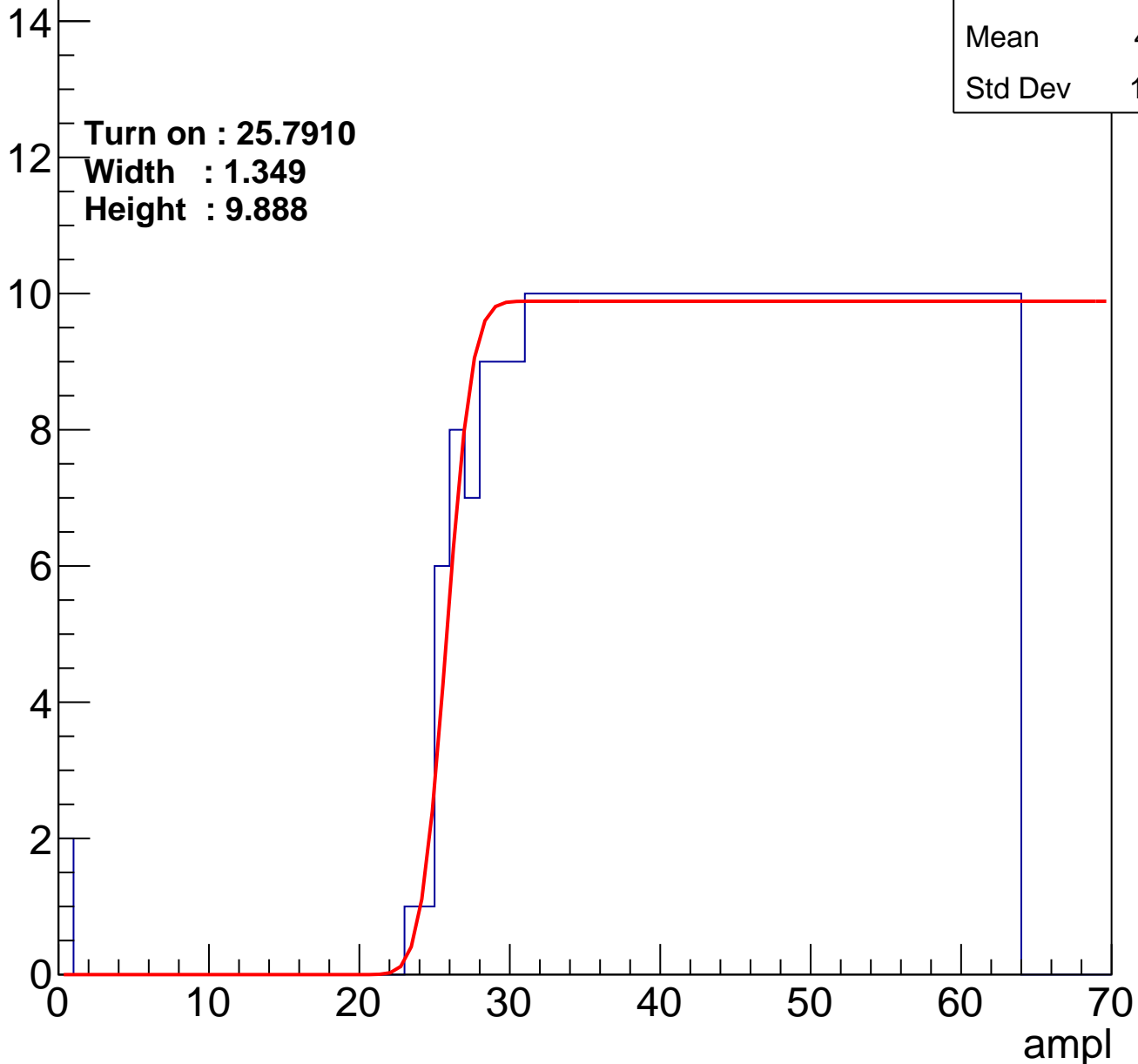
Entries	382
Mean	44.21
Std Dev	11.49

Turn on : 25.7910

Width : 1.349

Height : 9.888

Entry



B1L002S, U25-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.86
Std Dev	11.51

Turn on : 25.5288

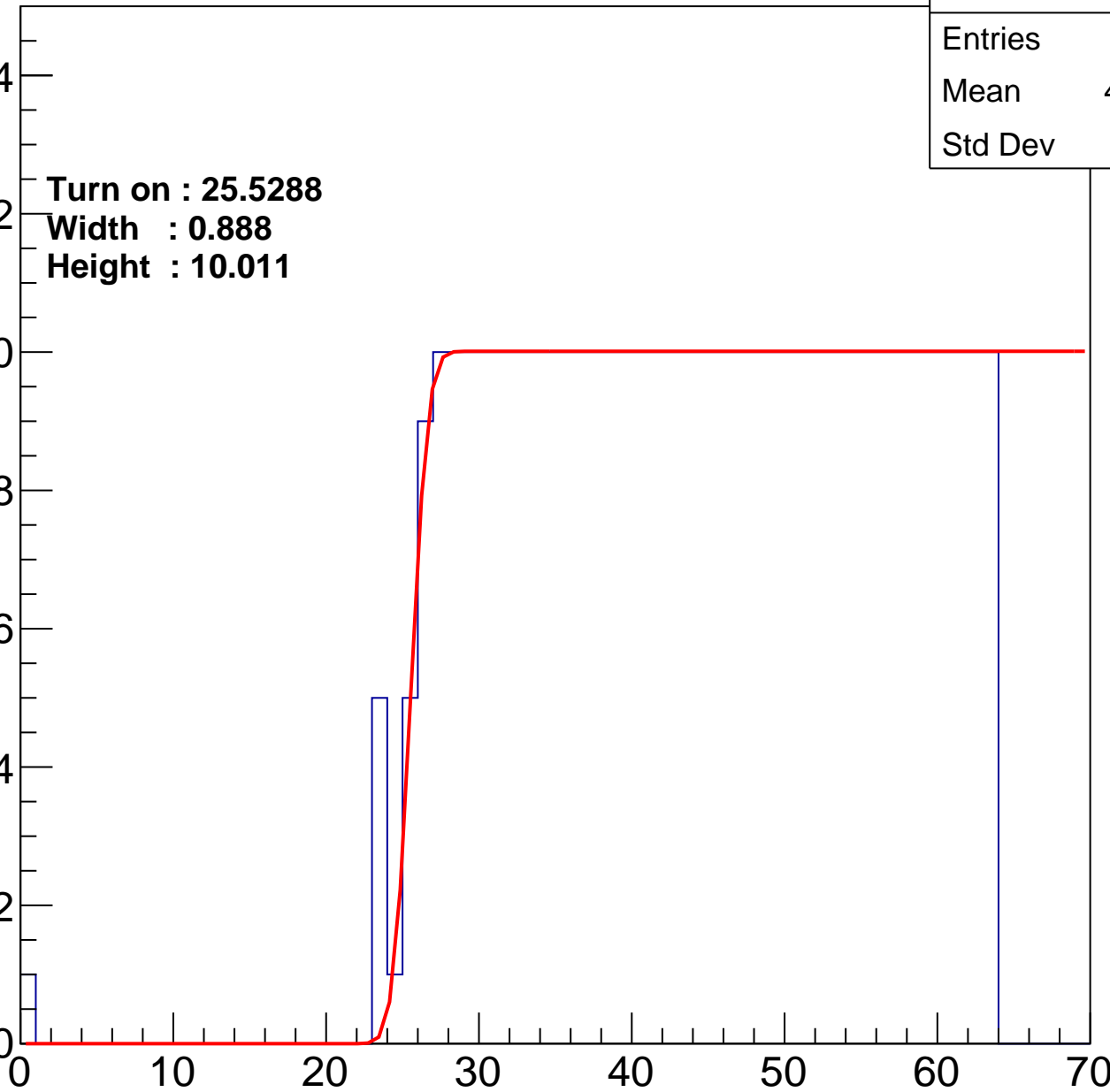
Width : 0.888

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch49

calib_packv5_042523_0143.root, FC#10, port B3

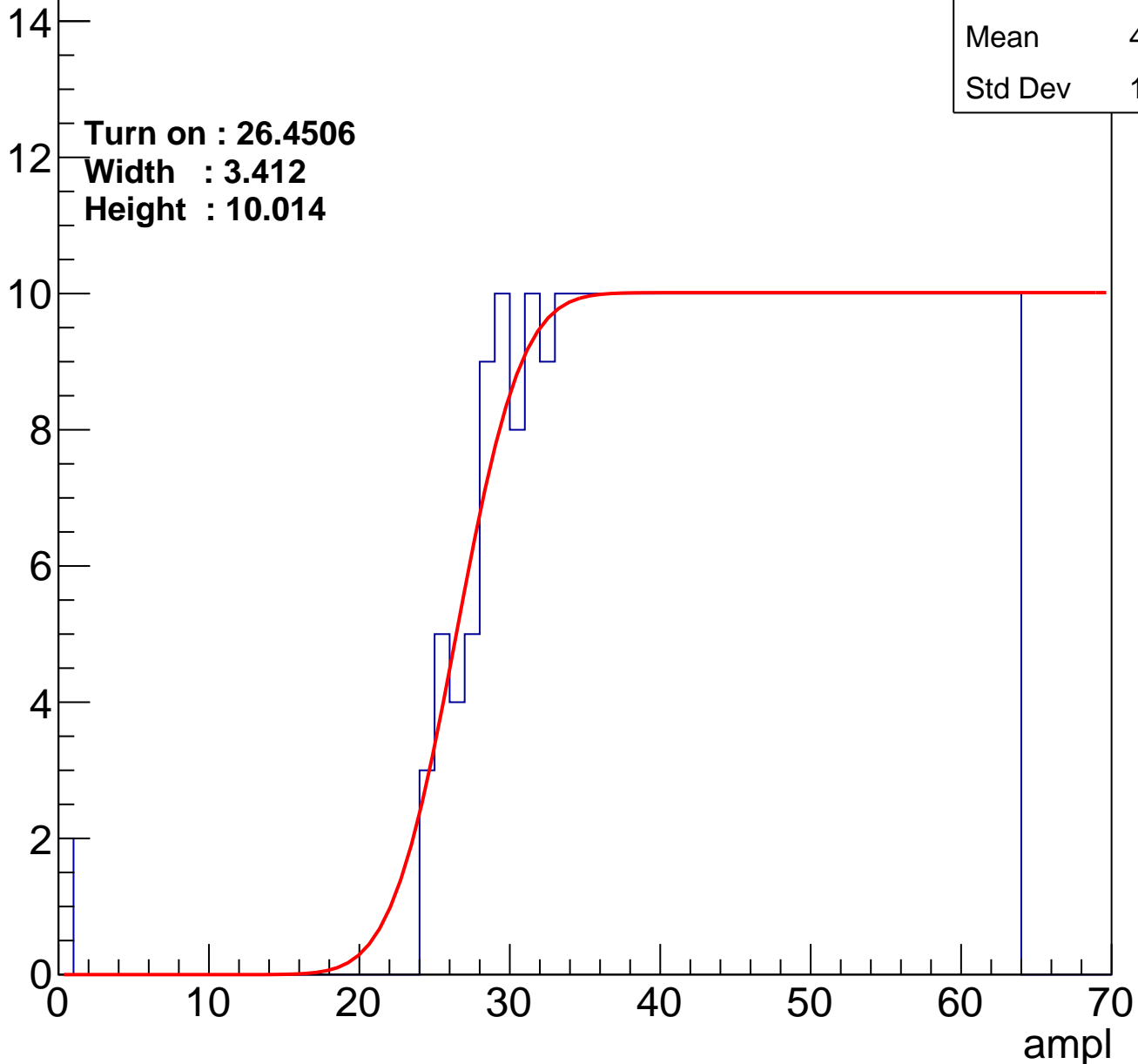
Entries	375
Mean	44.52
Std Dev	11.36

Turn on : 26.4506

Width : 3.412

Height : 10.014

Entry



B1L002S, U25-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.98
Std Dev	12.36

Turn on : 24.2251

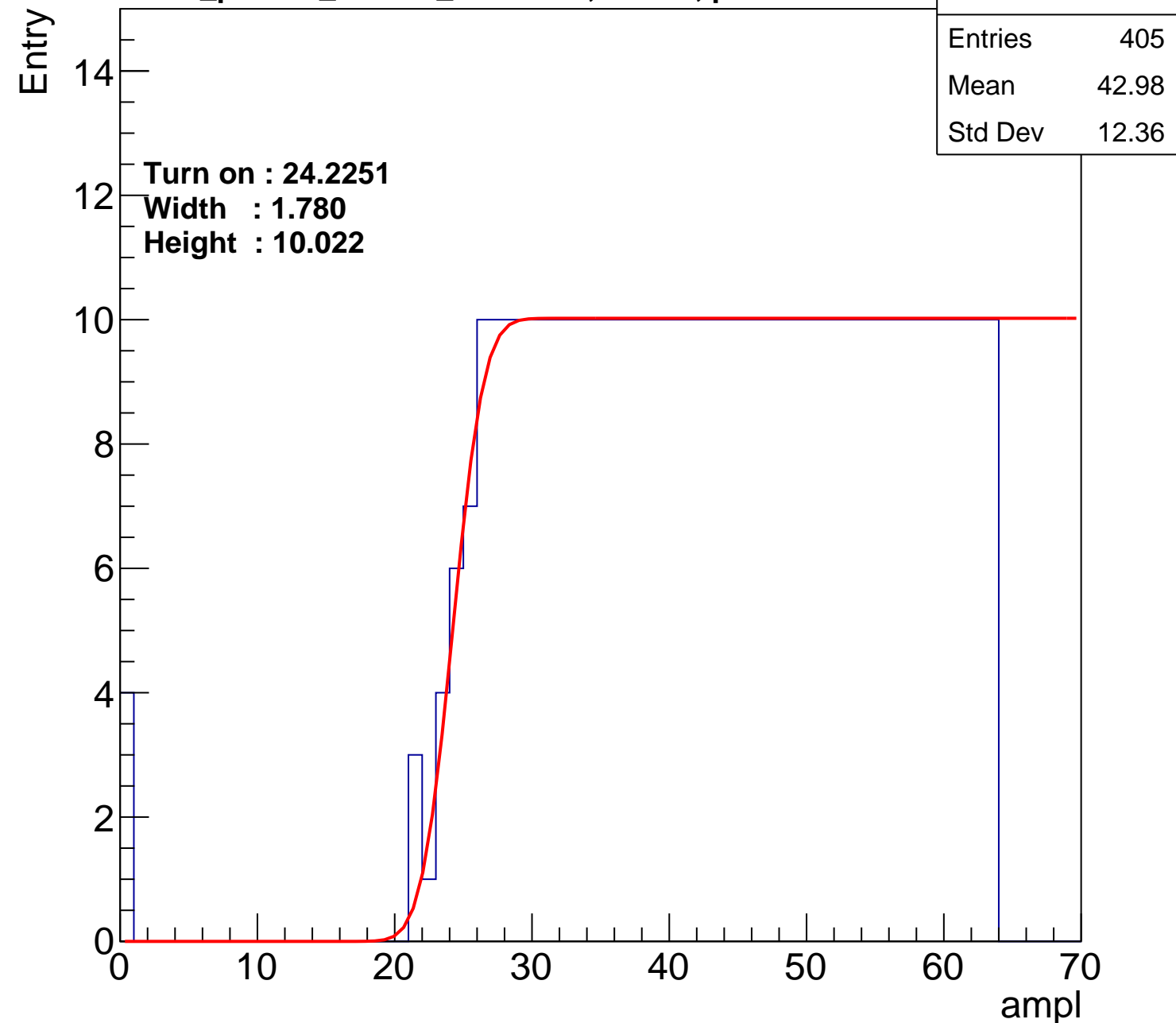
Width : 1.780

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.4651

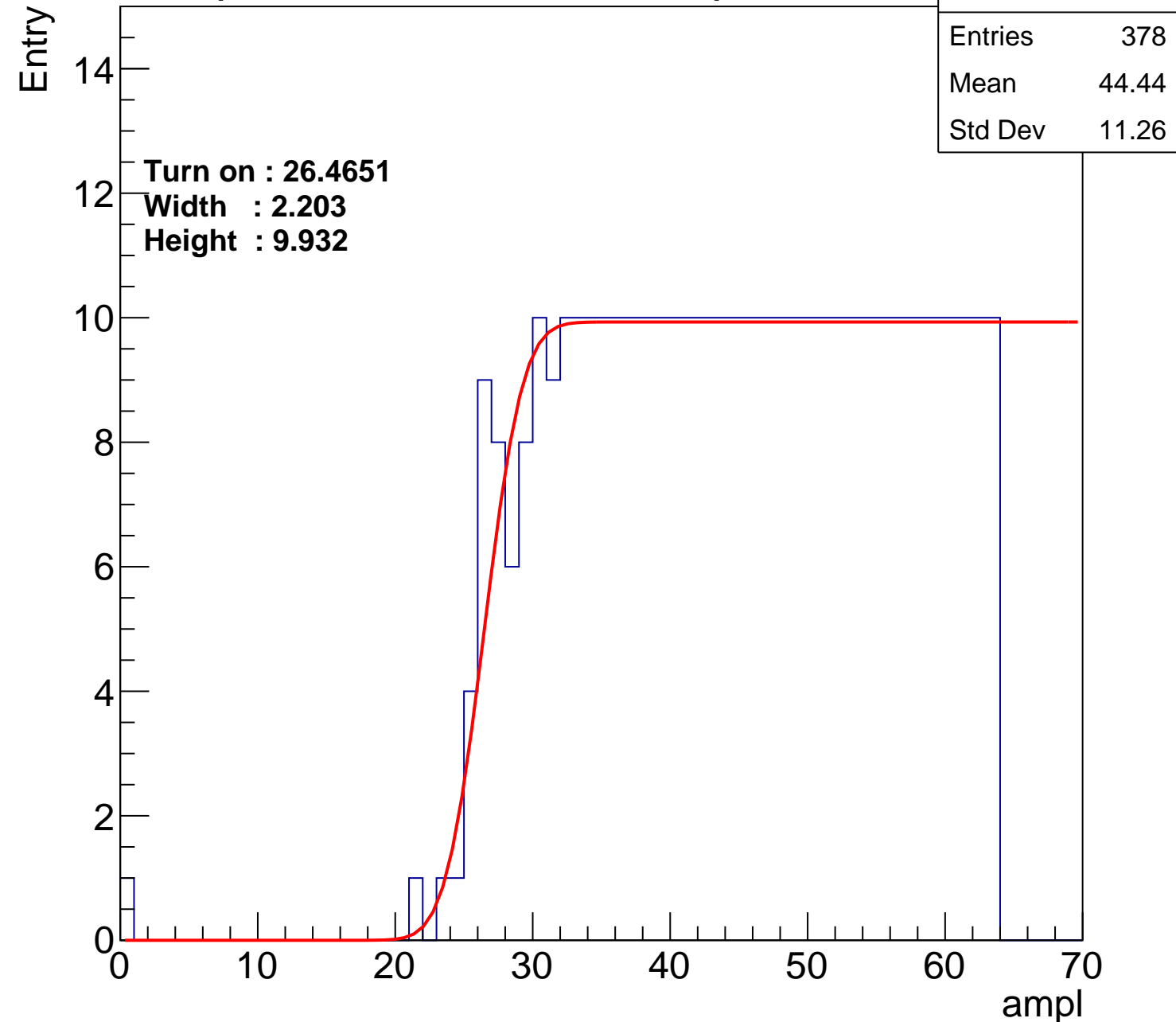
Width : 2.203

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch52

calib_packv5_042523_0143.root, FC#10, port B3

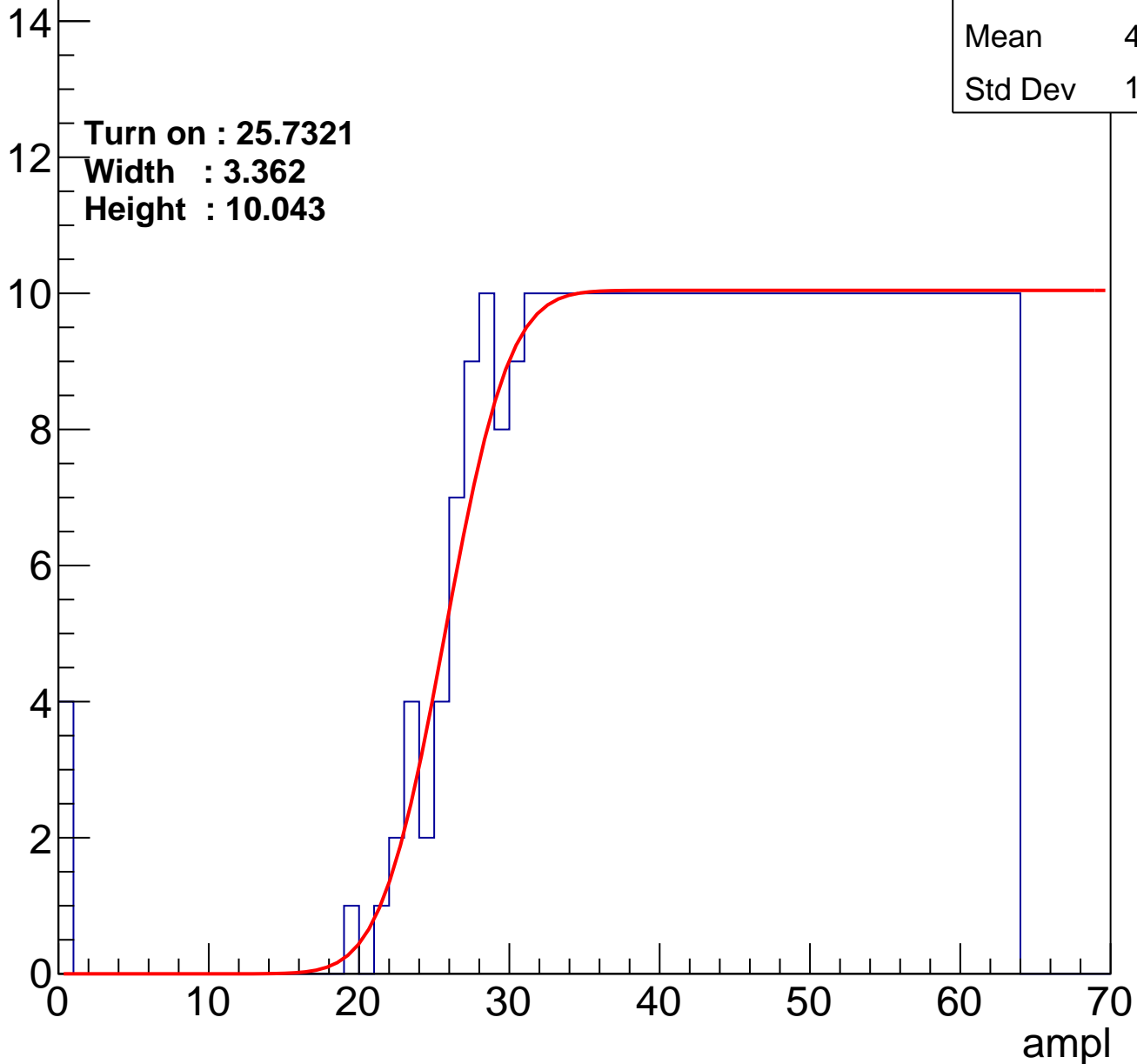
Entries	391
Mean	43.58
Std Dev	12.15

Turn on : 25.7321

Width : 3.362

Height : 10.043

Entry



B1L002S, U25-ch53

calib_packv5_042523_0143.root, FC#10, port B3

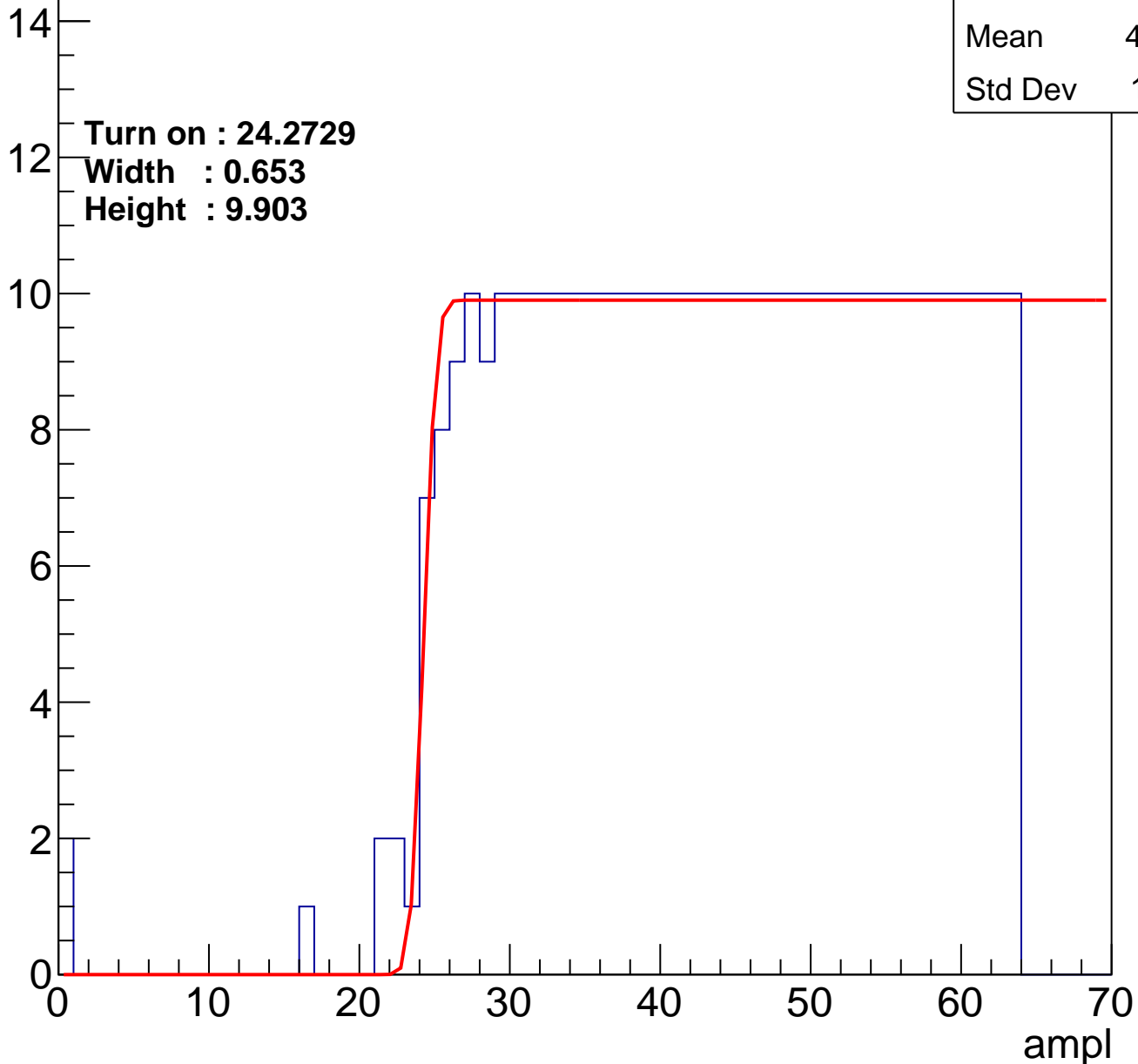
Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.2729

Width : 0.653

Height : 9.903

Entry



B1L002S, U25-ch54

calib_packv5_042523_0143.root, FC#10, port B3

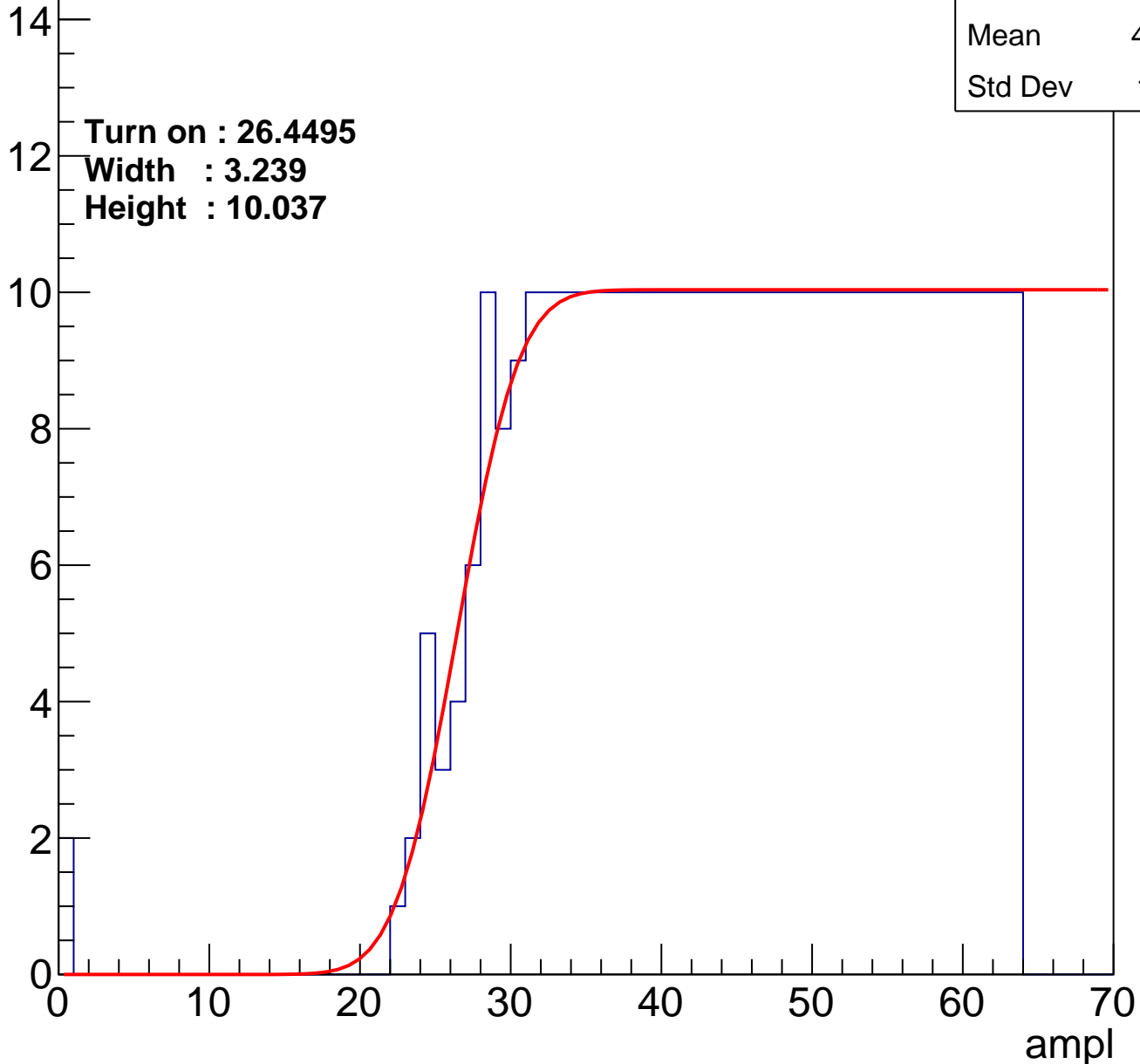
Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.4495

Width : 3.239

Height : 10.037

Entry



B1L002S, U25-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.7738

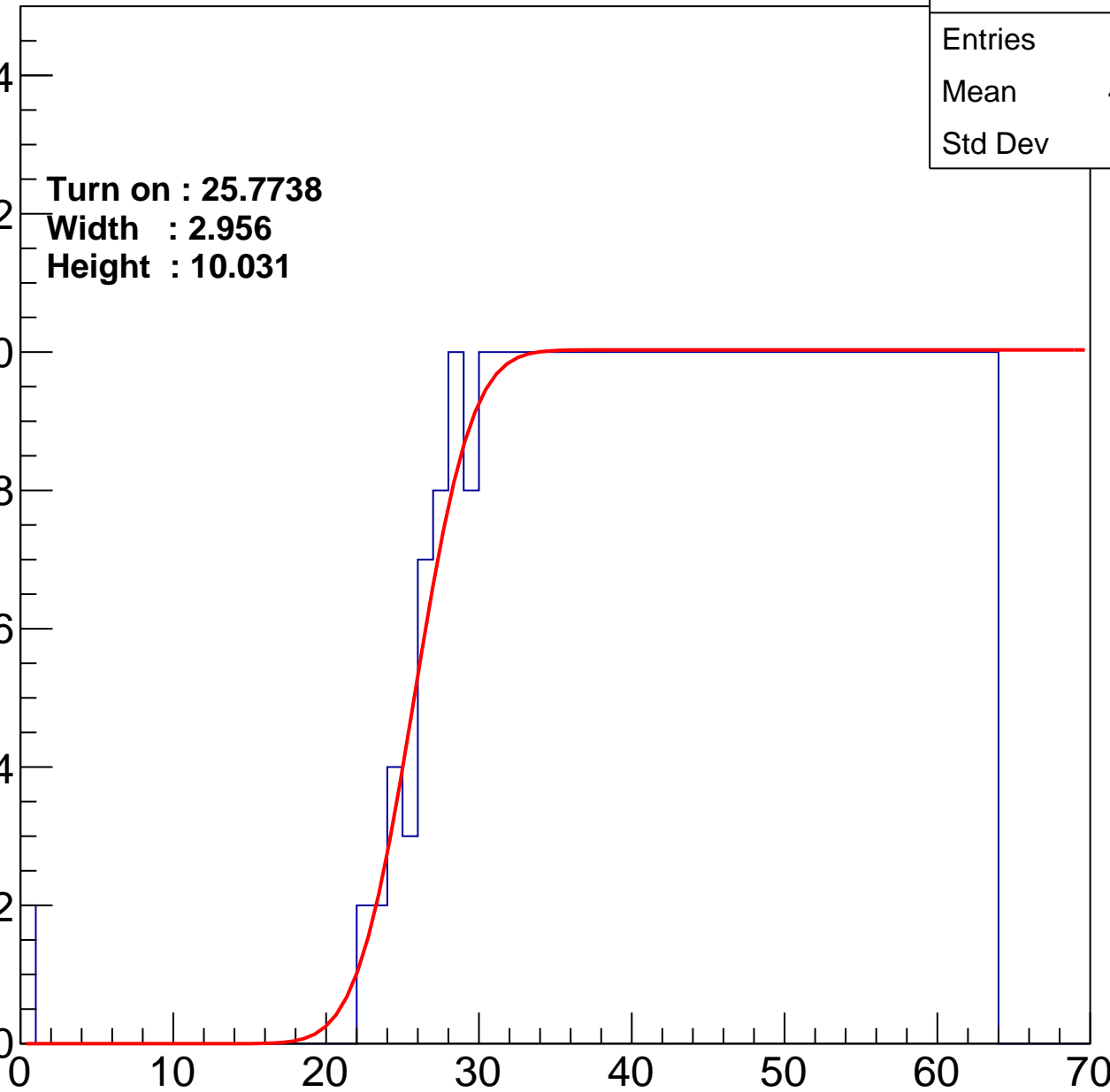
Width : 2.956

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 24.1447

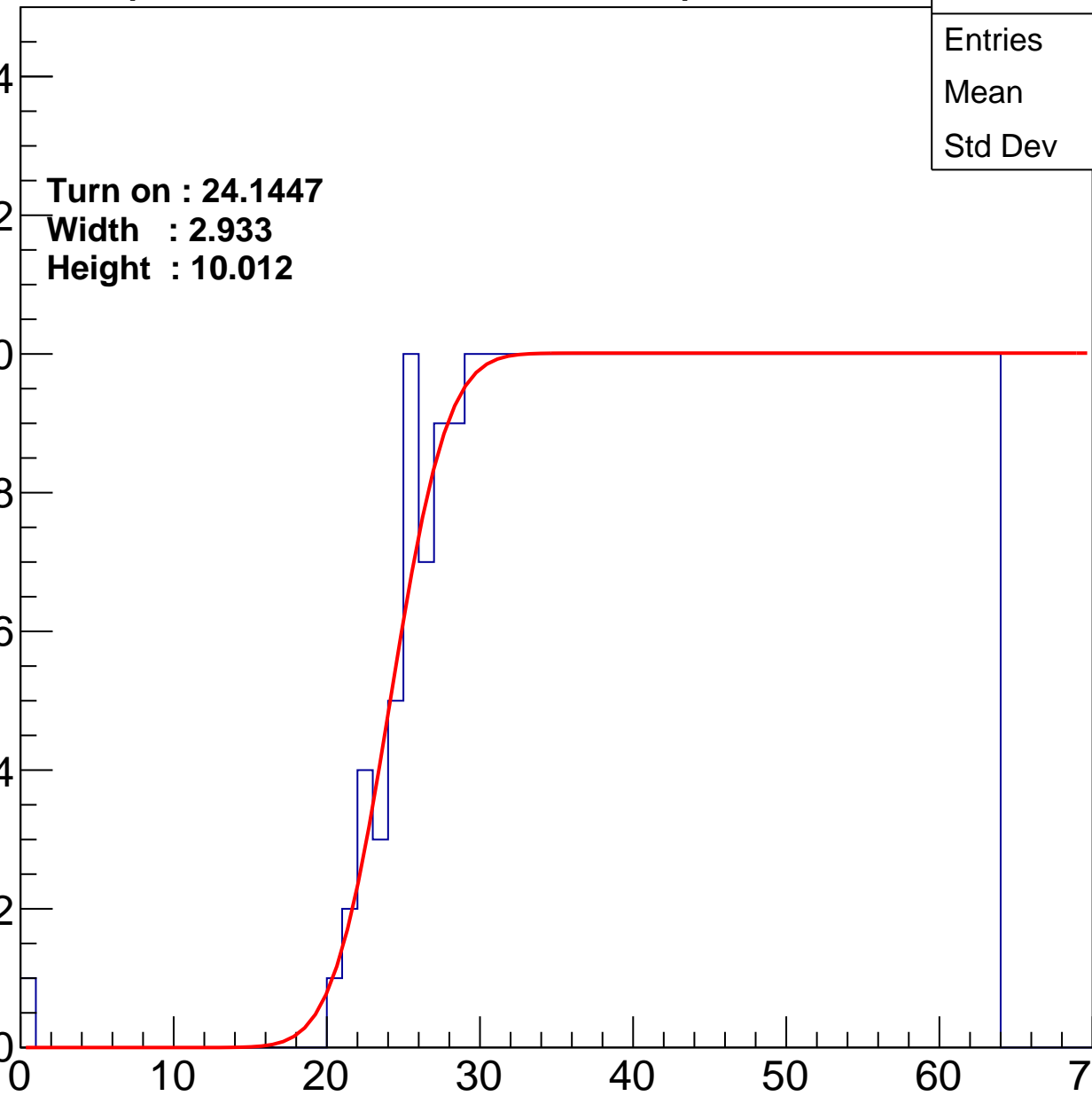
Width : 2.933

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 27.0167

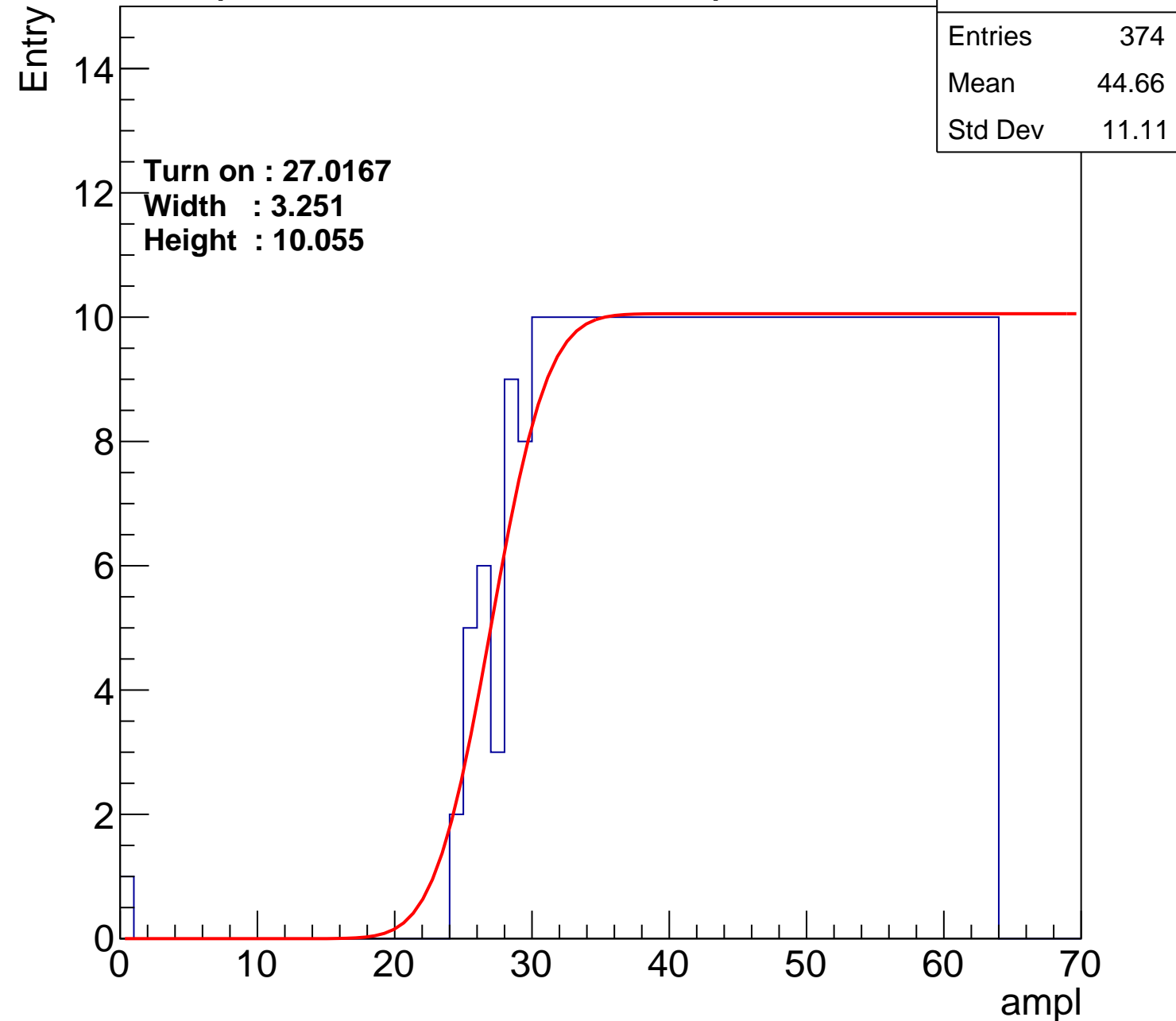
Width : 3.251

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.52
Std Dev	11.8

Turn on : 24.6869

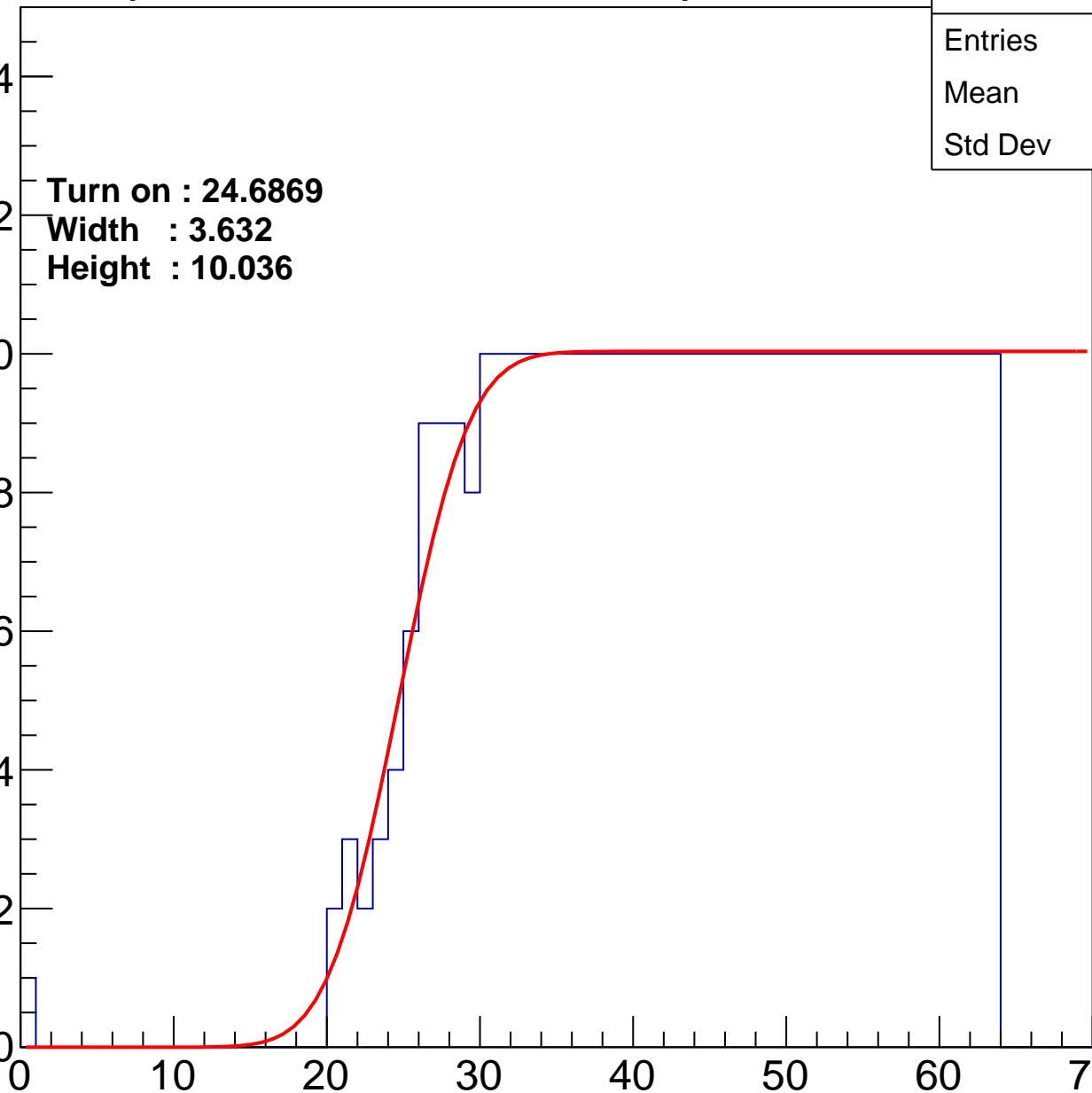
Width : 3.632

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch59

calib_packv5_042523_0143.root, FC#10, port B3

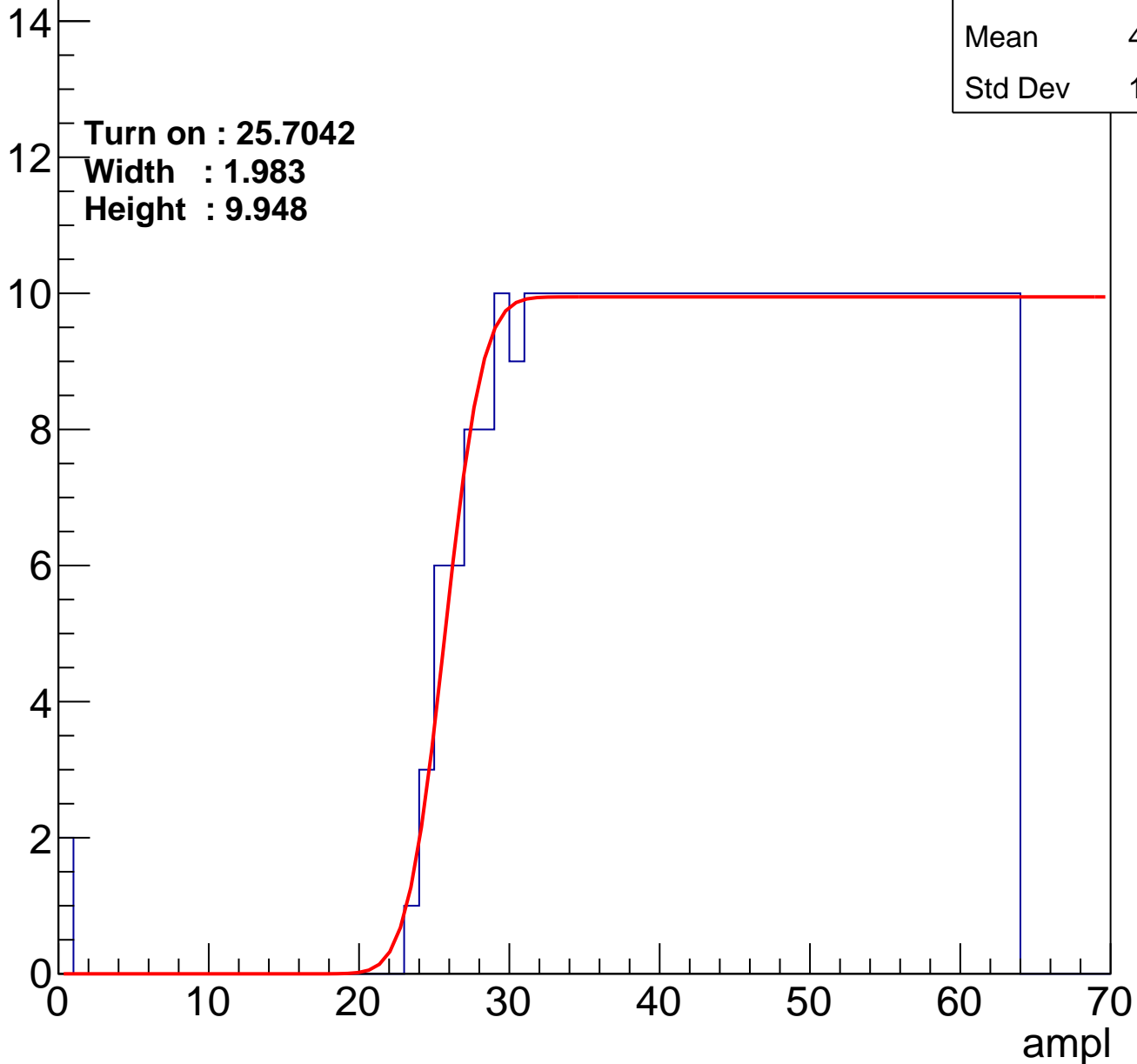
Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.7042

Width : 1.983

Height : 9.948

Entry



B1L002S, U25-ch60

calib_packv5_042523_0143.root, FC#10, port B3

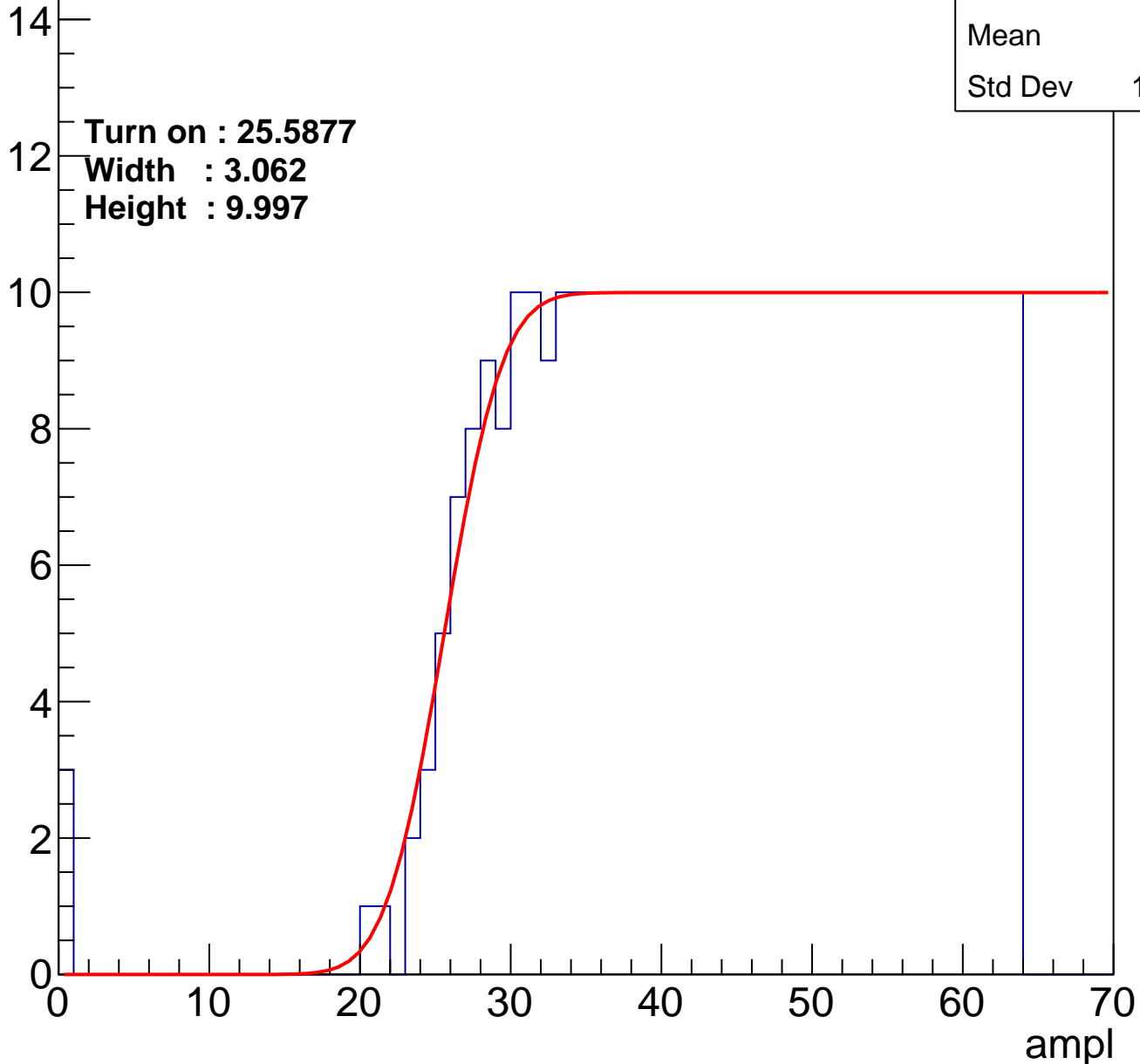
Entries	386
Mean	43.9
Std Dev	11.85

Turn on : 25.5877

Width : 3.062

Height : 9.997

Entry



B1L002S, U25-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	44.99
Std Dev	10.94

Turn on : 27.4840

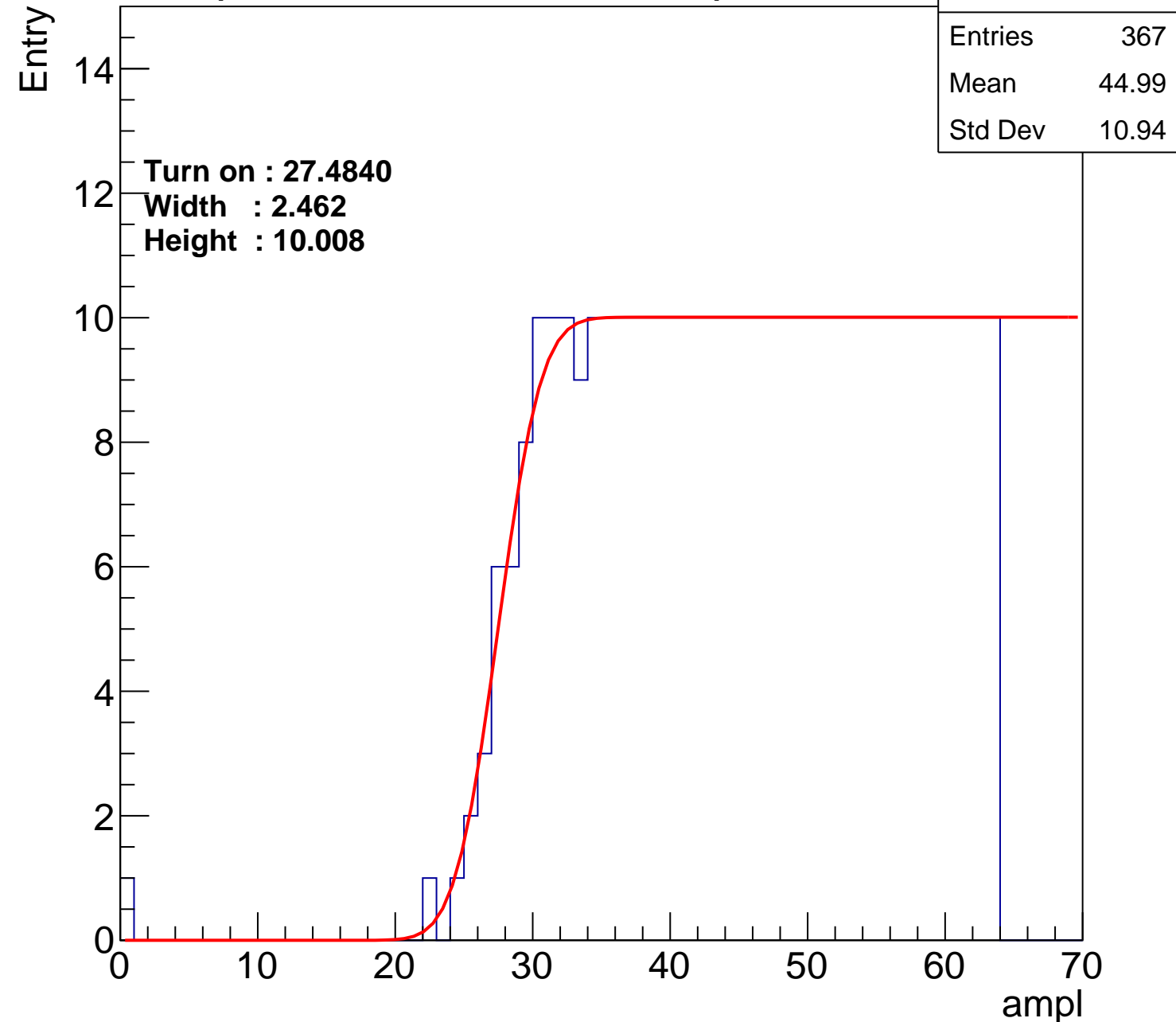
Width : 2.462

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 26.1220

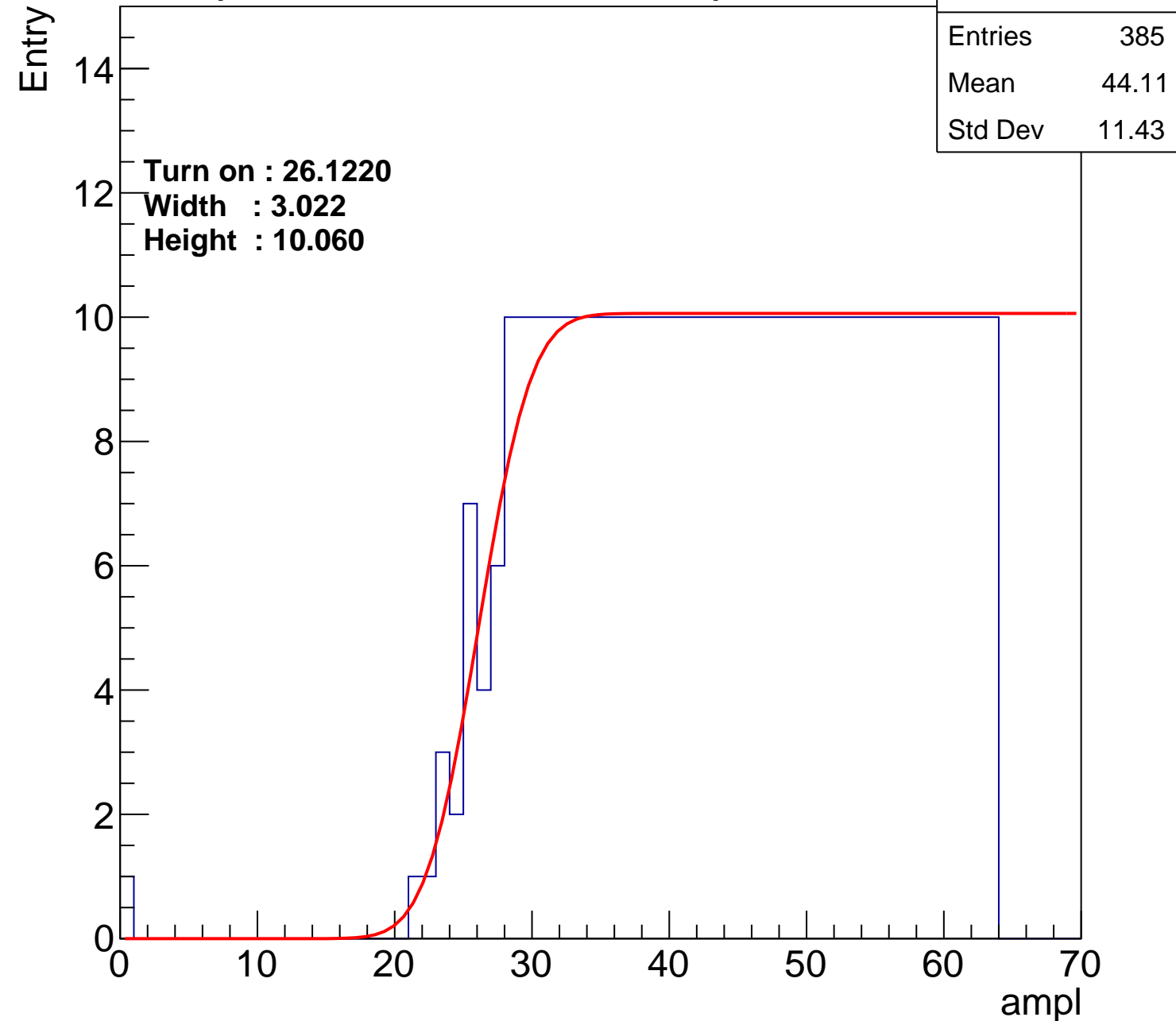
Width : 3.022

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch63

calib_packv5_042523_0143.root, FC#10, port B3

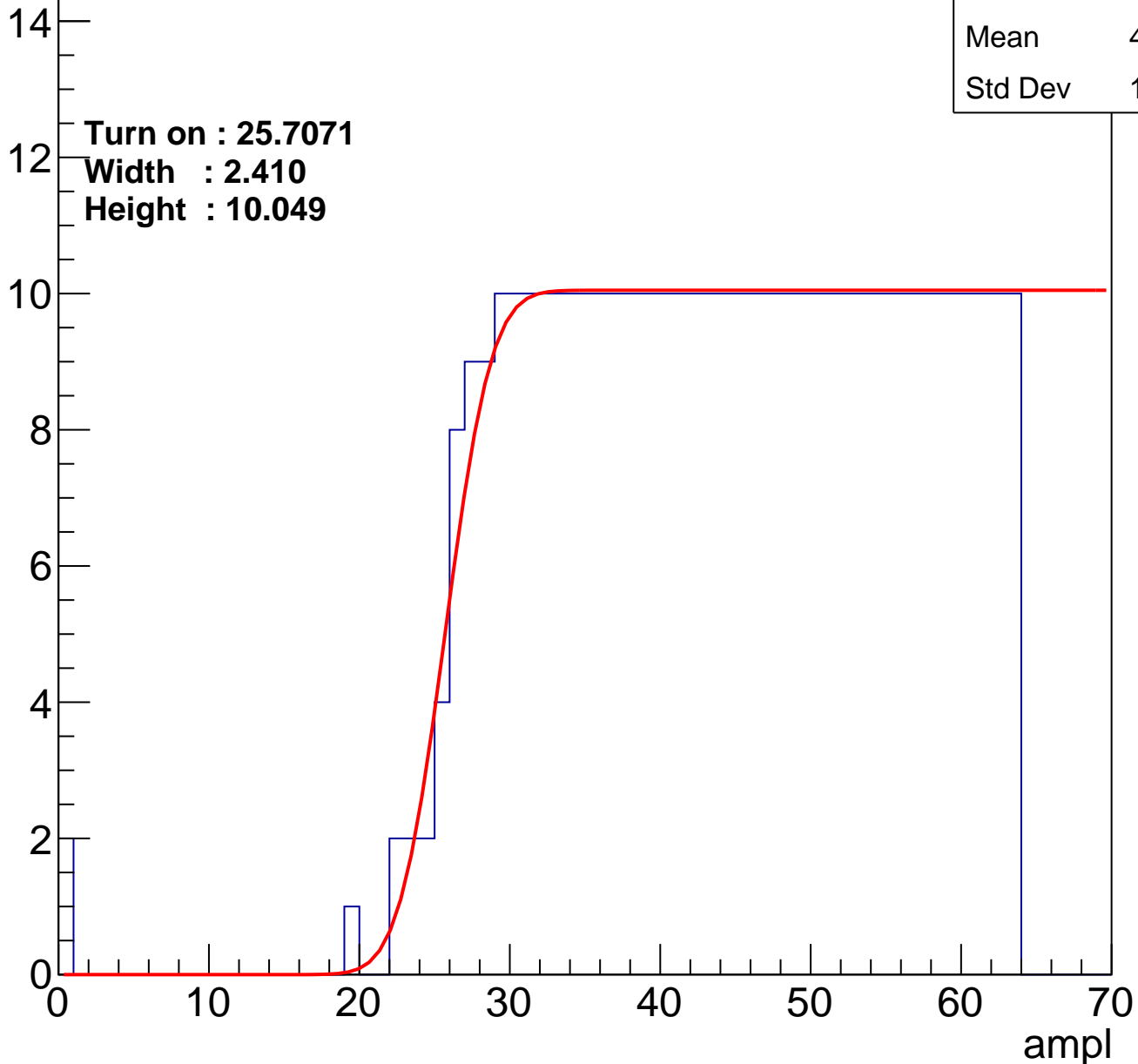
Entries	389
Mean	43.86
Std Dev	11.69

Turn on : 25.7071

Width : 2.410

Height : 10.049

Entry



B1L002S, U25-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
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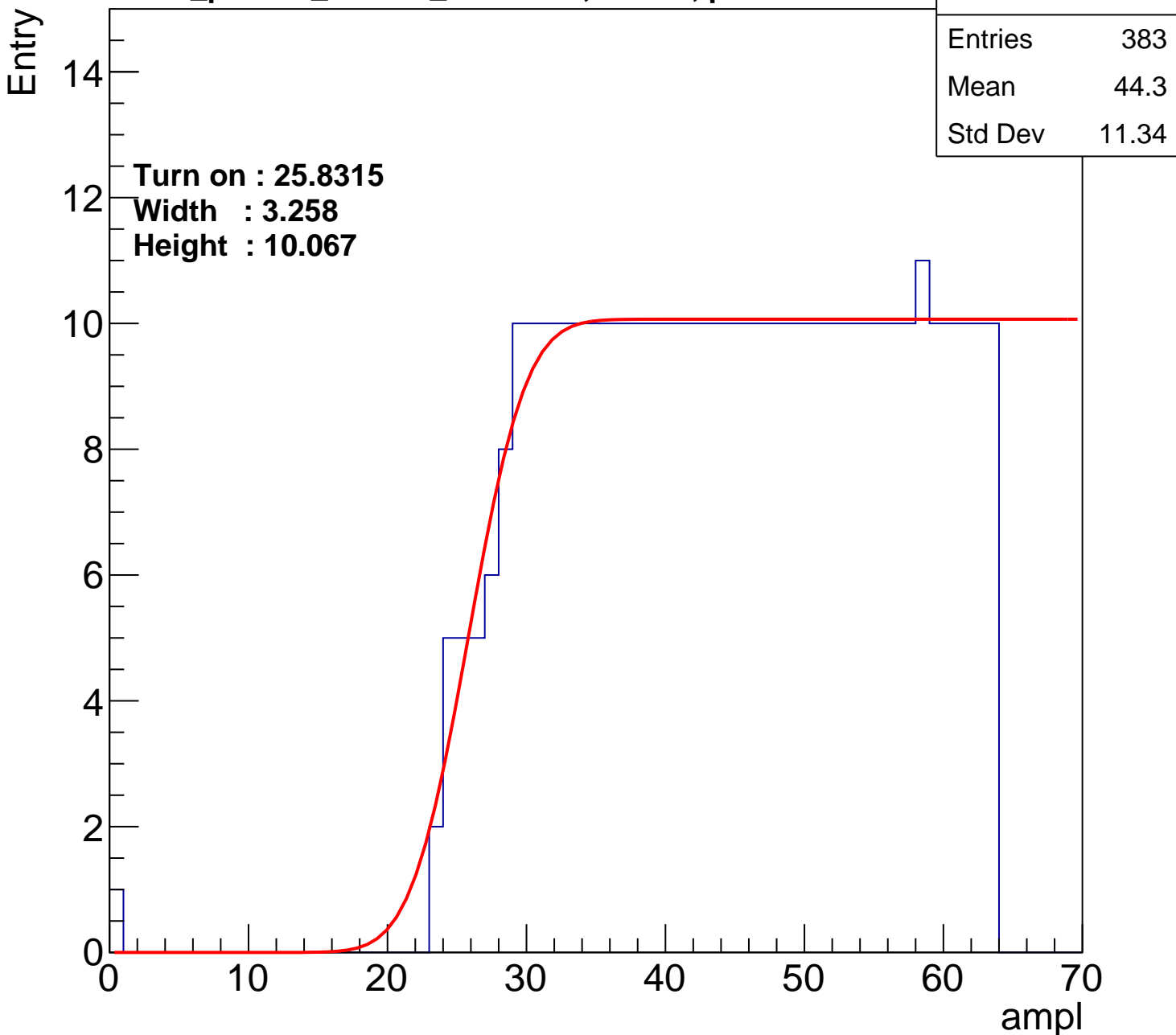
Mean	44.3
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Std Dev	11.34
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Turn on : 25.8315

Width : 3.258

Height : 10.067



B1L002S, U25-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.38
Std Dev	11.81

Turn on : 24.1501

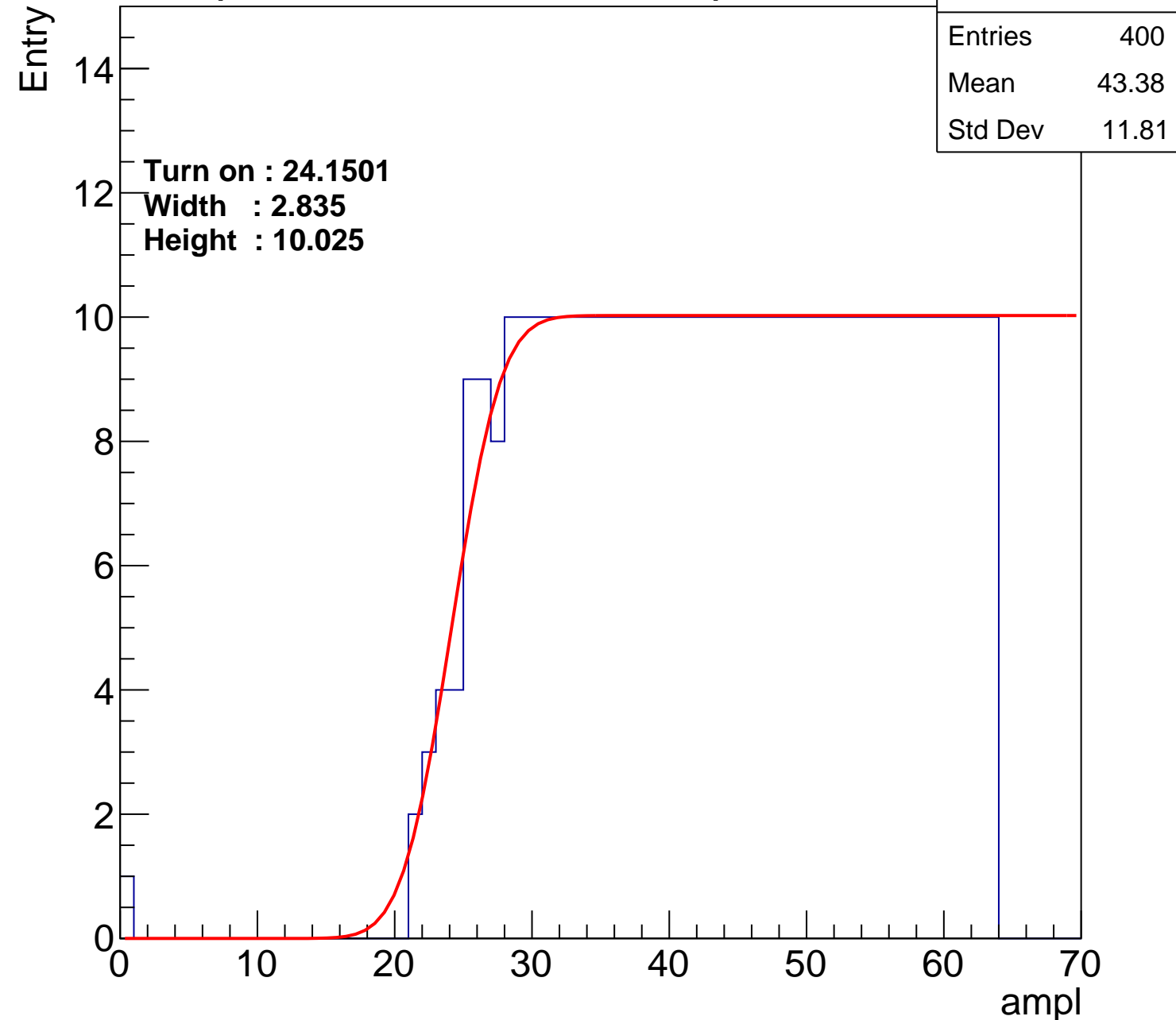
Width : 2.835

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch66

calib_packv5_042523_0143.root, FC#10, port B3

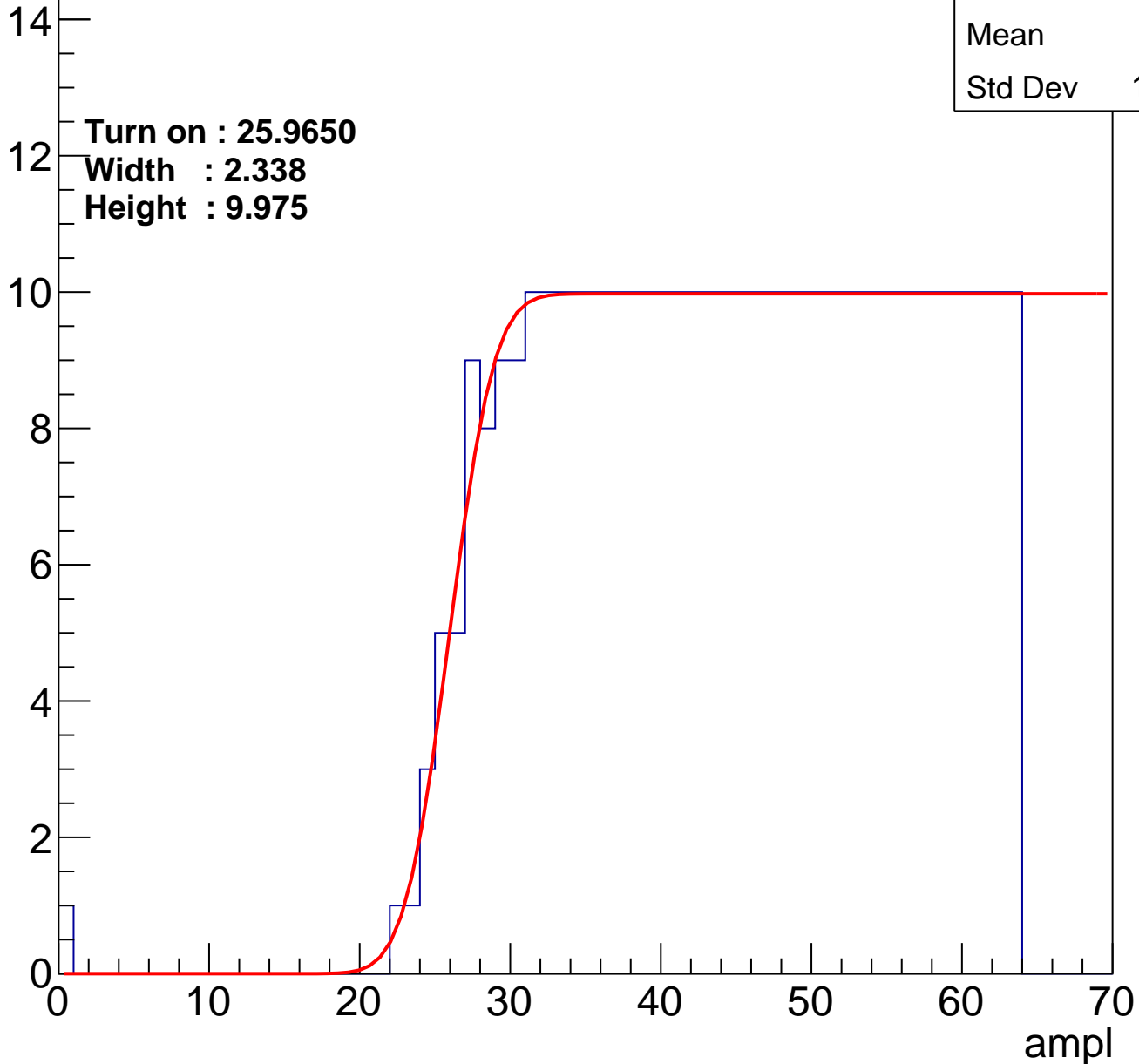
Entries	381
Mean	44.3
Std Dev	11.31

Turn on : 25.9650

Width : 2.338

Height : 9.975

Entry



B1L002S, U25-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.47
Std Dev	12.22

Turn on : 24.9415

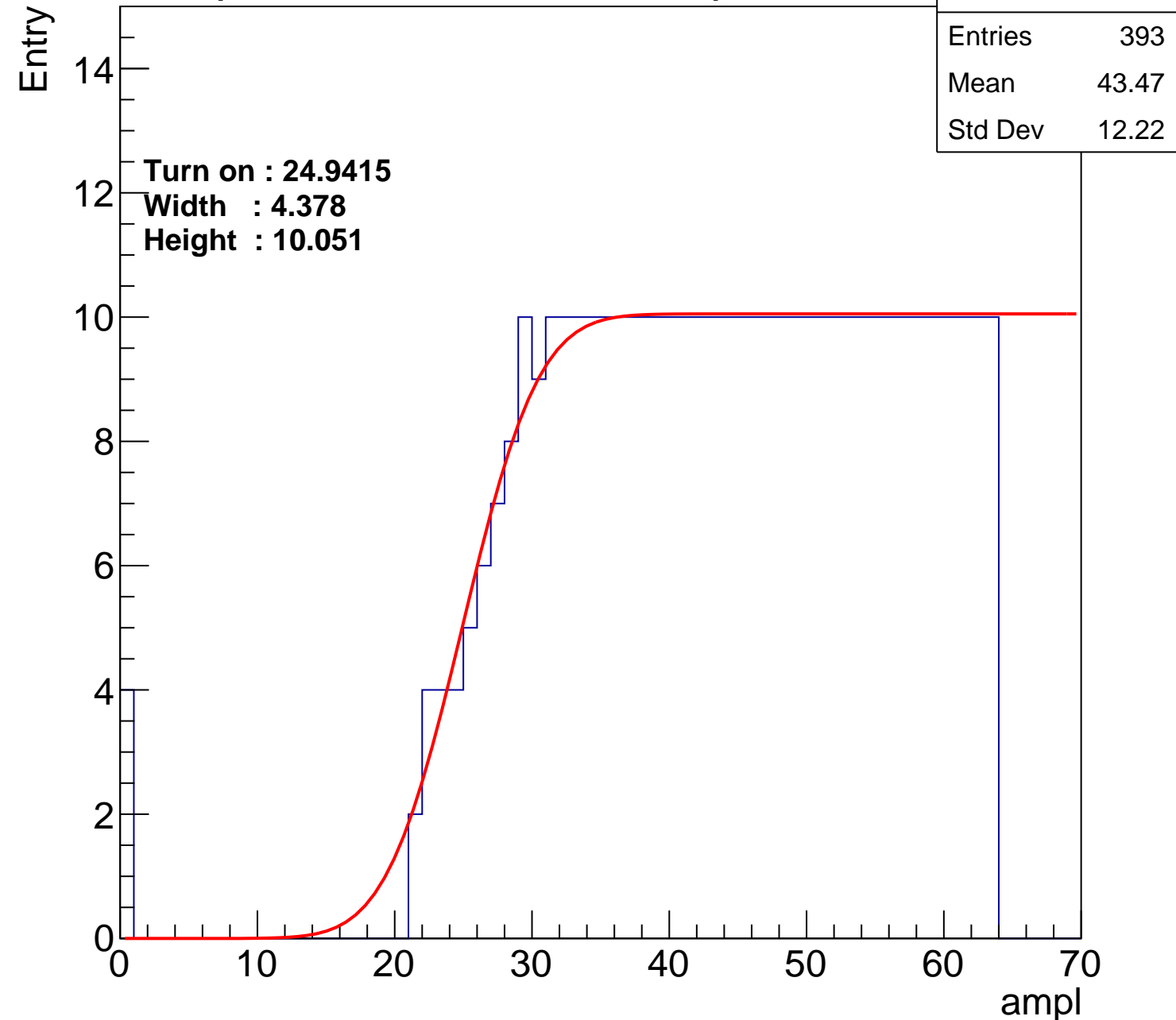
Width : 4.378

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.46
Std Dev	11.41

Turn on : 26.4399

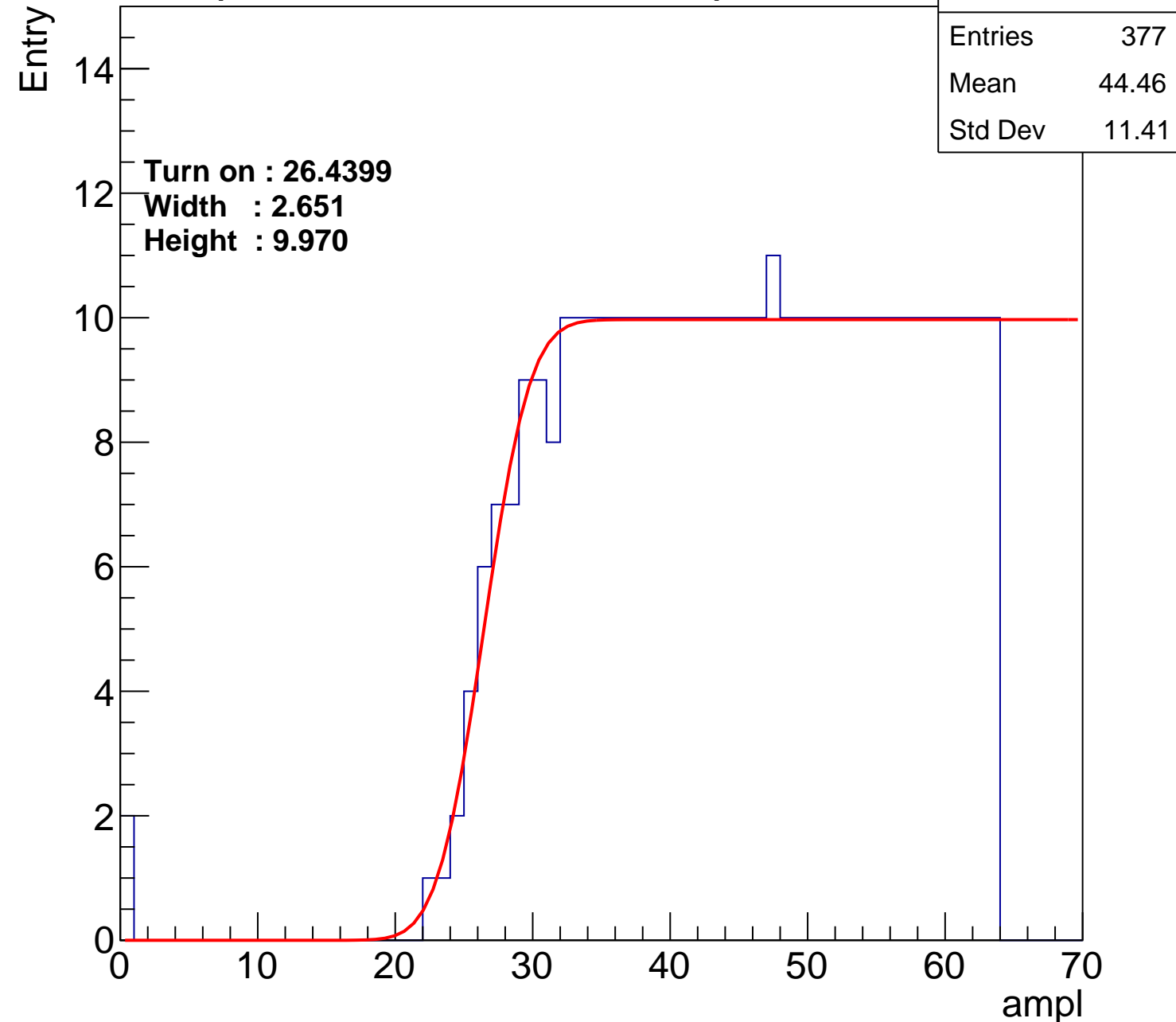
Width : 2.651

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch69

calib_packv5_042523_0143.root, FC#10, port B3

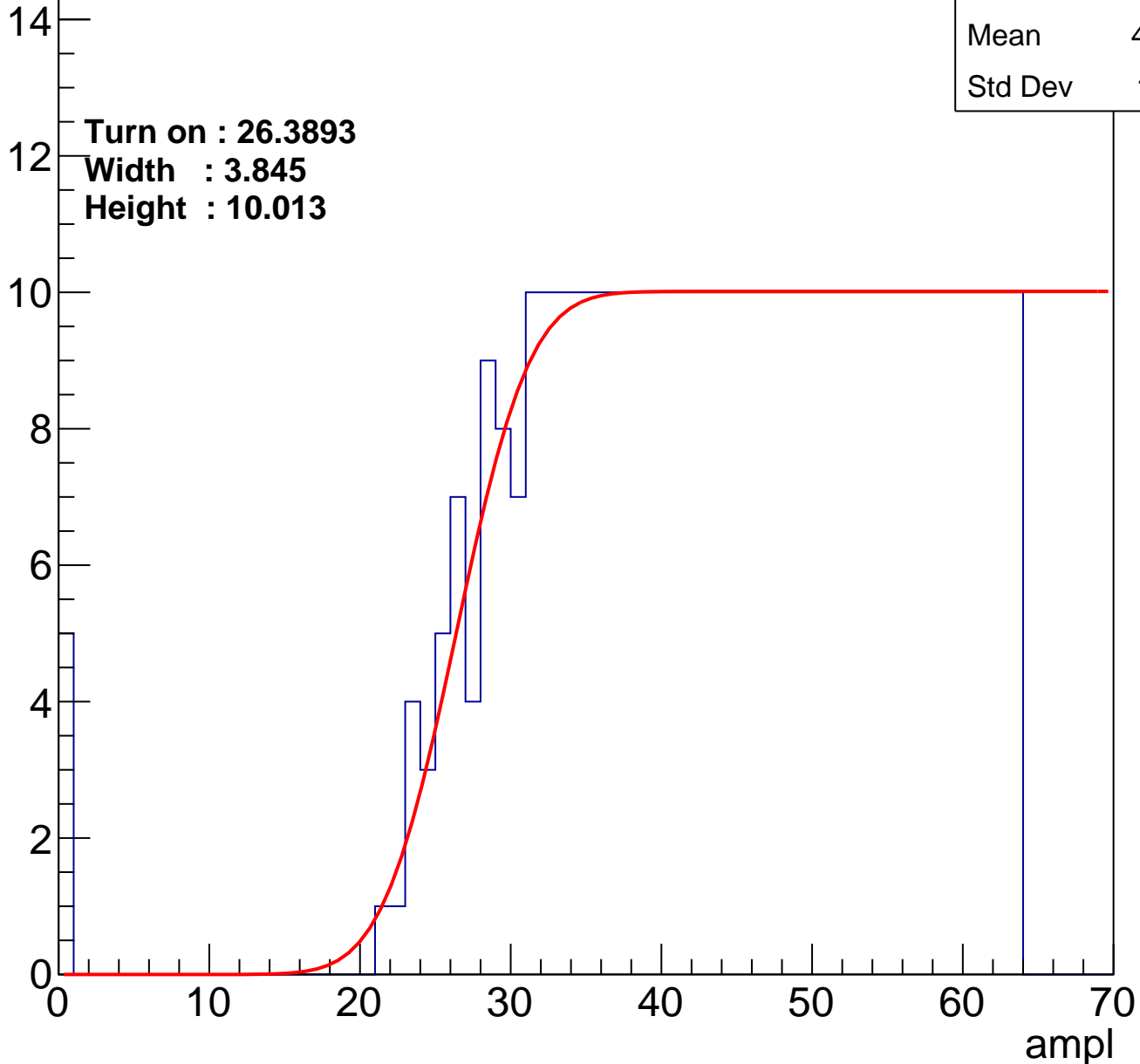
Entries	384
Mean	43.82
Std Dev	12.21

Turn on : 26.3893

Width : 3.845

Height : 10.013

Entry



B1L002S, U25-ch70

calib_packv5_042523_0143.root, FC#10, port B3

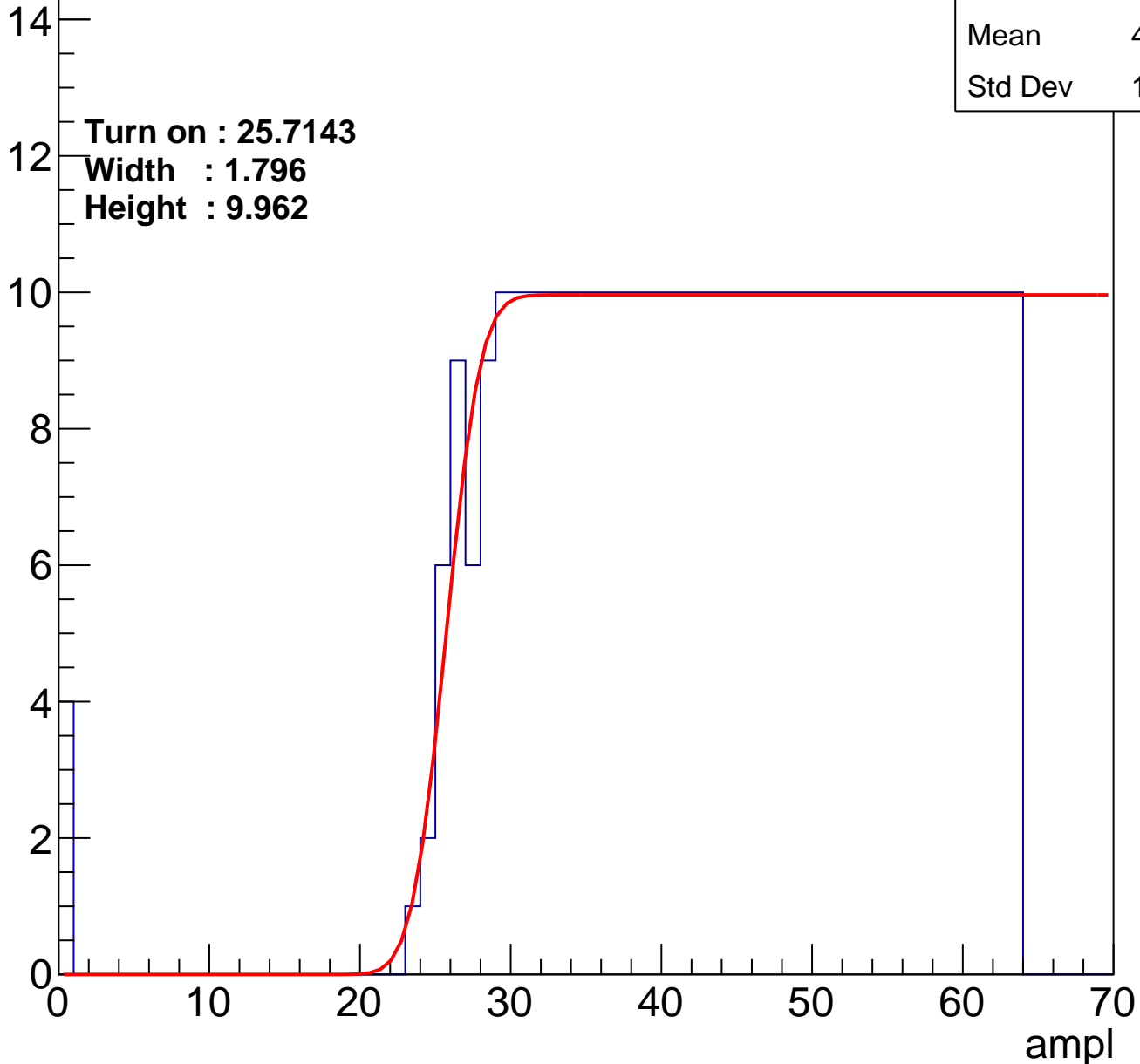
Entries	387
Mean	43.85
Std Dev	11.94

Turn on : 25.7143

Width : 1.796

Height : 9.962

Entry



B1L002S, U25-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.59
Std Dev	11.32

Turn on : 27.4188

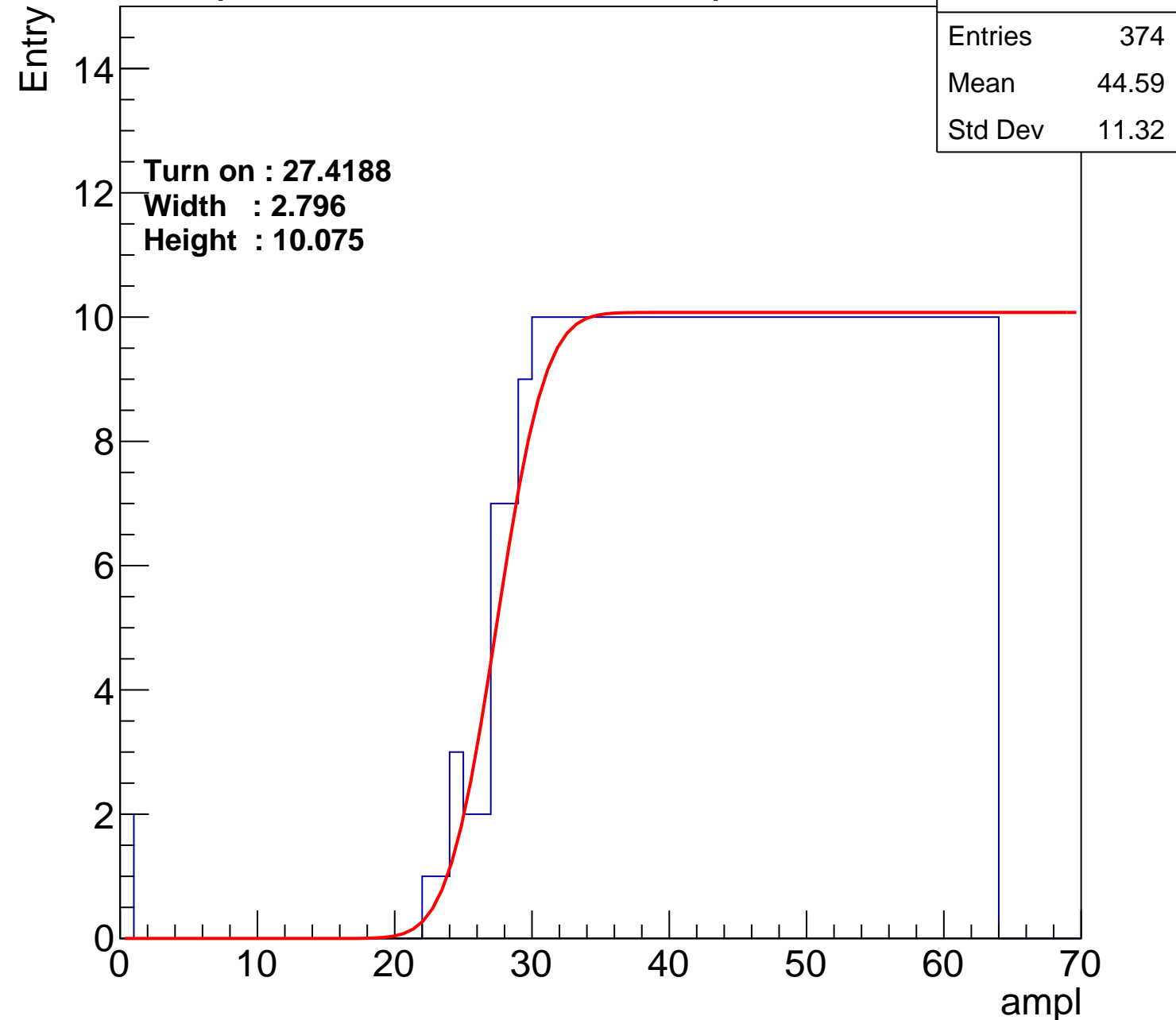
Width : 2.796

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.85
Std Dev	12.35

Turn on : 23.4103

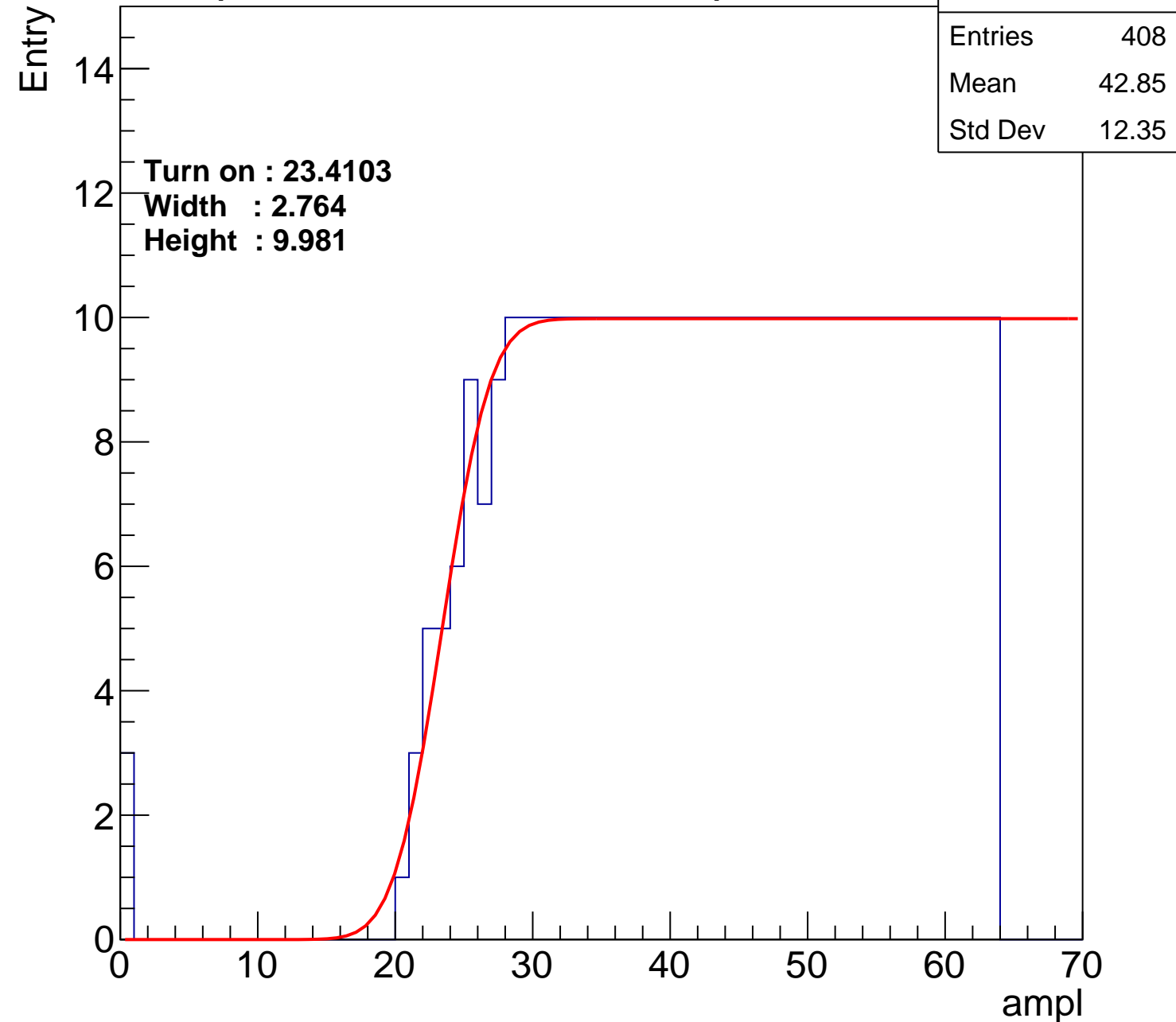
Width : 2.764

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	412
Mean	42.65
Std Dev	12.46

Turn on : 23.4244

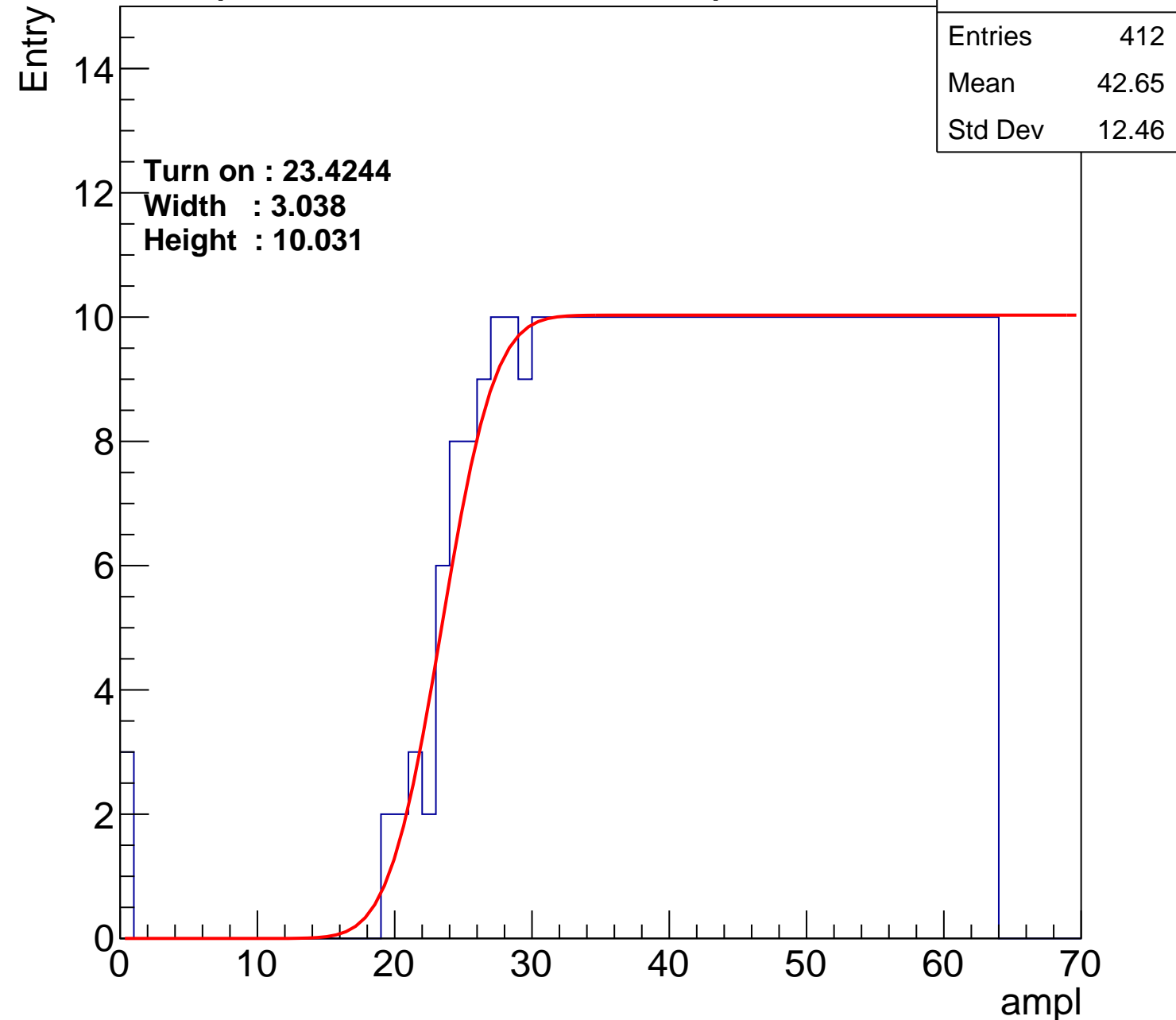
Width : 3.038

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch74

calib_packv5_042523_0143.root, FC#10, port B3

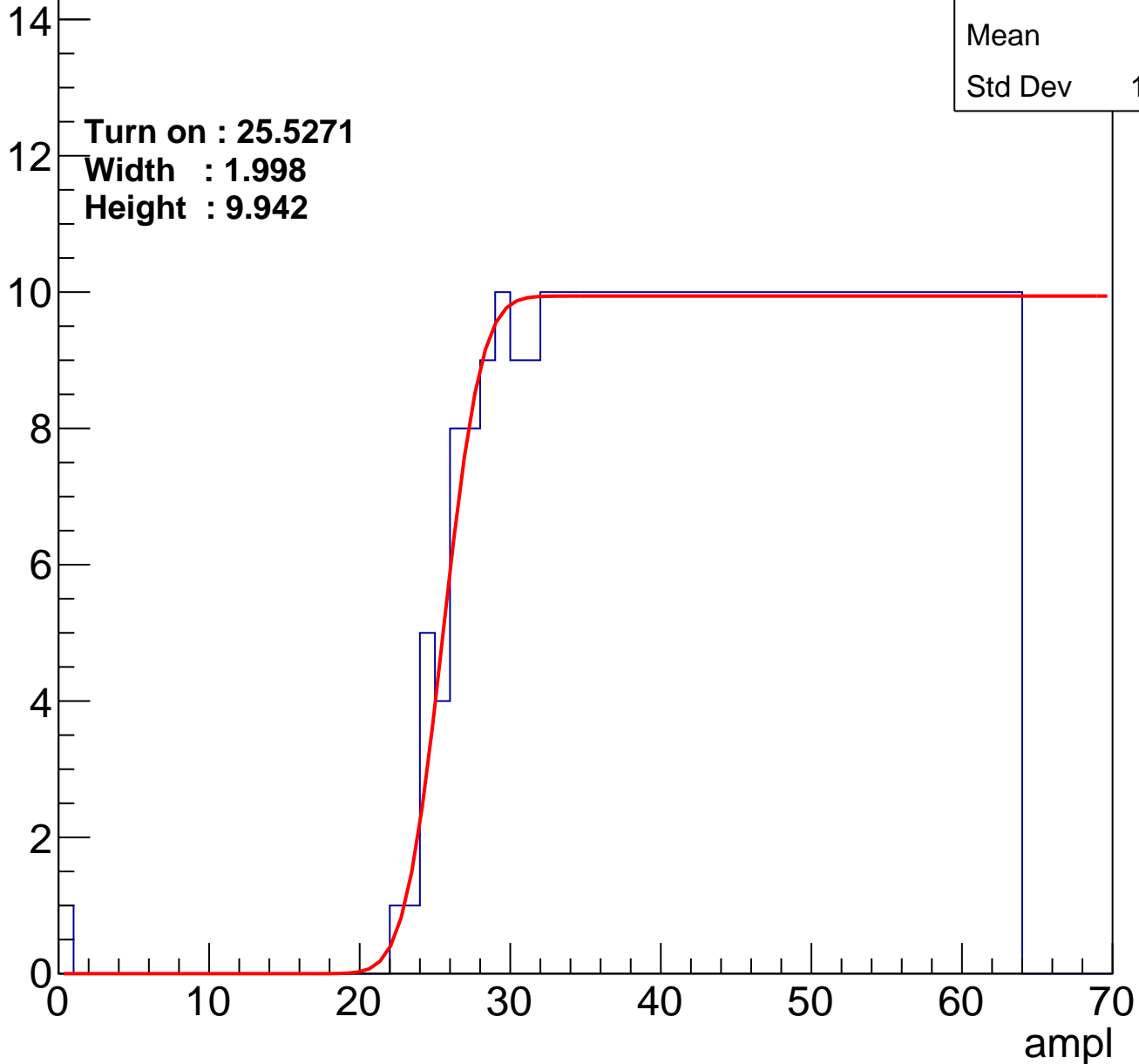
Entries	385
Mean	44.1
Std Dev	11.42

Turn on : 25.5271

Width : 1.998

Height : 9.942

Entry



B1L002S, U25-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.87
Std Dev	11.67

Turn on : 25.0004

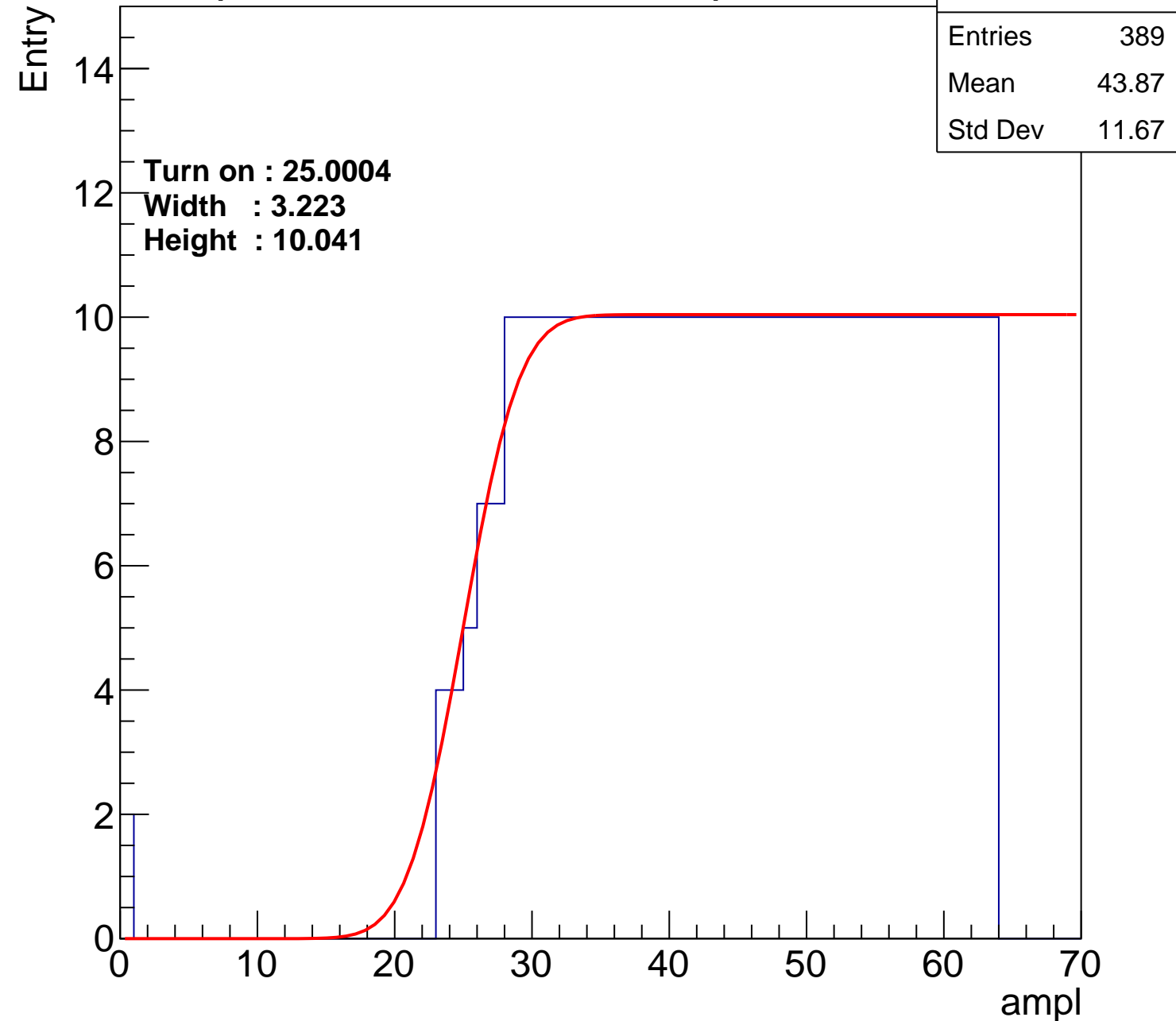
Width : 3.223

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch76

calib_packv5_042523_0143.root, FC#10, port B3

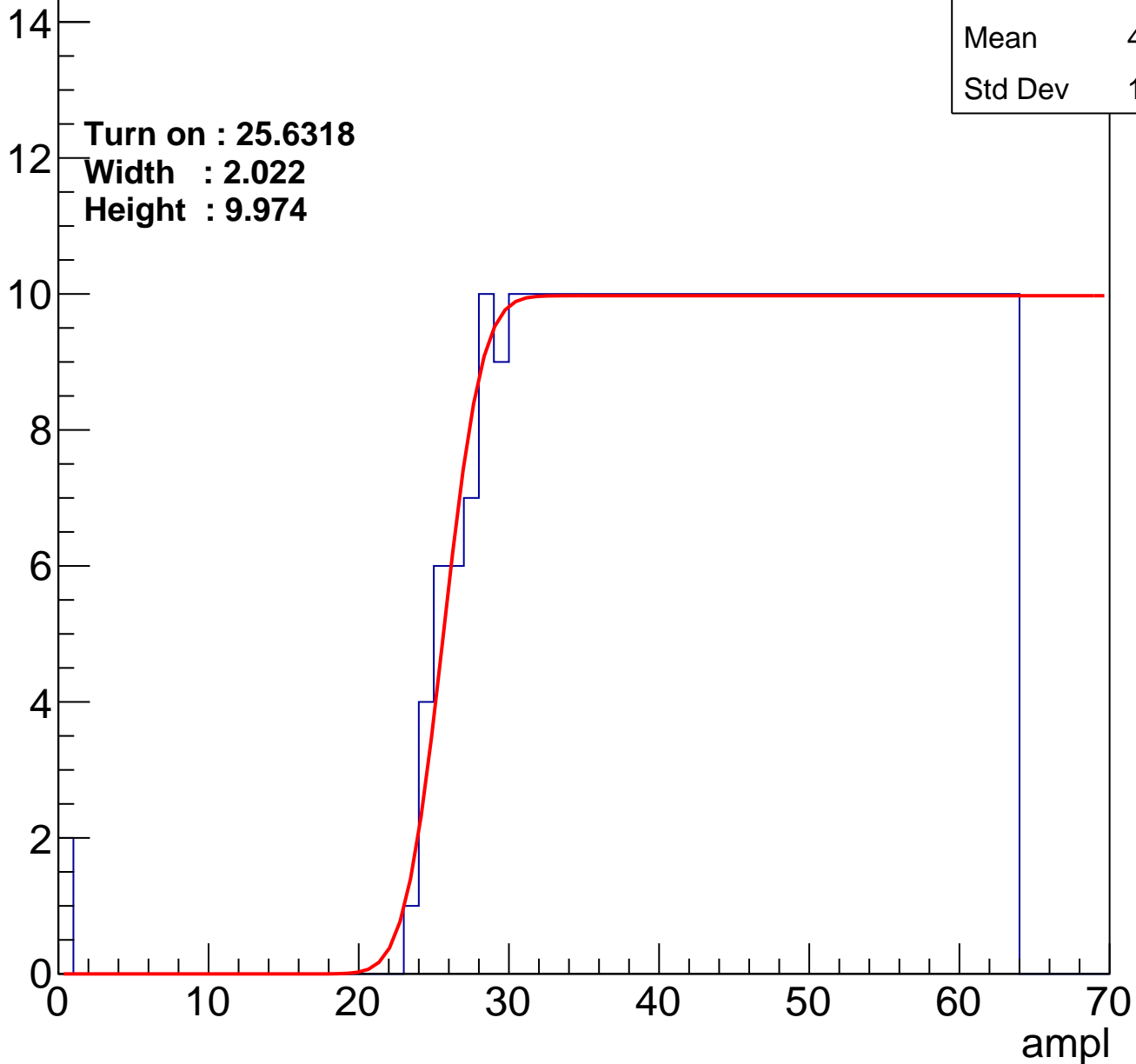
Entries	385
Mean	44.06
Std Dev	11.56

Turn on : 25.6318

Width : 2.022

Height : 9.974

Entry



B1L002S, U25-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.8034

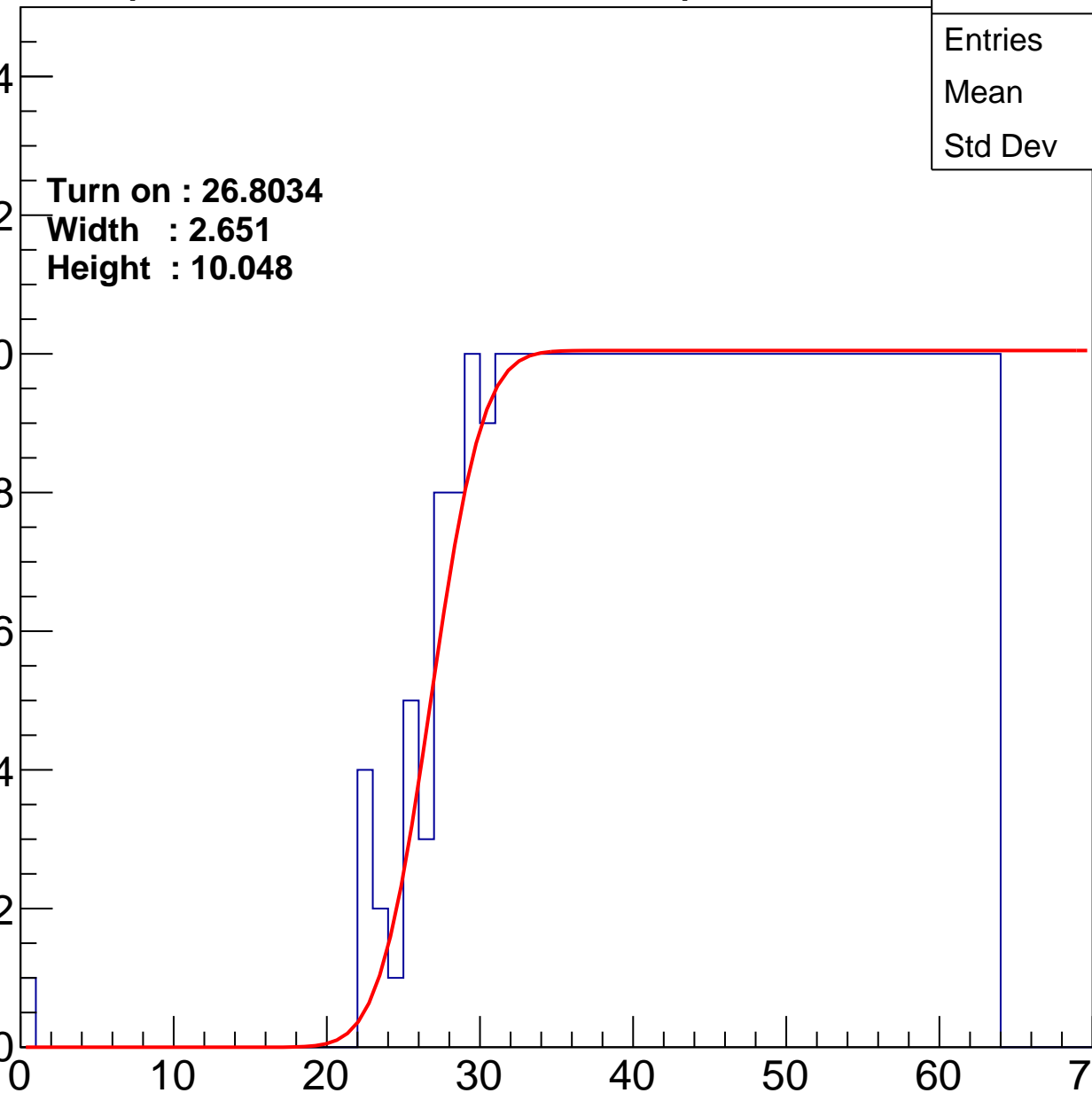
Width : 2.651

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.4472

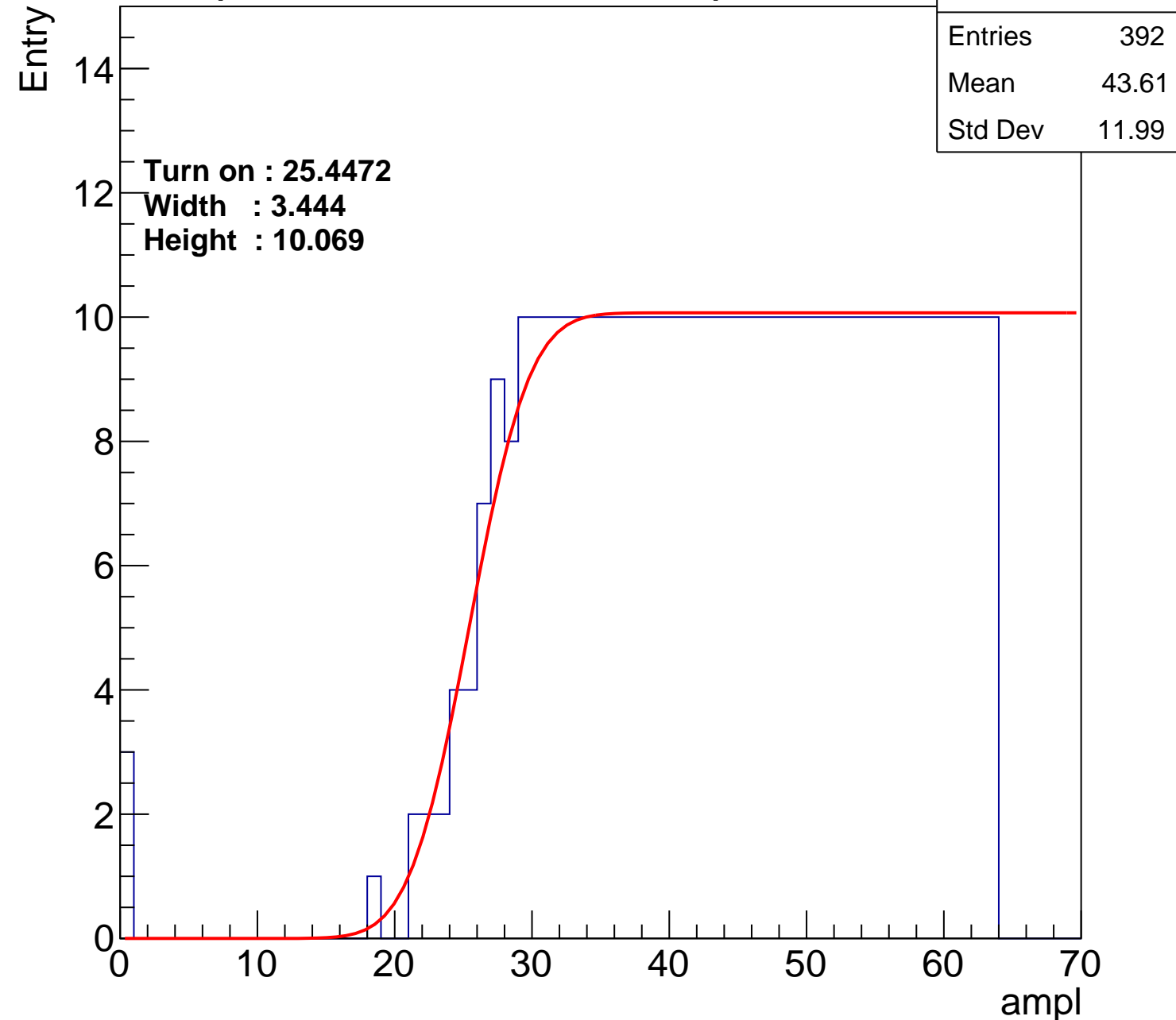
Width : 3.444

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.57
Std Dev	11.72

Turn on : 24.7669

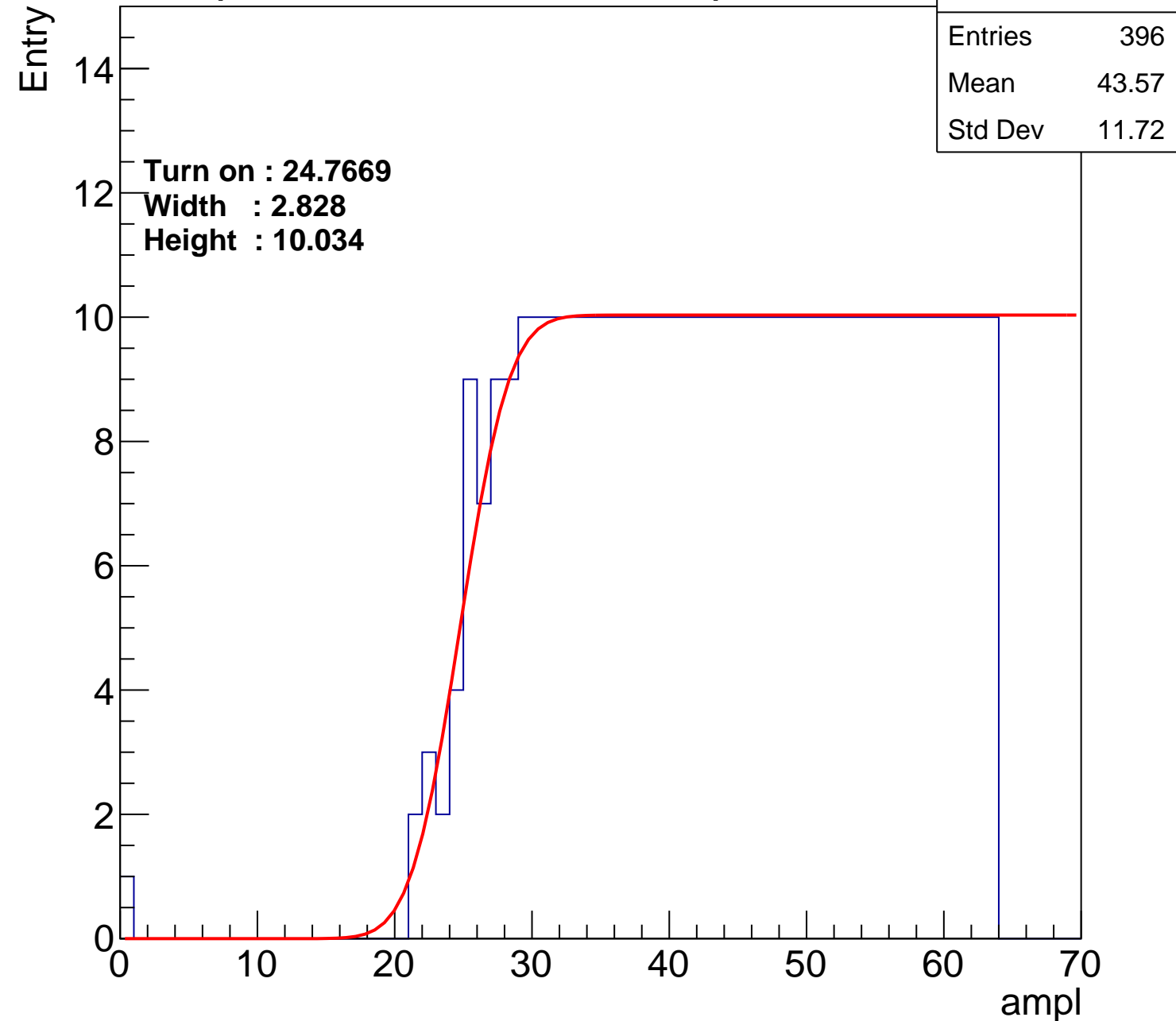
Width : 2.828

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 25.3102

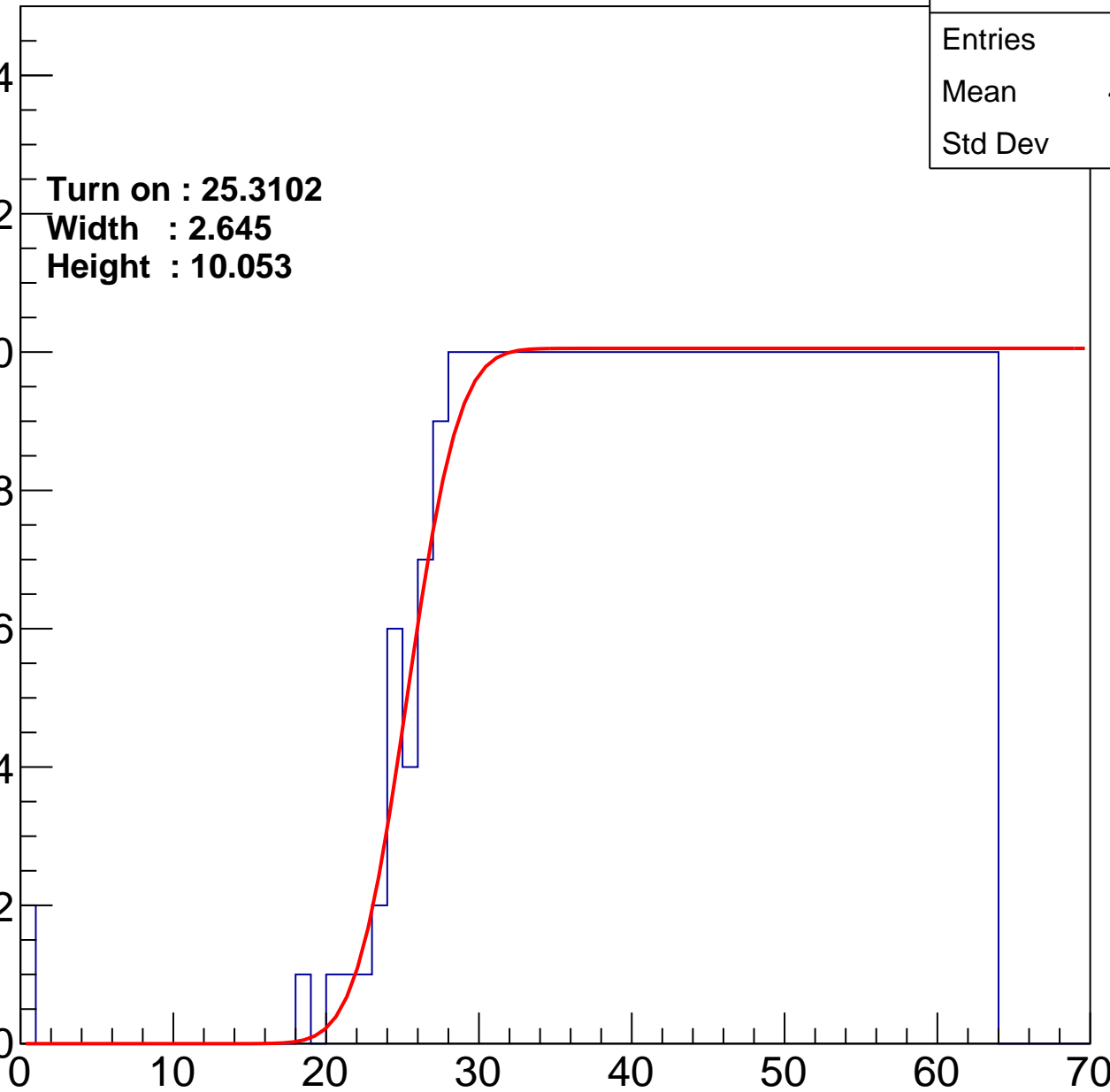
Width : 2.645

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch81

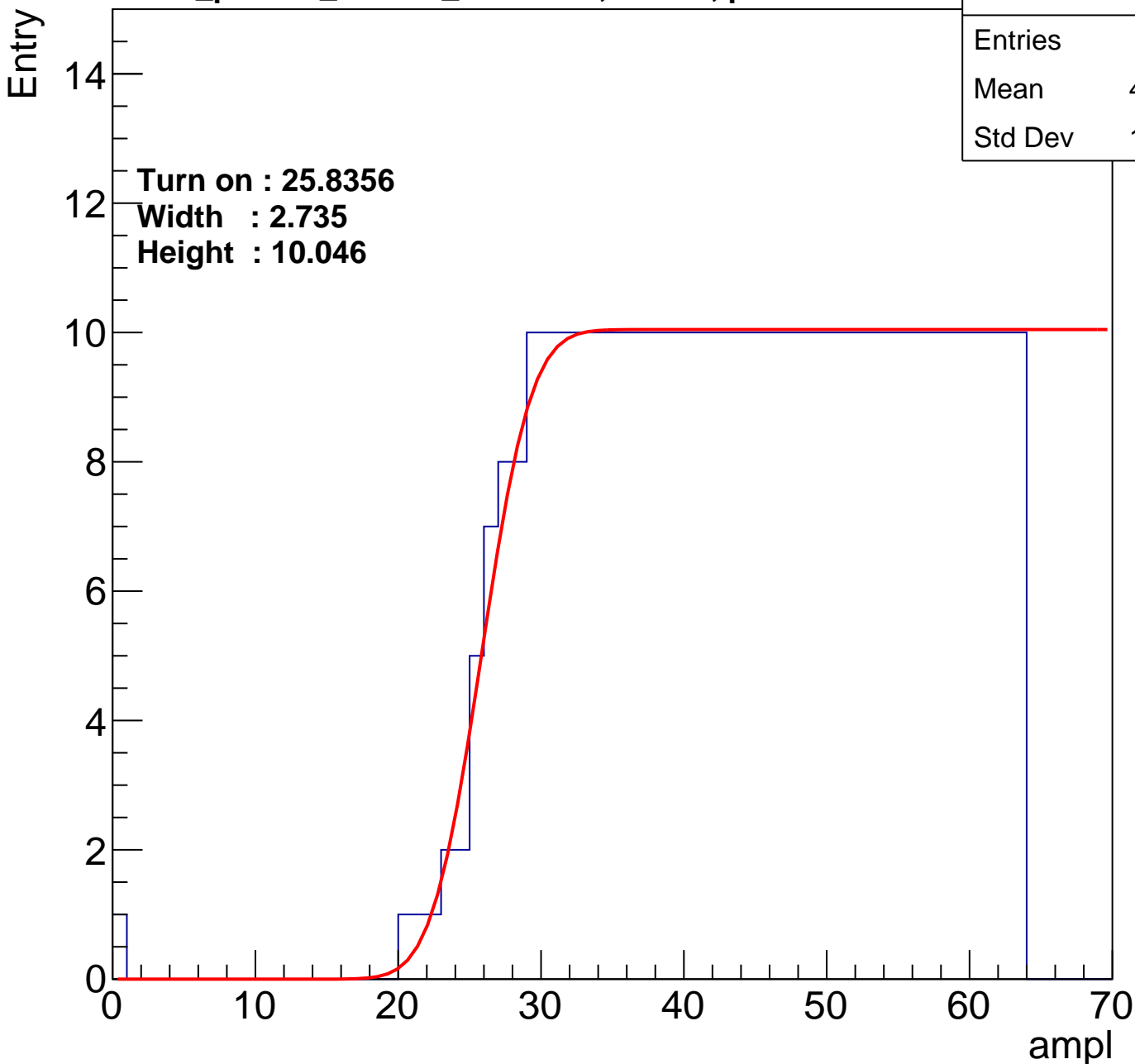
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.8356

Width : 2.735

Height : 10.046

Entries	386
Mean	44.05
Std Dev	11.47



B1L002S, U25-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.36
Std Dev	12.23

Turn on : 25.0529

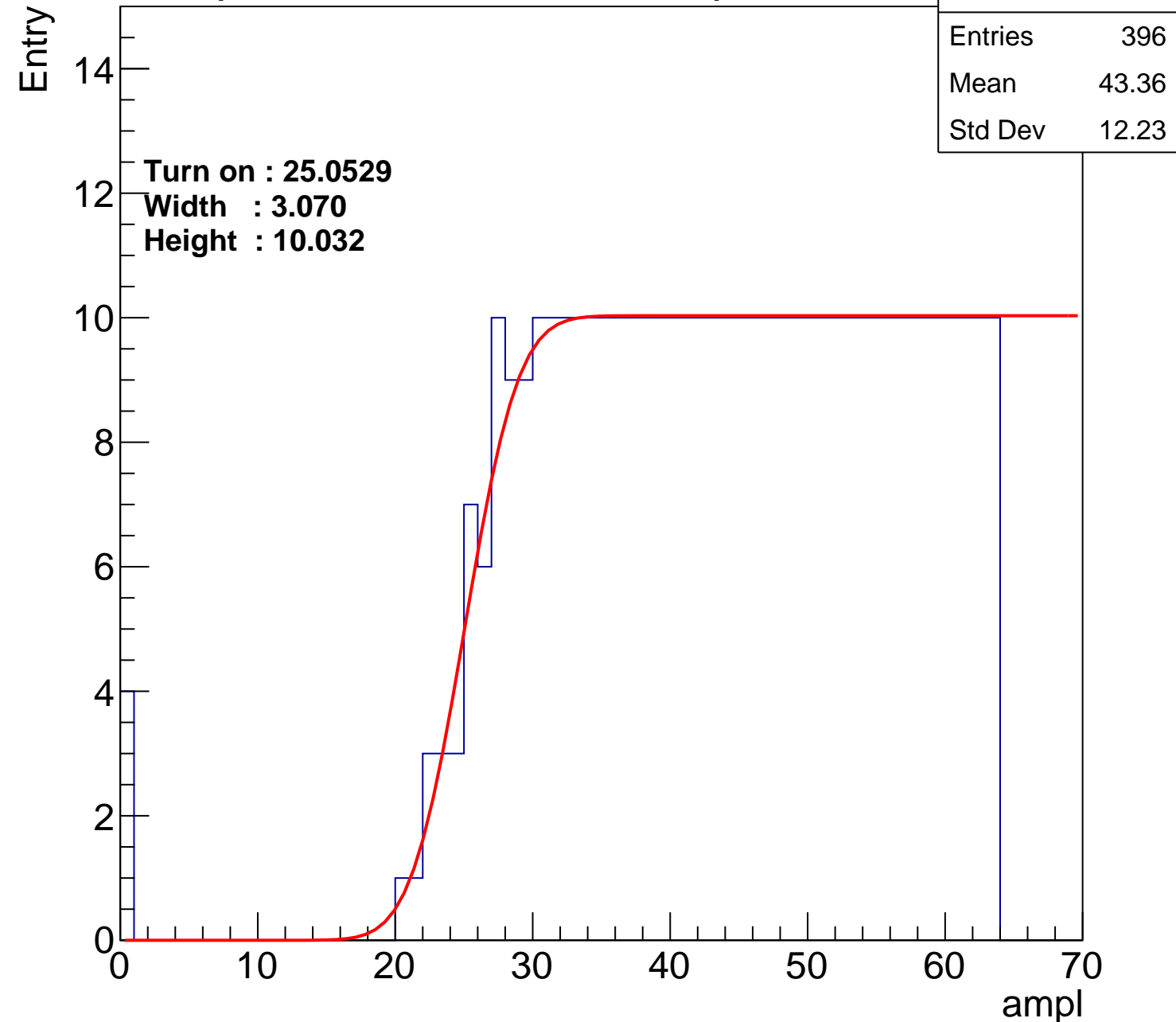
Width : 3.070

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	416
Mean	42.58
Std Dev	12.26

Turn on : 22.6615

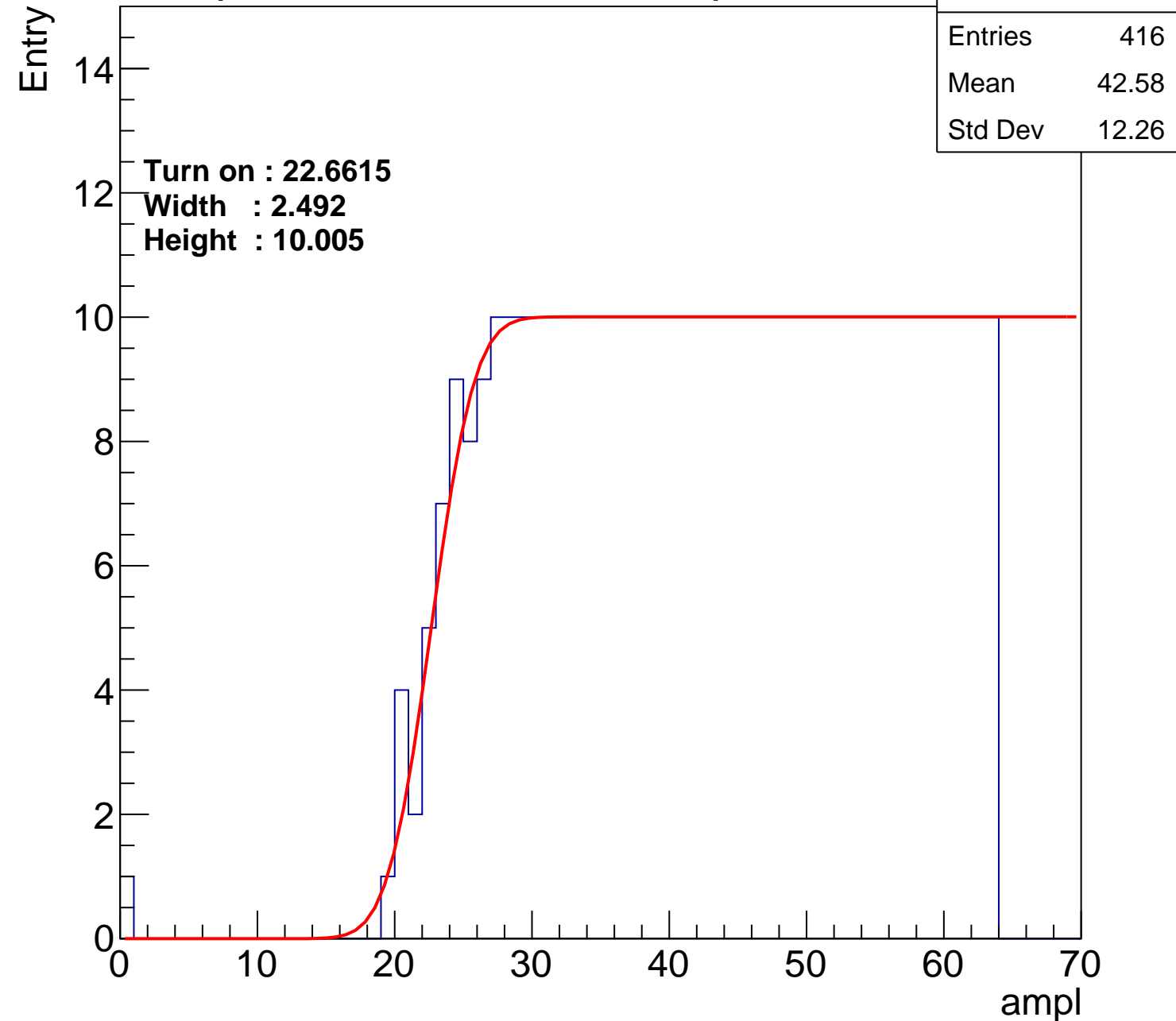
Width : 2.492

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.52
Std Dev	12.14

Turn on : 25.4733

Width : 3.136

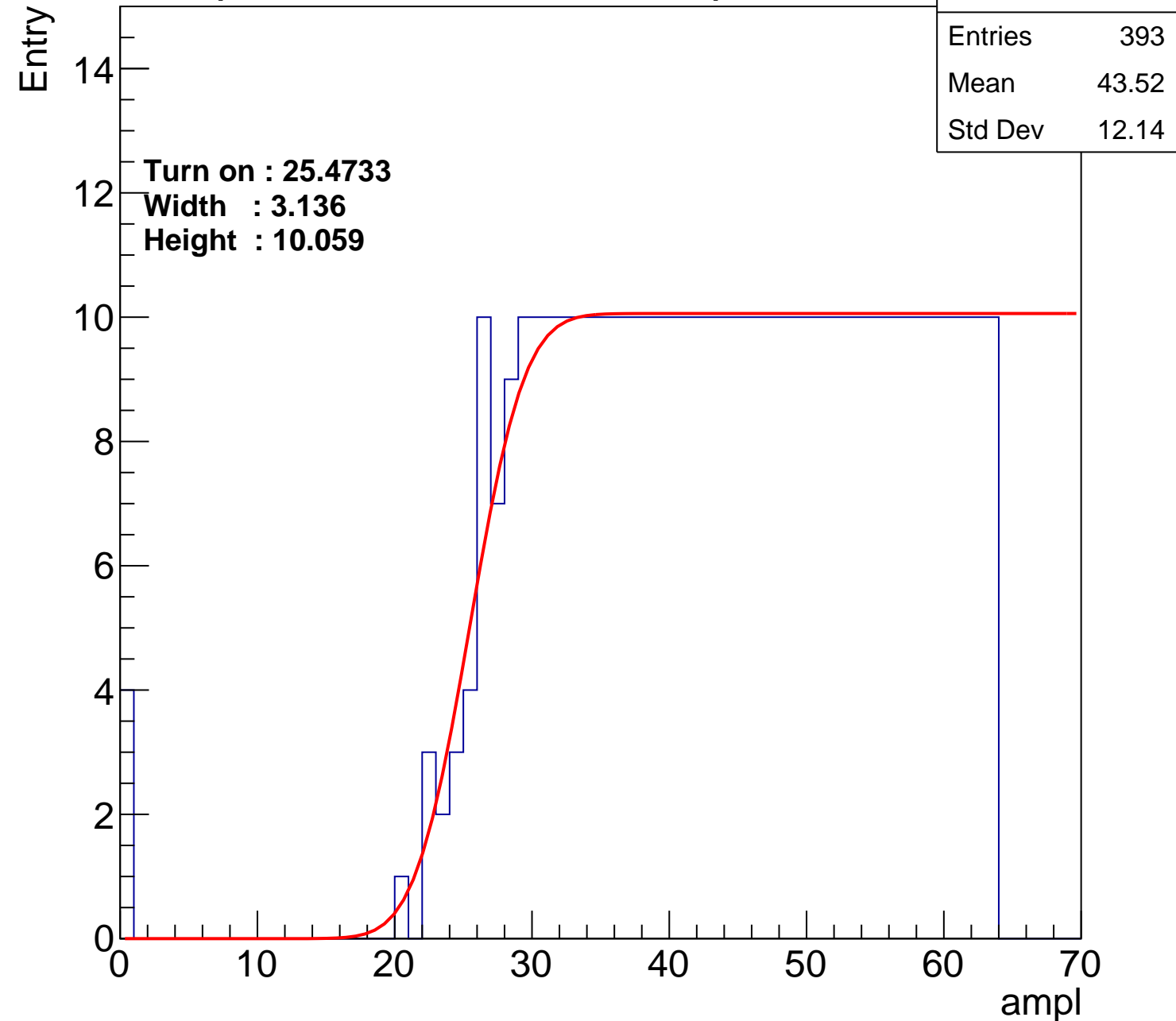
Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U25-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.28

Turn on : 27.1748

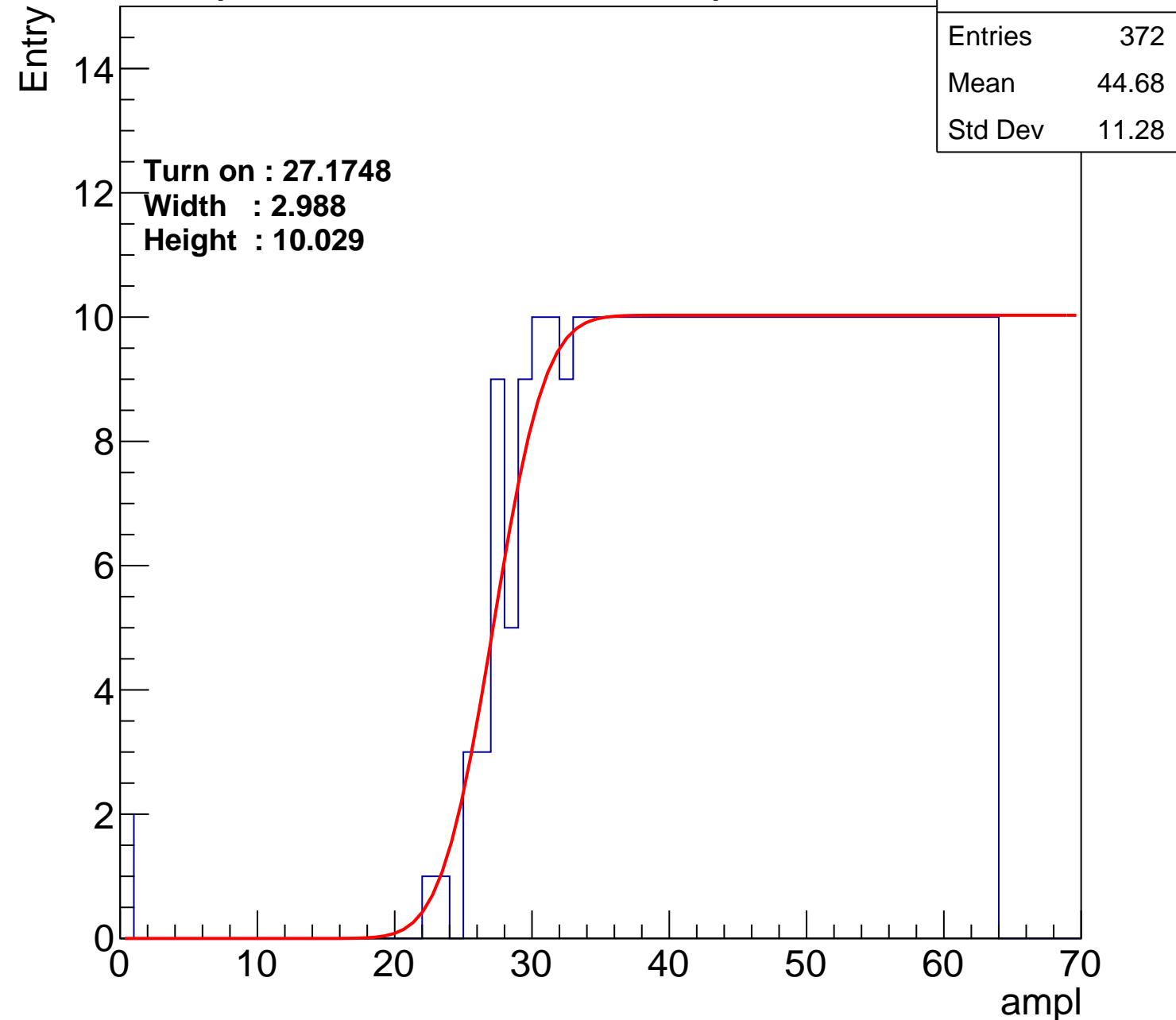
Width : 2.988

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch86

calib_packv5_042523_0143.root, FC#10, port B3

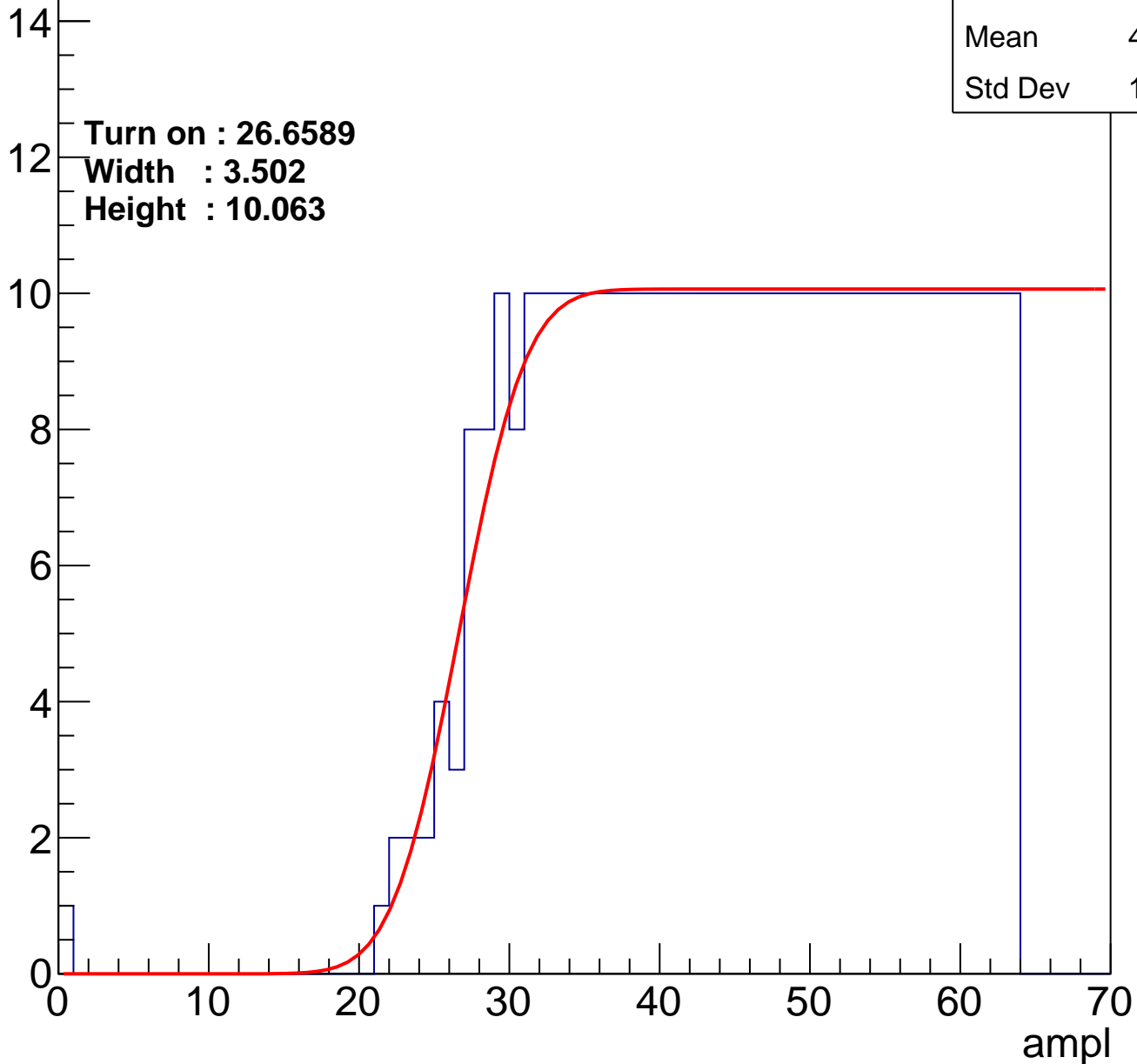
Entries	379
Mean	44.37
Std Dev	11.32

Turn on : 26.6589

Width : 3.502

Height : 10.063

Entry



B1L002S, U25-ch87

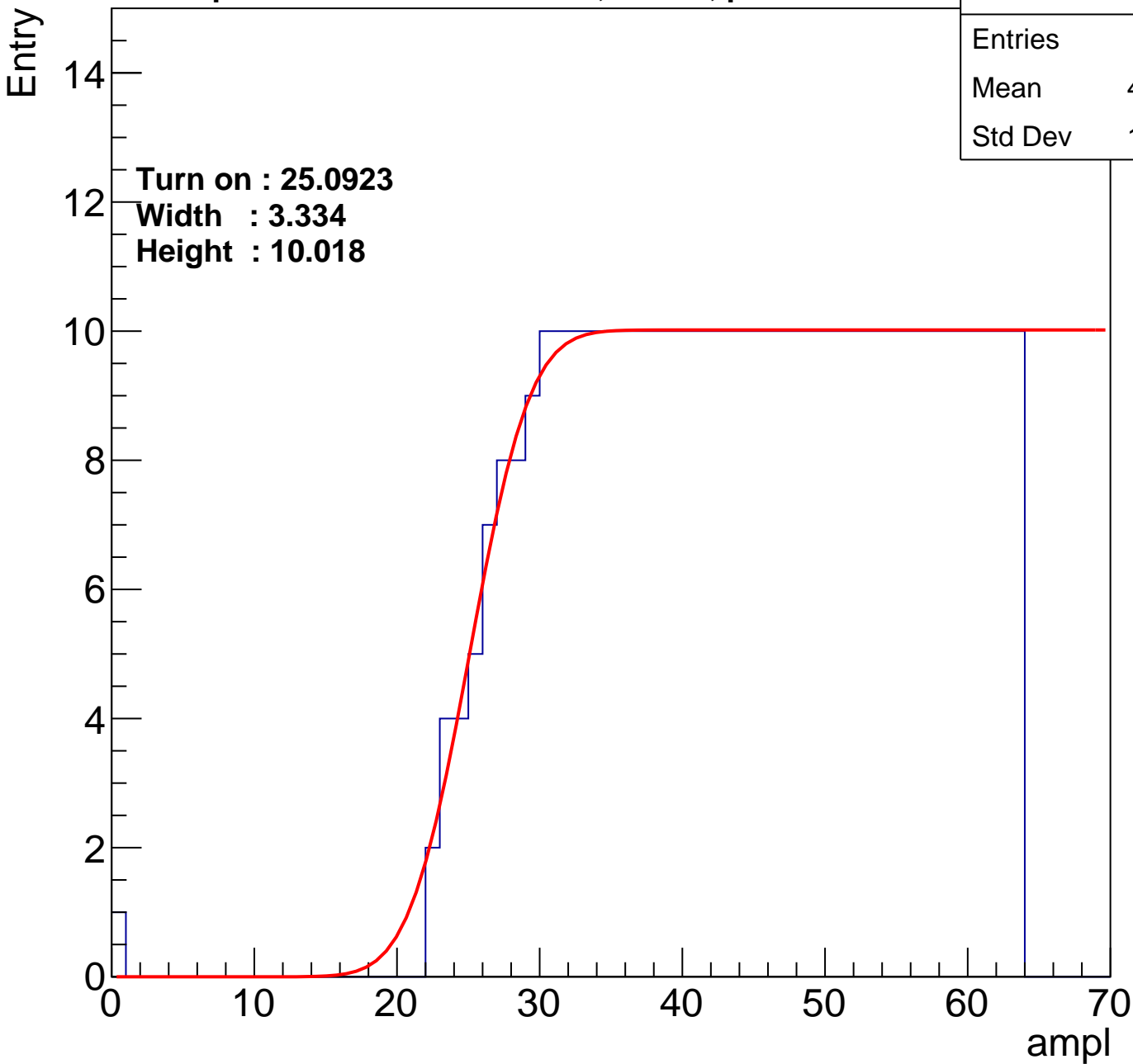
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.0923

Width : 3.334

Height : 10.018

Entries	388
Mean	43.94
Std Dev	11.53



B1L002S, U25-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.17
Std Dev	11.93

Turn on : 23.8685

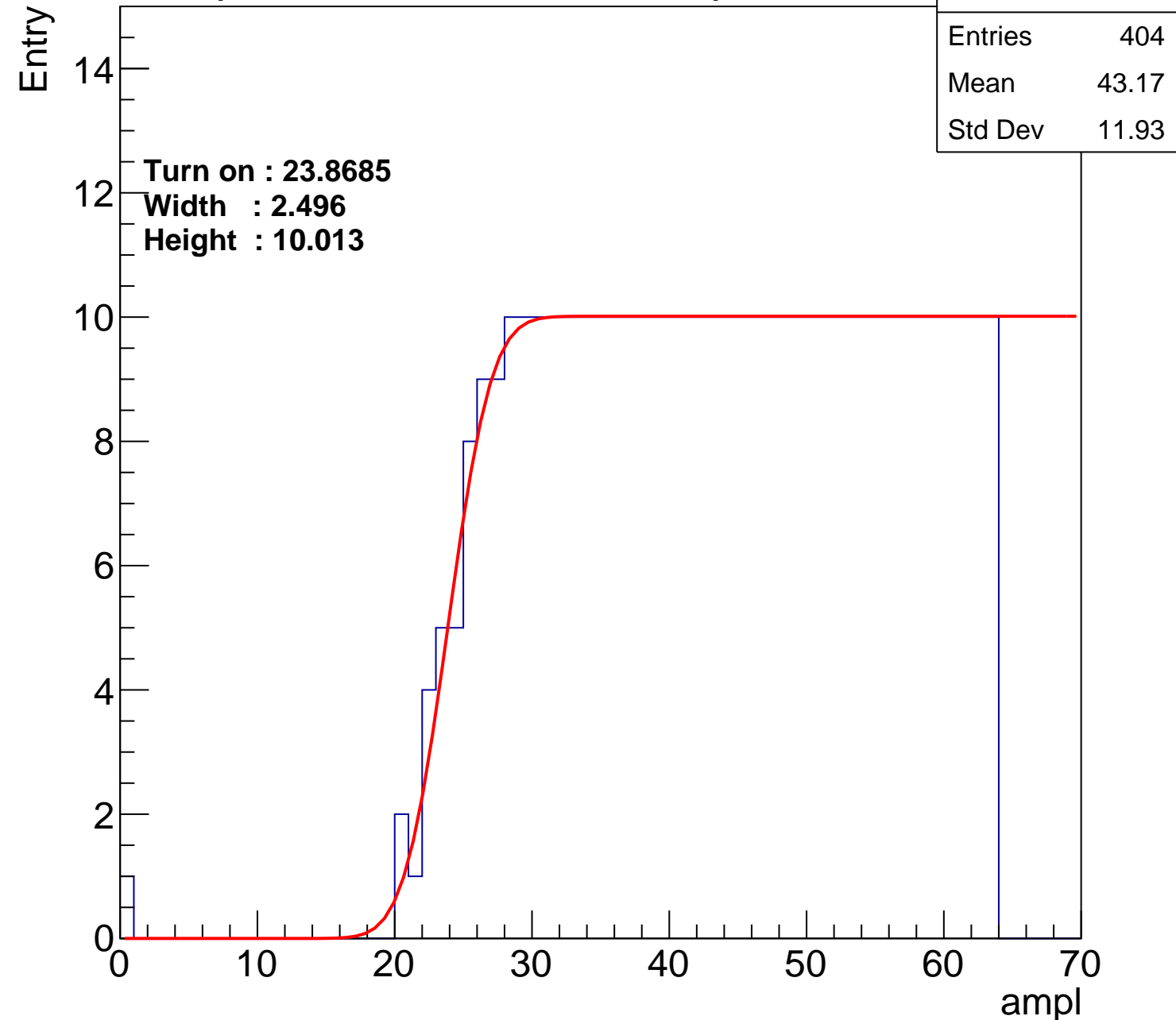
Width : 2.496

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.1982

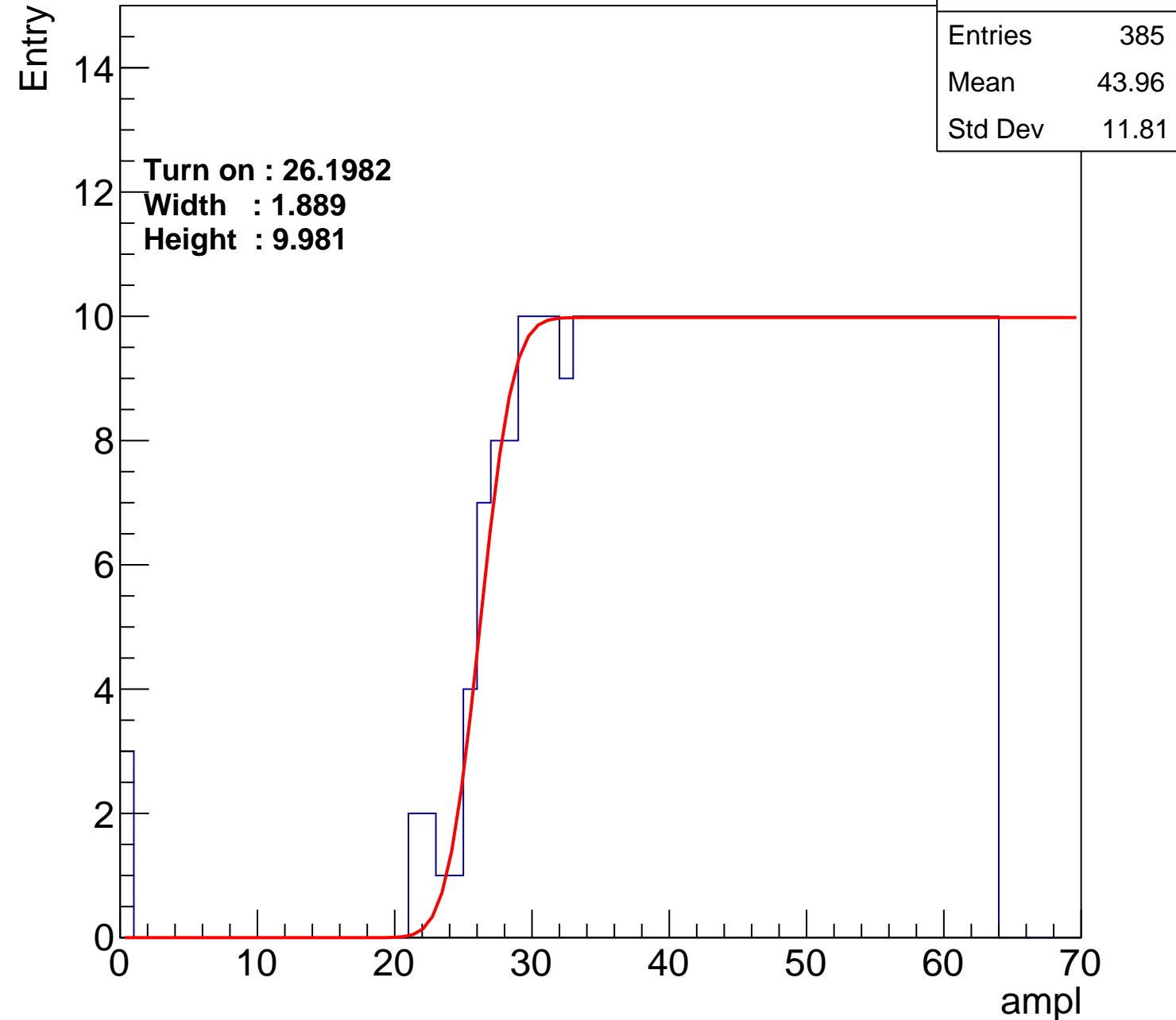
Width : 1.889

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch90

calib_packv5_042523_0143.root, FC#10, port B3

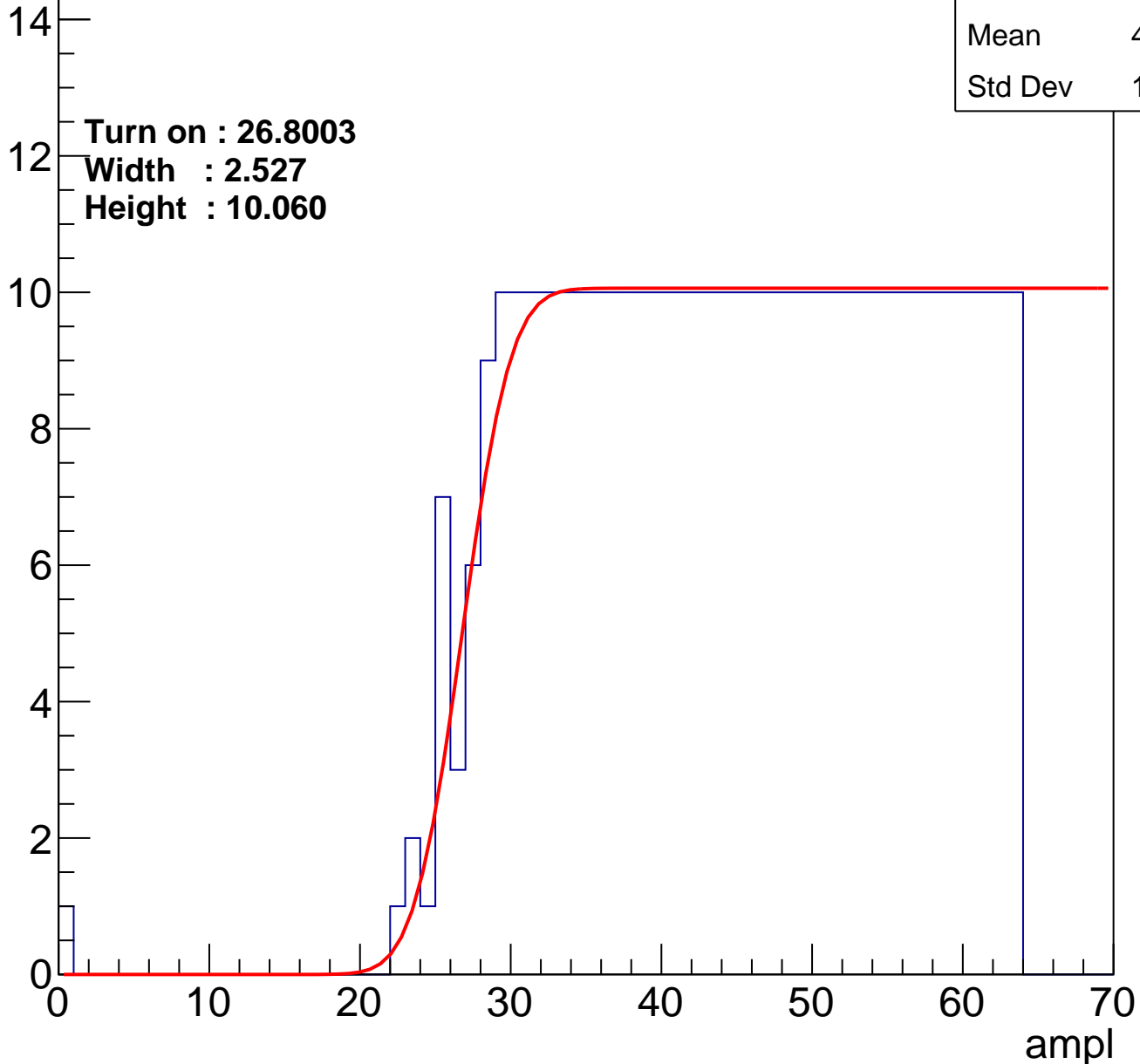
Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.8003

Width : 2.527

Height : 10.060

Entry



B1L002S, U25-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.4869

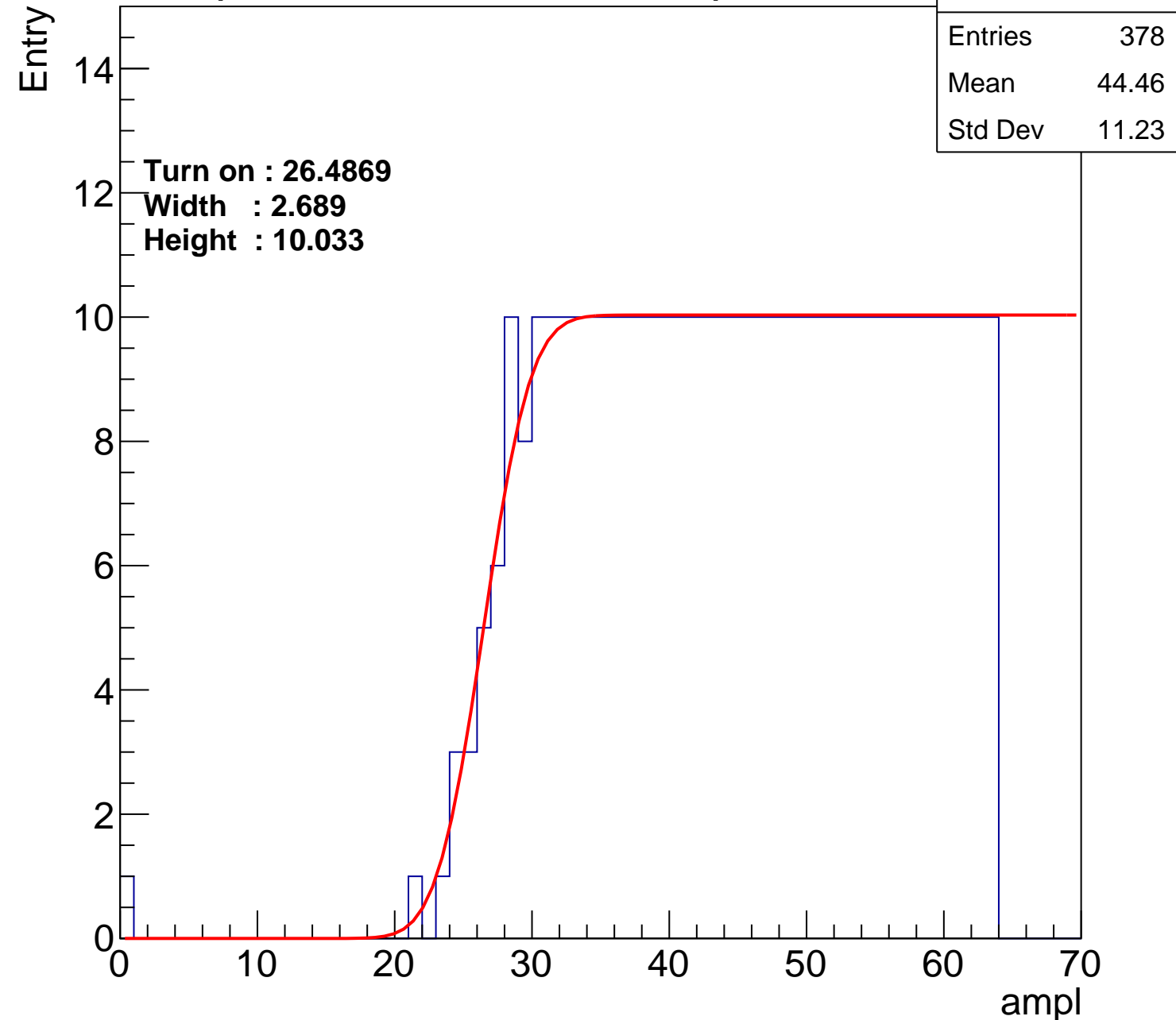
Width : 2.689

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch92

calib_packv5_042523_0143.root, FC#10, port B3

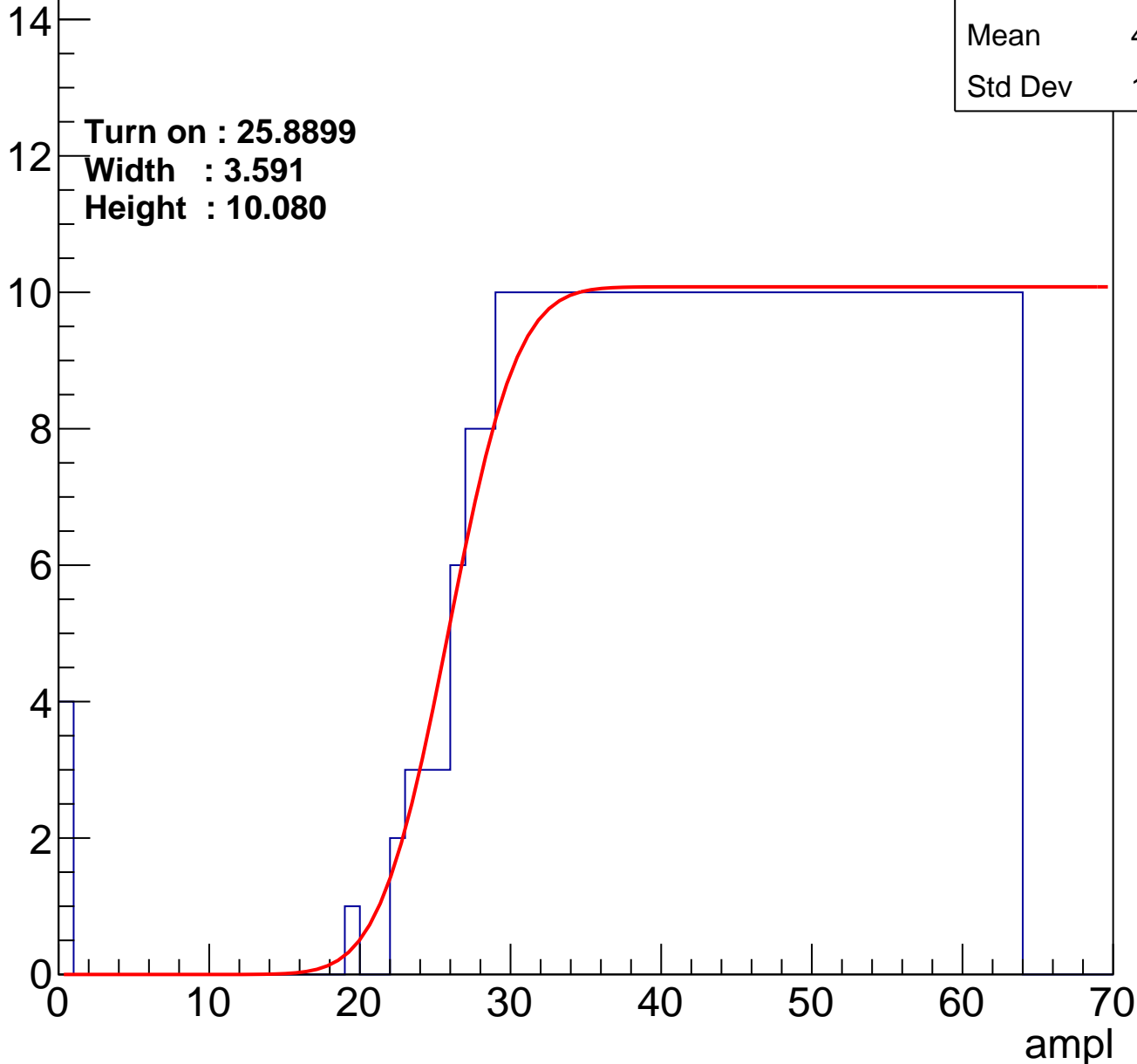
Entries	388
Mean	43.75
Std Dev	12.05

Turn on : 25.8899

Width : 3.591

Height : 10.080

Entry



B1L002S, U25-ch93

calib_packv5_042523_0143.root, FC#10, port B3

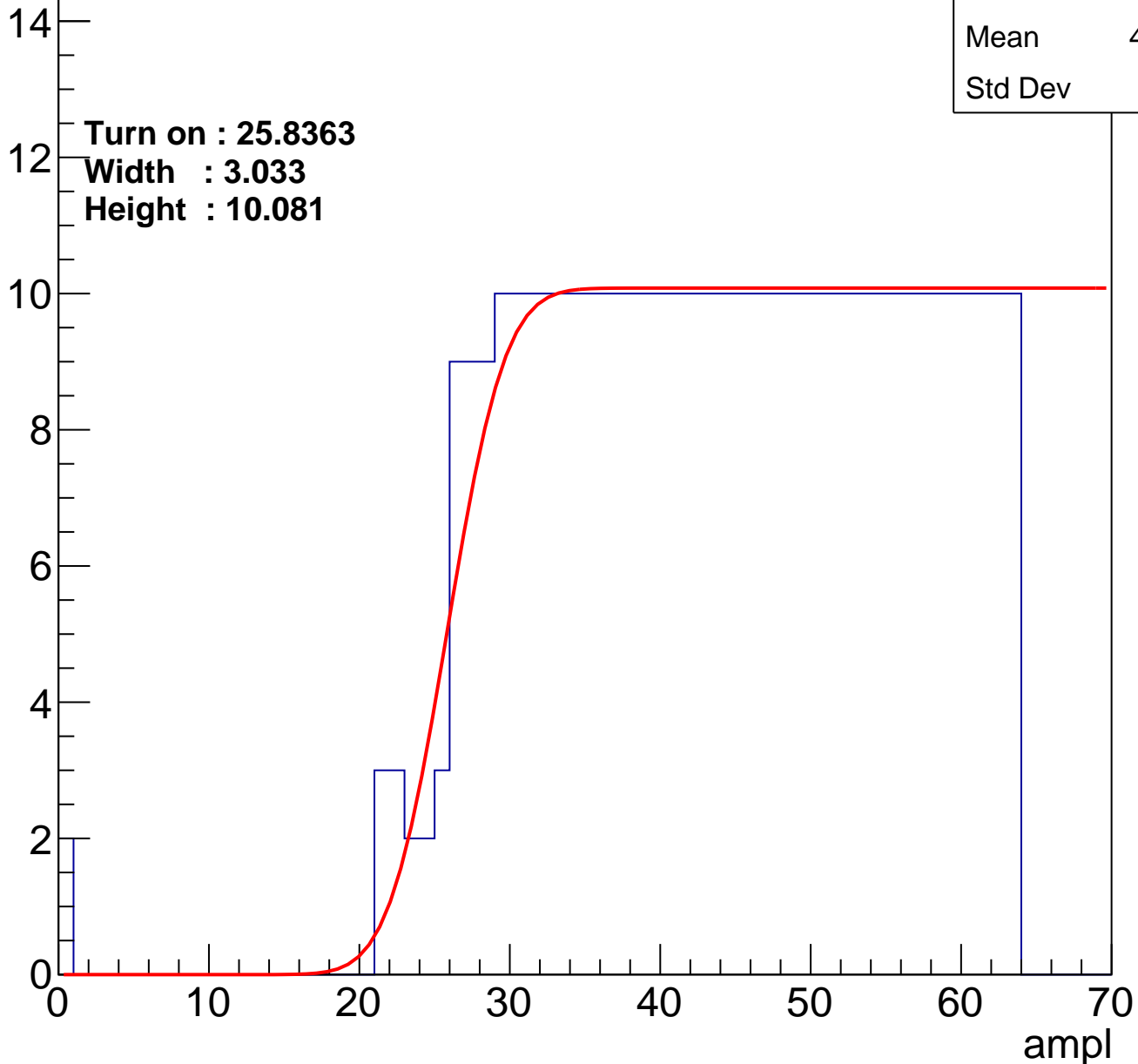
Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.8363

Width : 3.033

Height : 10.081

Entry



B1L002S, U25-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.86
Std Dev	11.56

Turn on : 25.4572

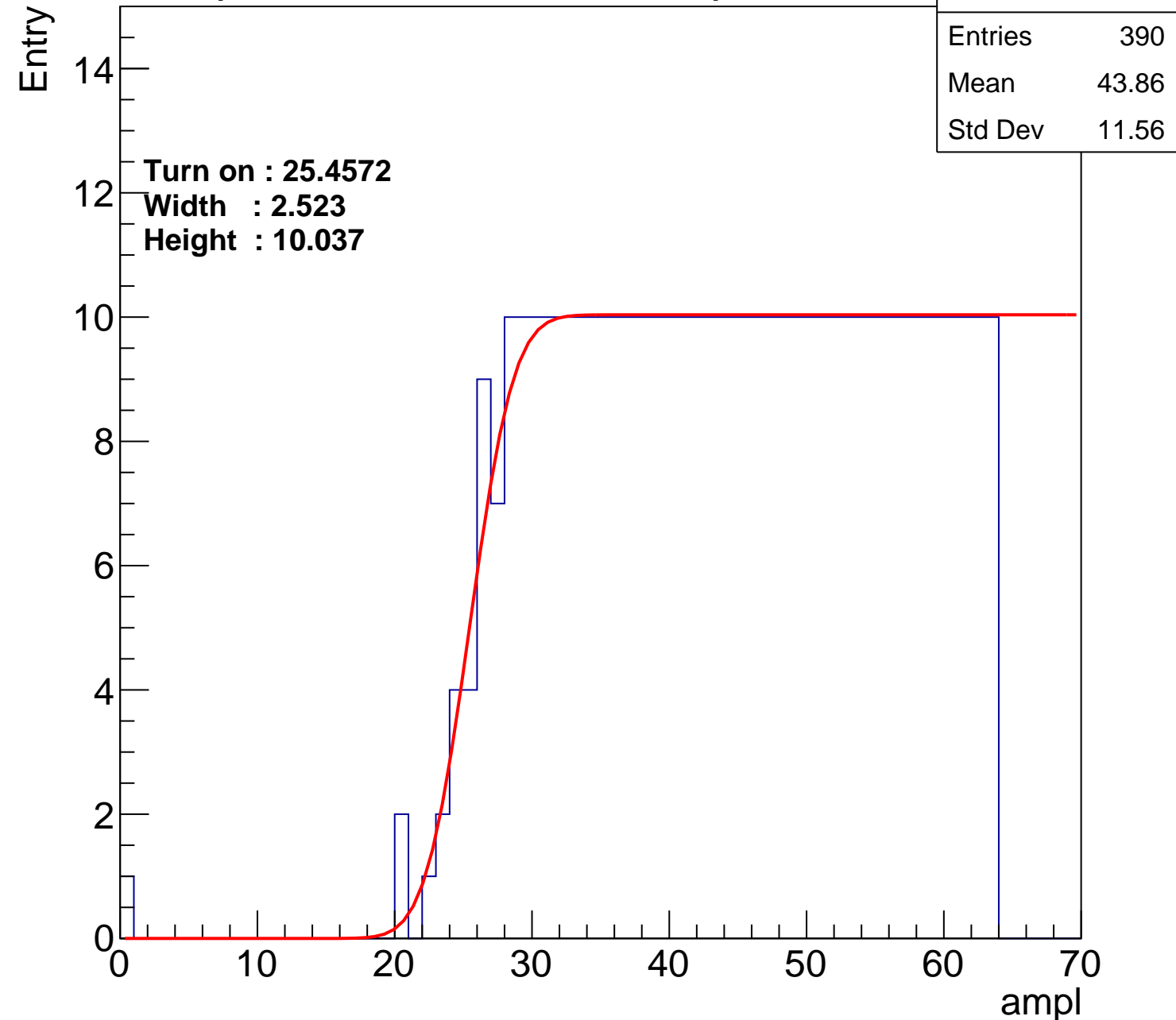
Width : 2.523

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.79
Std Dev	11.39

Turn on : 27.8685

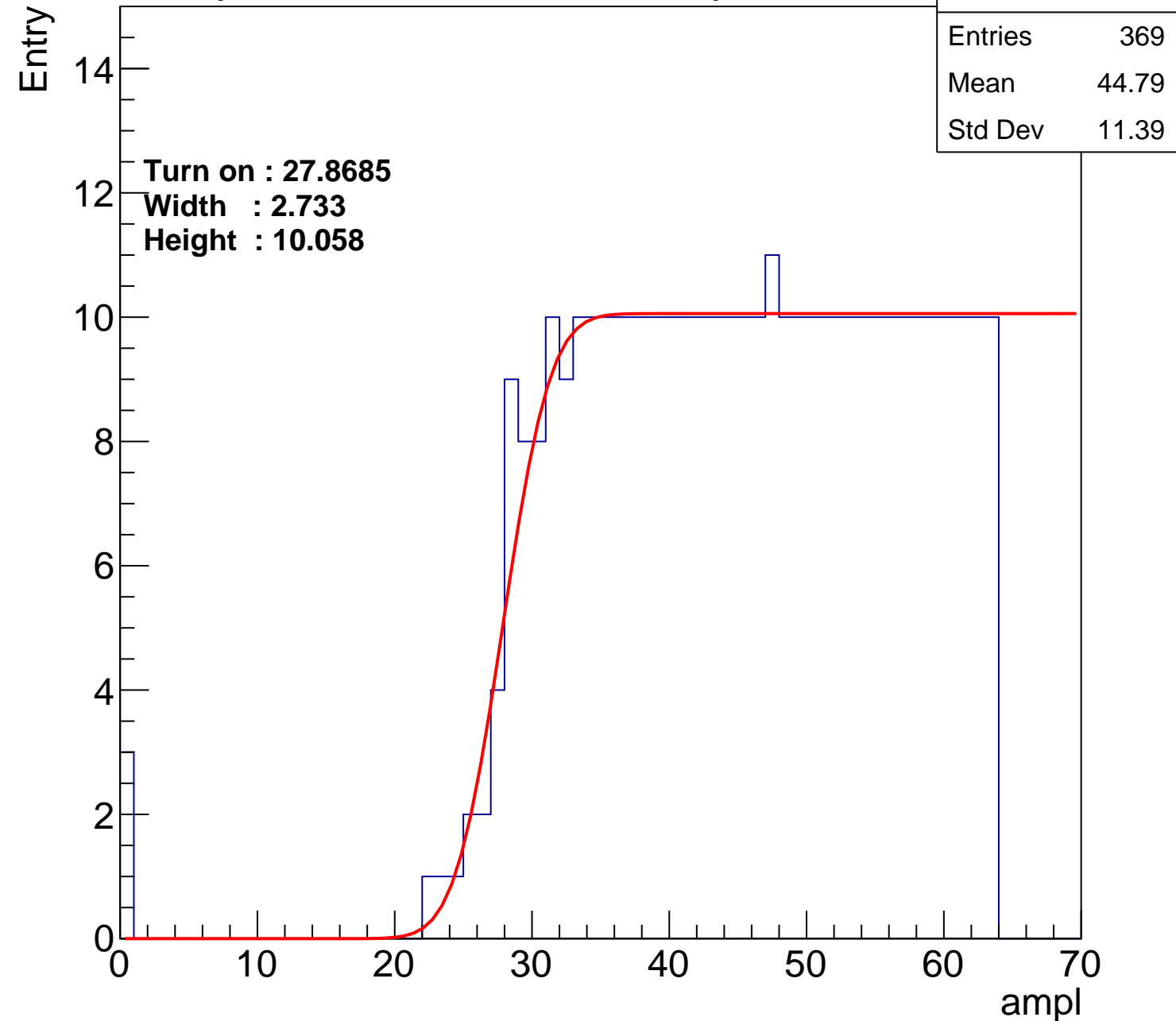
Width : 2.733

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.3705

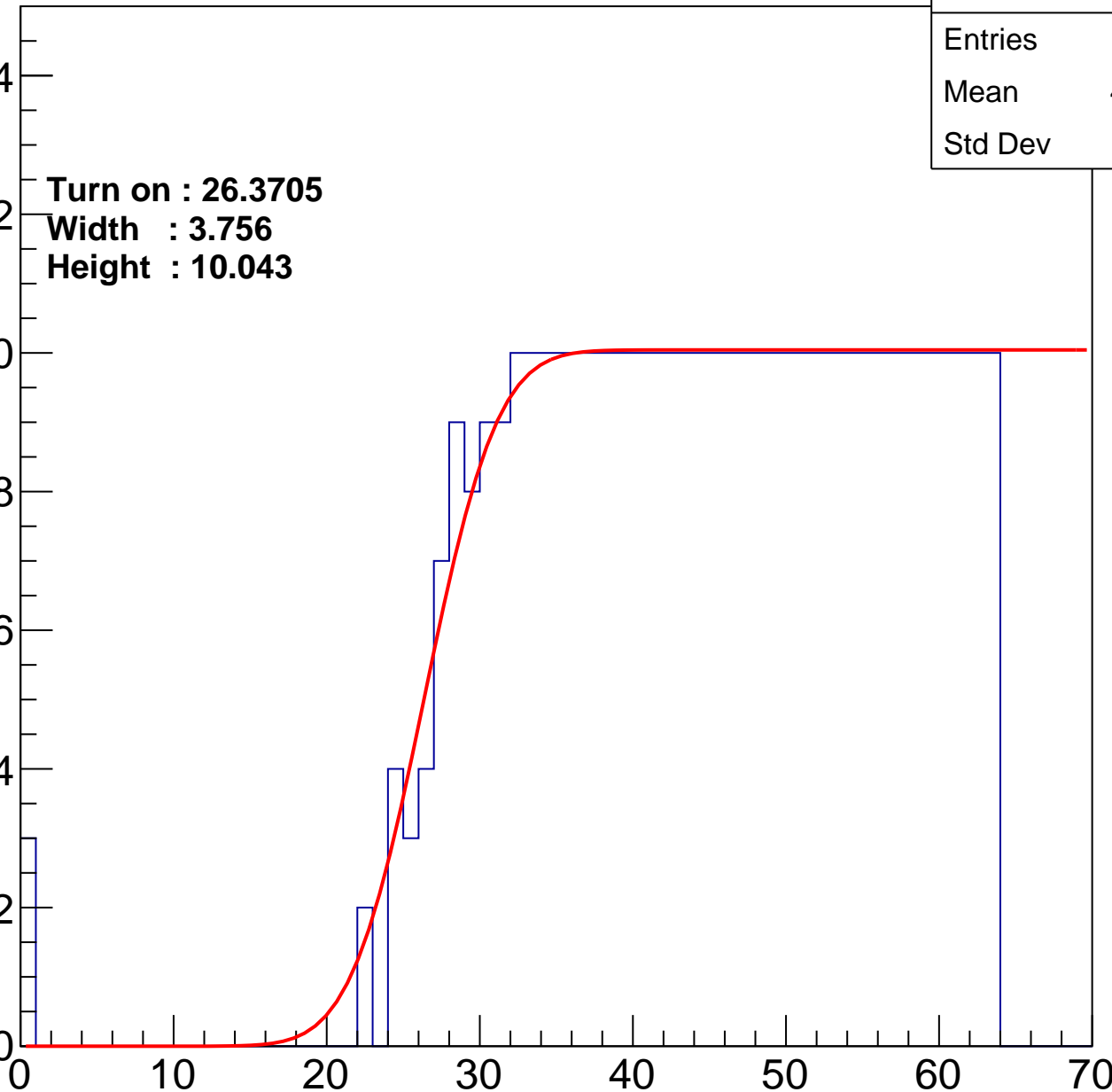
Width : 3.756

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	352
Mean	45.71
Std Dev	10.72

Turn on : 29.3516

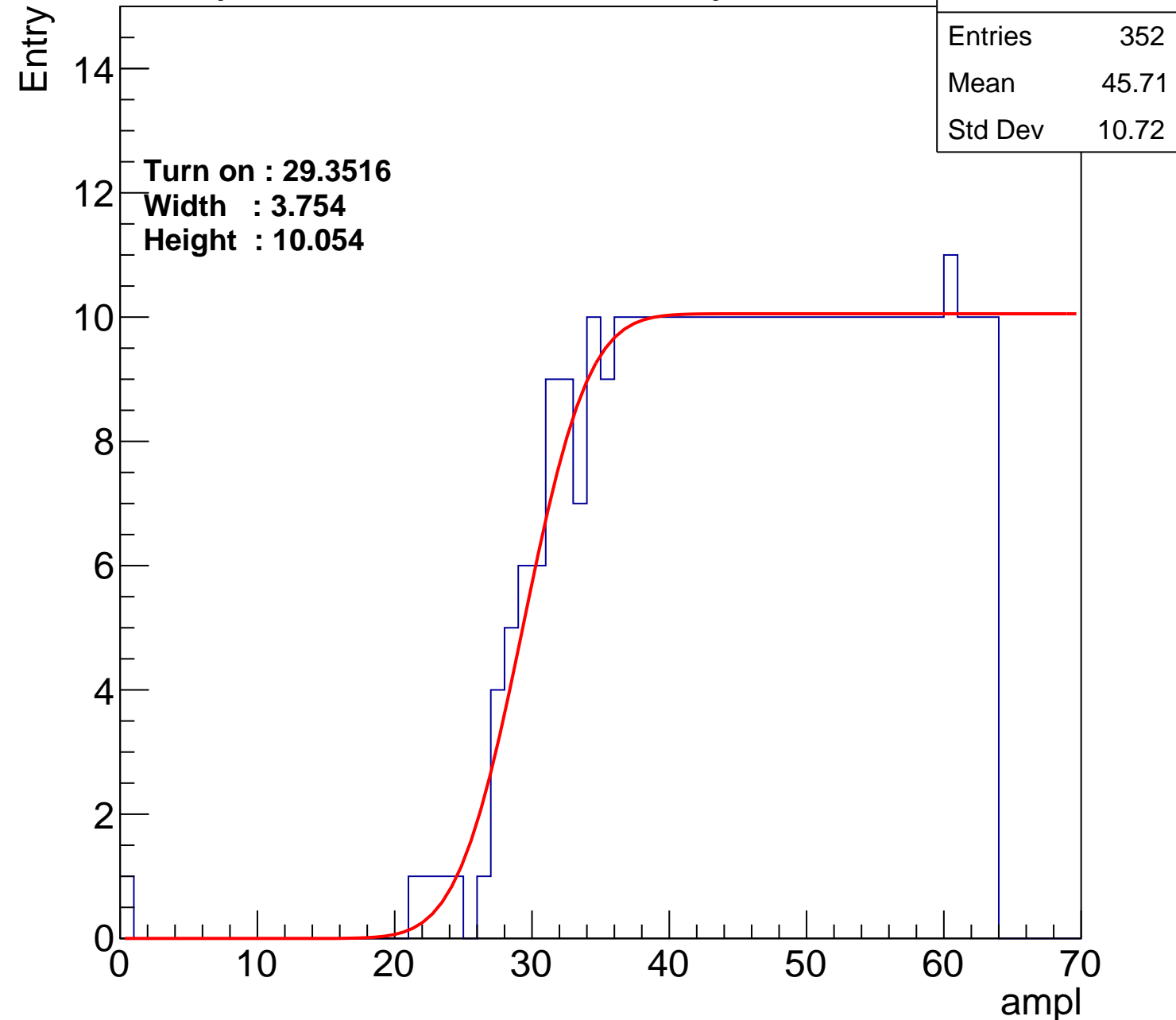
Width : 3.754

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.93
Std Dev	11.95

Turn on : 25.5299

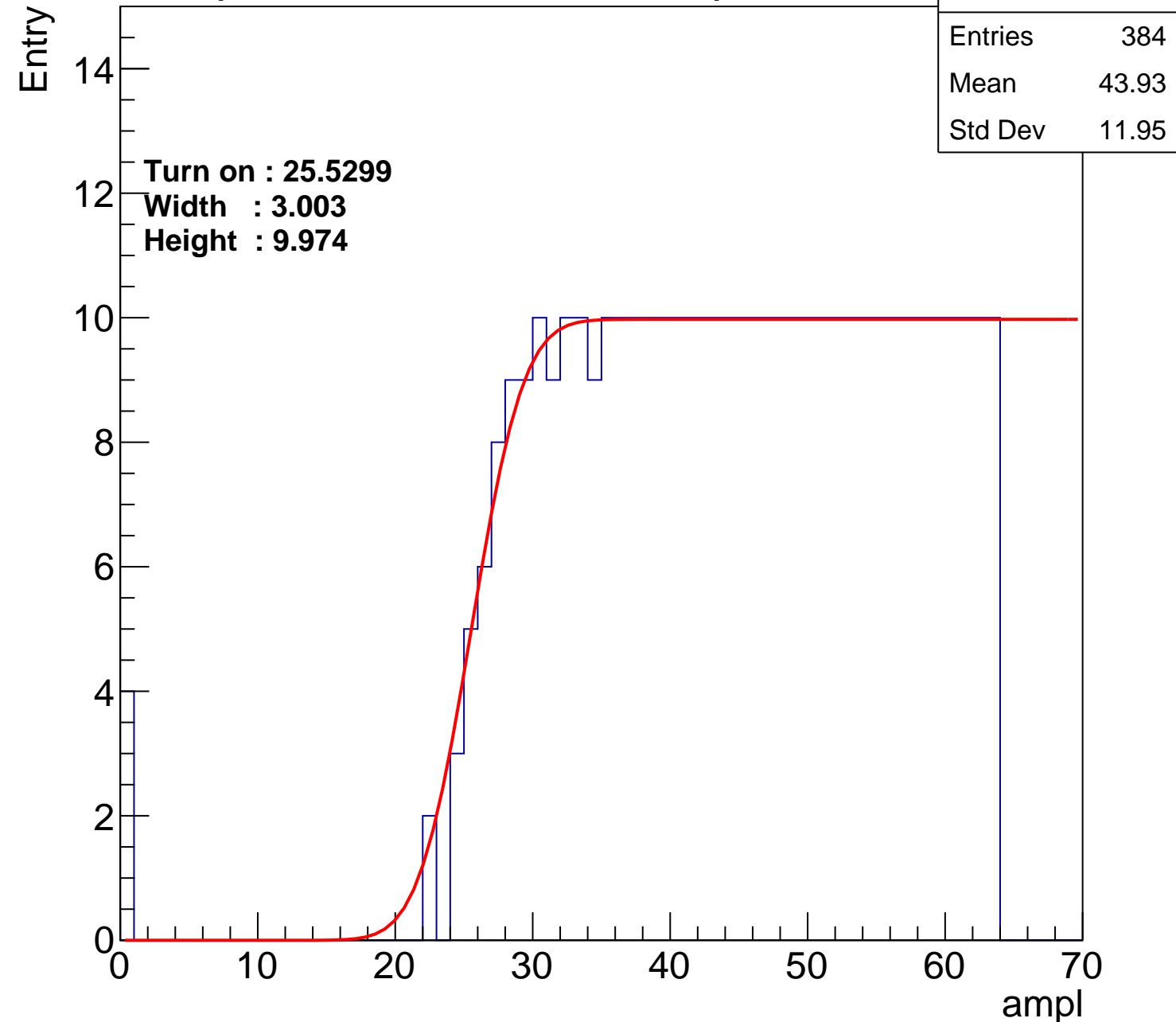
Width : 3.003

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch99

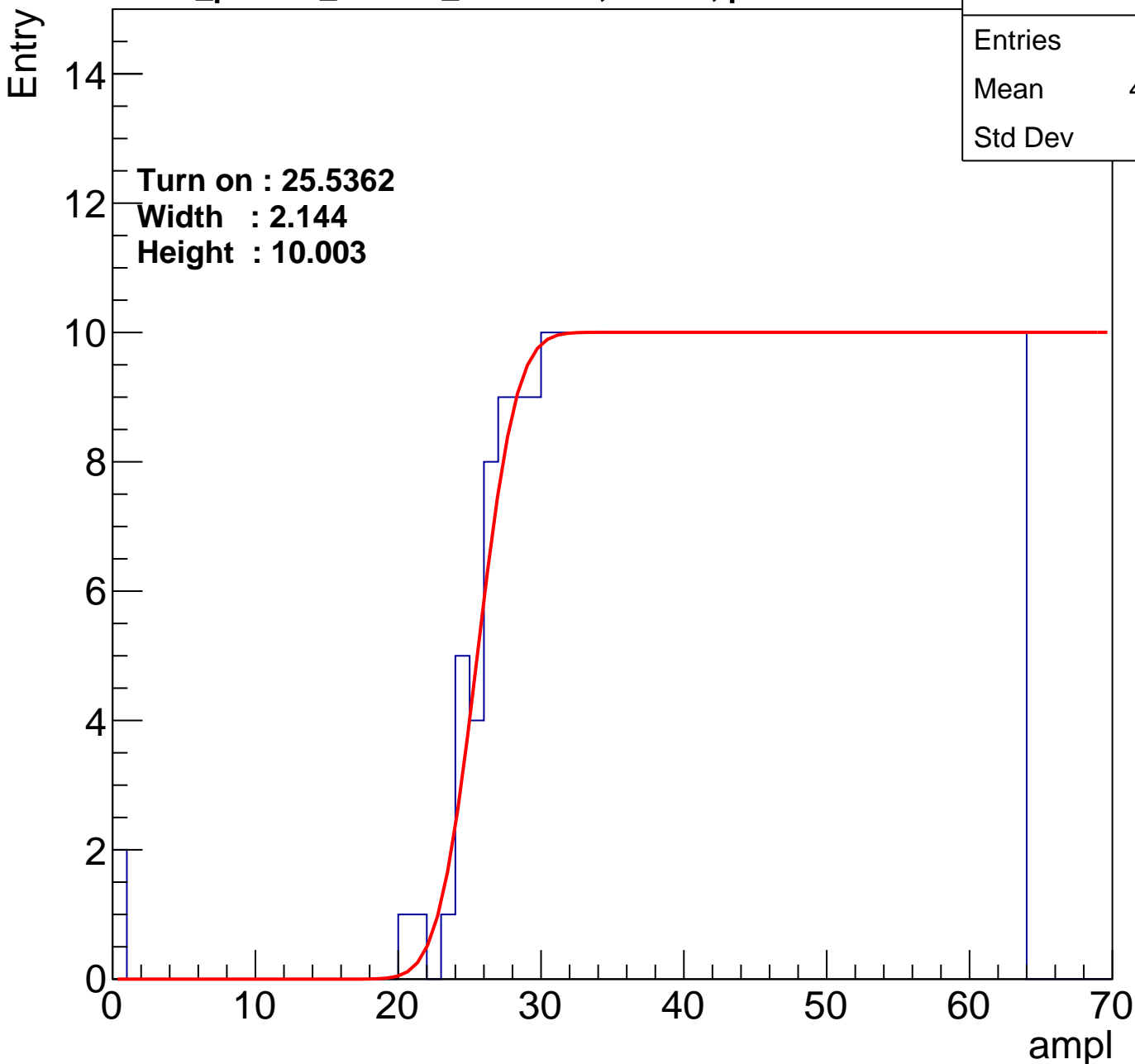
calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.5362

Width : 2.144

Height : 10.003



B1L002S, U25-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	364
Mean	45.13
Std Dev	10.89

Turn on : 27.8131

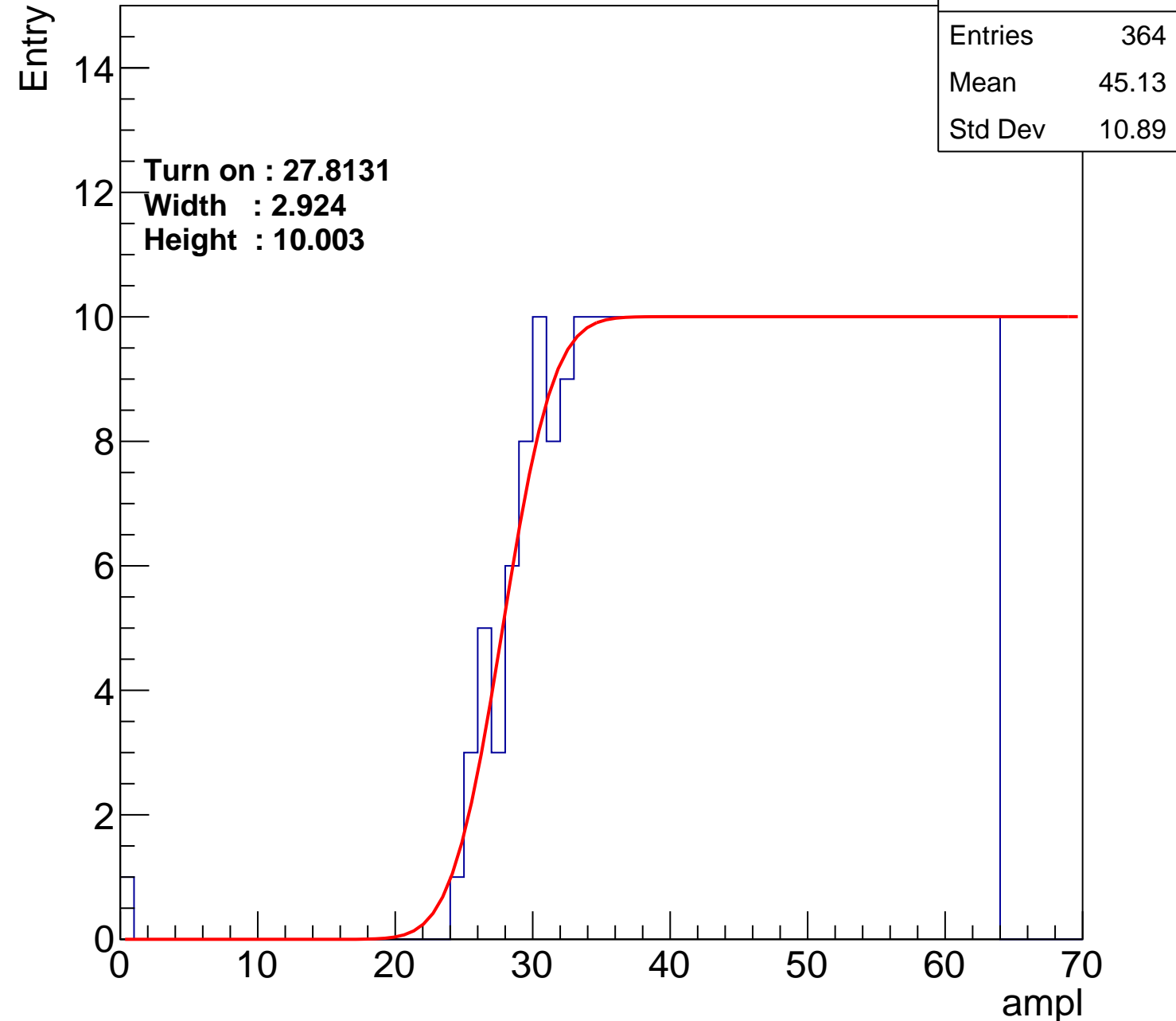
Width : 2.924

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch101

calib_packv5_042523_0143.root, FC#10, port B3

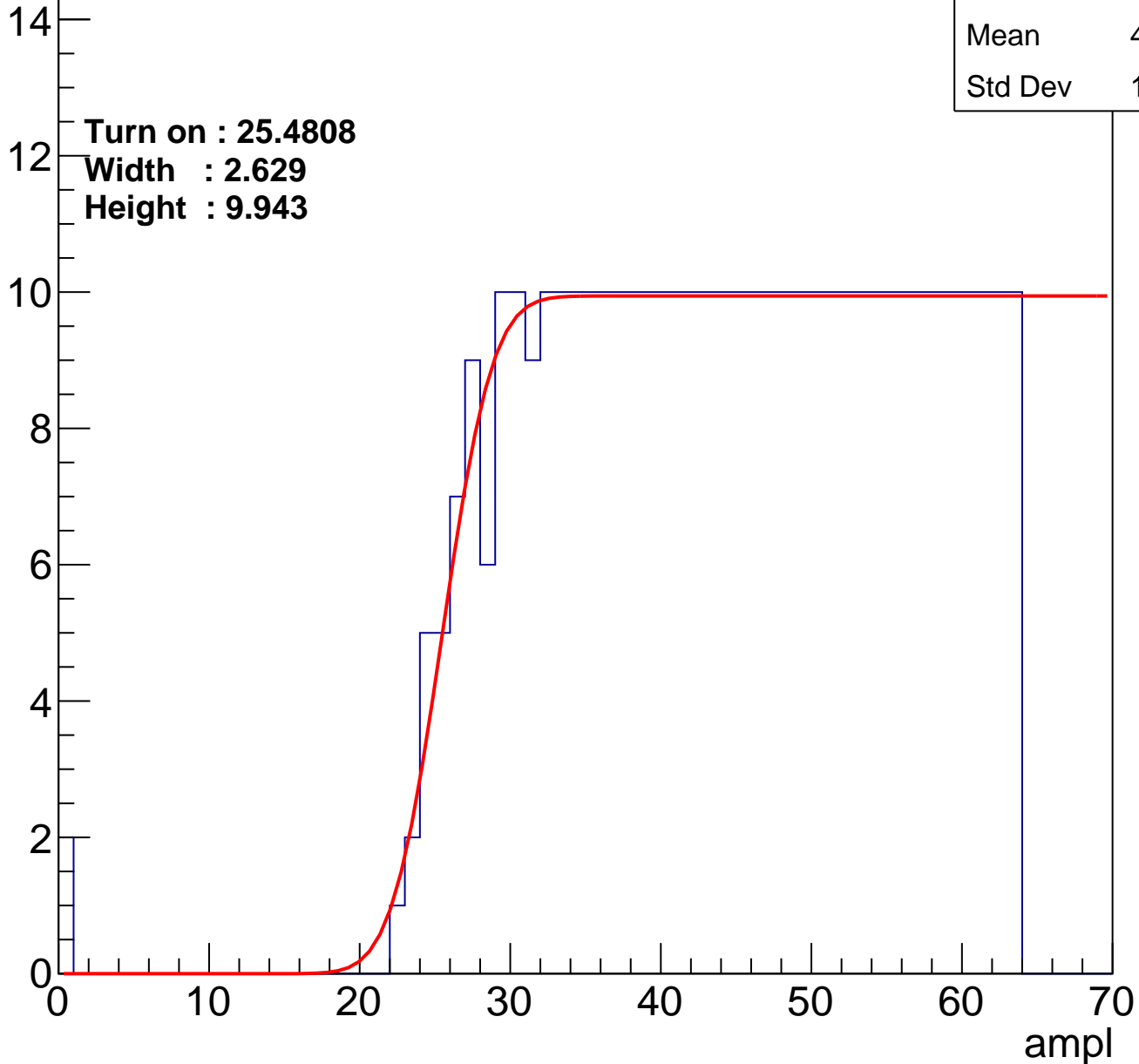
Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.4808

Width : 2.629

Height : 9.943

Entry



B1L002S, U25-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.81
Std Dev	11.59

Turn on : 25.0890

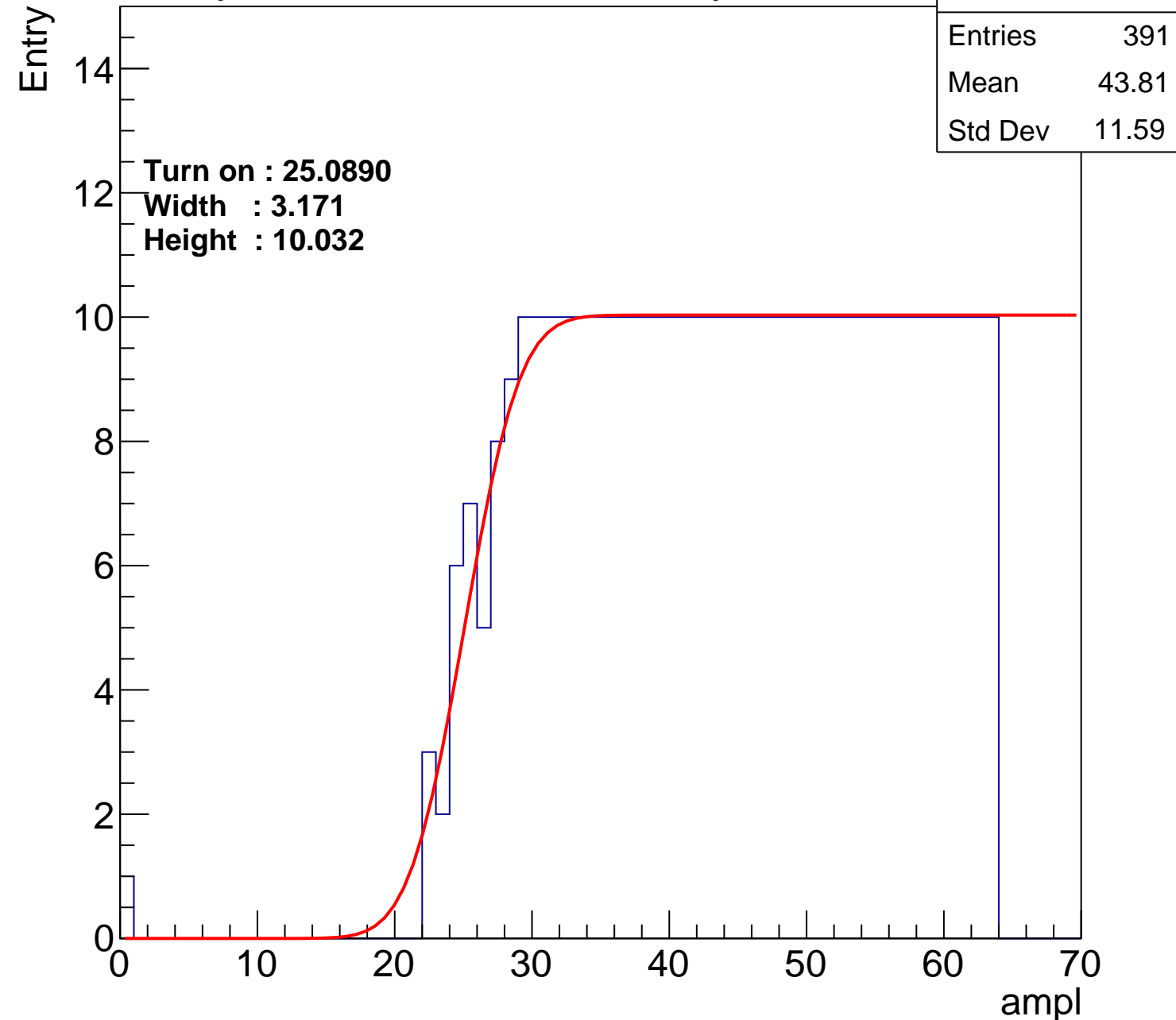
Width : 3.171

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch103

calib_packv5_042523_0143.root, FC#10, port B3

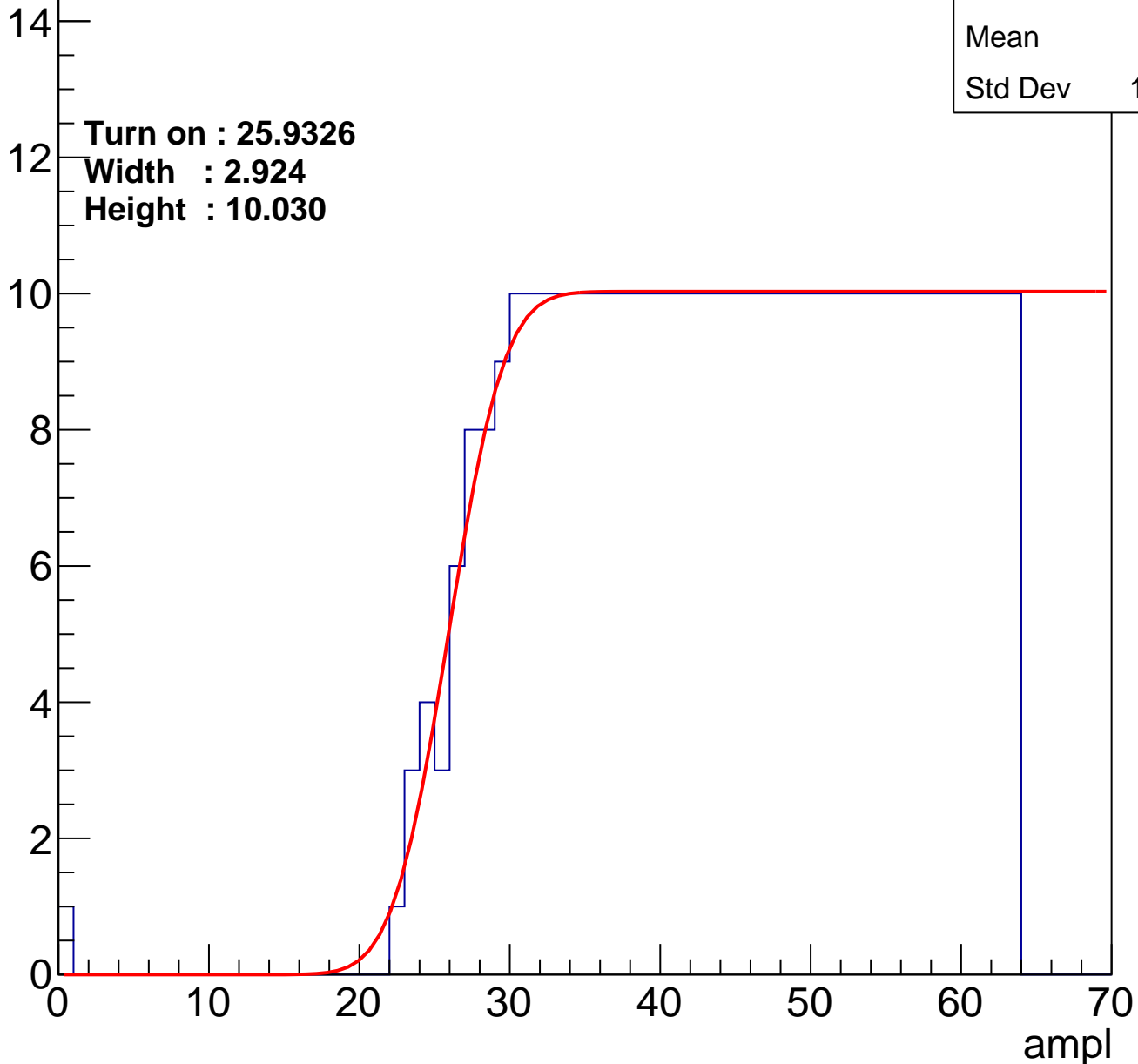
Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.9326

Width : 2.924

Height : 10.030

Entry



B1L002S, U25-ch104

calib_packv5_042523_0143.root, FC#10, port B3

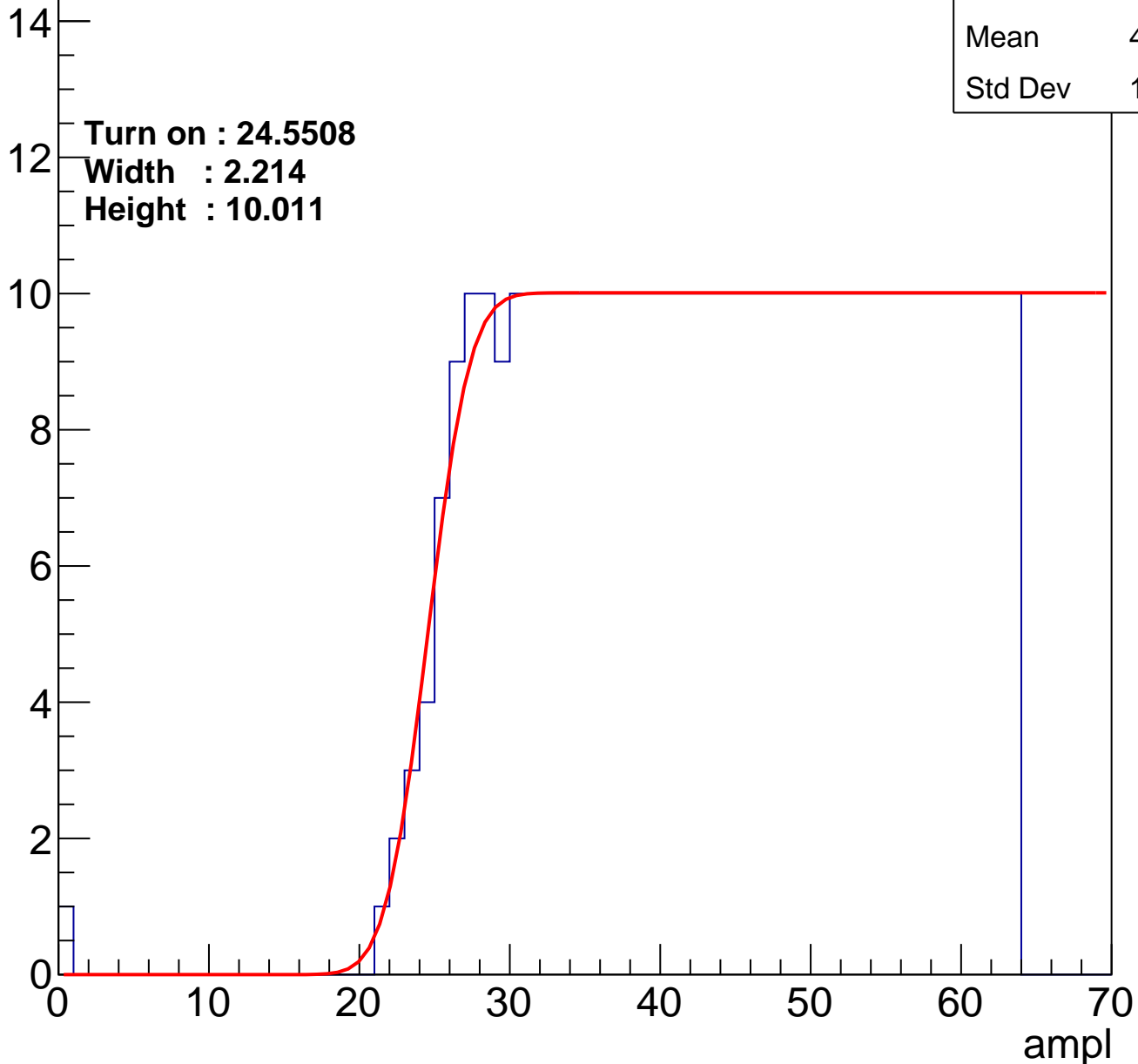
Entries	396
Mean	43.59
Std Dev	11.68

Turn on : 24.5508

Width : 2.214

Height : 10.011

Entry



B1L002S, U25-ch105

calib_packv5_042523_0143.root, FC#10, port B3

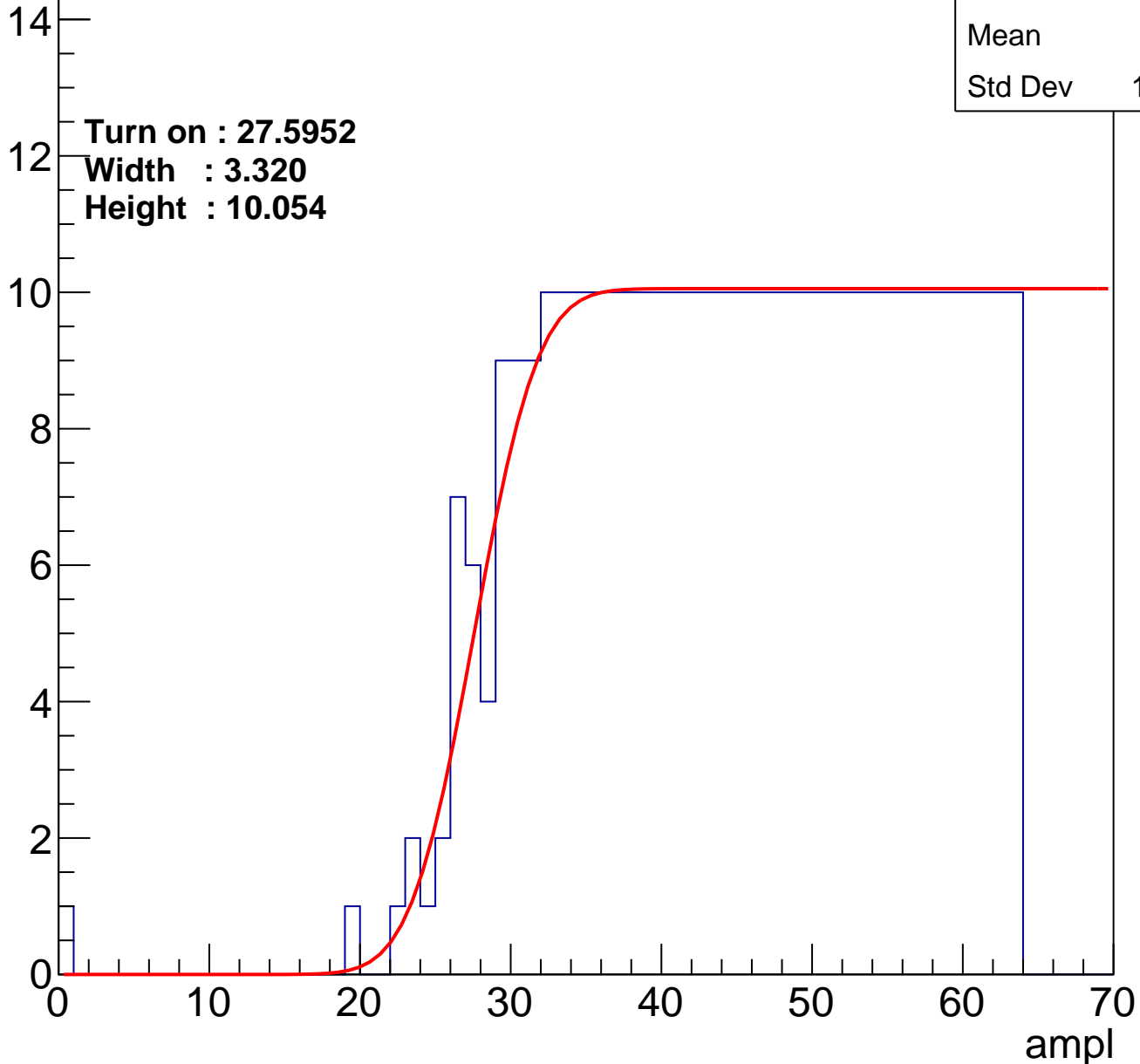
Entries	372
Mean	44.7
Std Dev	11.17

Turn on : 27.5952

Width : 3.320

Height : 10.054

Entry



B1L002S, U25-ch106

calib_packv5_042523_0143.root, FC#10, port B3

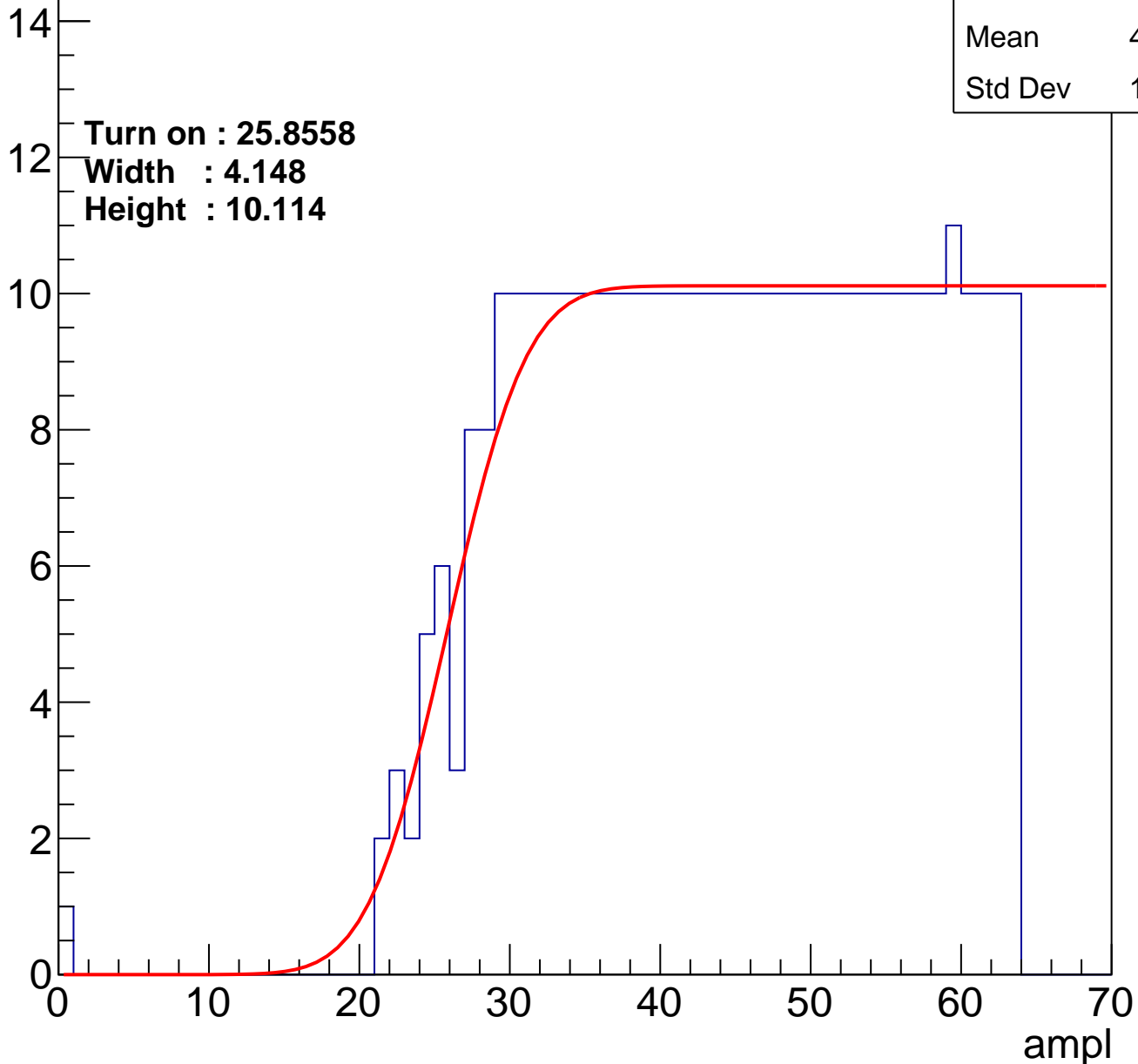
Entries	389
Mean	43.96
Std Dev	11.58

Turn on : 25.8558

Width : 4.148

Height : 10.114

Entry



B1L002S, U25-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.57
Std Dev	11.88

Turn on : 24.5205

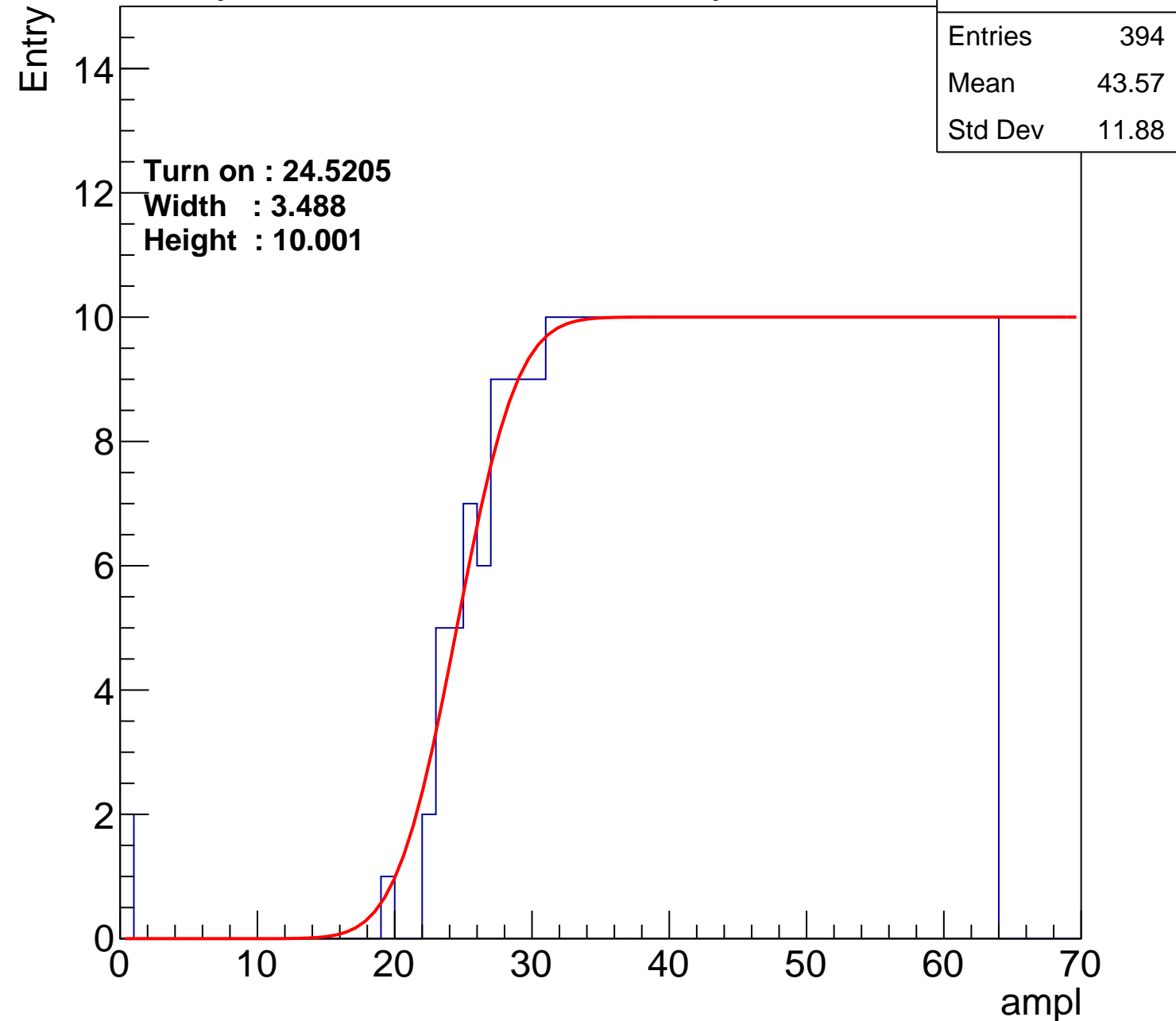
Width : 3.488

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch108

calib_packv5_042523_0143.root, FC#10, port B3

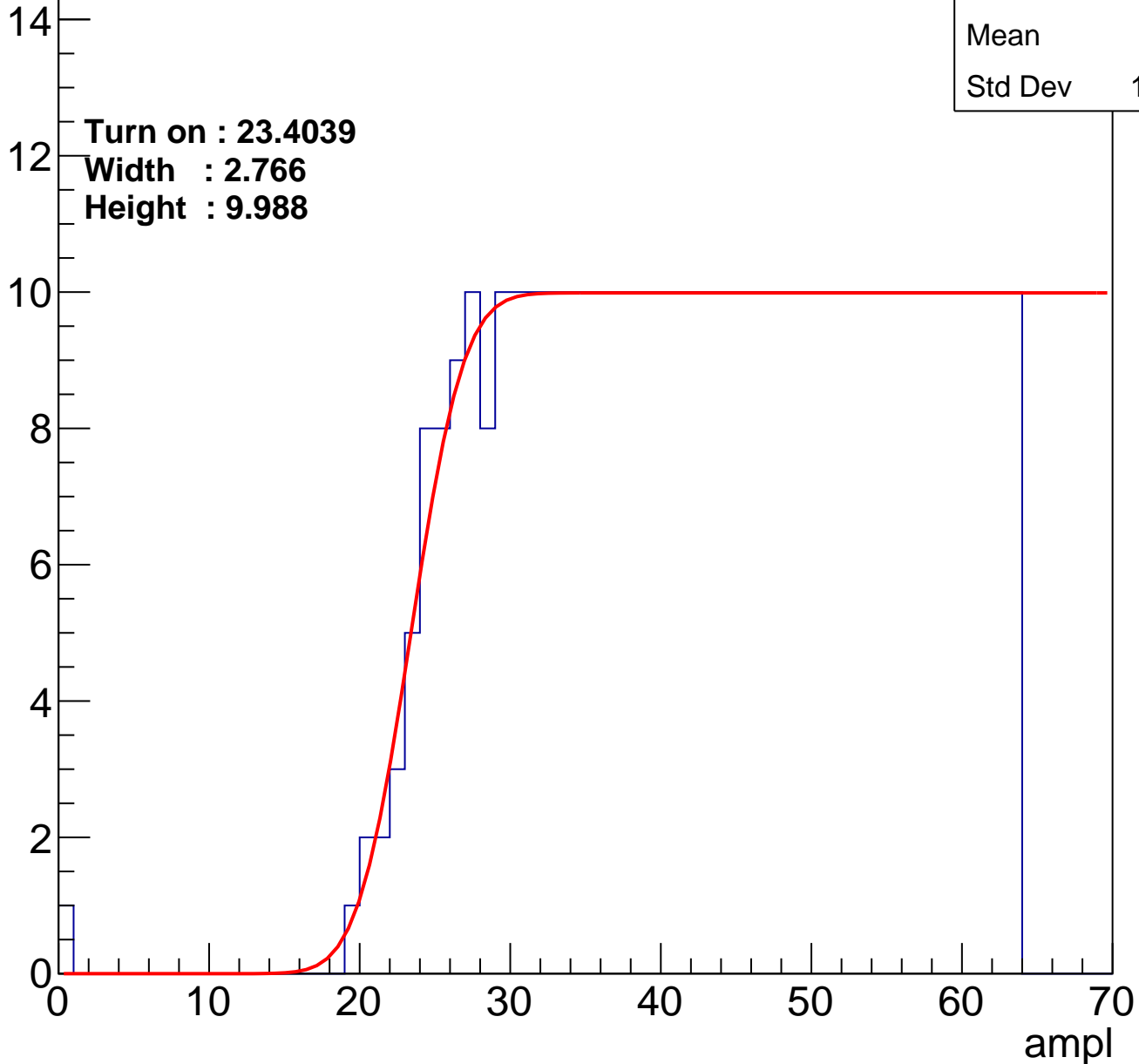
Entries	407
Mean	43
Std Dev	12.05

Turn on : 23.4039

Width : 2.766

Height : 9.988

Entry



B1L002S, U25-ch109

calib_packv5_042523_0143.root, FC#10, port B3

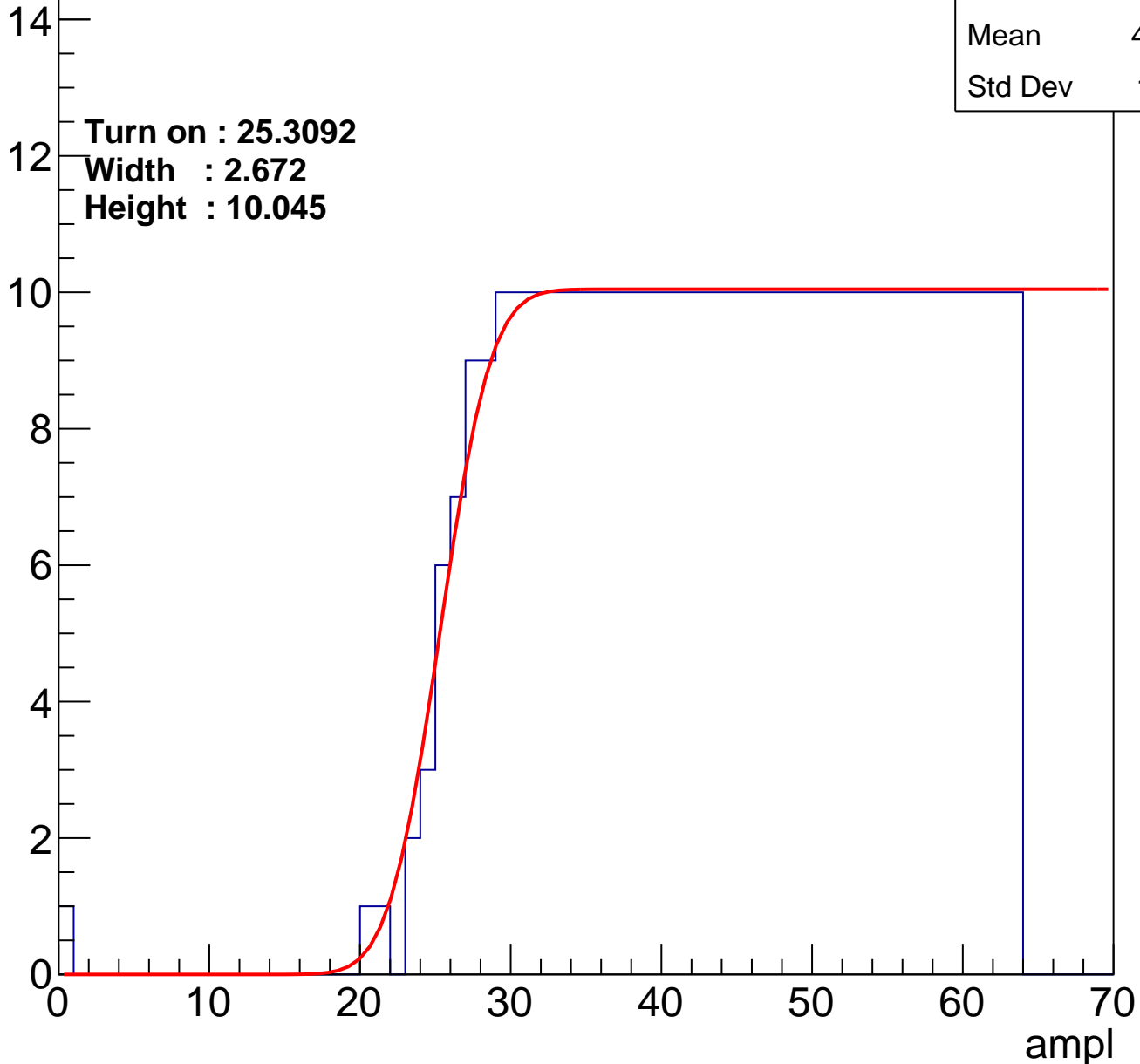
Entries	389
Mean	43.92
Std Dev	11.51

Turn on : 25.3092

Width : 2.672

Height : 10.045

Entry



B1L002S, U25-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.32
Std Dev	11.33

Turn on : 25.9743

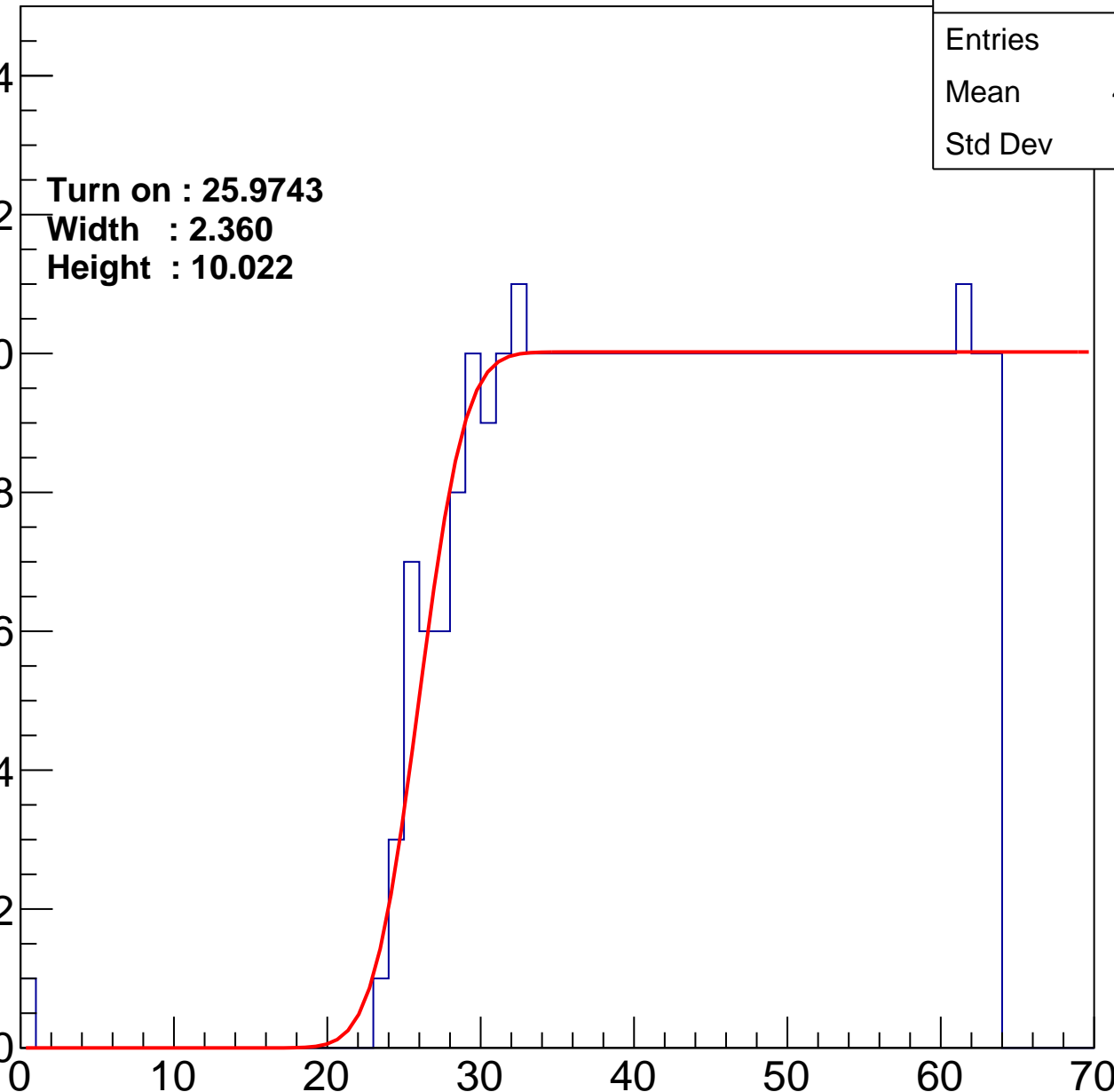
Width : 2.360

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch111

calib_packv5_042523_0143.root, FC#10, port B3

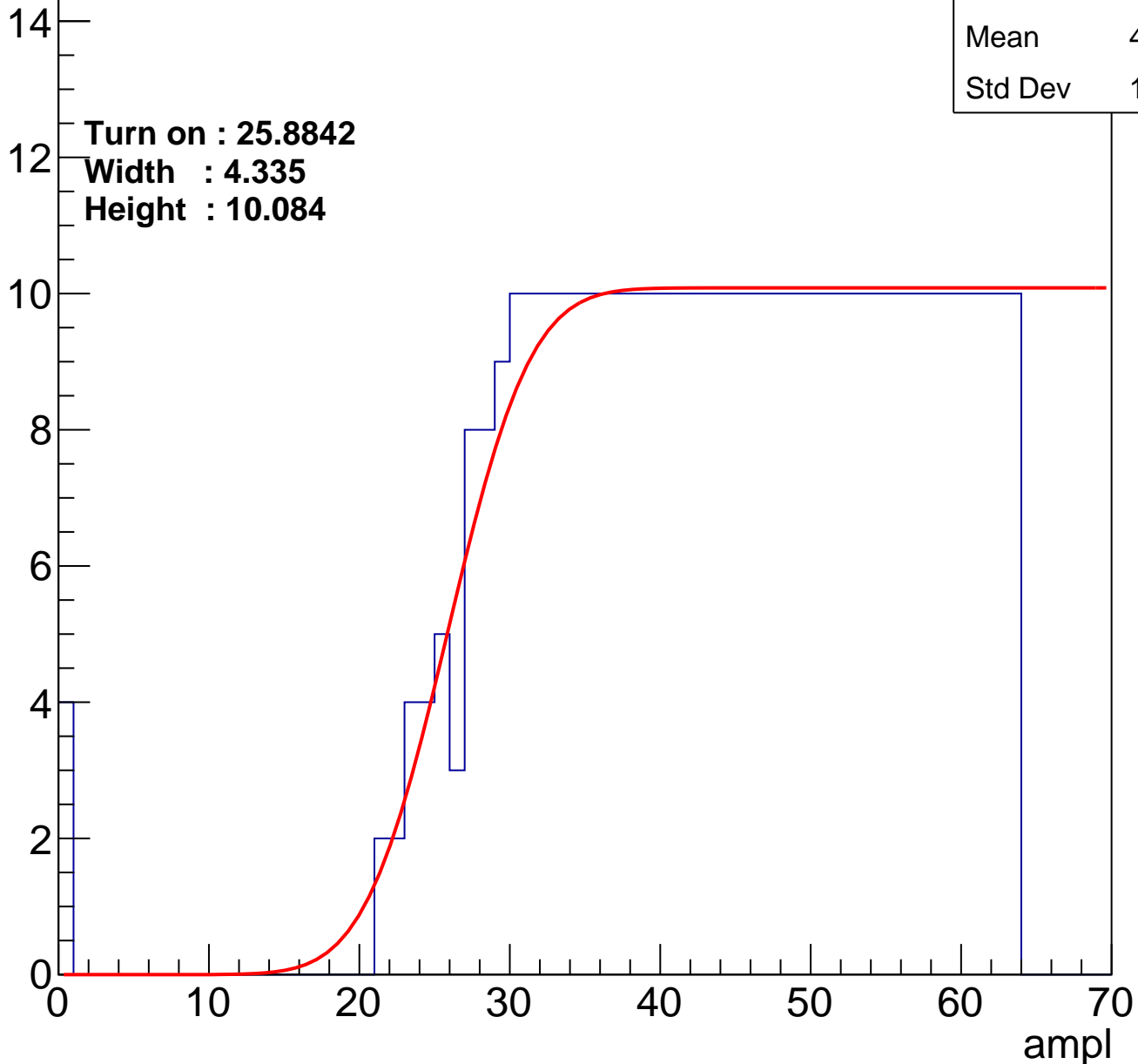
Entries	389
Mean	43.67
Std Dev	12.12

Turn on : 25.8842

Width : 4.335

Height : 10.084

Entry



B1L002S, U25-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.88
Std Dev	11.12

Turn on : 27.4948

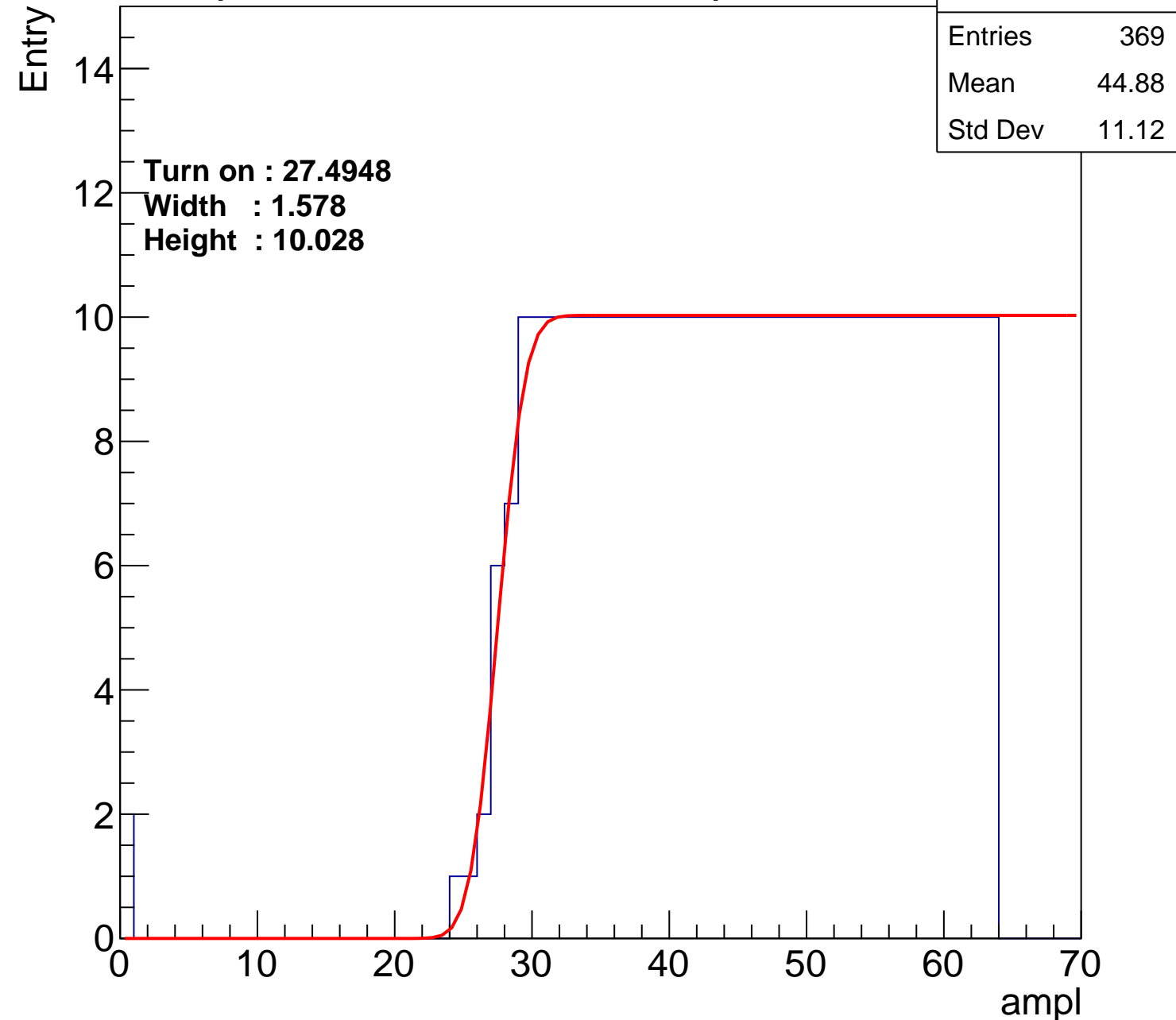
Width : 1.578

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch113

calib_packv5_042523_0143.root, FC#10, port B3

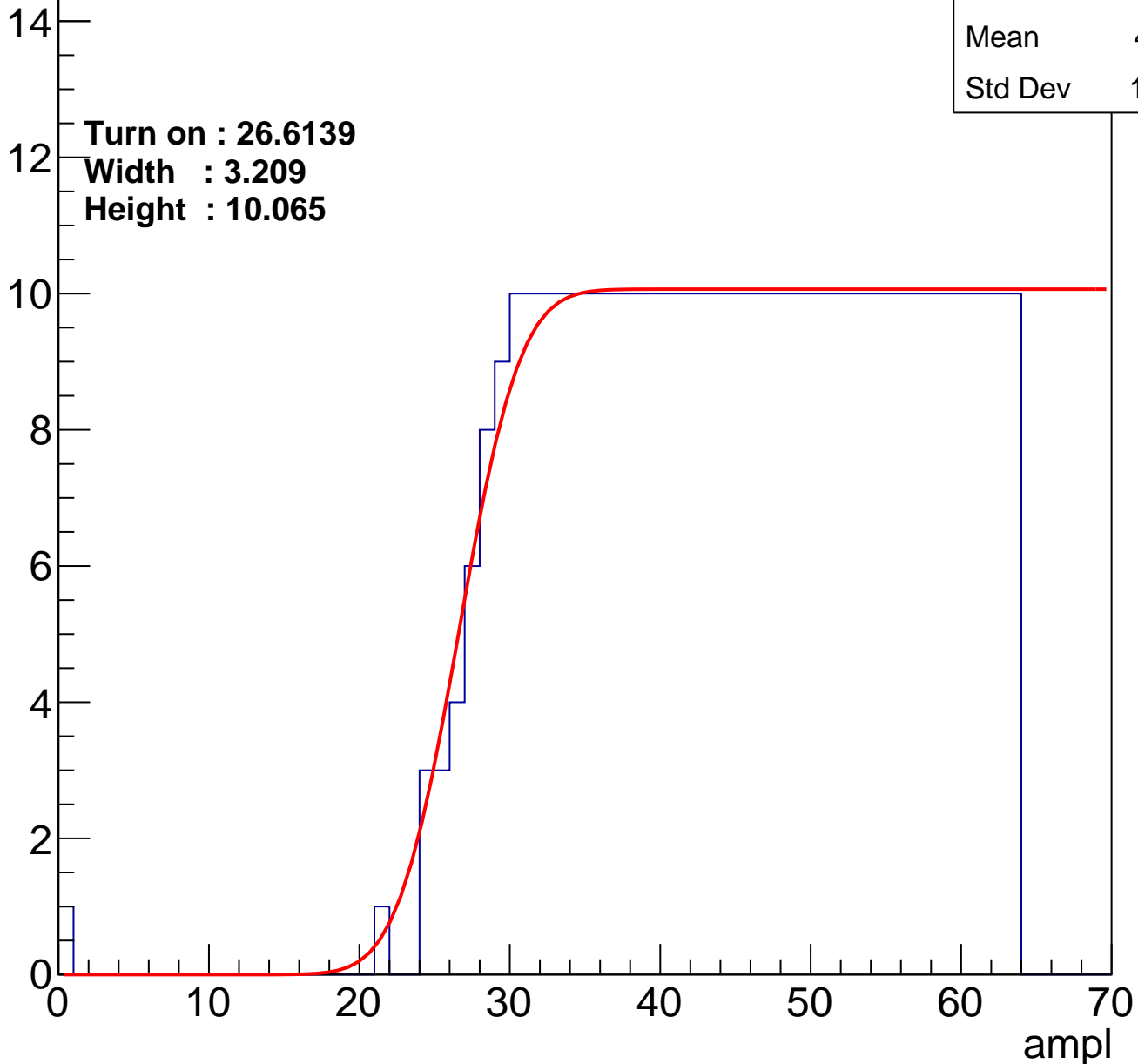
Entries	375
Mean	44.61
Std Dev	11.14

Turn on : 26.6139

Width : 3.209

Height : 10.065

Entry



B1L002S, U25-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
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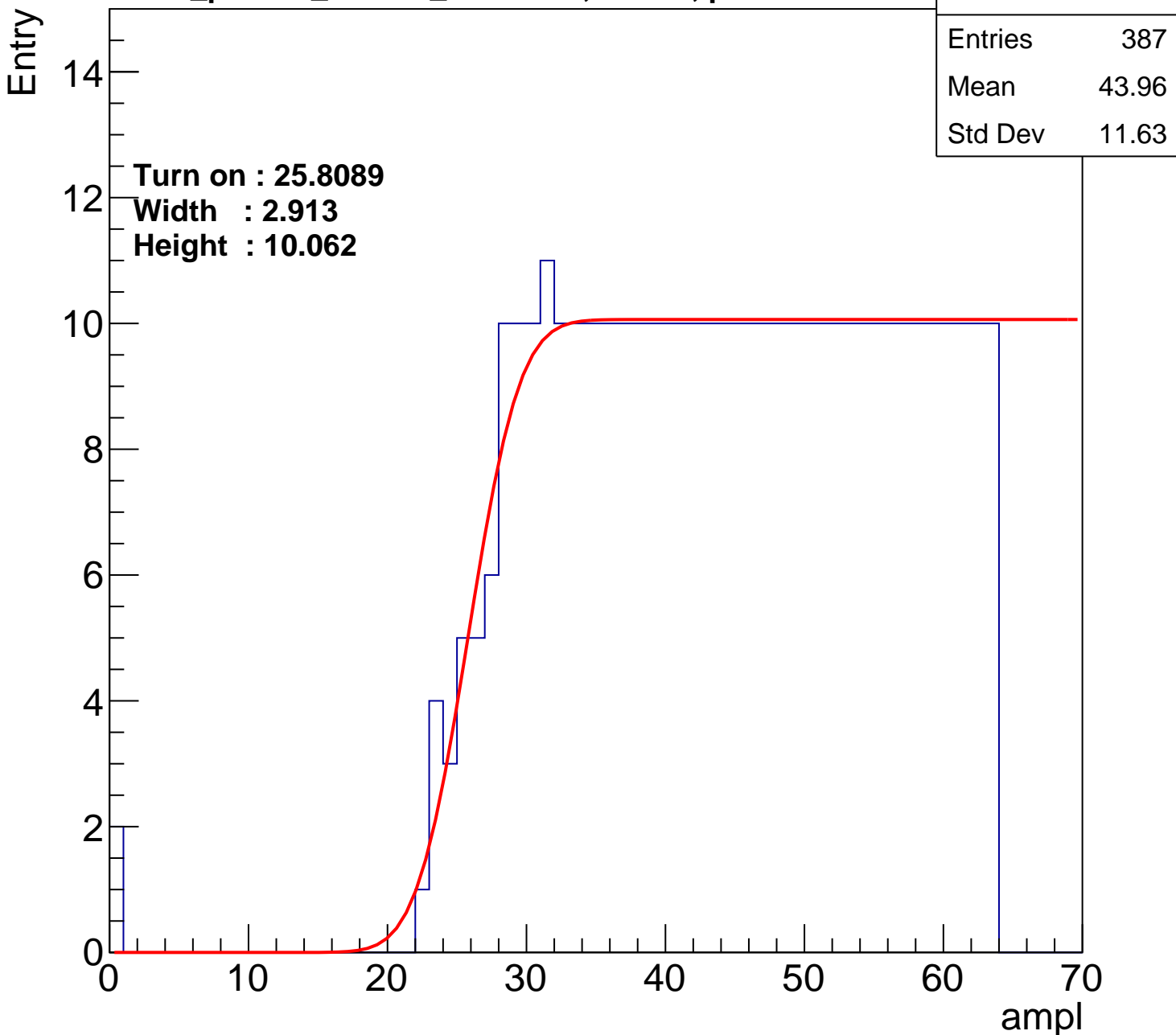
Mean	43.96
------	-------

Std Dev	11.63
---------	-------

Turn on : 25.8089

Width : 2.913

Height : 10.062



B1L002S, U25-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.01
Std Dev	11.6

Turn on : 26.0379

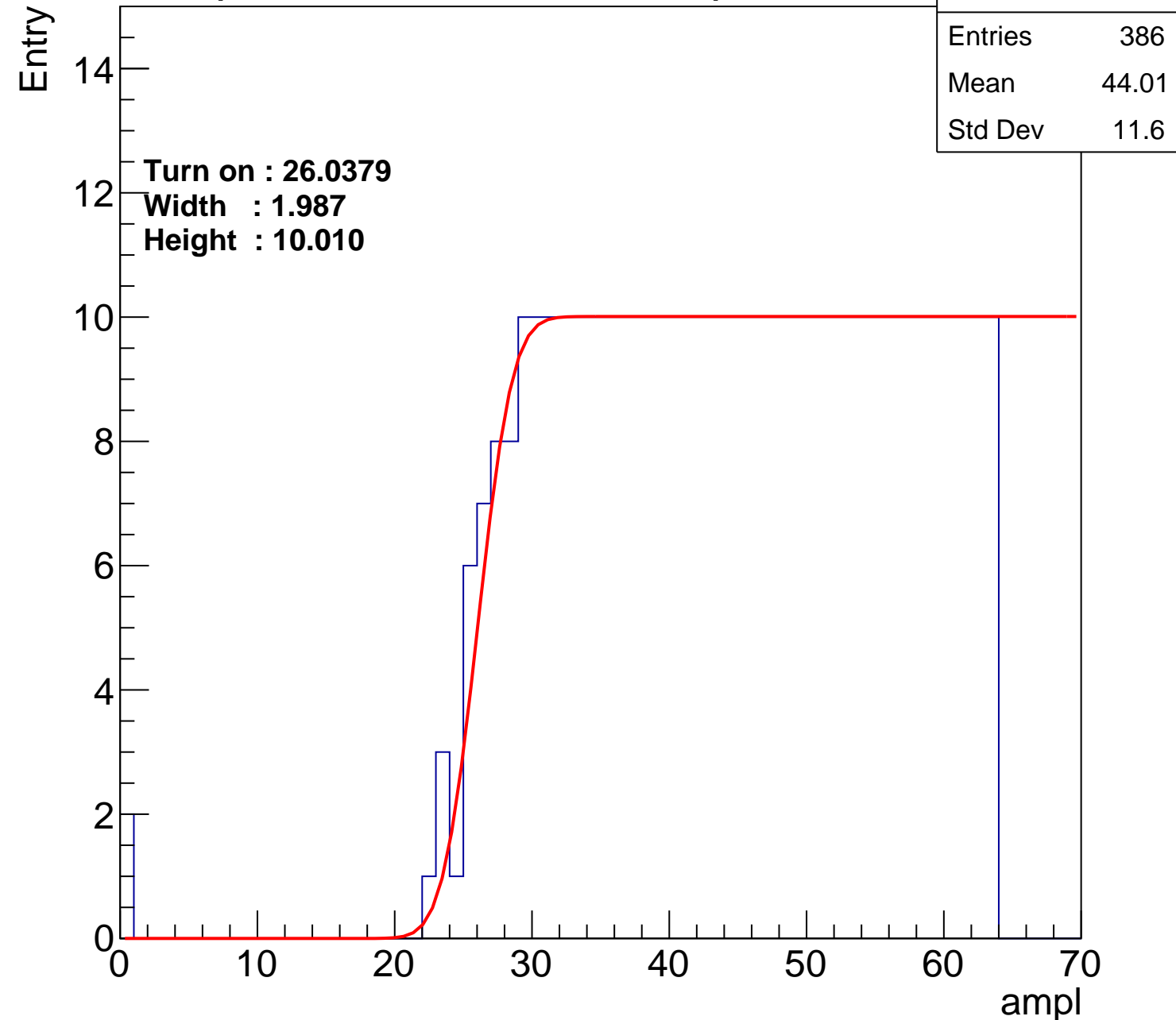
Width : 1.987

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.71
Std Dev	11.93

Turn on : 25.6007

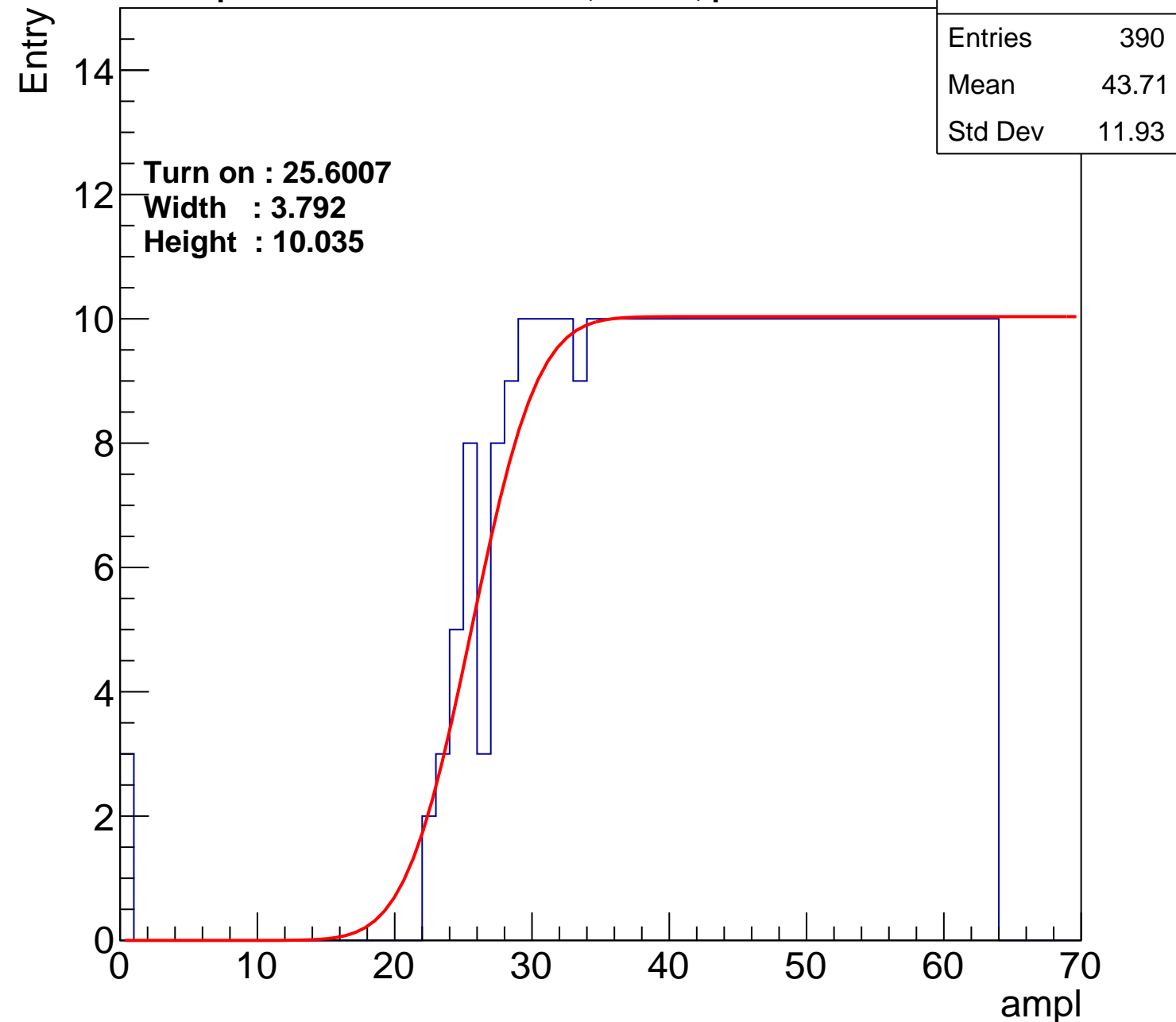
Width : 3.792

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.27
Std Dev	11.88

Turn on : 23.9706

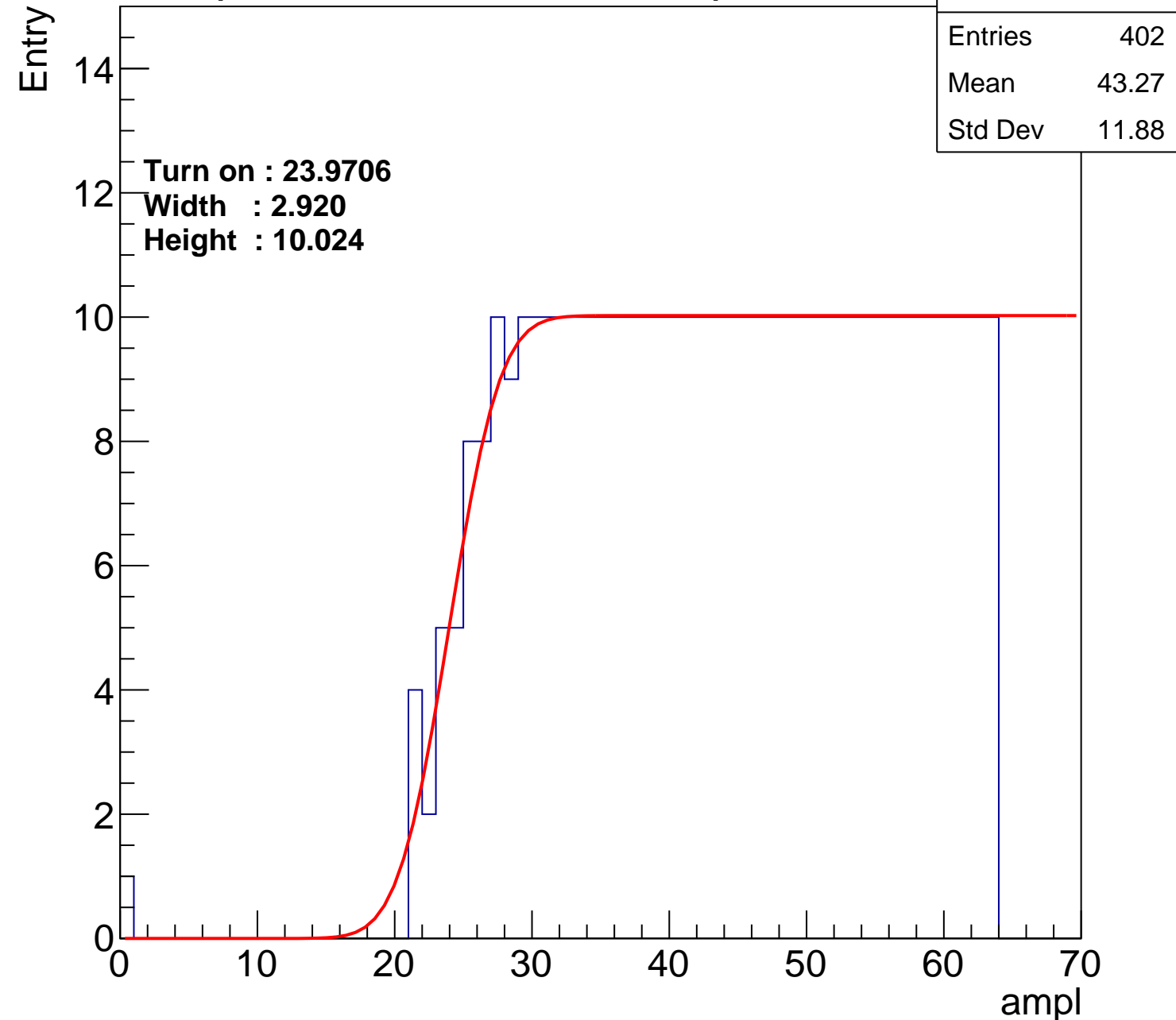
Width : 2.920

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch118

calib_packv5_042523_0143.root, FC#10, port B3

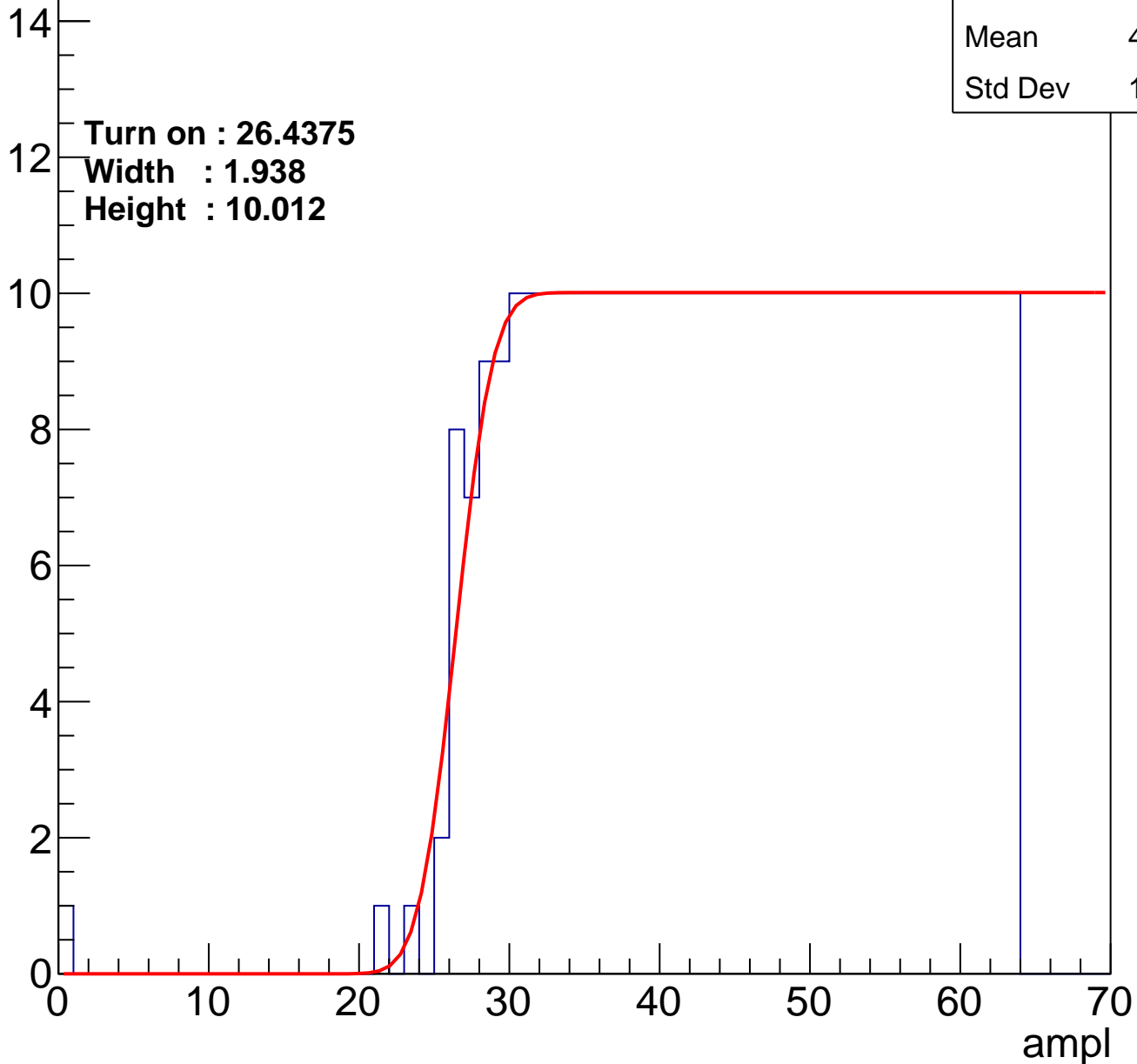
Entries	378
Mean	44.48
Std Dev	11.19

Turn on : 26.4375

Width : 1.938

Height : 10.012

Entry



B1L002S, U25-ch119

calib_packv5_042523_0143.root, FC#10, port B3

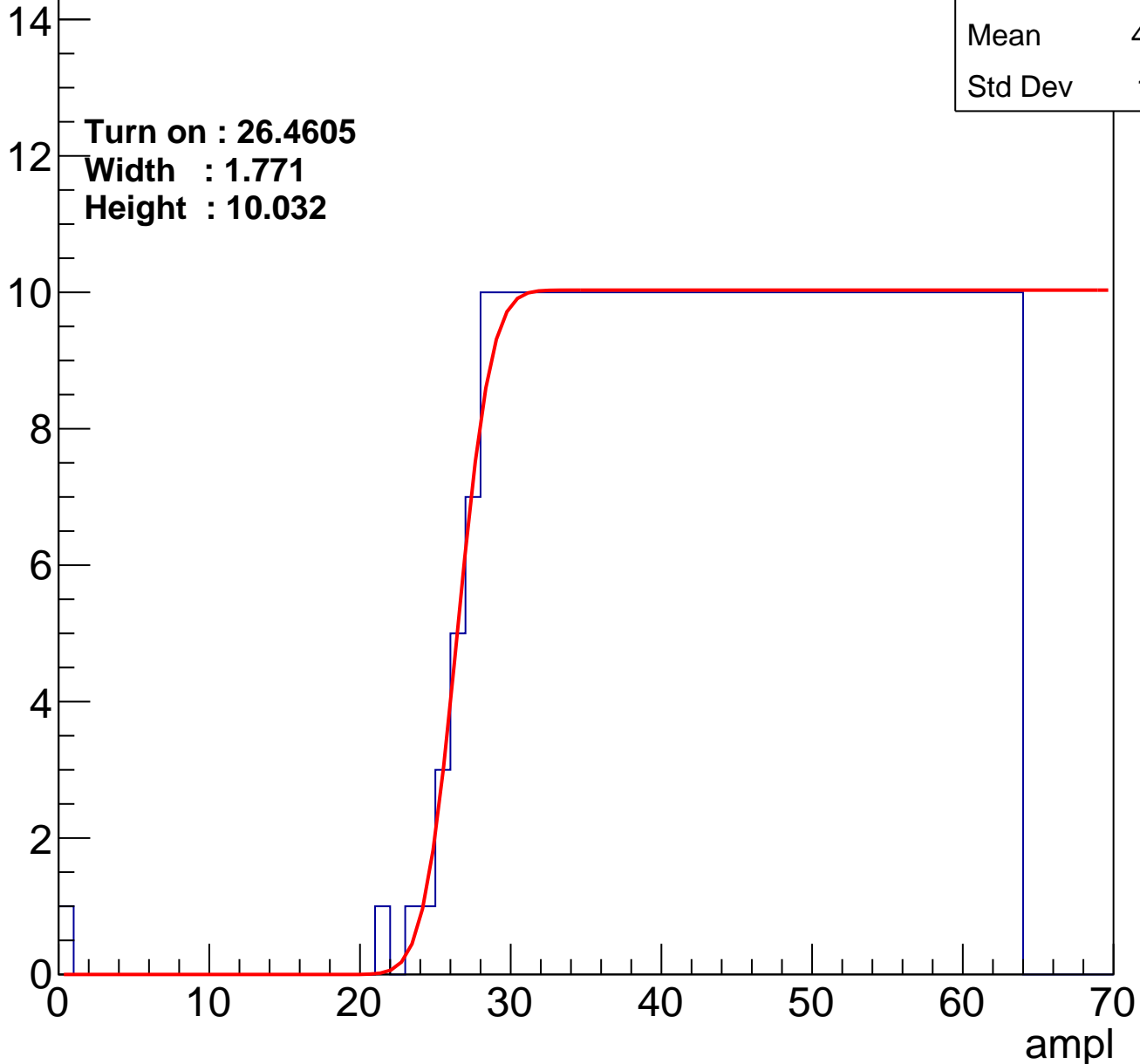
Entries	379
Mean	44.44
Std Dev	11.21

Turn on : 26.4605

Width : 1.771

Height : 10.032

Entry



B1L002S, U25-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.82
Std Dev	11.61

Turn on : 24.7116

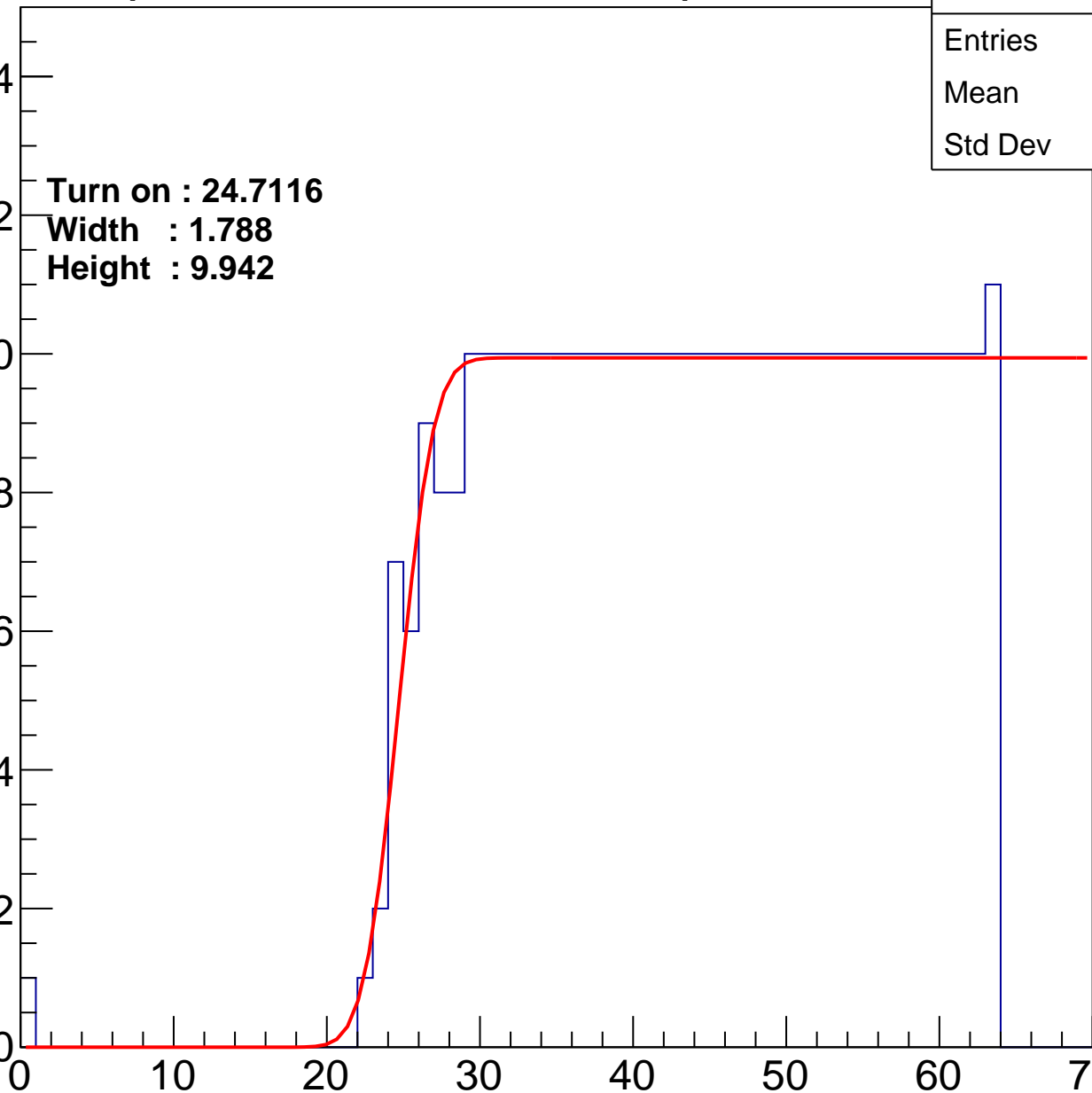
Width : 1.788

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch121

calib_packv5_042523_0143.root, FC#10, port B3

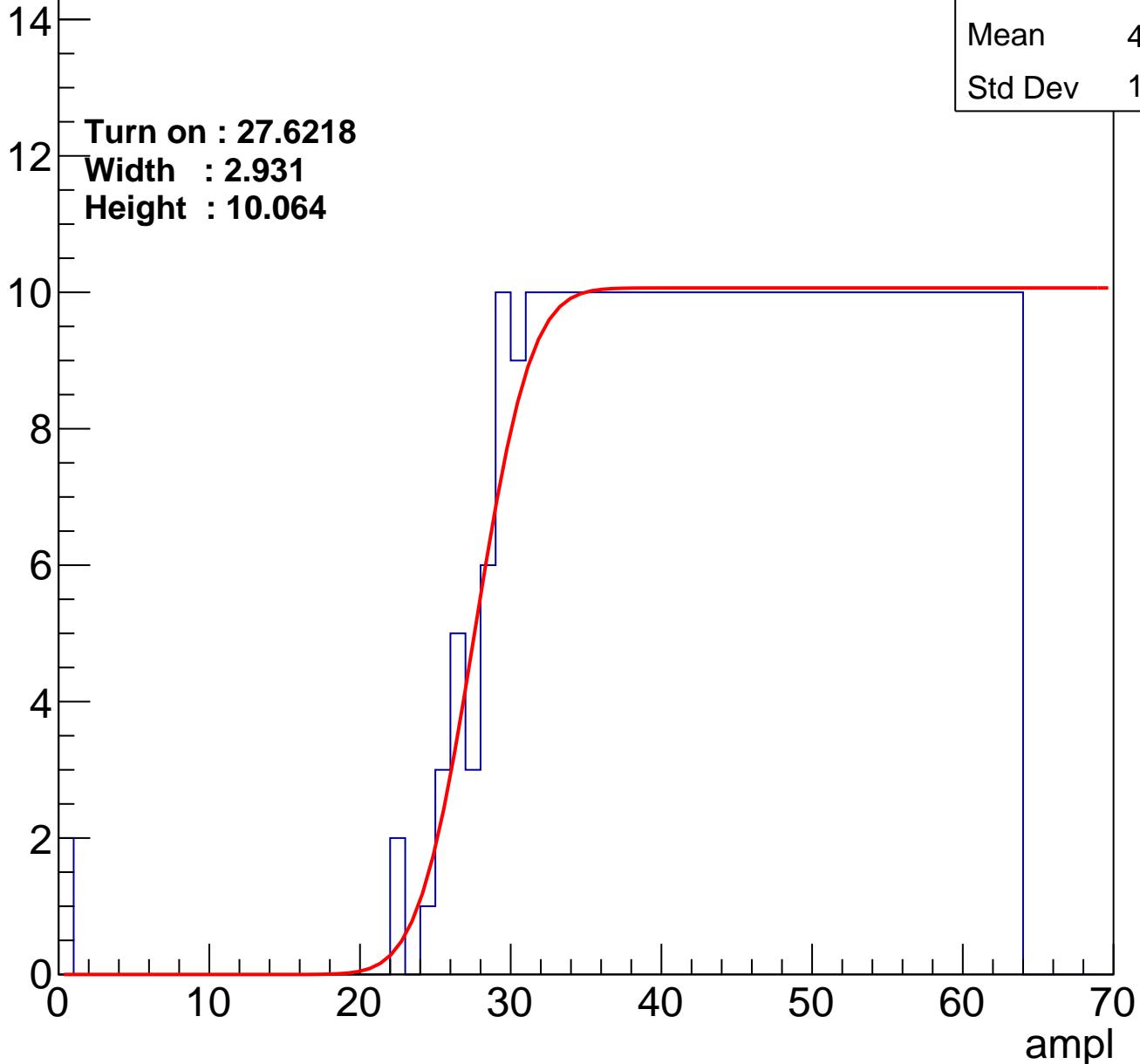
Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.6218

Width : 2.931

Height : 10.064

Entry



B1L002S, U25-ch122

calib_packv5_042523_0143.root, FC#10, port B3

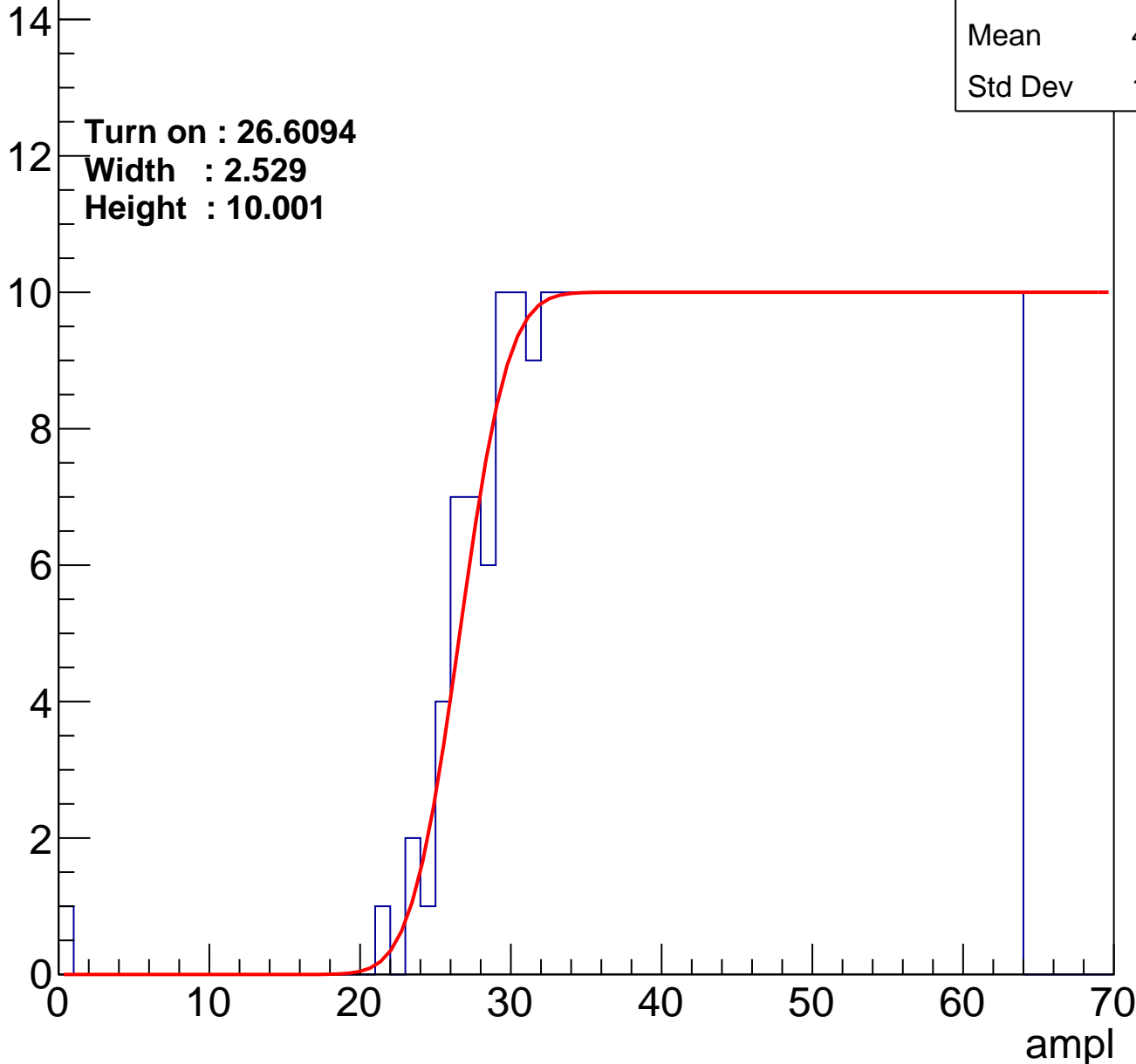
Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.6094

Width : 2.529

Height : 10.001

Entry



B1L002S, U25-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.86
Std Dev	12.14

Turn on : 25.8773

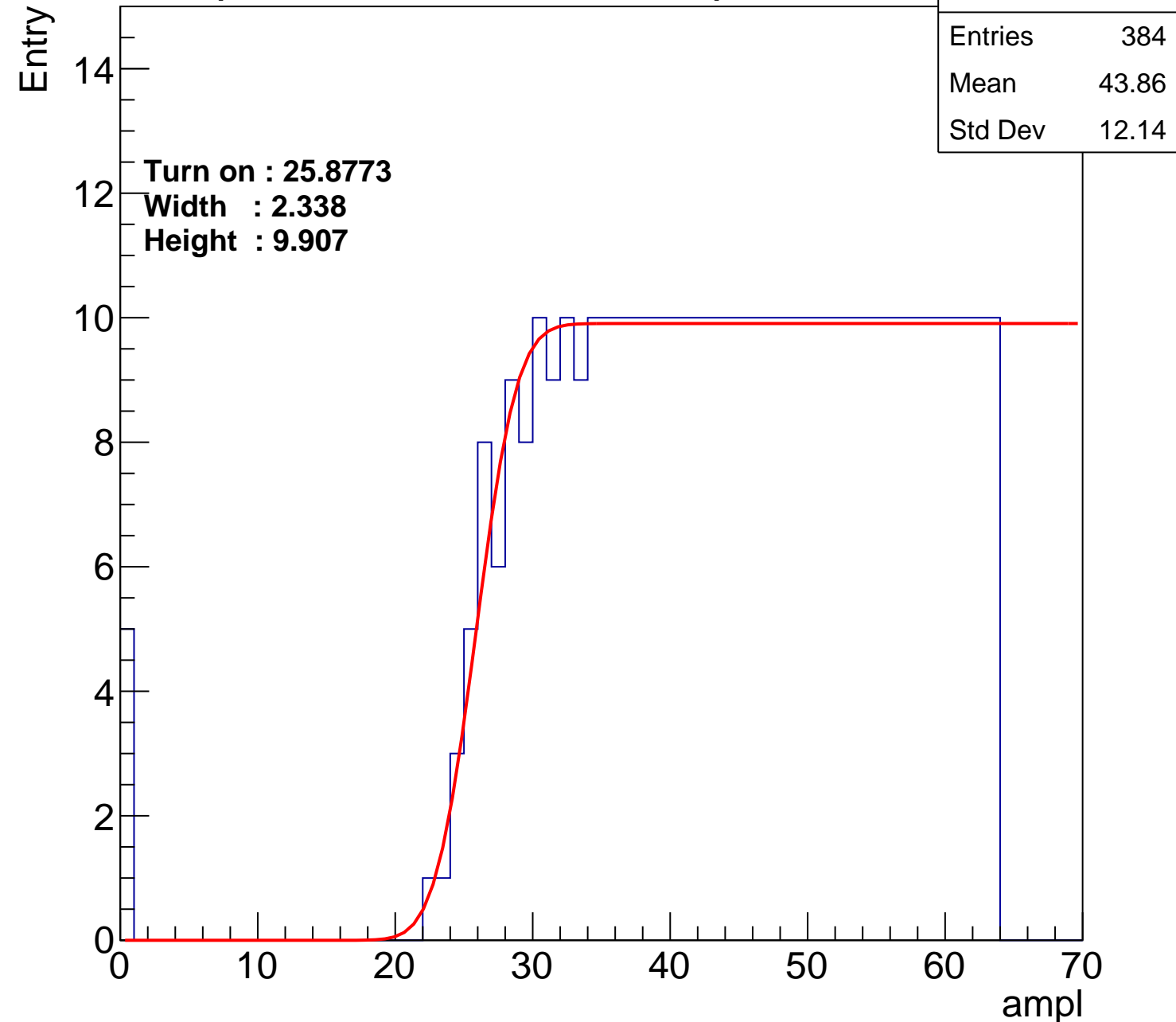
Width : 2.338

Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.9
Std Dev	11.55

Turn on : 26.2004

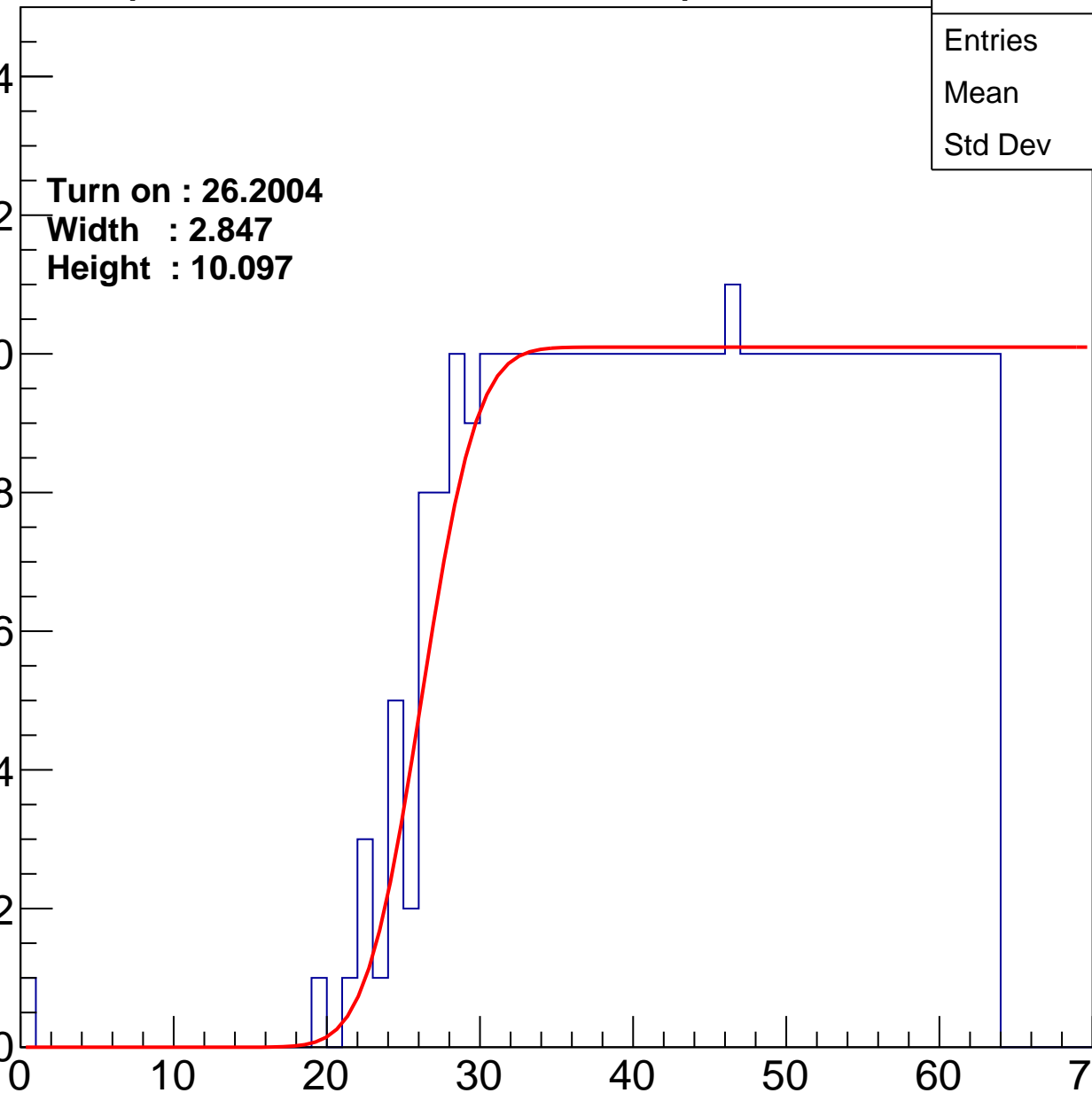
Width : 2.847

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch125

calib_packv5_042523_0143.root, FC#10, port B3

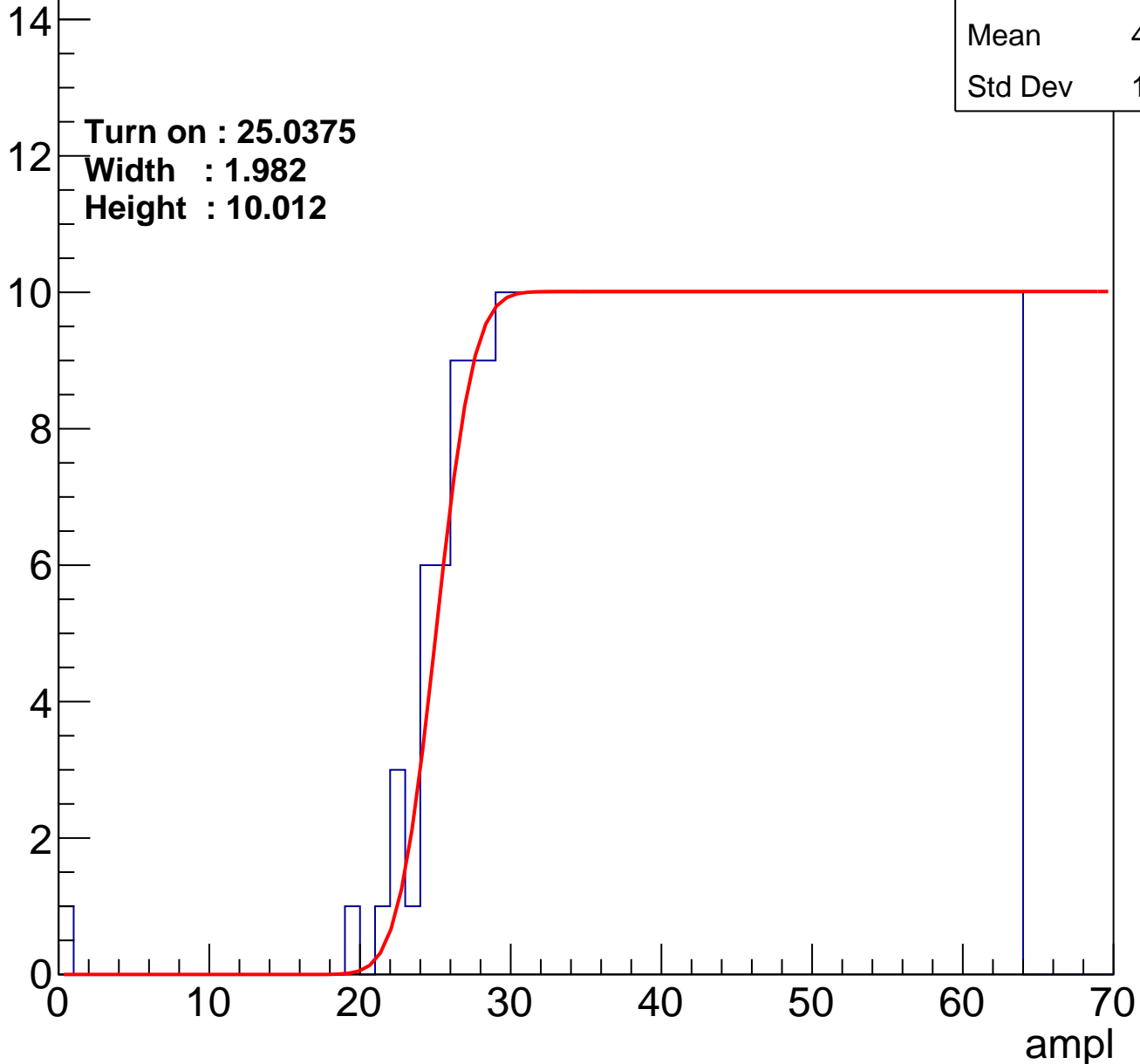
Entries	396
Mean	43.57
Std Dev	11.72

Turn on : 25.0375

Width : 1.982

Height : 10.012

Entry



B1L002S, U25-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 26.1216

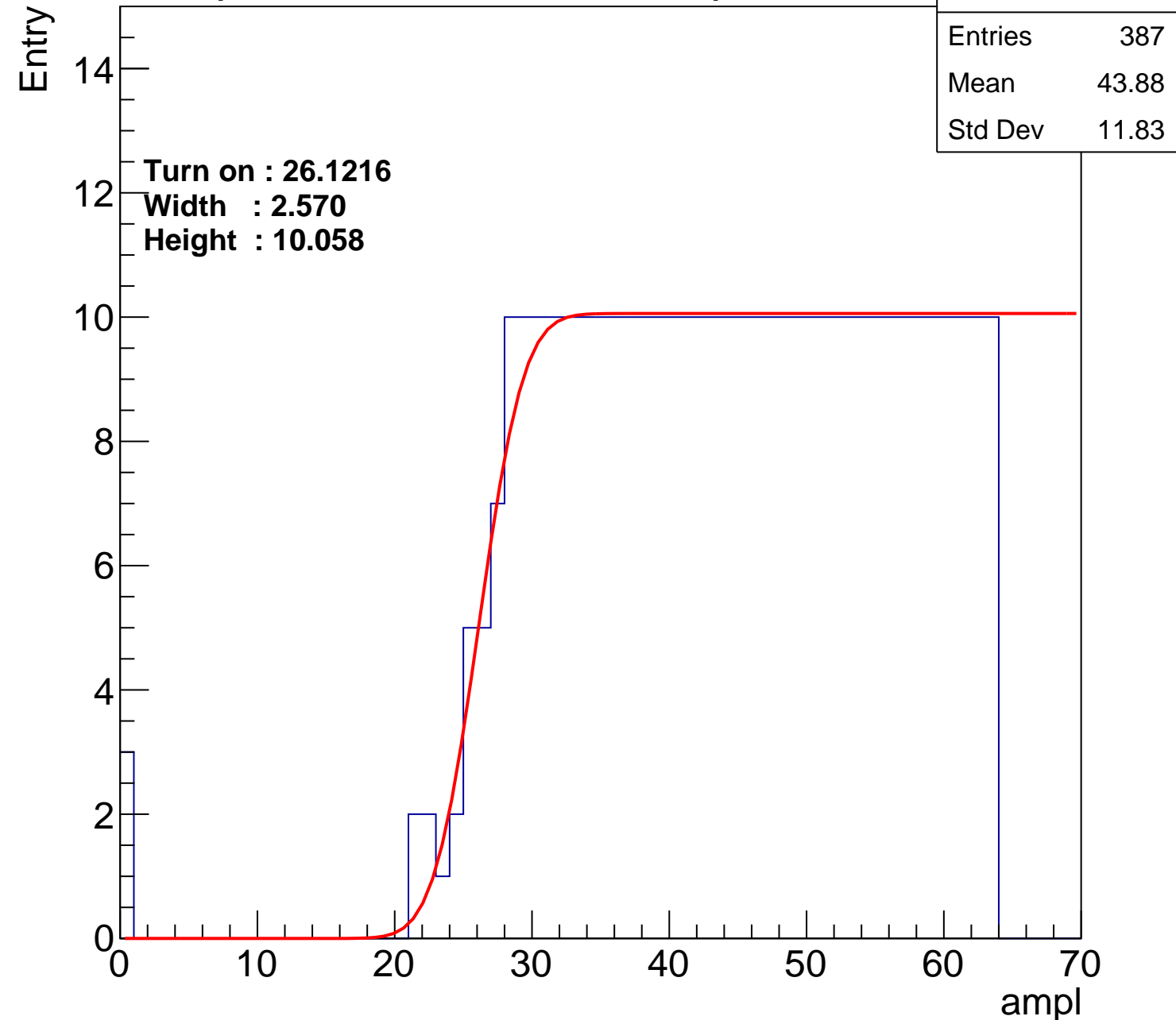
Width : 2.570

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U25-ch127

calib_packv5_042523_0143.root, FC#10, port B3

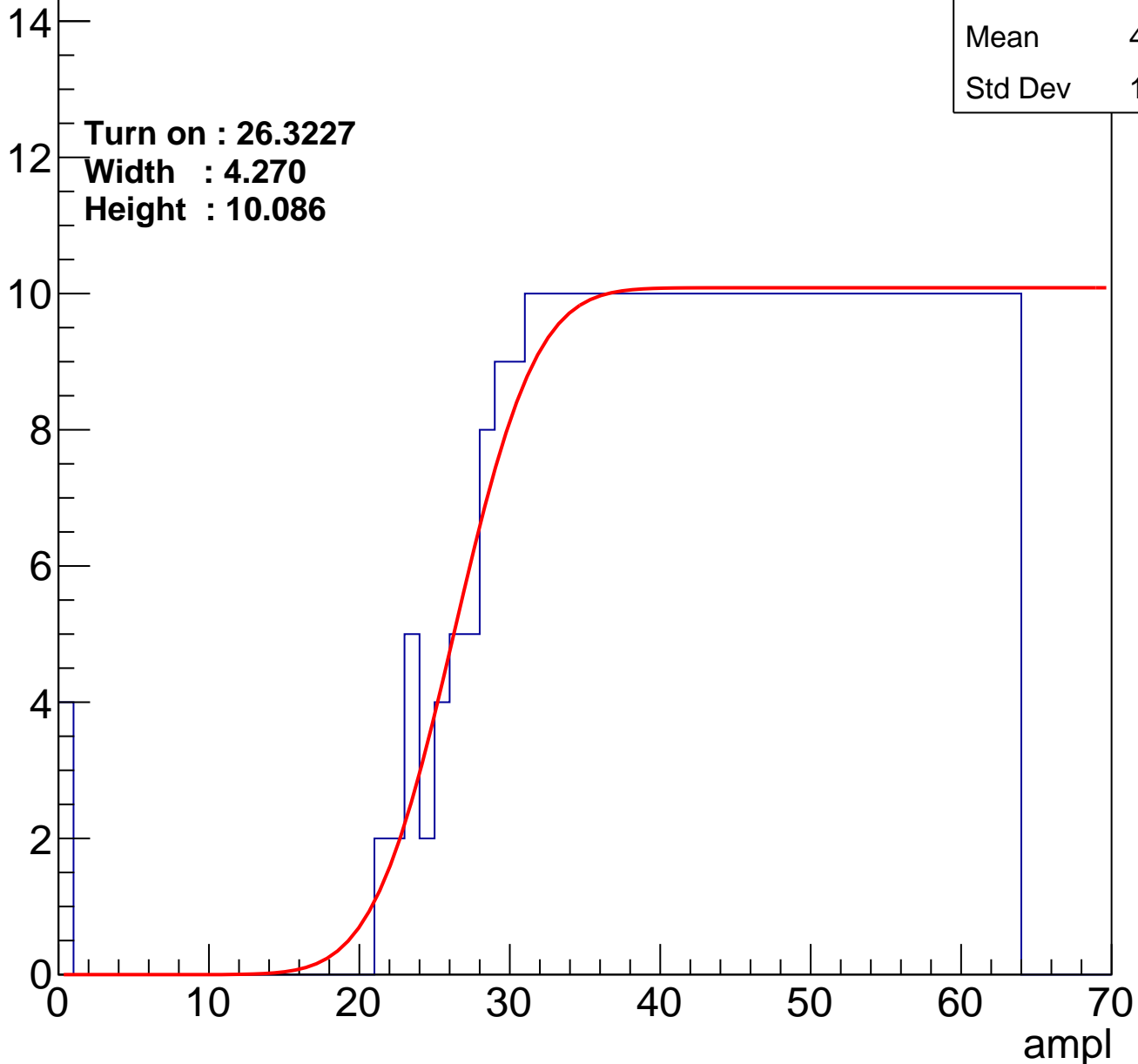
Entries	385
Mean	43.84
Std Dev	12.06

Turn on : 26.3227

Width : 4.270

Height : 10.086

Entry



B1L002S, U25-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.84
Std Dev	12.06

Turn on : 26.3227

Width : 4.270

Height : 10.086

Entry

