

B1L100S, U25-ch0, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	27.28
Std Dev	5.552

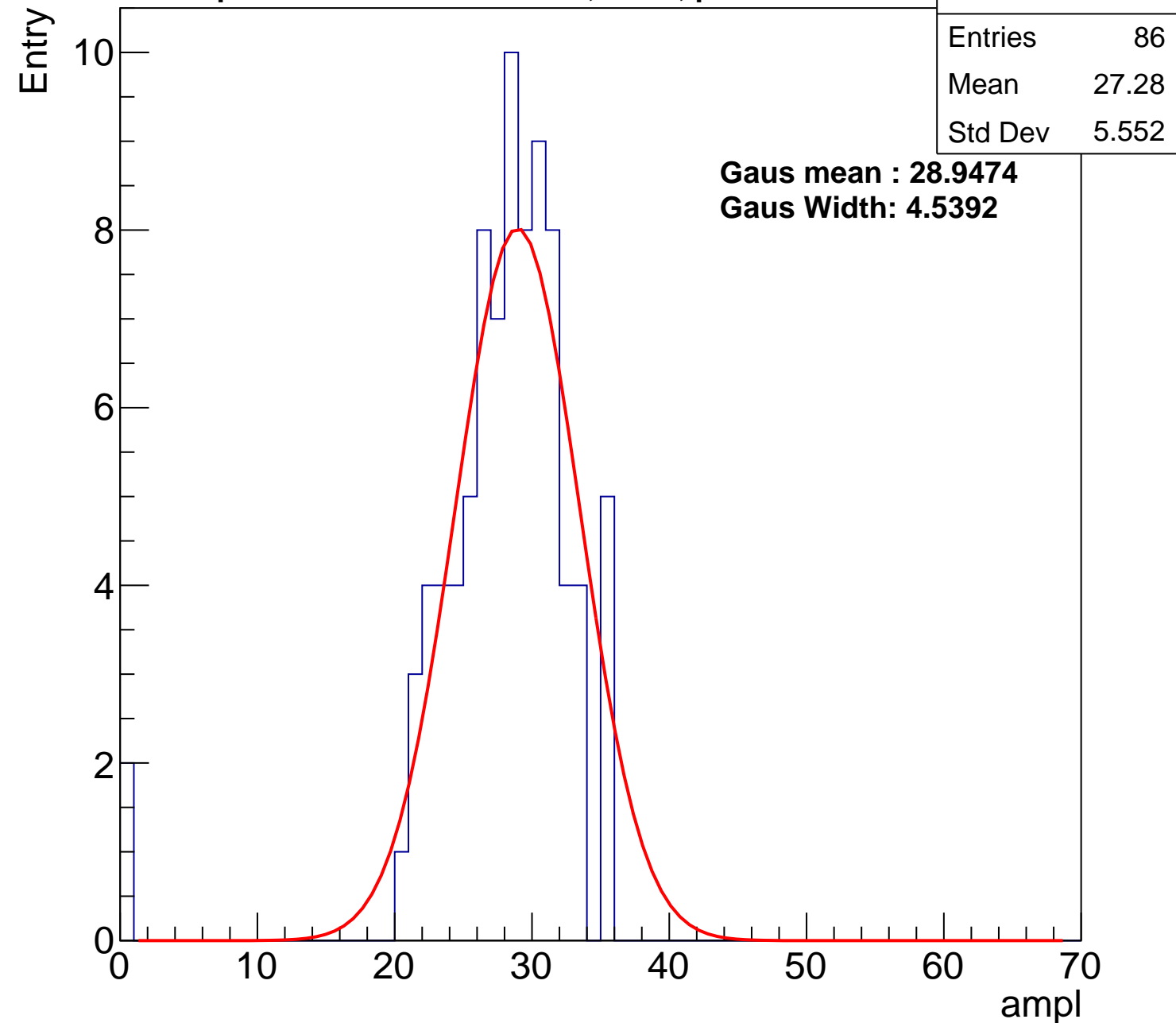
Gaus mean : 28.9474

Gaus Width: 4.5392

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch0, adc1

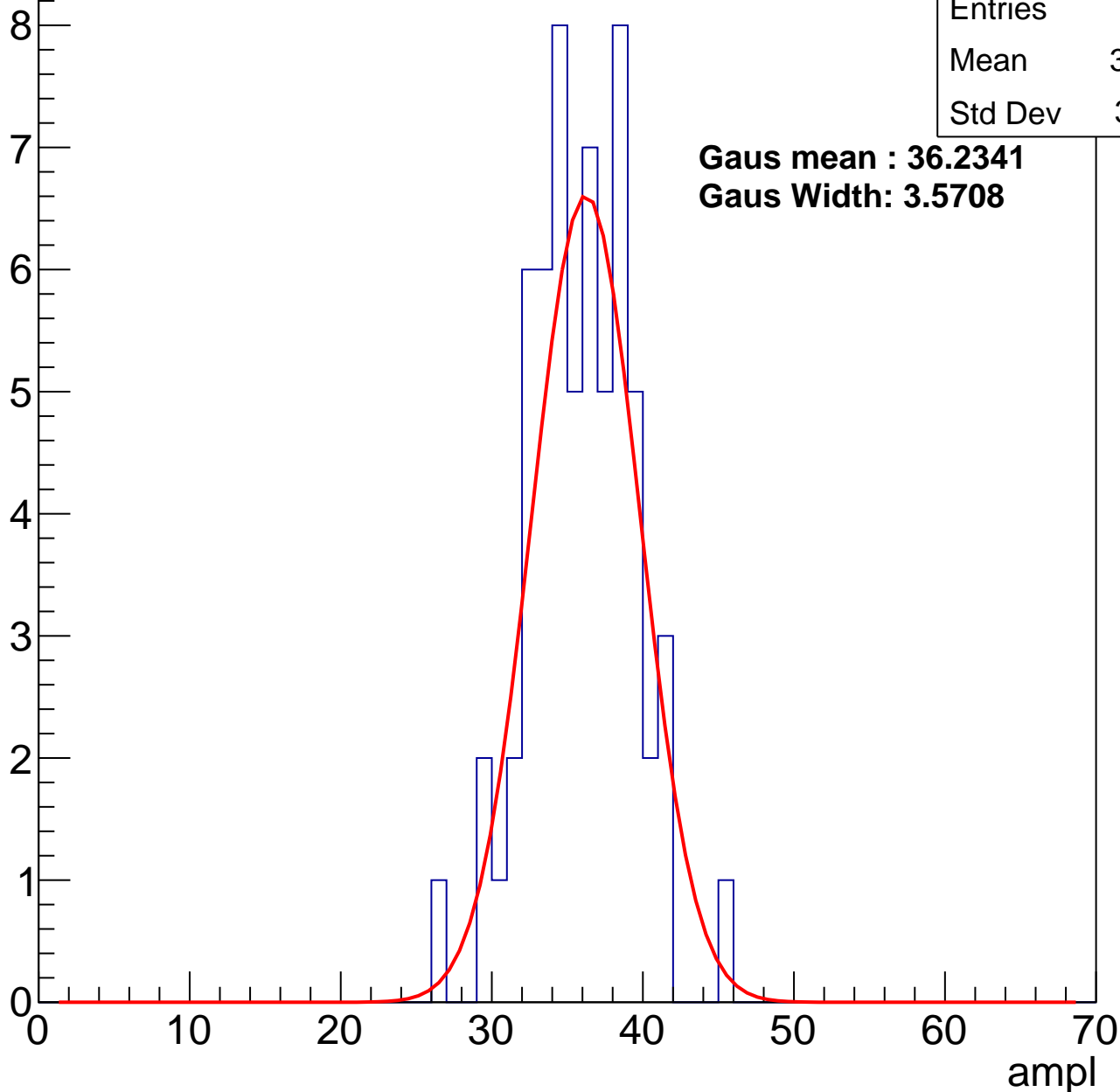
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.44
Std Dev	3.411

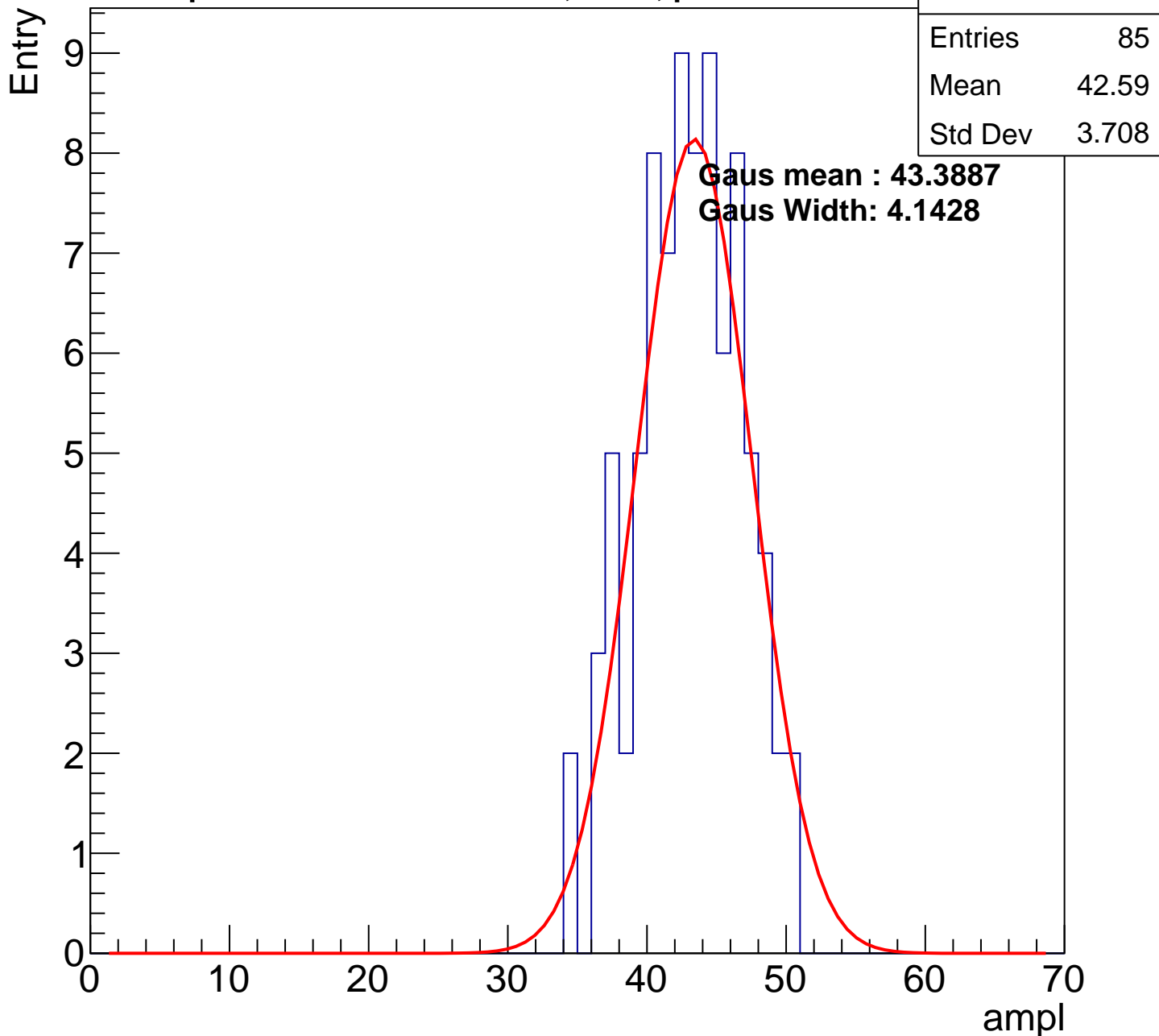
Gaus mean : 36.2341

Gaus Width: 3.5708



B1L100S, U25-ch0, adc2

calib_packv5_042523_0143.root, FC#4, port A2

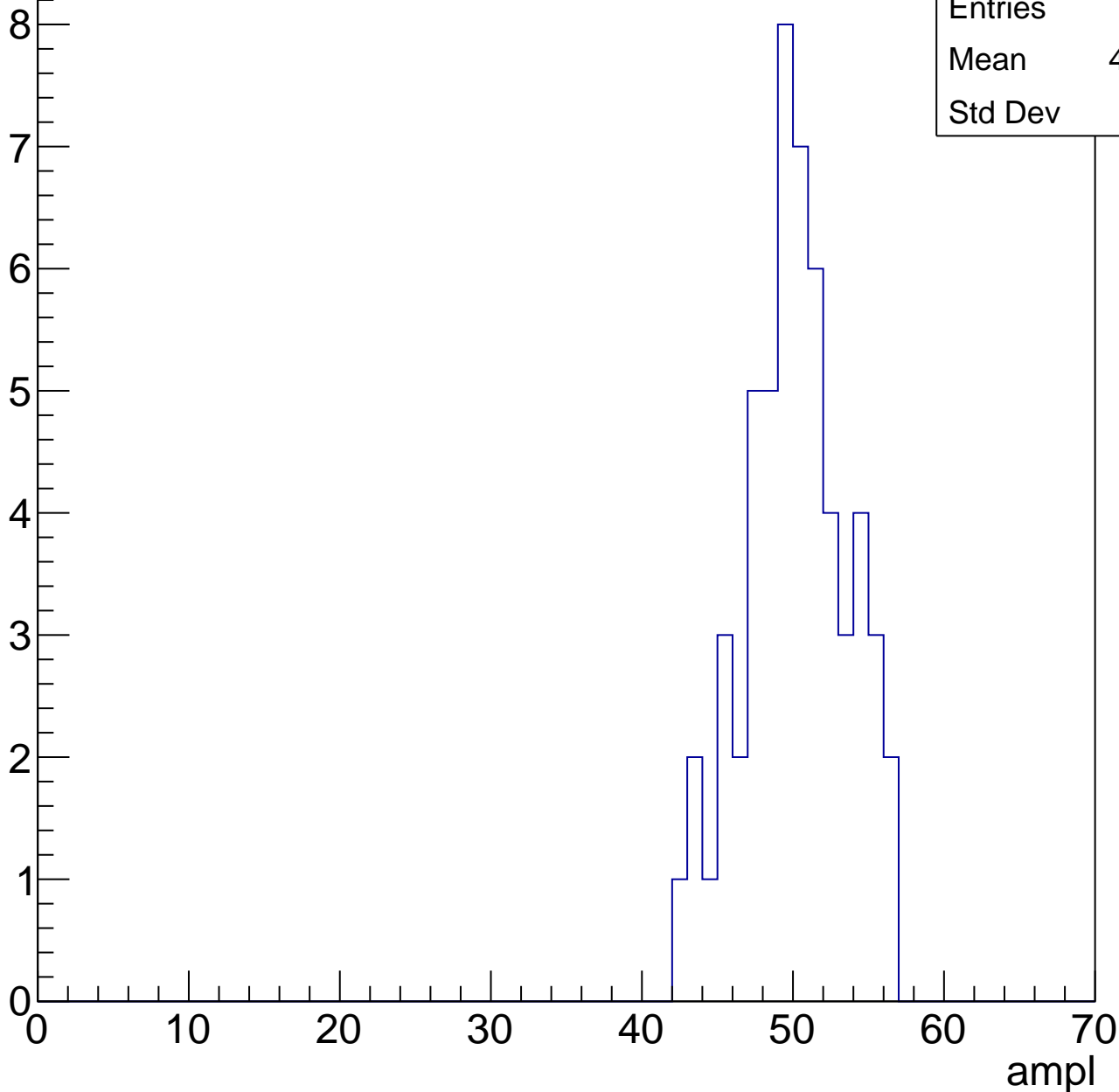


B1L100S, U25-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	49.68
Std Dev	3.36

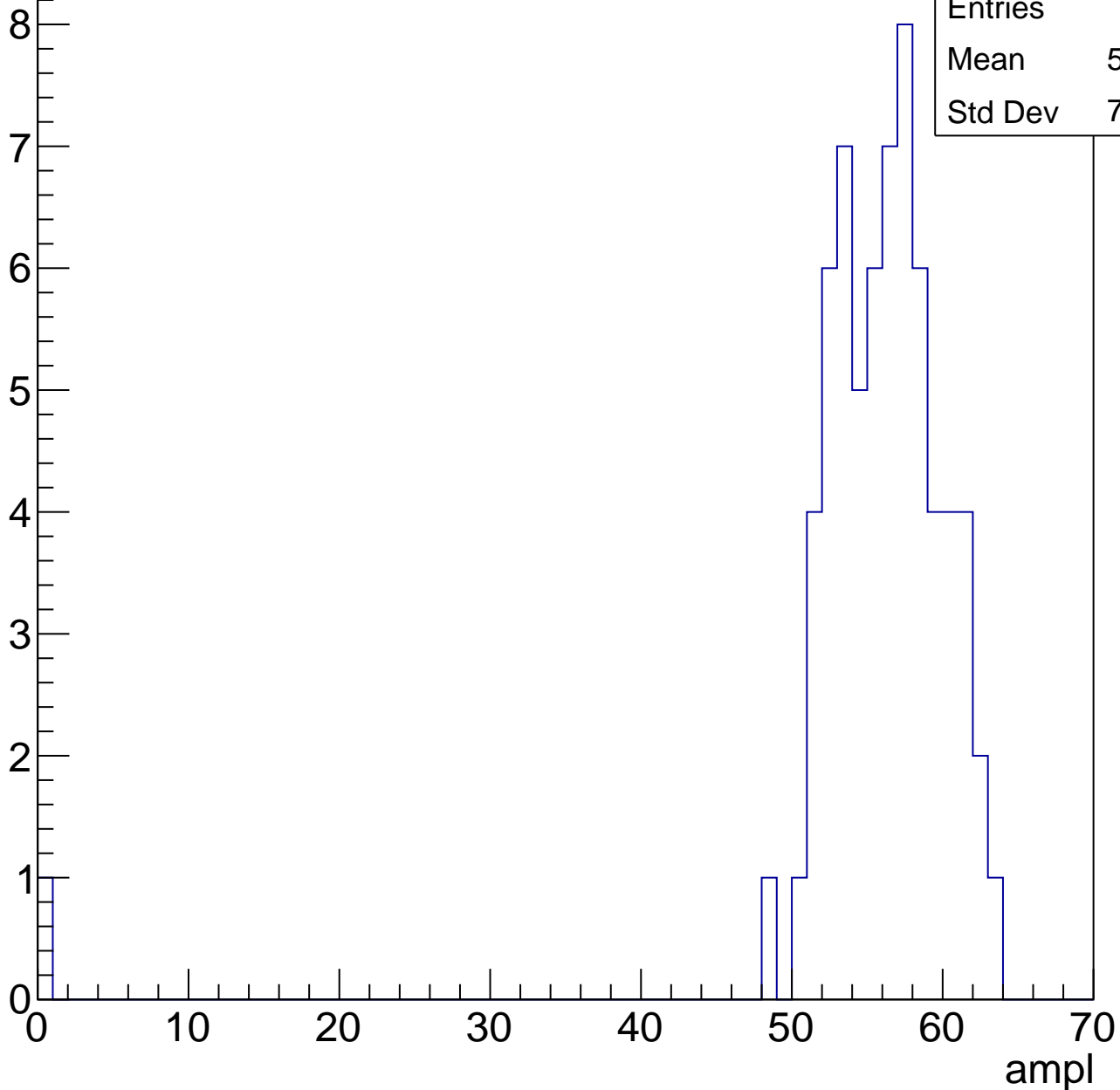


B1L100S, U25-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	55.04
Std Dev	7.544



B1L100S, U25-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.22
Std Dev	2.322

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L100S, U25-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch1, adc0

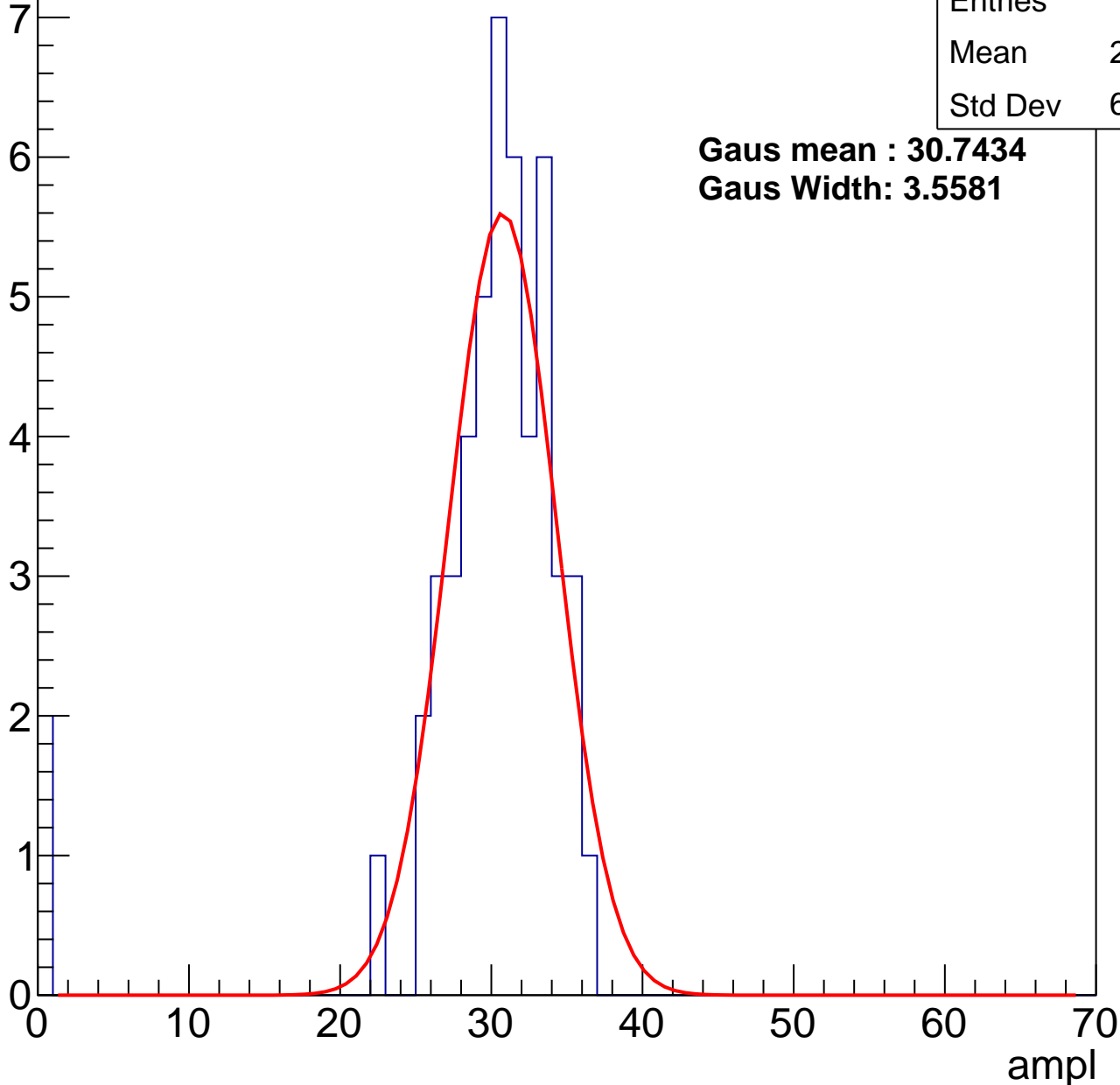
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	29.06
Std Dev	6.634

Gaus mean : 30.7434

Gaus Width: 3.5581



B1L100S, U25-ch1, adc1

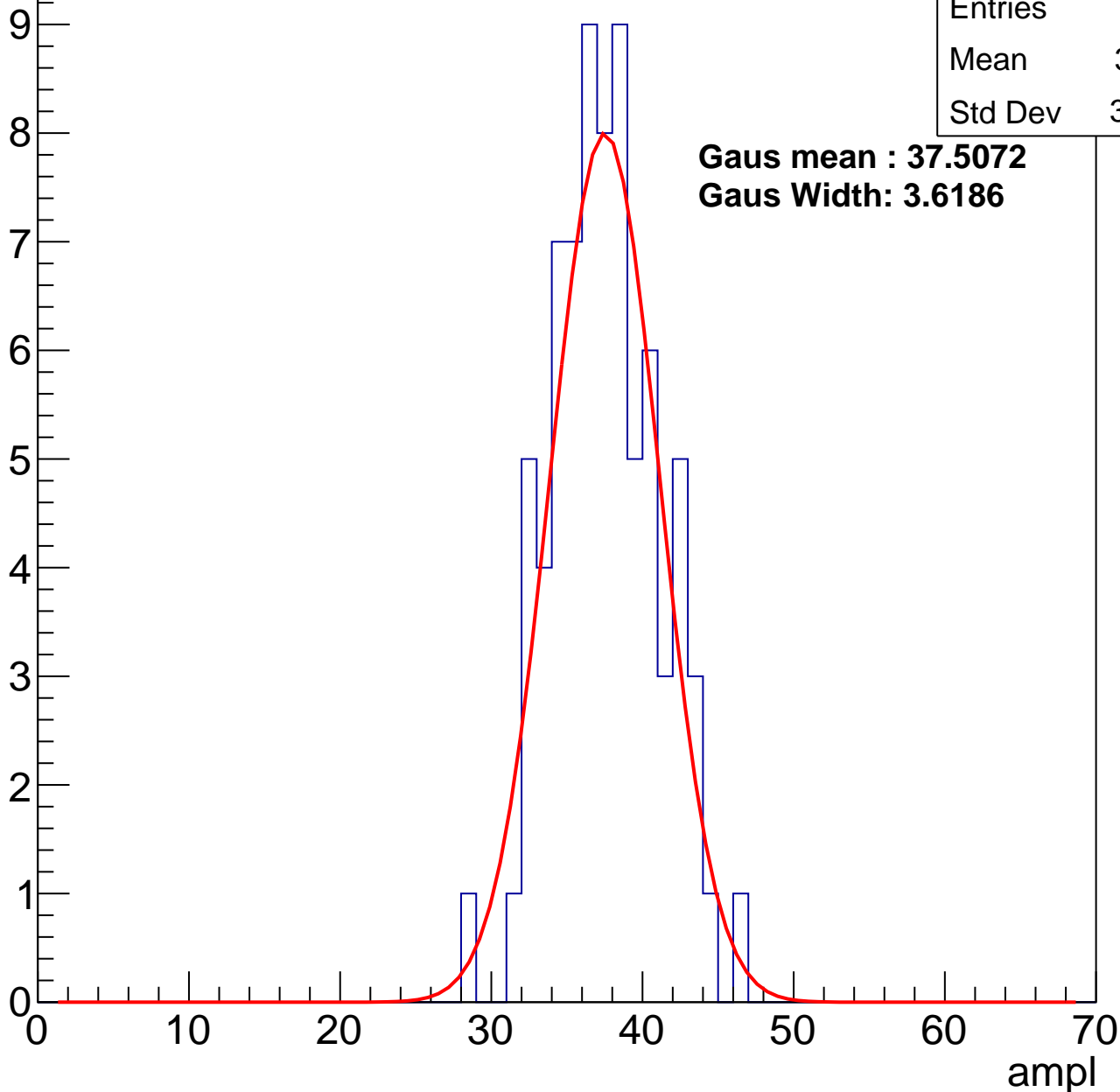
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.11
Std Dev	3.462

Gaus mean : 37.5072

Gaus Width: 3.6186



B1L100S, U25-ch1, adc2

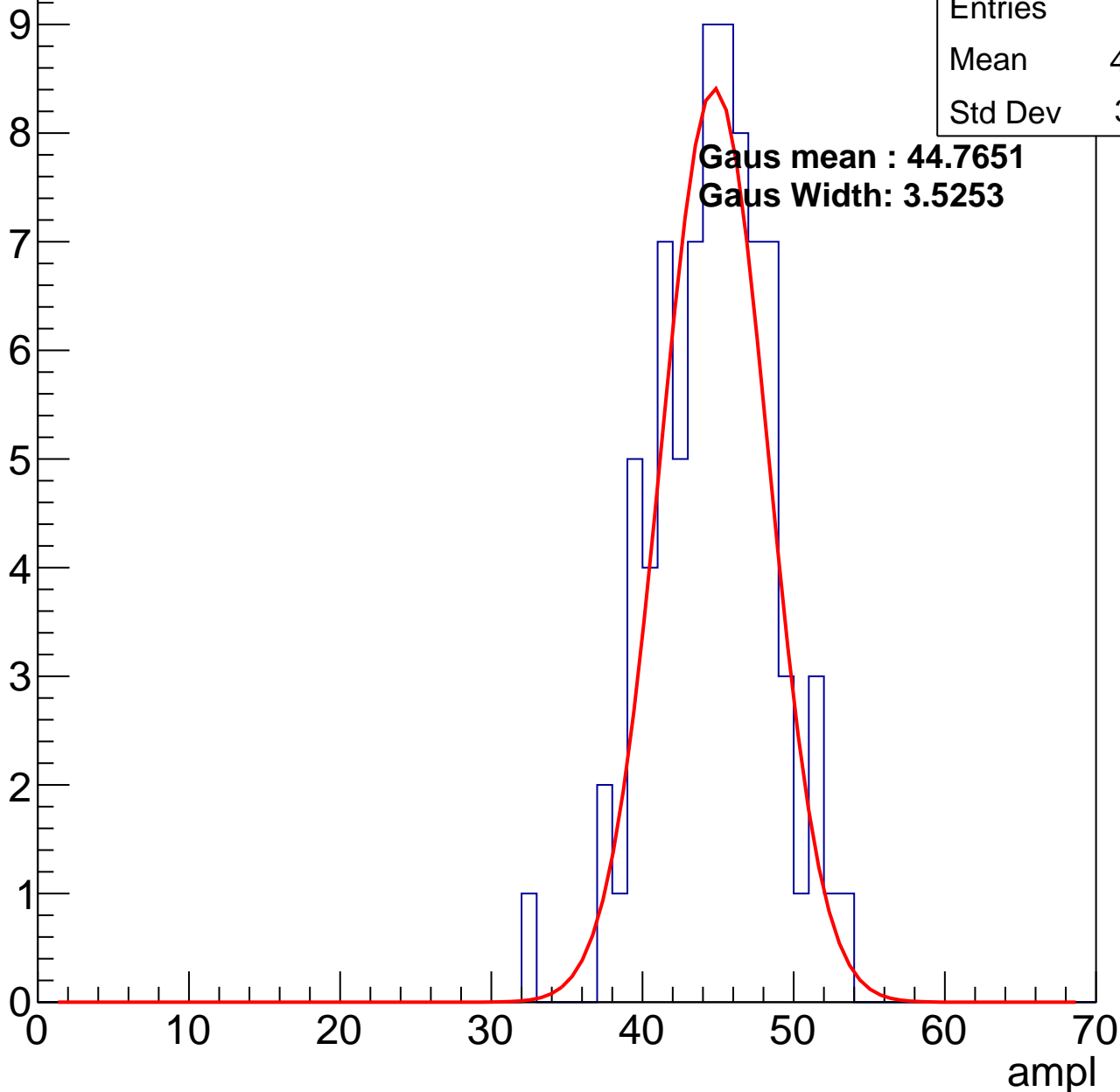
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	44.27
Std Dev	3.801

Gaus mean : 44.7651

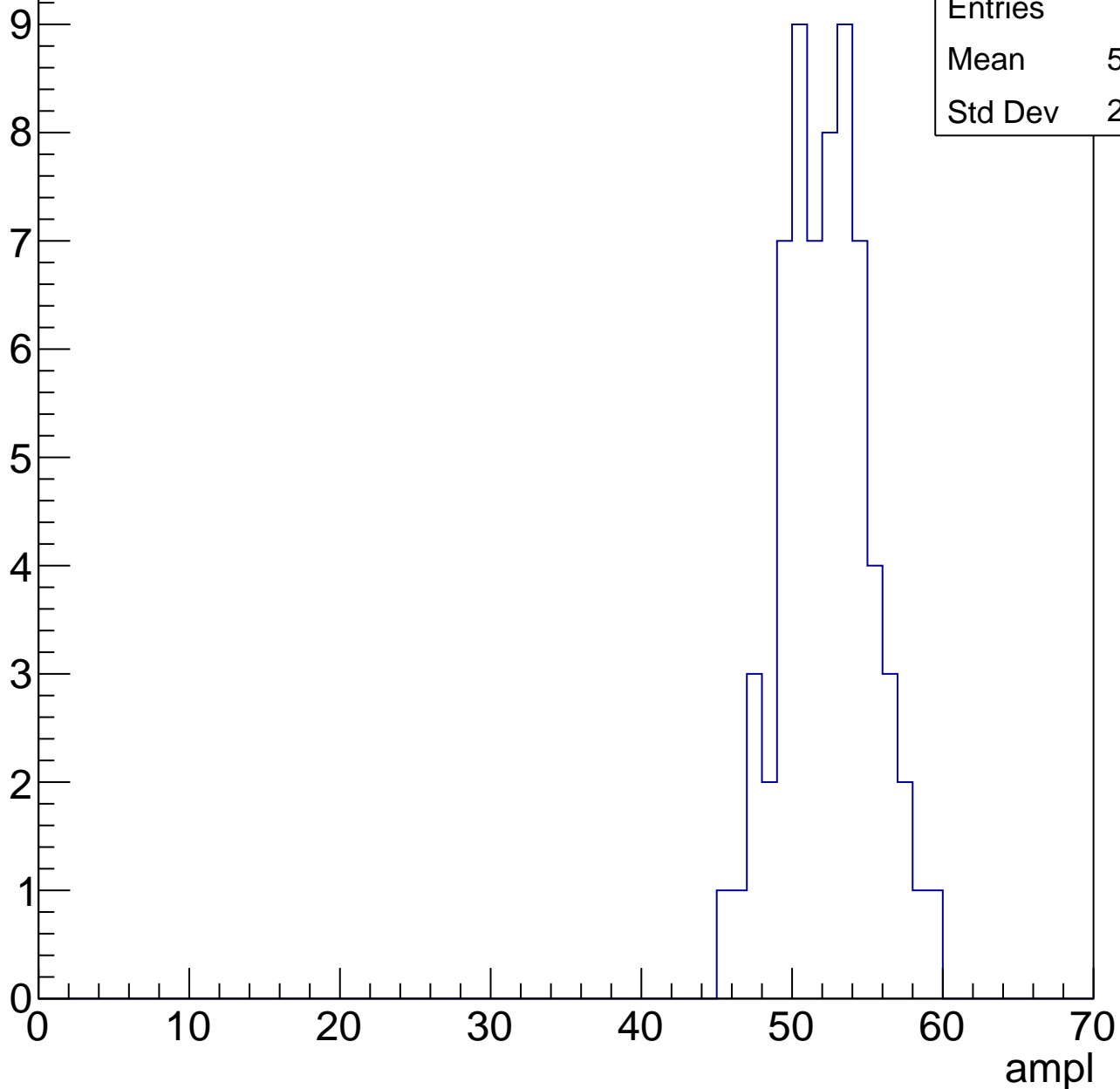
Gaus Width: 3.5253



B1L100S, U25-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



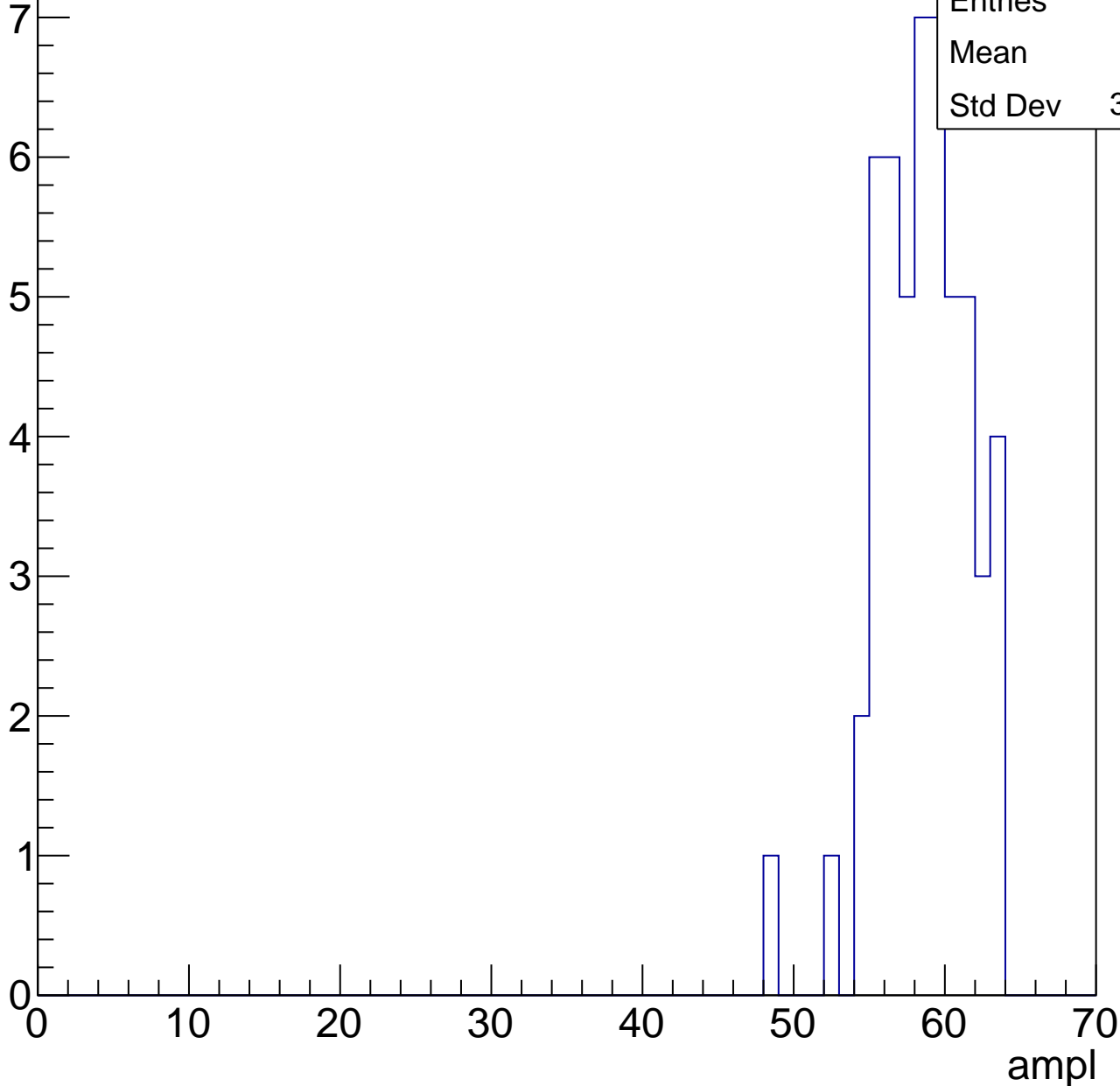
Entries	65
Mean	51.82
Std Dev	2.914

B1L100S, U25-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.1
Std Dev	3.008

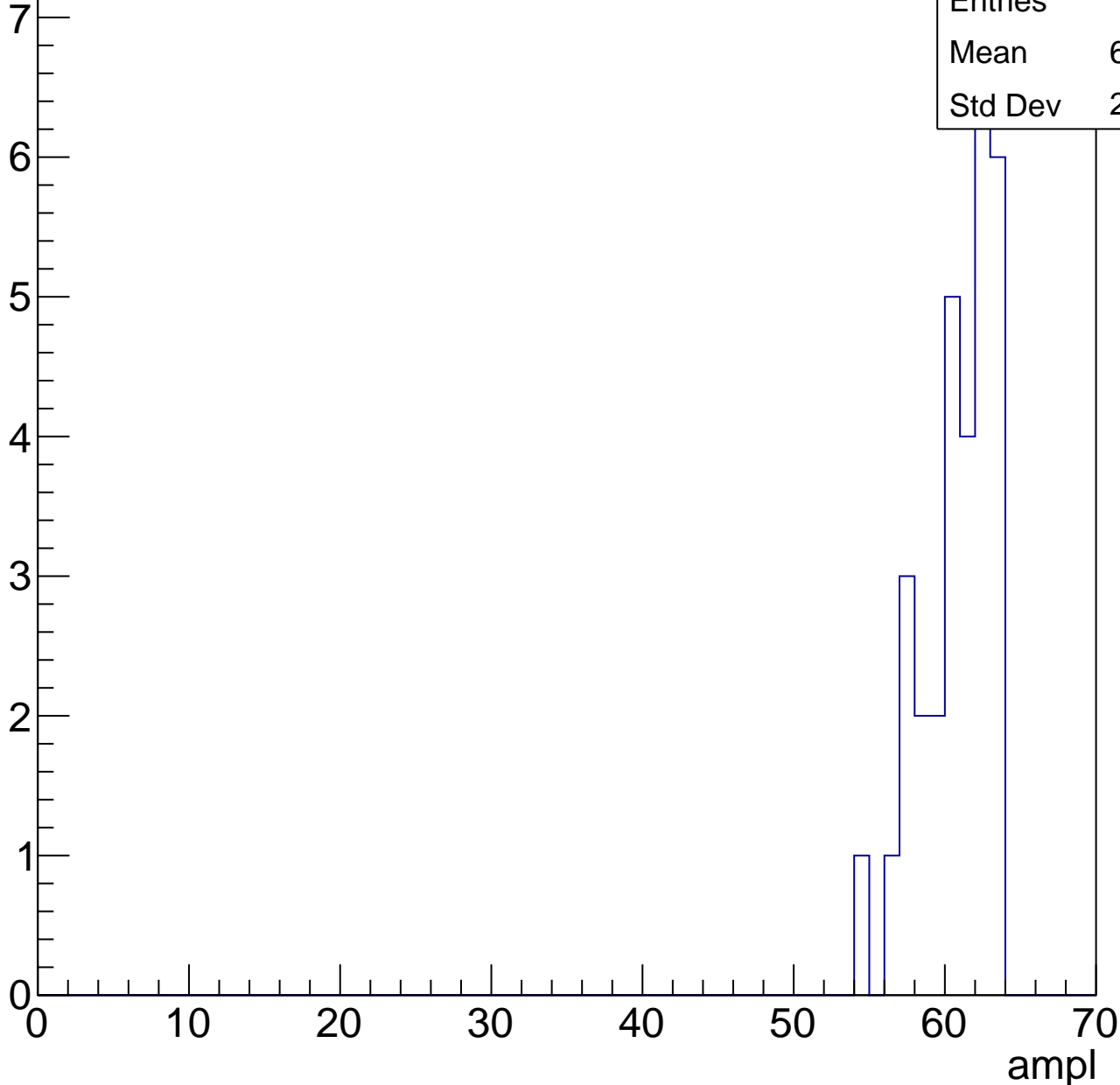


B1L100S, U25-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

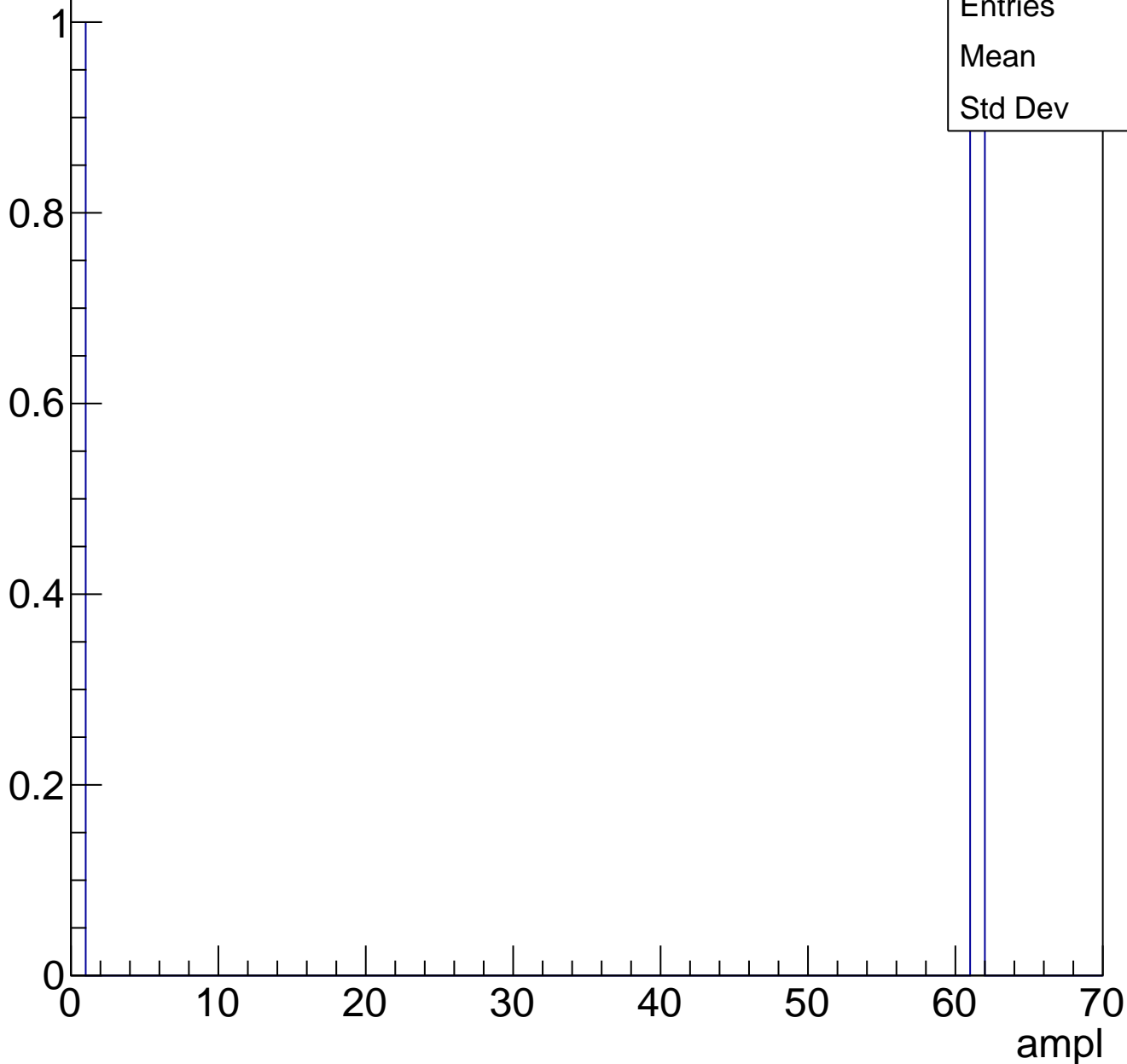
Entries	31
Mean	60.35
Std Dev	2.349



B1L100S, U25-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch2, adc0

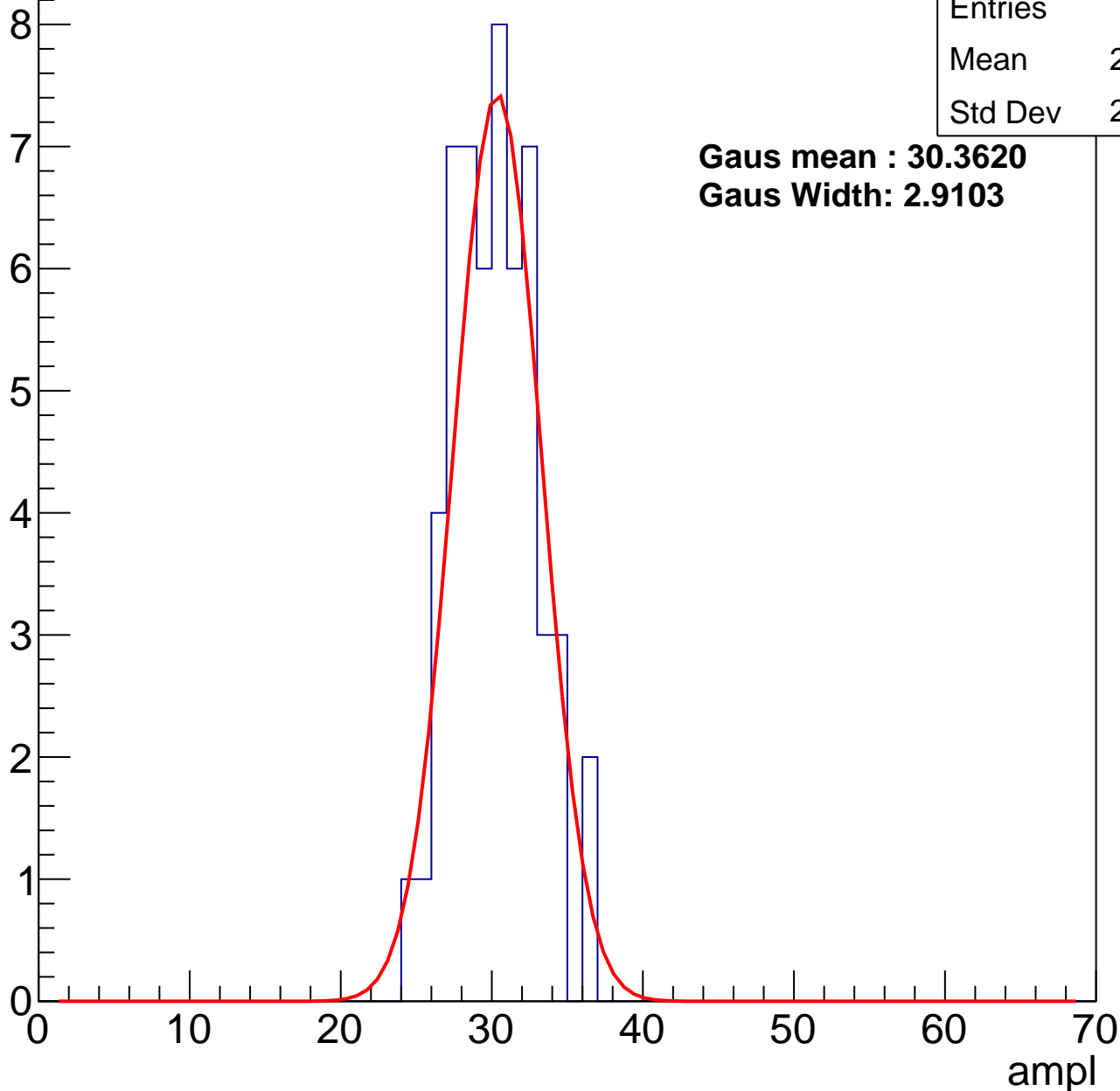
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.73
Std Dev	2.693

Gaus mean : 30.3620

Gaus Width: 2.9103



B1L100S, U25-ch2, adc1

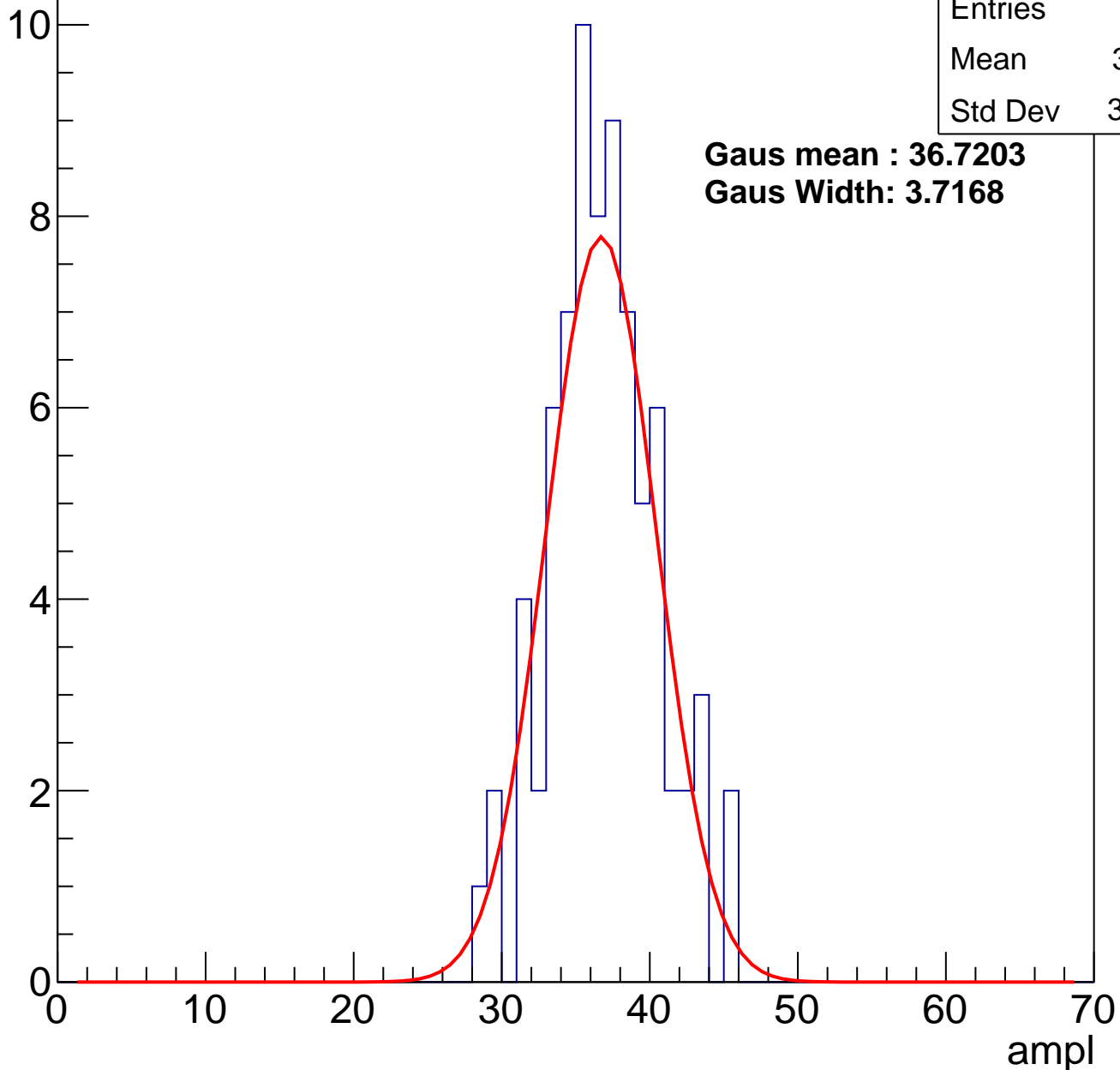
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	36.41
Std Dev	3.595

Gaus mean : 36.7203

Gaus Width: 3.7168

Entry



B1L100S, U25-ch2, adc2

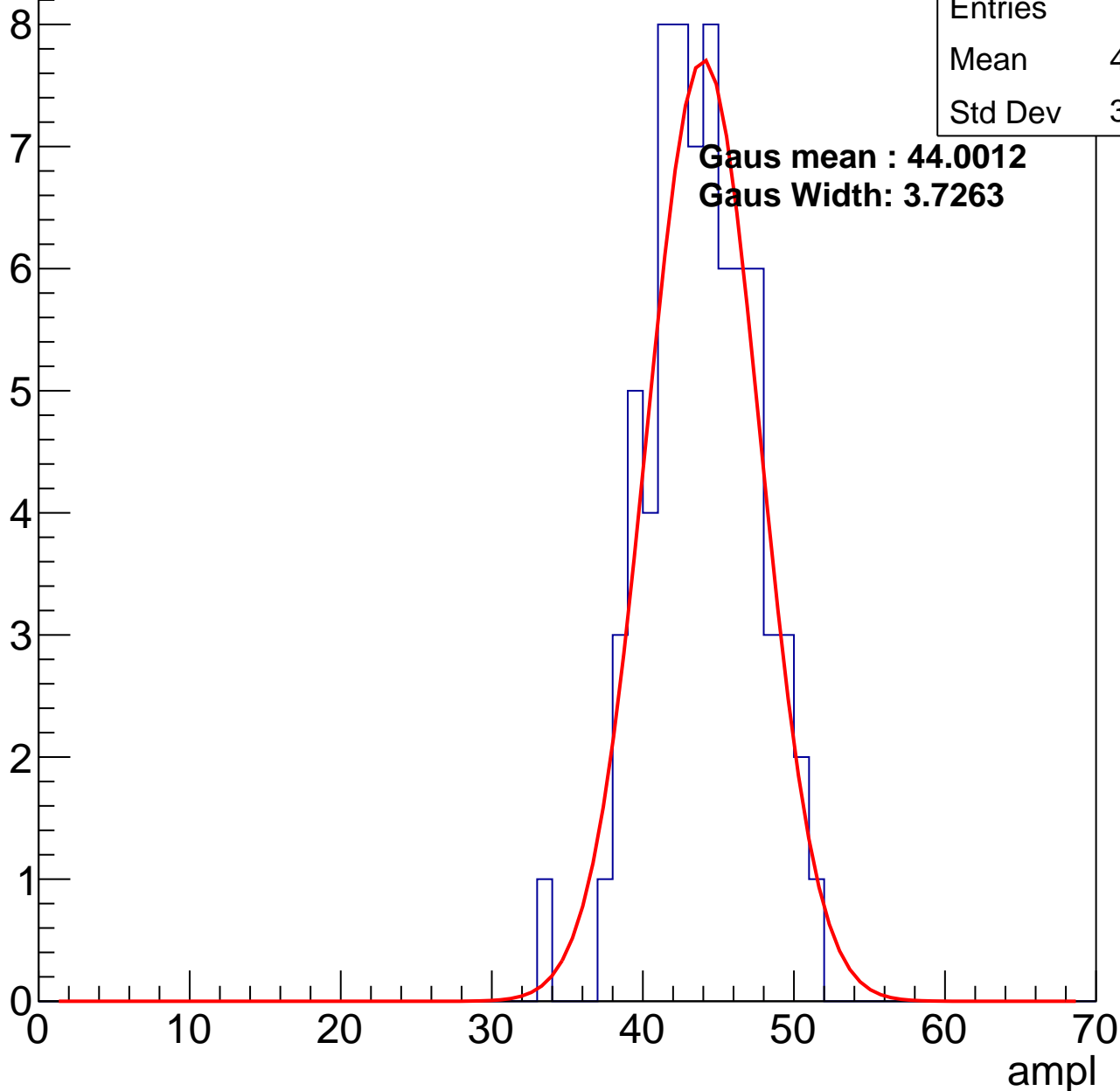
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.42
Std Dev	3.499

Gaus mean : 44.0012

Gaus Width: 3.7263

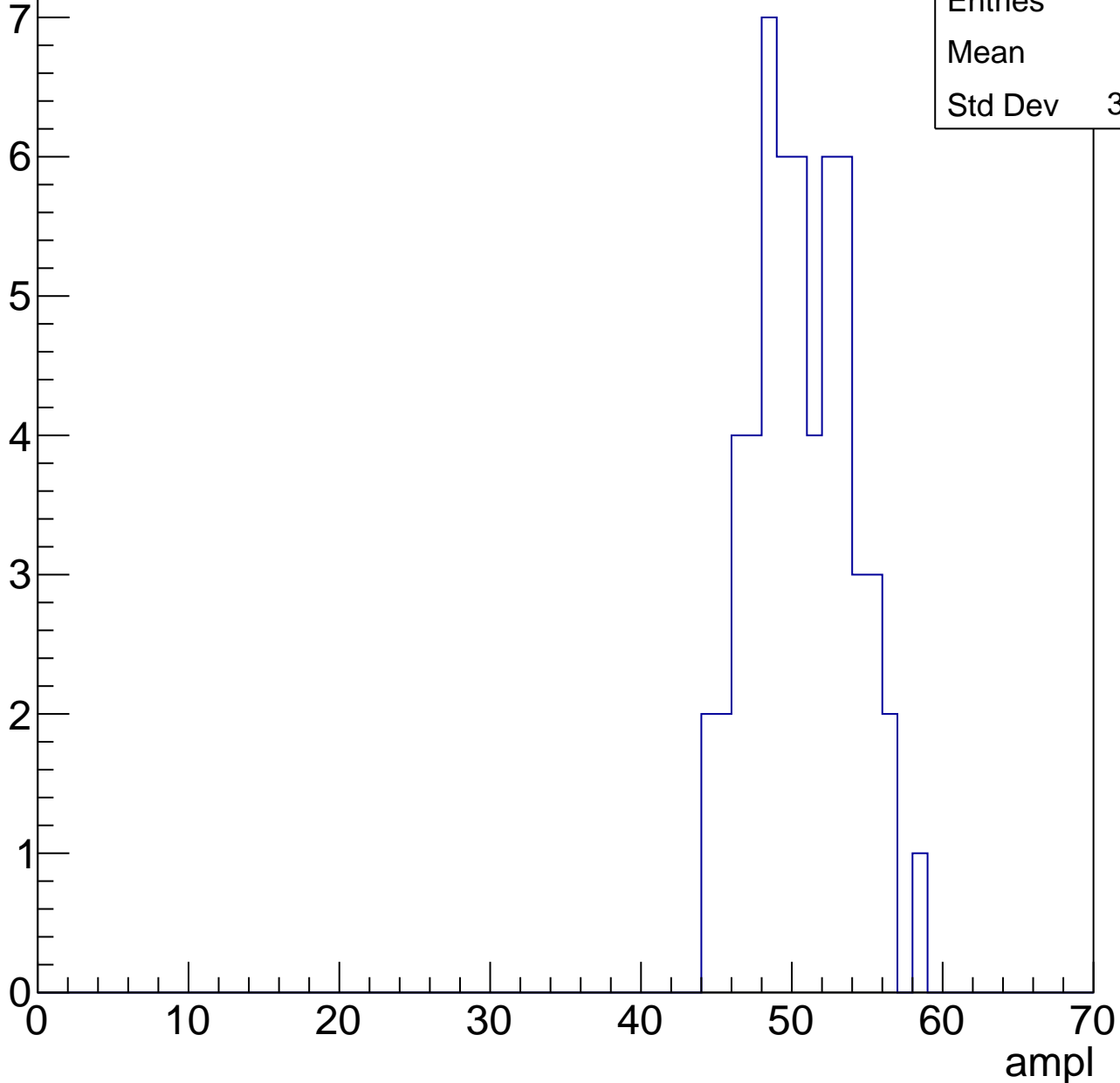


B1L100S, U25-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	50.2
Std Dev	3.259

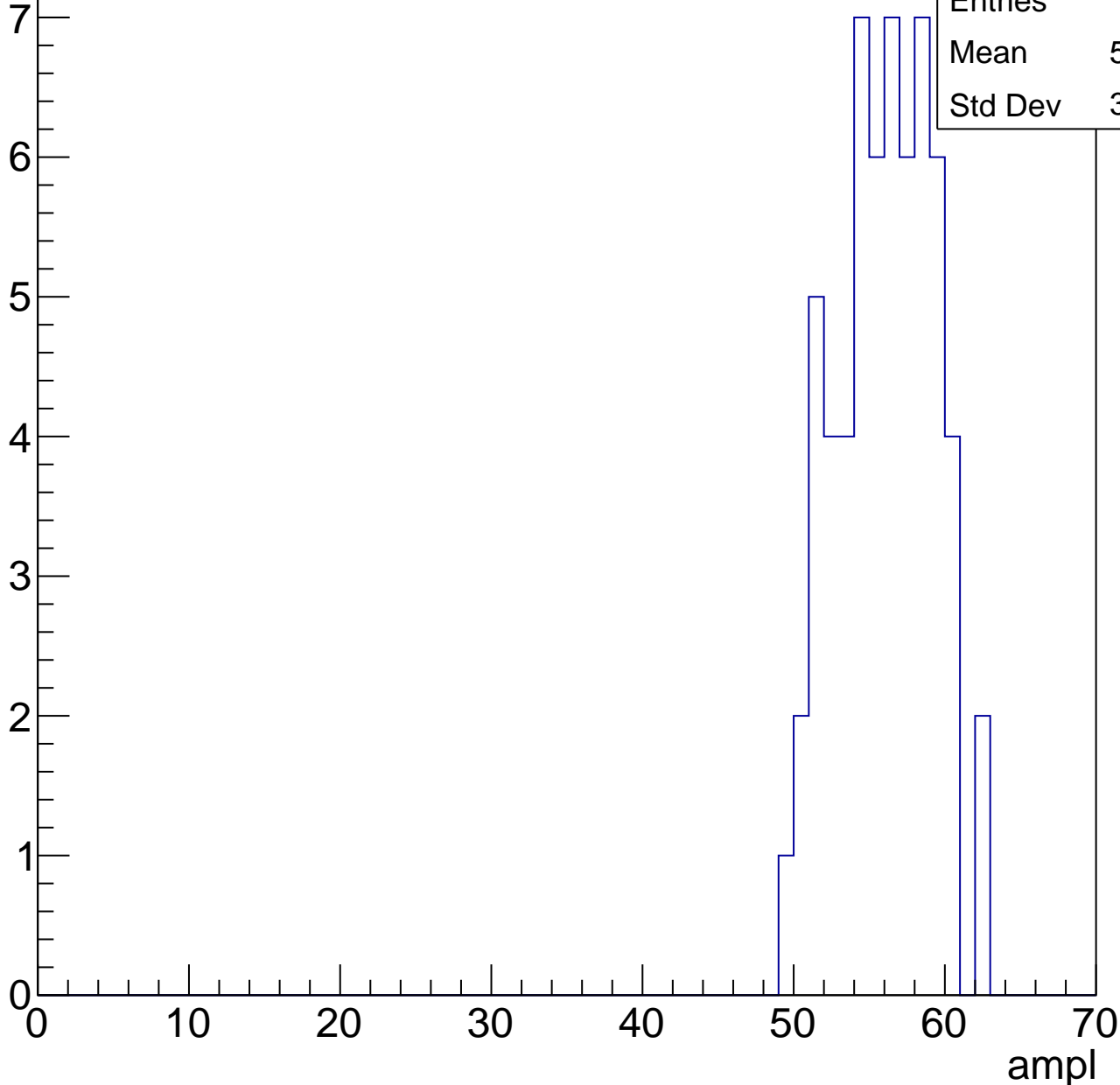


B1L100S, U25-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	55.57
Std Dev	3.118

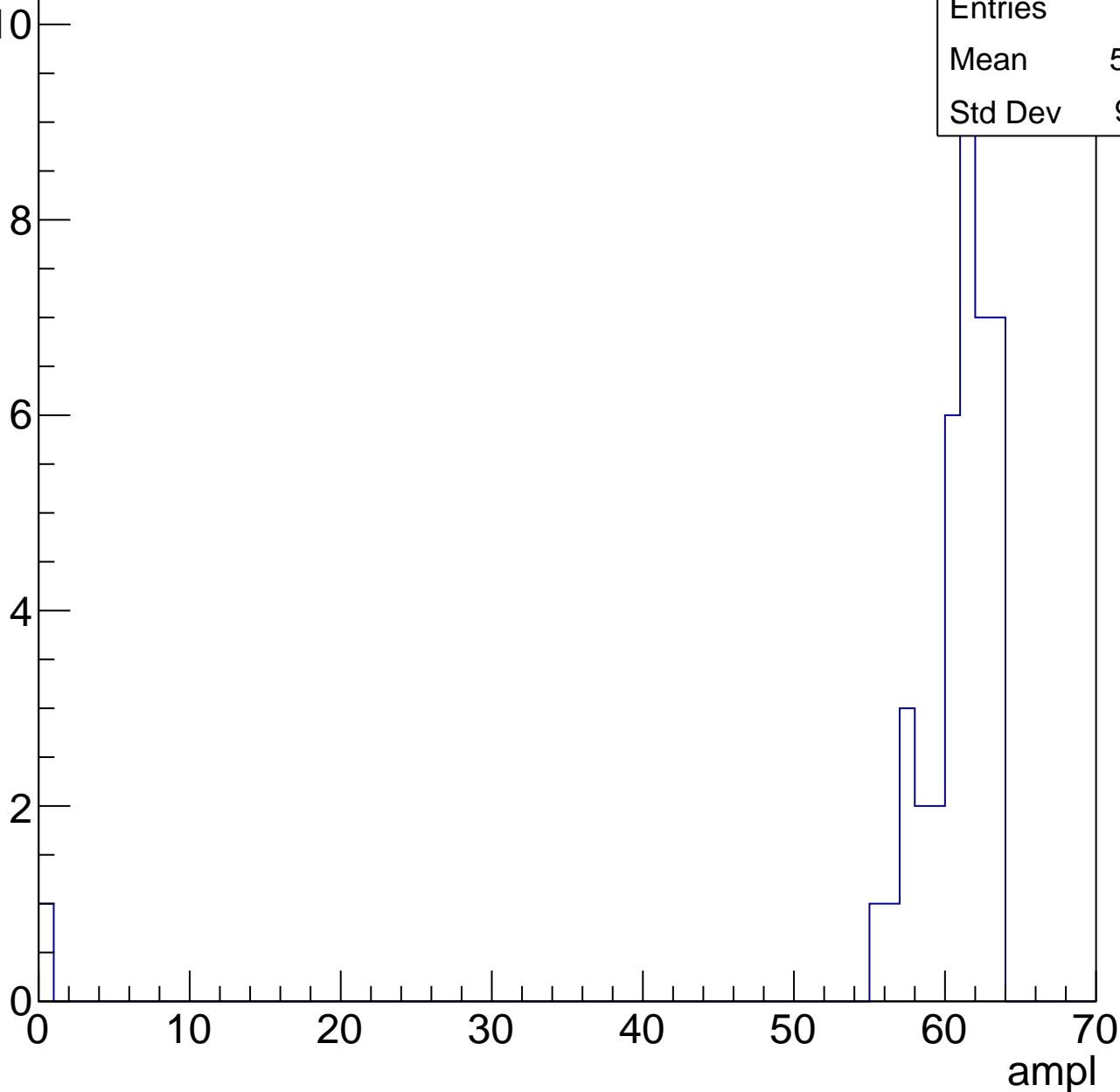


B1L100S, U25-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59.02
Std Dev	9.671



B1L100S, U25-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

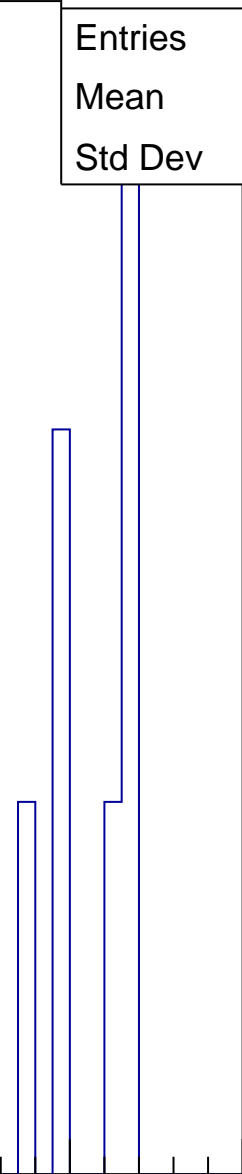
Entry

3
2.5
2
1.5
1
0.5
0

Entries	7
Mean	60.86
Std Dev	2.295

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch3, adc0

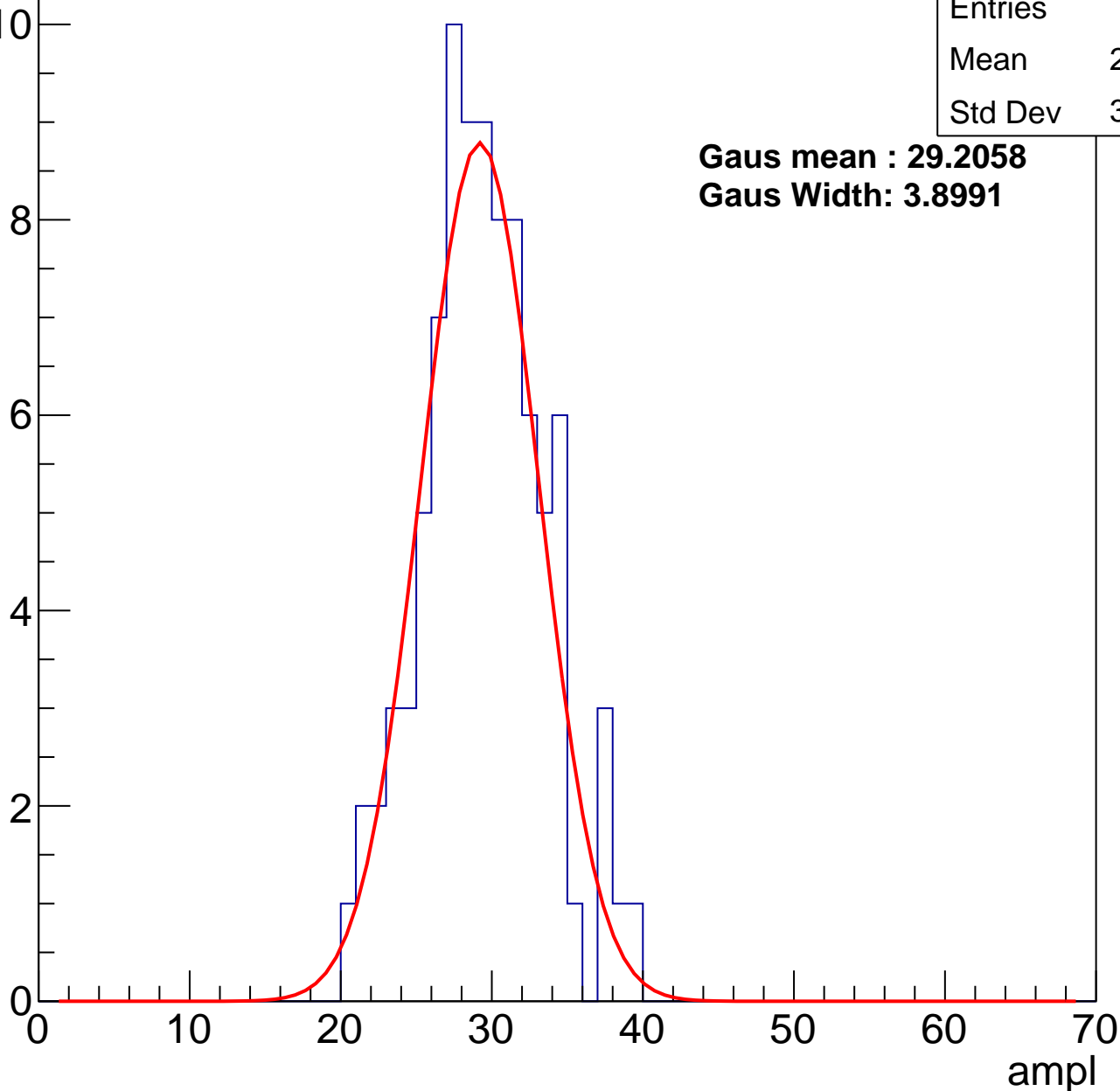
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	28.99
Std Dev	3.965

Gaus mean : 29.2058

Gaus Width: 3.8991



B1L100S, U25-ch3, adc1

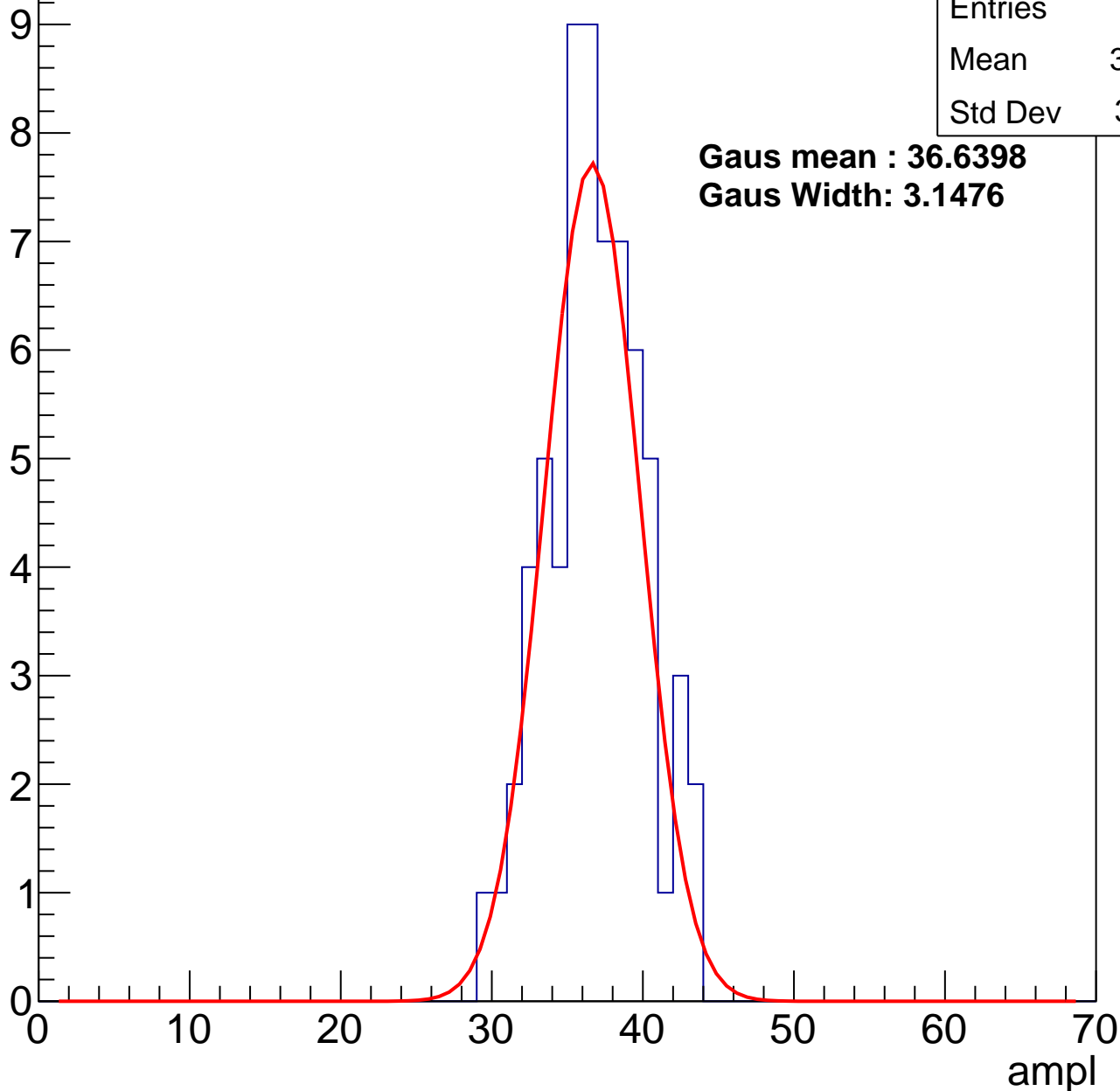
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.38
Std Dev	3.161

Gaus mean : 36.6398

Gaus Width: 3.1476



B1L100S, U25-ch3, adc2

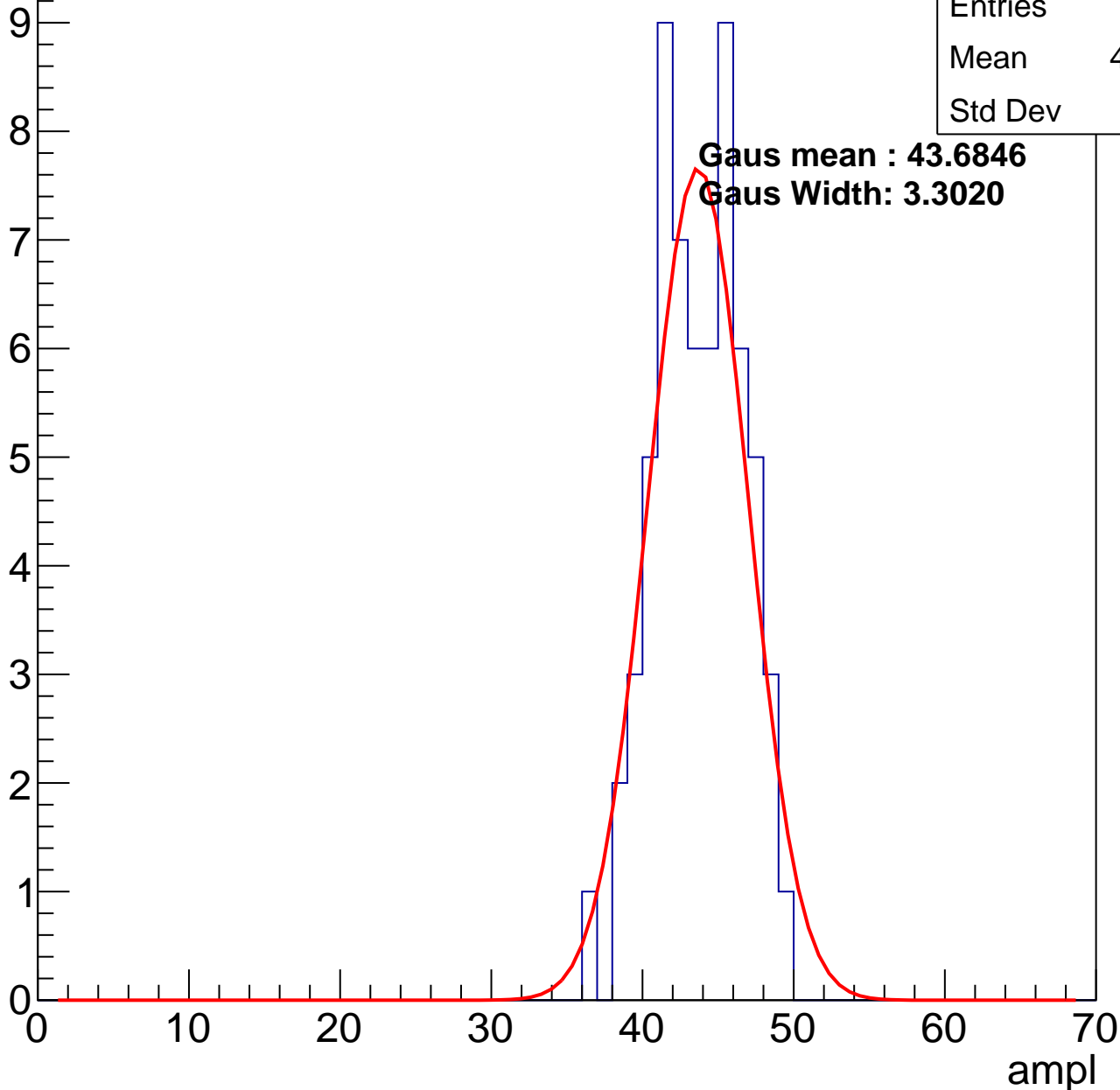
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.22
Std Dev	2.87

Gaus mean : 43.6846

Gaus Width: 3.3020

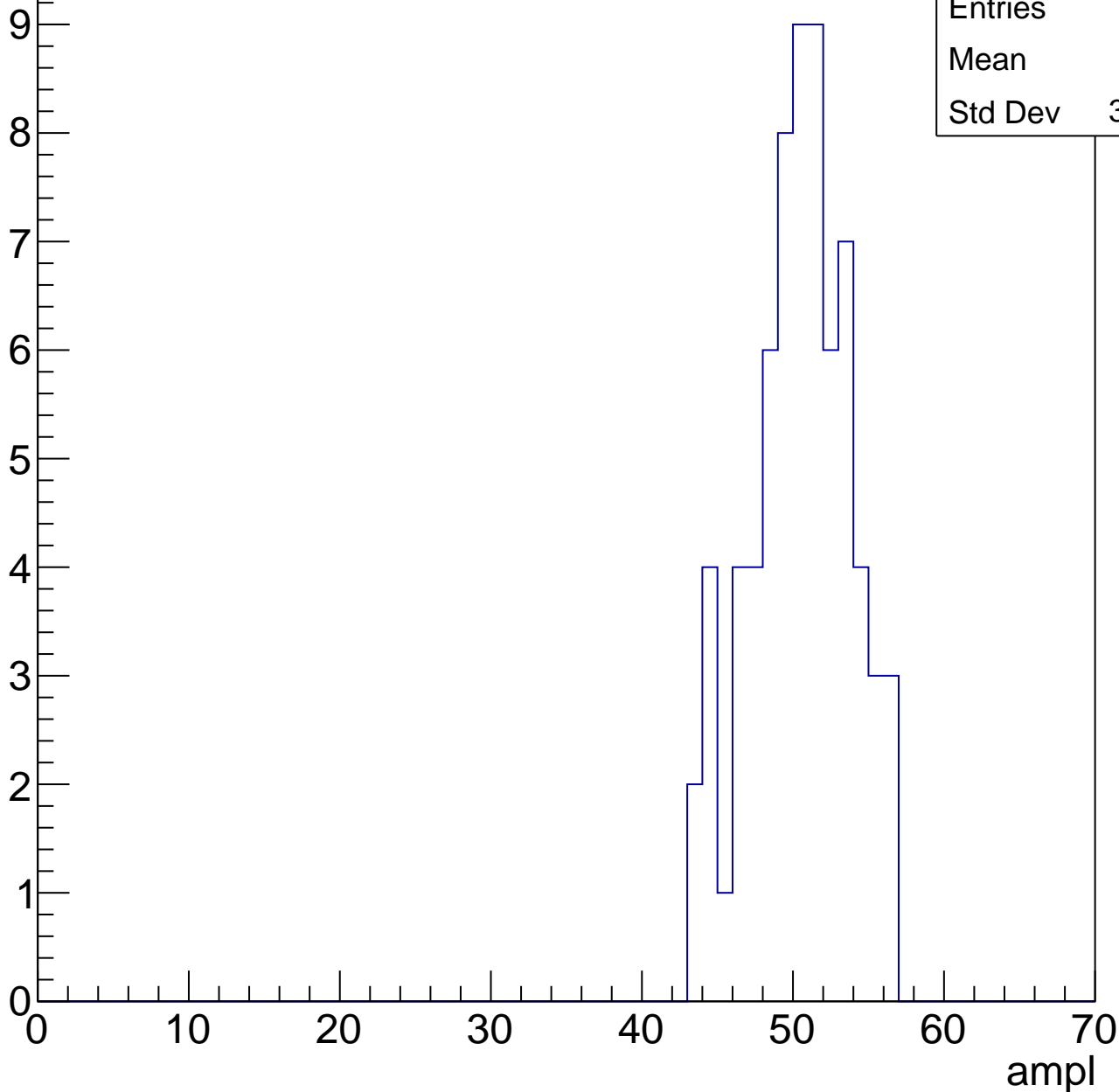


B1L100S, U25-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	50
Std Dev	3.256



B1L100S, U25-ch3, adc4

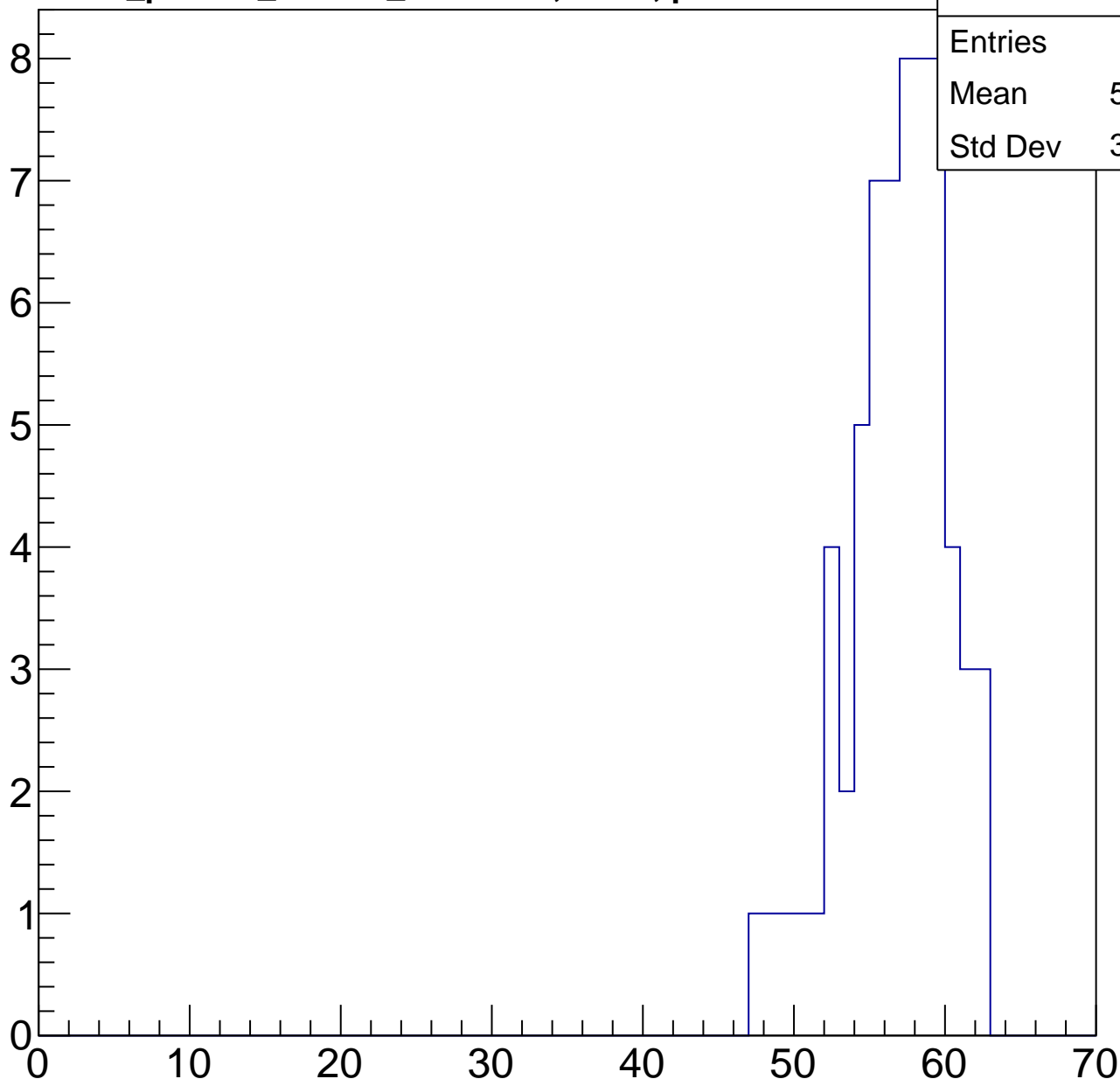
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	64
Mean	56.36
Std Dev	3.342

ampl

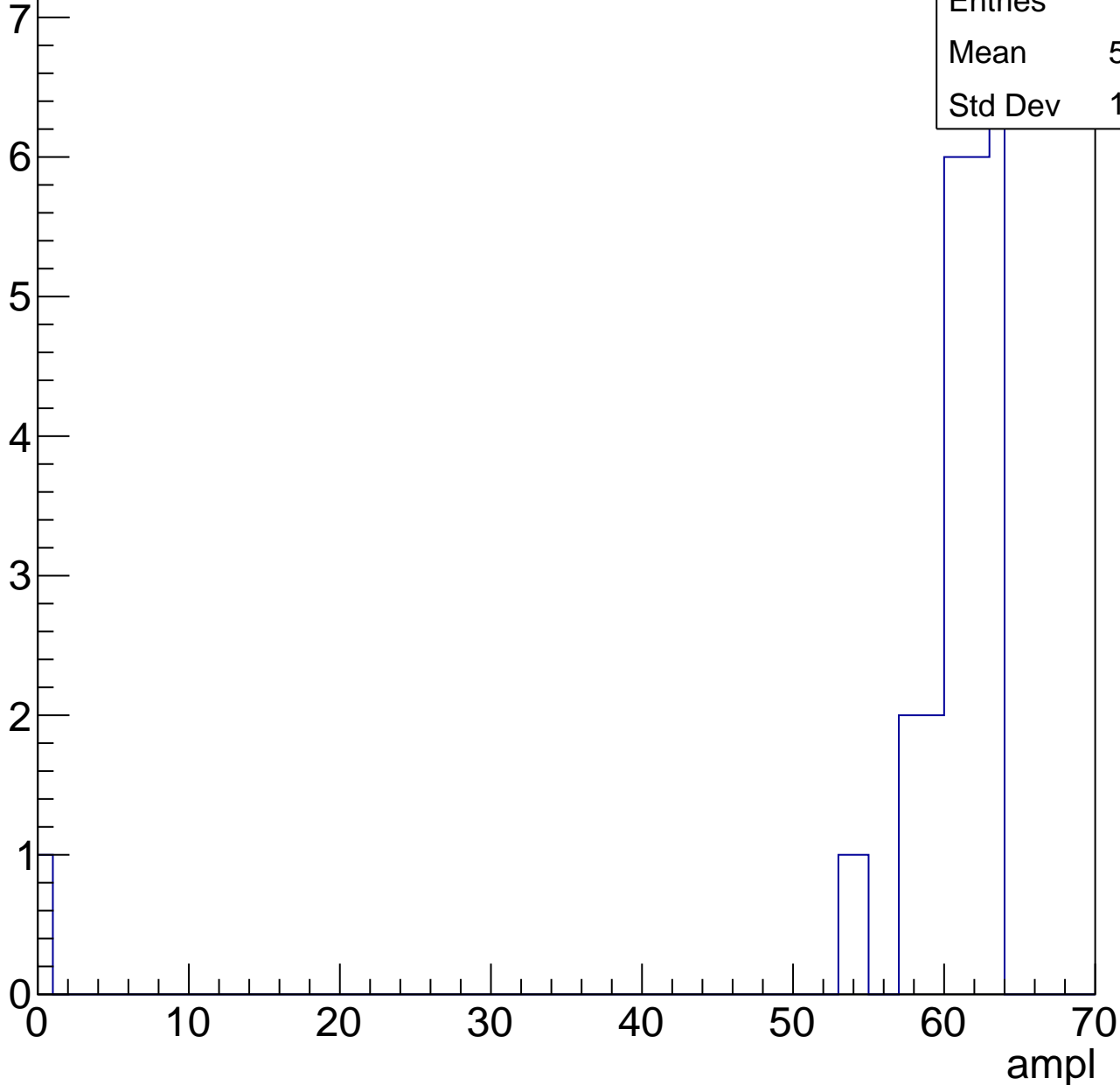


B1L100S, U25-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

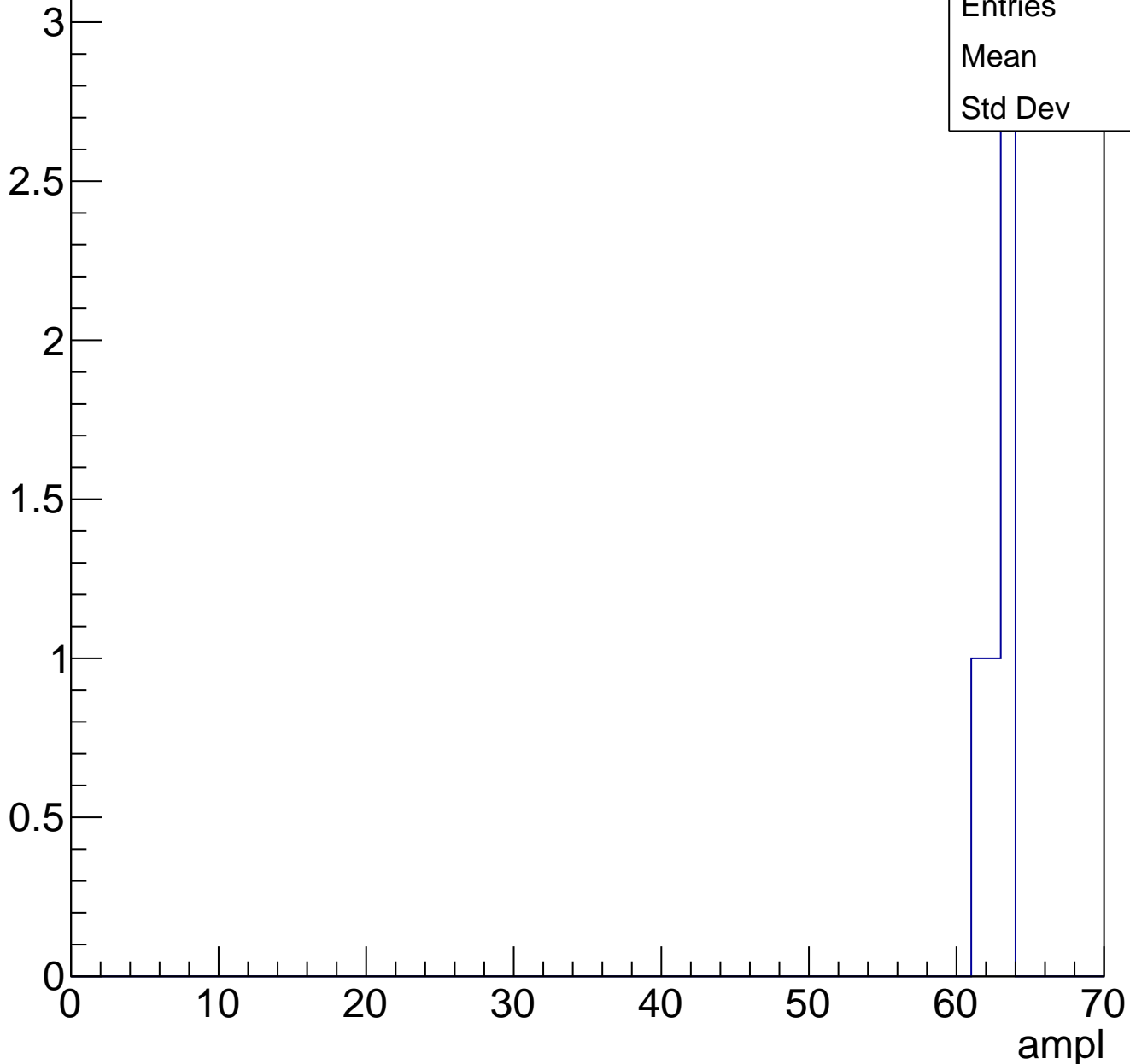
Entries	34
Mean	58.65
Std Dev	10.49



B1L100S, U25-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch4, adc0

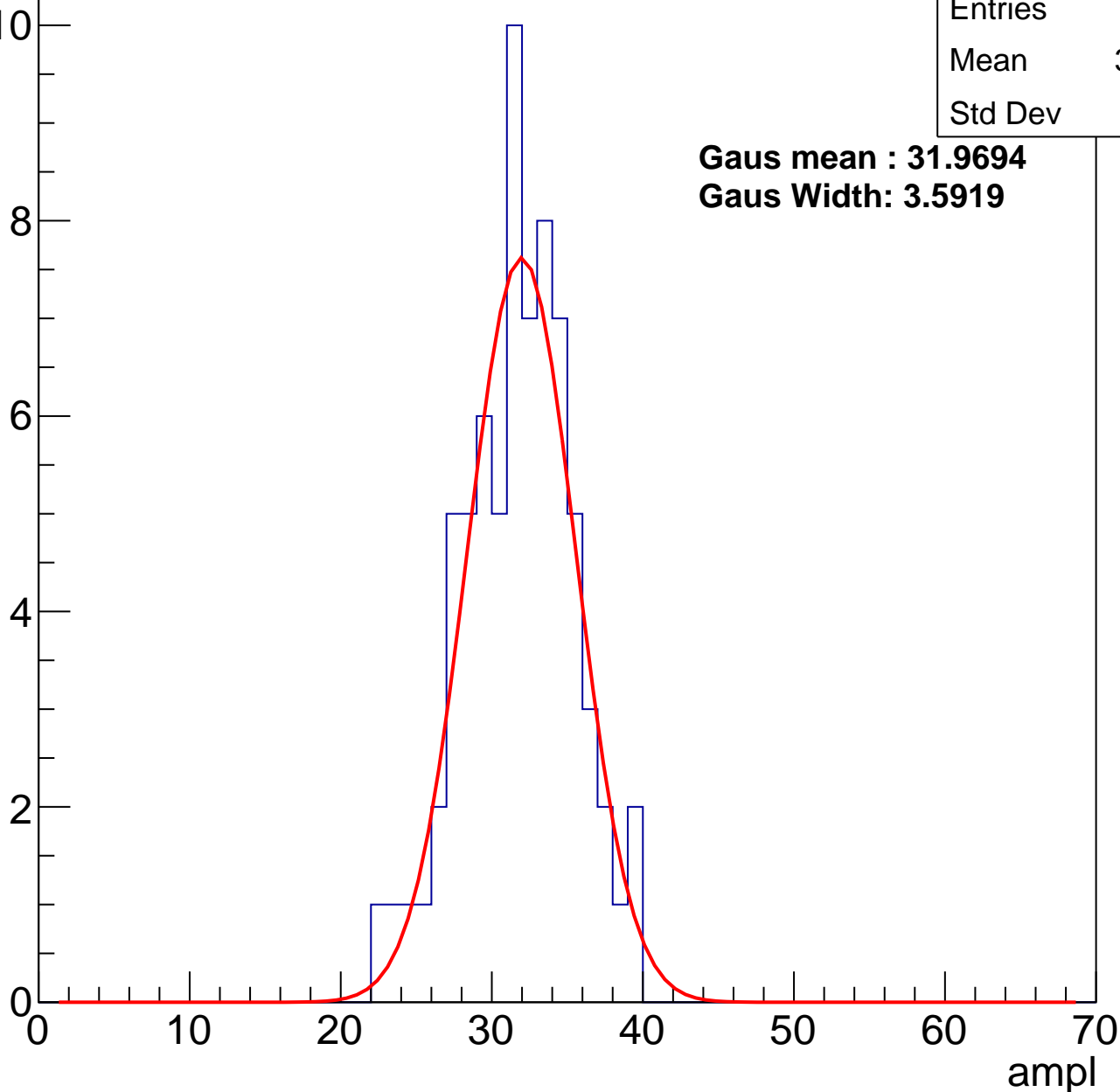
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	31.31
Std Dev	3.6

Gaus mean : 31.9694

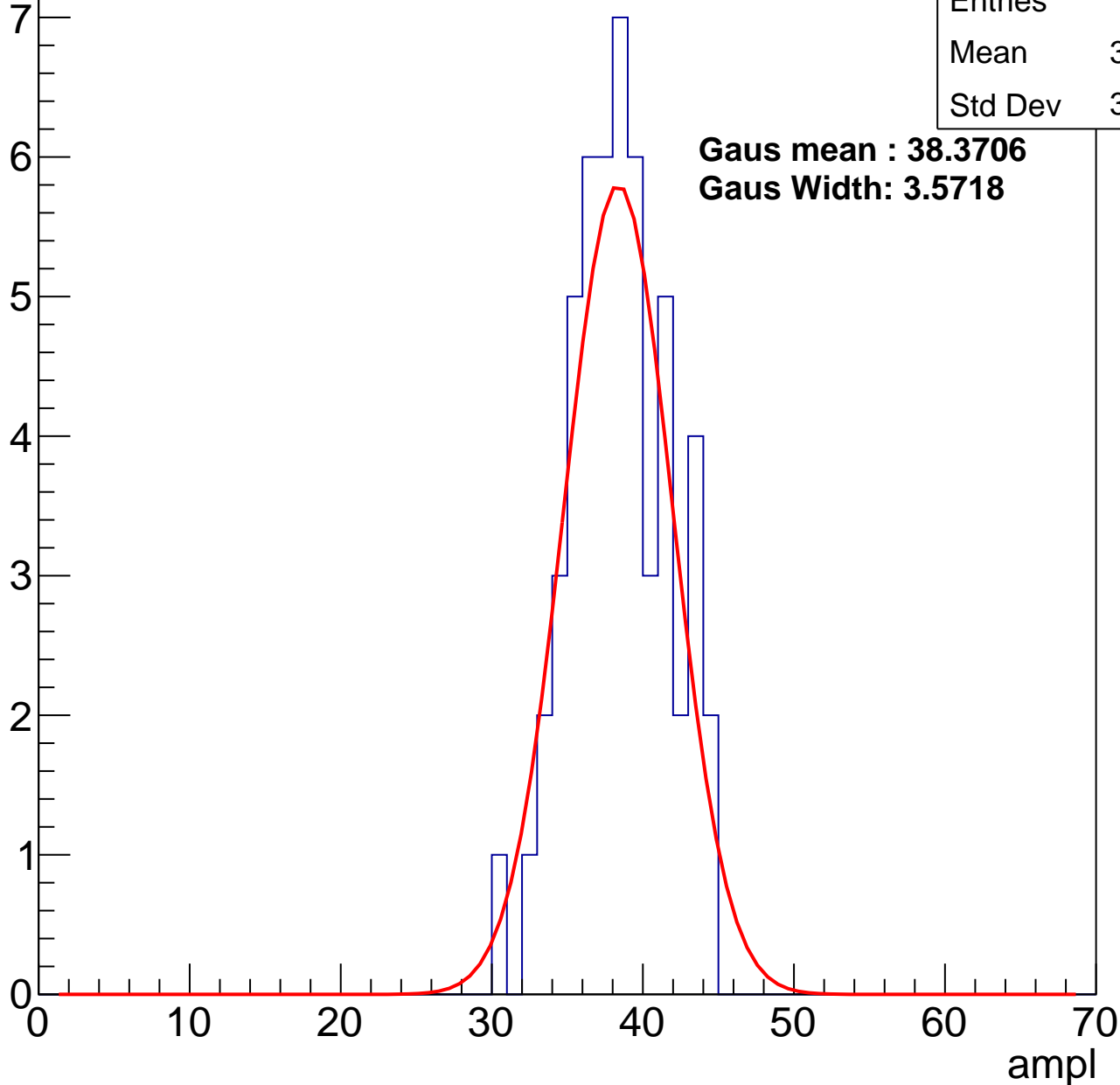
Gaus Width: 3.5919



B1L100S, U25-ch4, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch4, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	94
Mean	45.07
Std Dev	4.072

Gaus mean : 45.4667

Gaus Width: 4.1275

Entry

10

8

6

4

2

0

0

10

20

30

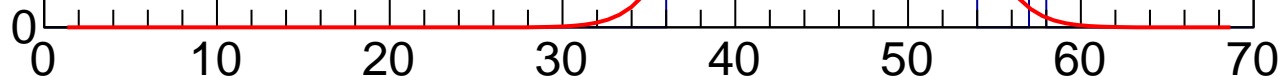
40

50

60

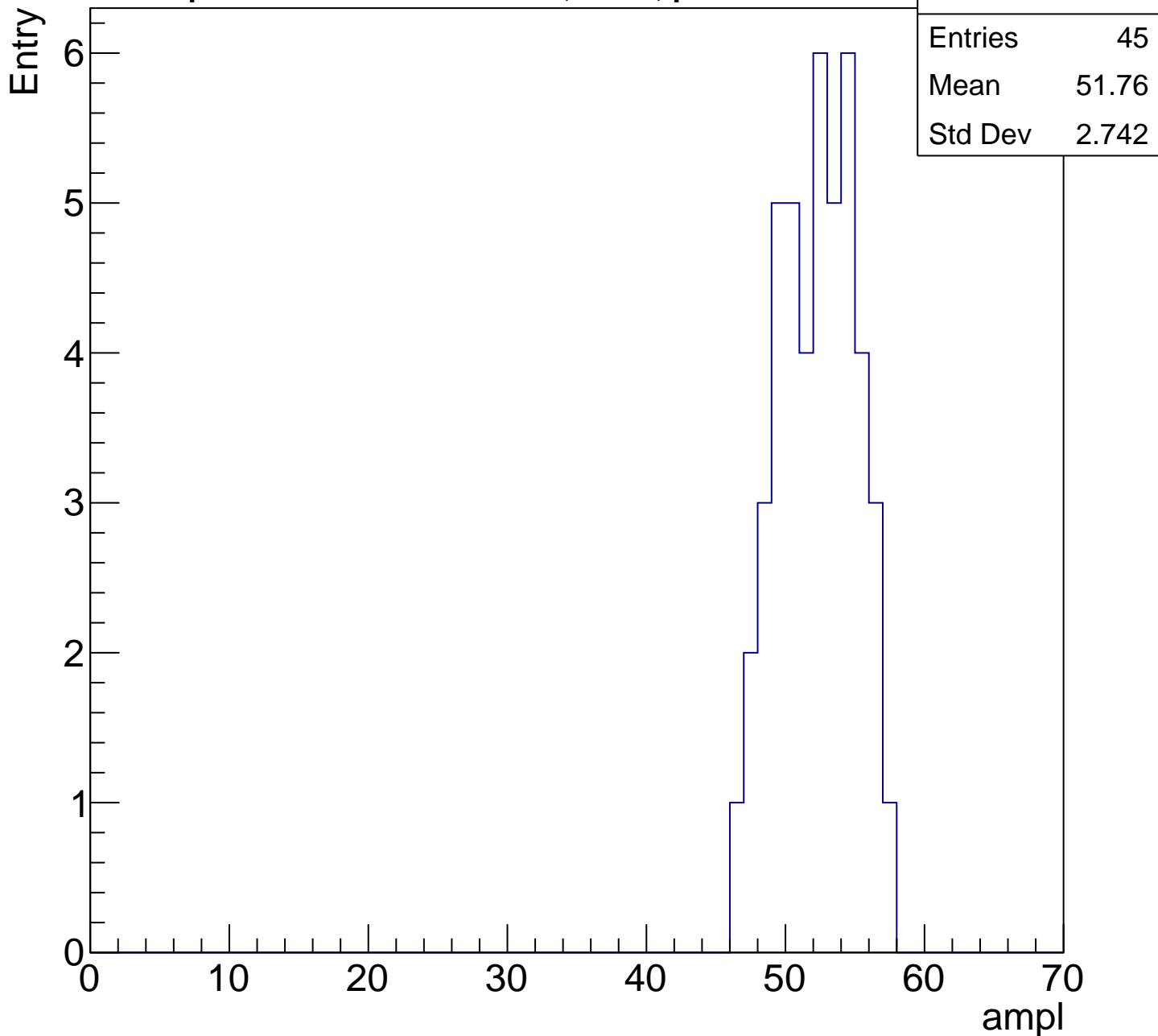
70

ampl



B1L100S, U25-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	60
Mean	57.32
Std Dev	2.992

ampl

0 10 20 30 40 50 60 70

1

3

4

6

7

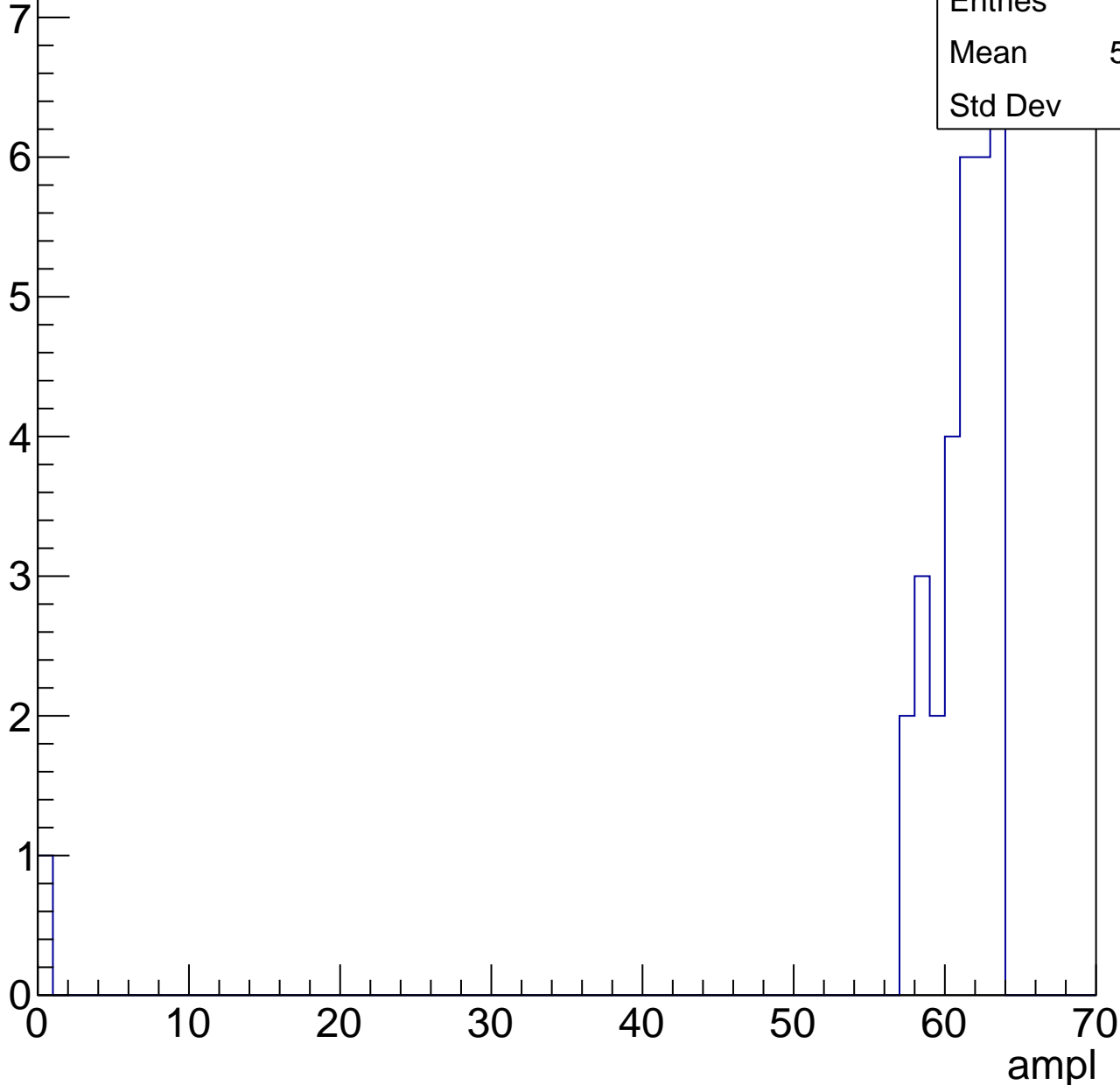
8

B1L100S, U25-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.87
Std Dev	10.9



B1L100S, U25-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch5, adc0

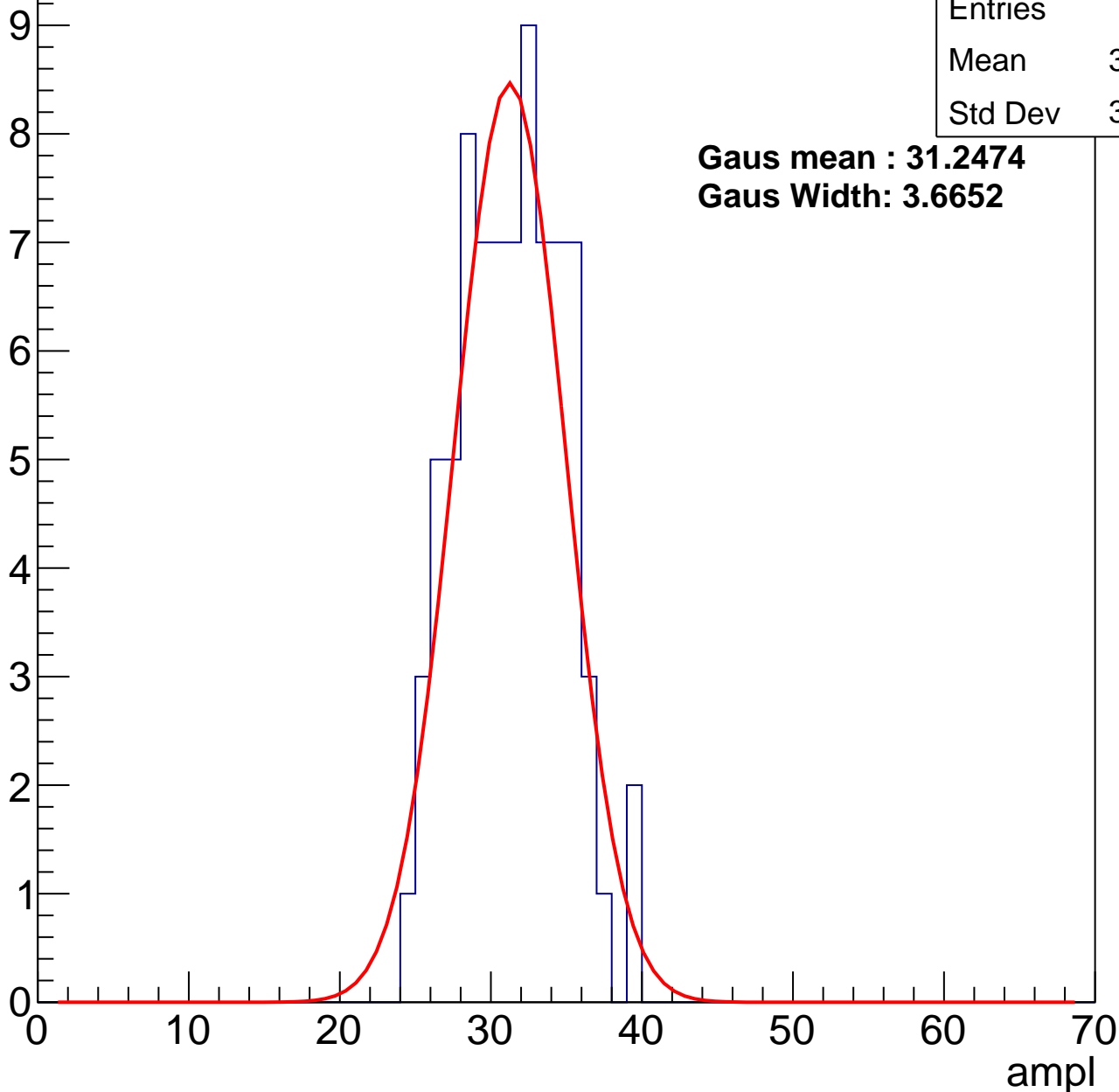
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.92
Std Dev	3.412

Gaus mean : 31.2474

Gaus Width: 3.6652



B1L100S, U25-ch5, adc1

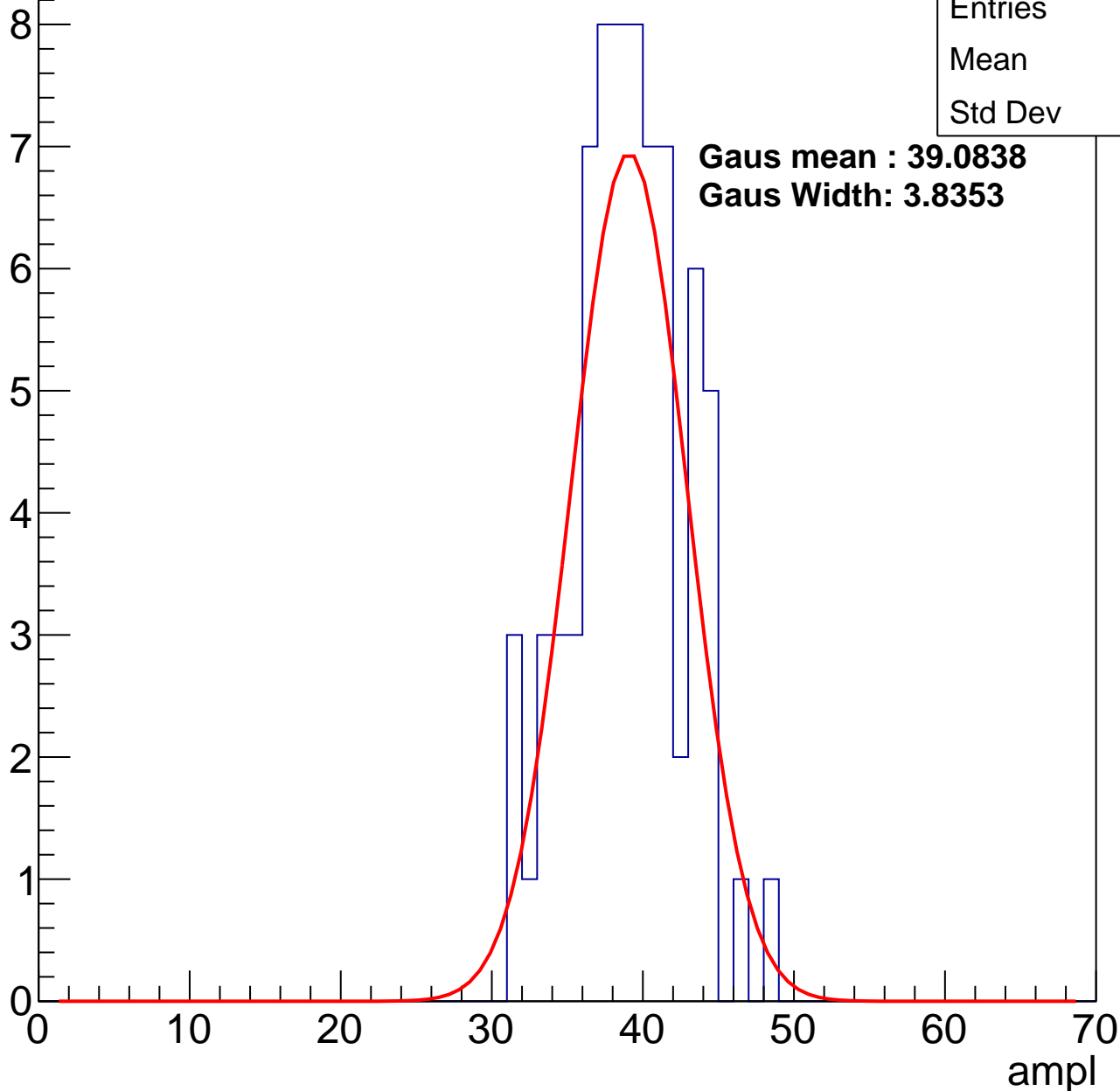
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	38.6
Std Dev	3.64

Gaus mean : 39.0838

Gaus Width: 3.8353



B1L100S, U25-ch5, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	45.36
Std Dev	3.345

Gaus mean : 45.9135

Gaus Width: 3.8047

10

8

6

4

2

0

ampl

0

10

20

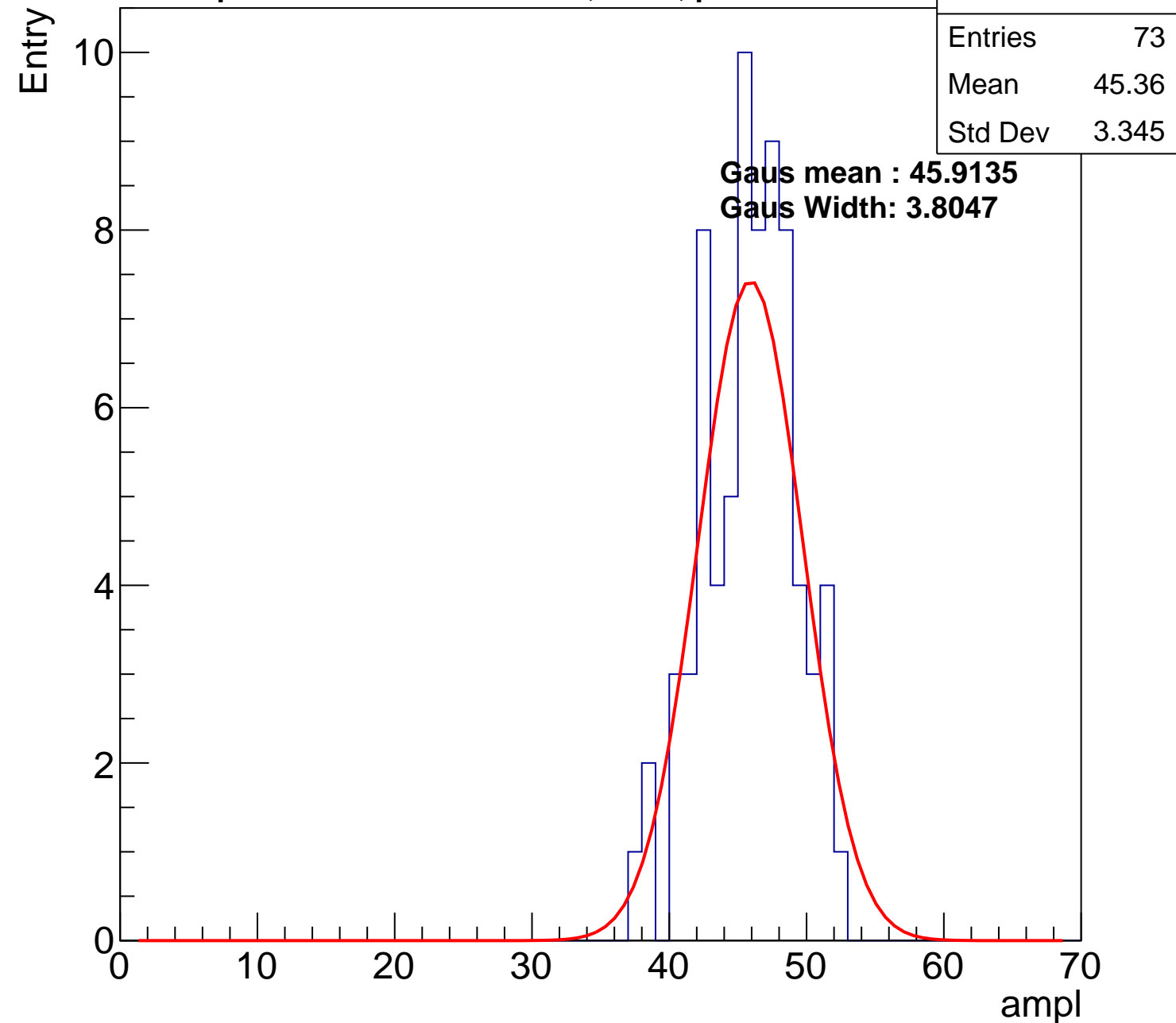
30

40

50

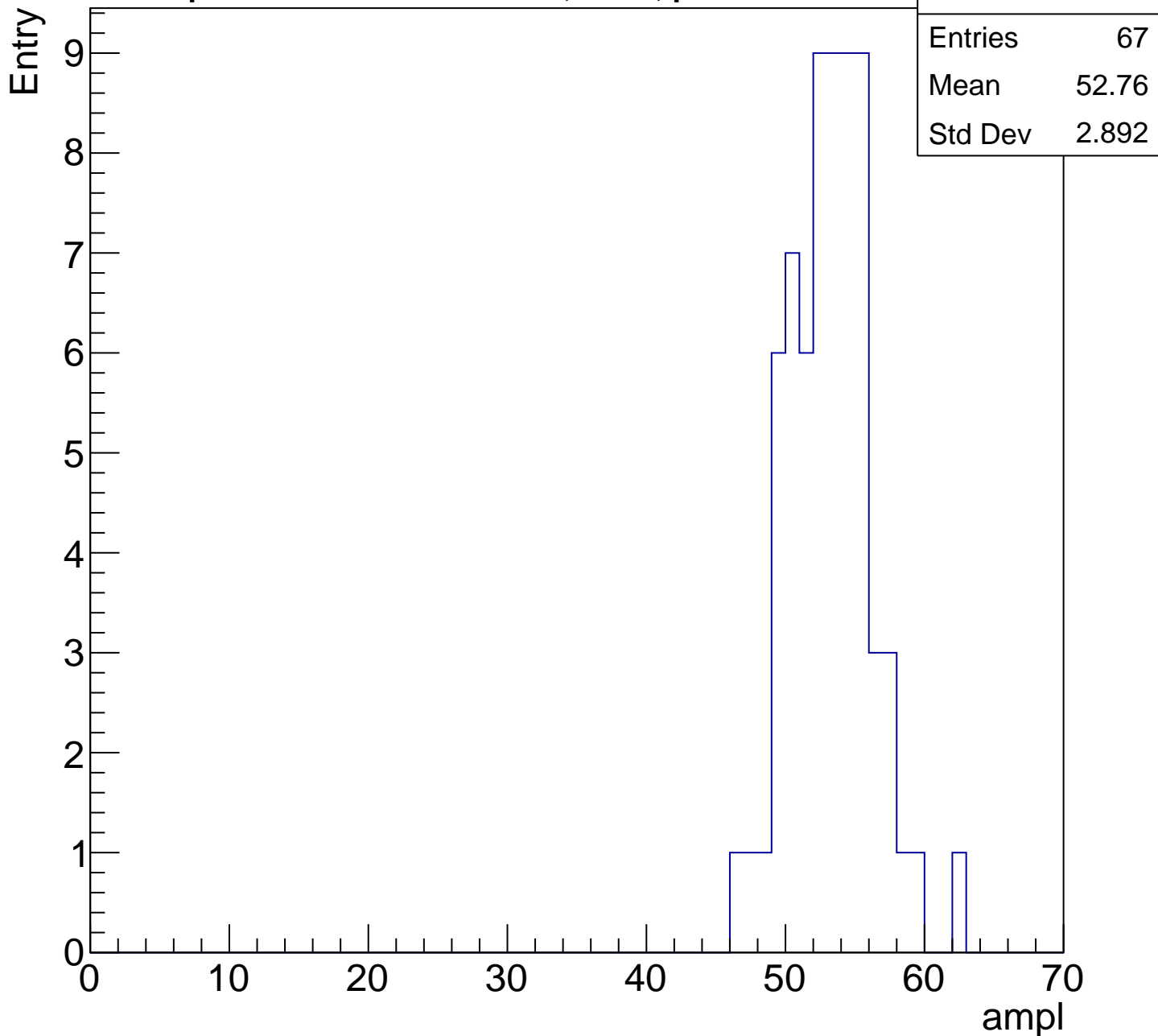
60

70



B1L100S, U25-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch5, adc4

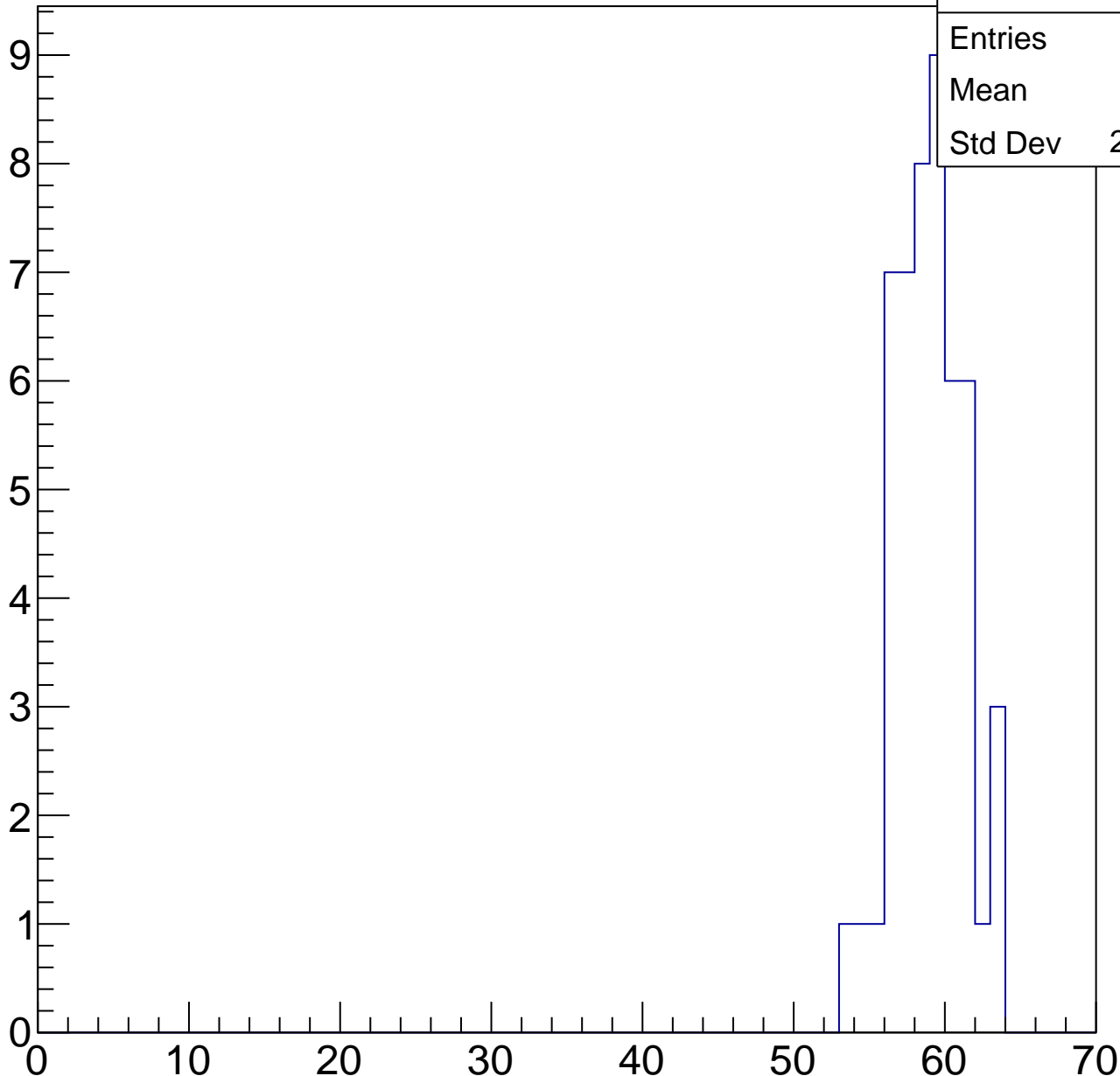
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.5
Std Dev	2.238

ampl

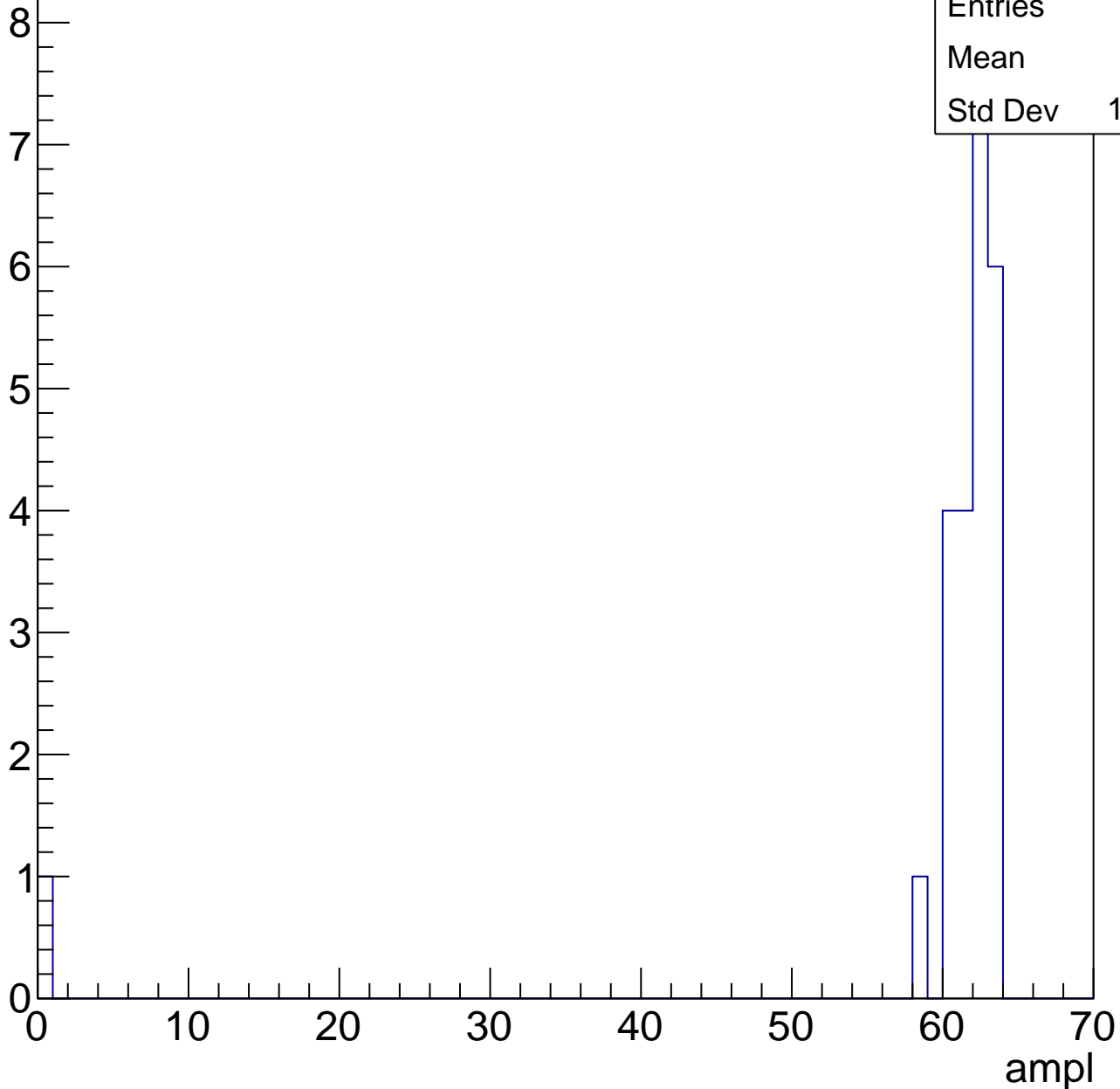


B1L100S, U25-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	59
Std Dev	12.37



B1L100S, U25-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

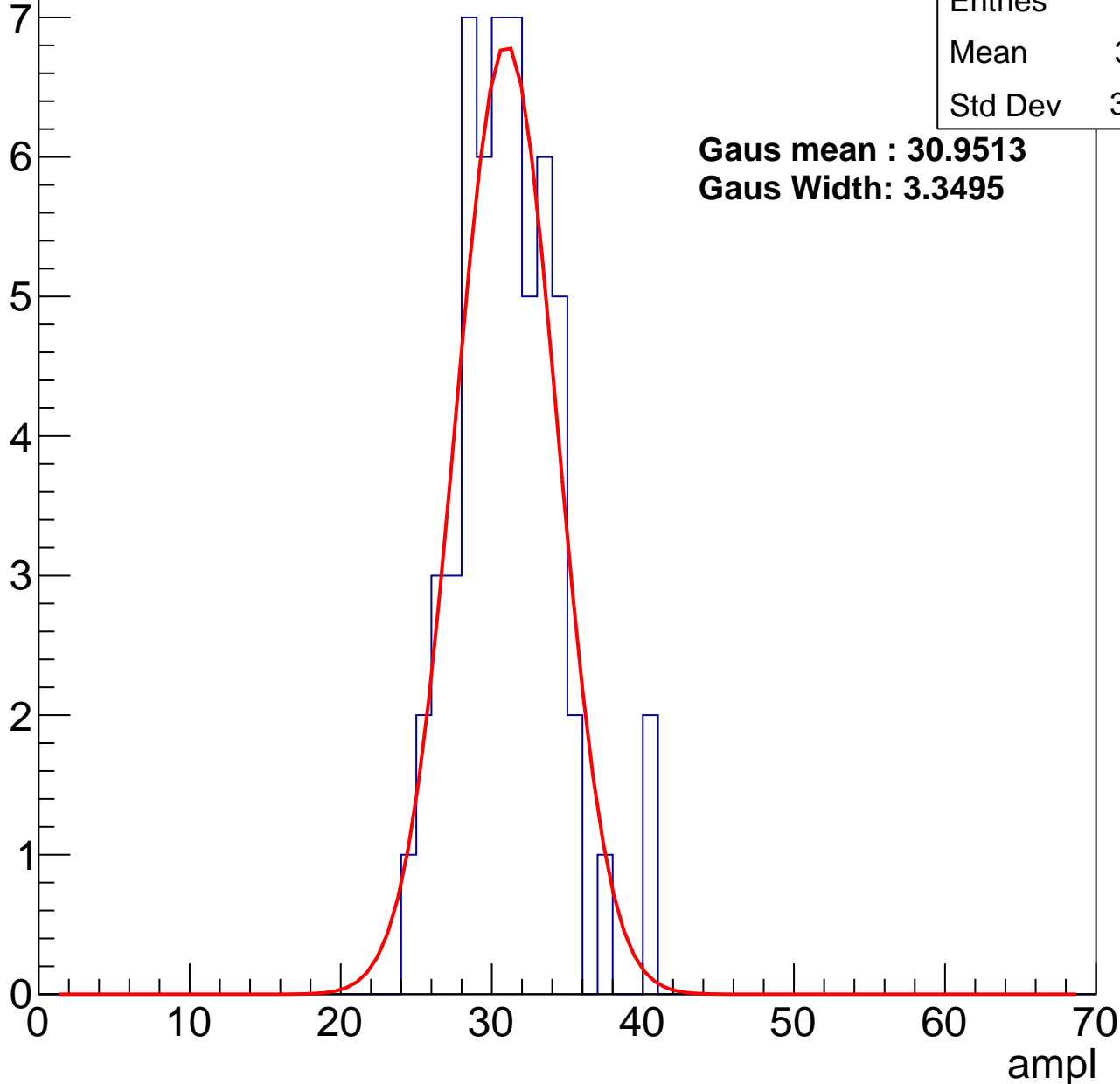
B1L100S, U25-ch6, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	30.61
Std Dev	3.334

Gaus mean : 30.9513
Gaus Width: 3.3495



B1L100S, U25-ch6, adc1

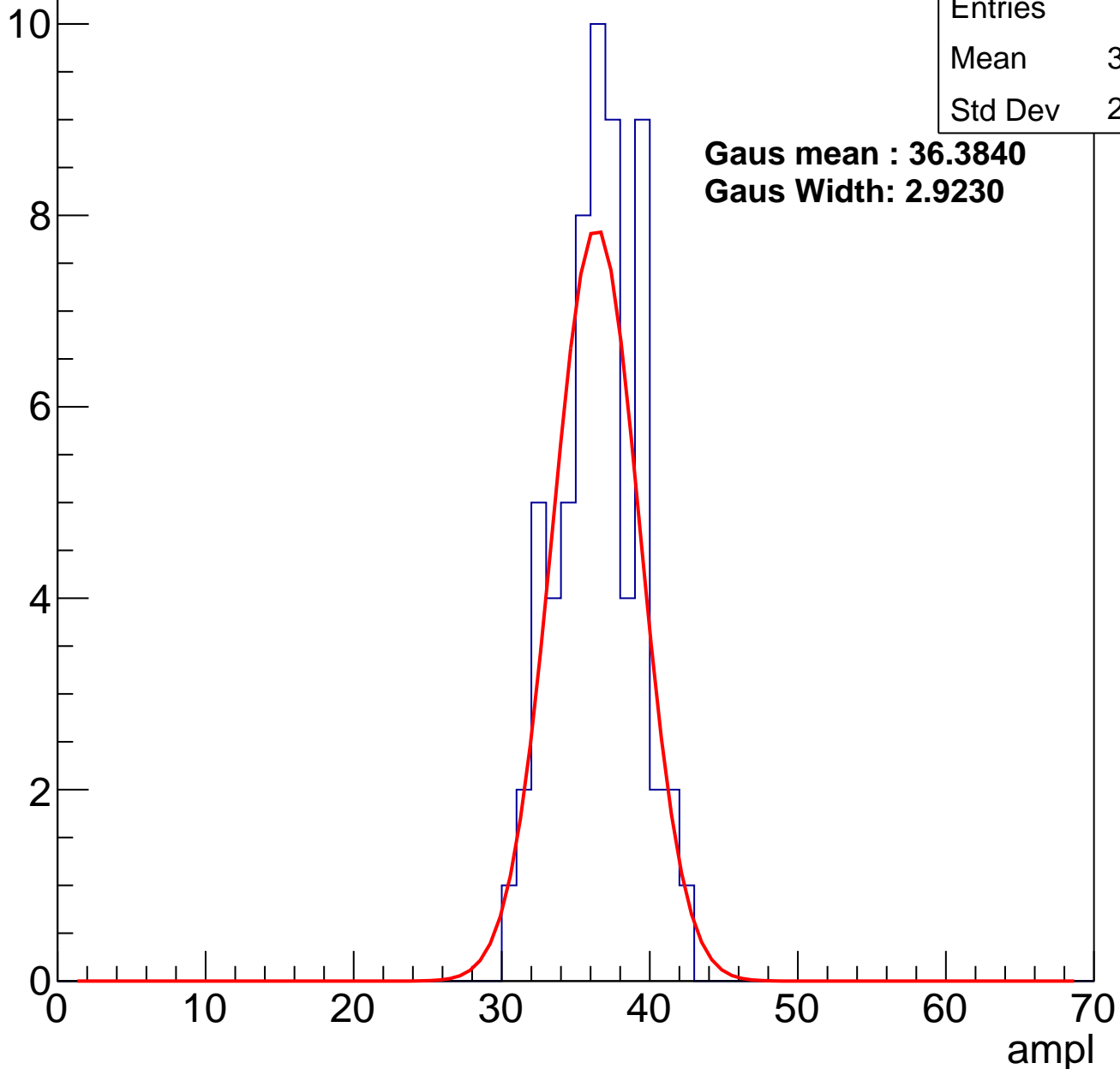
calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	36.03
Std Dev	2.706

Gaus mean : 36.3840

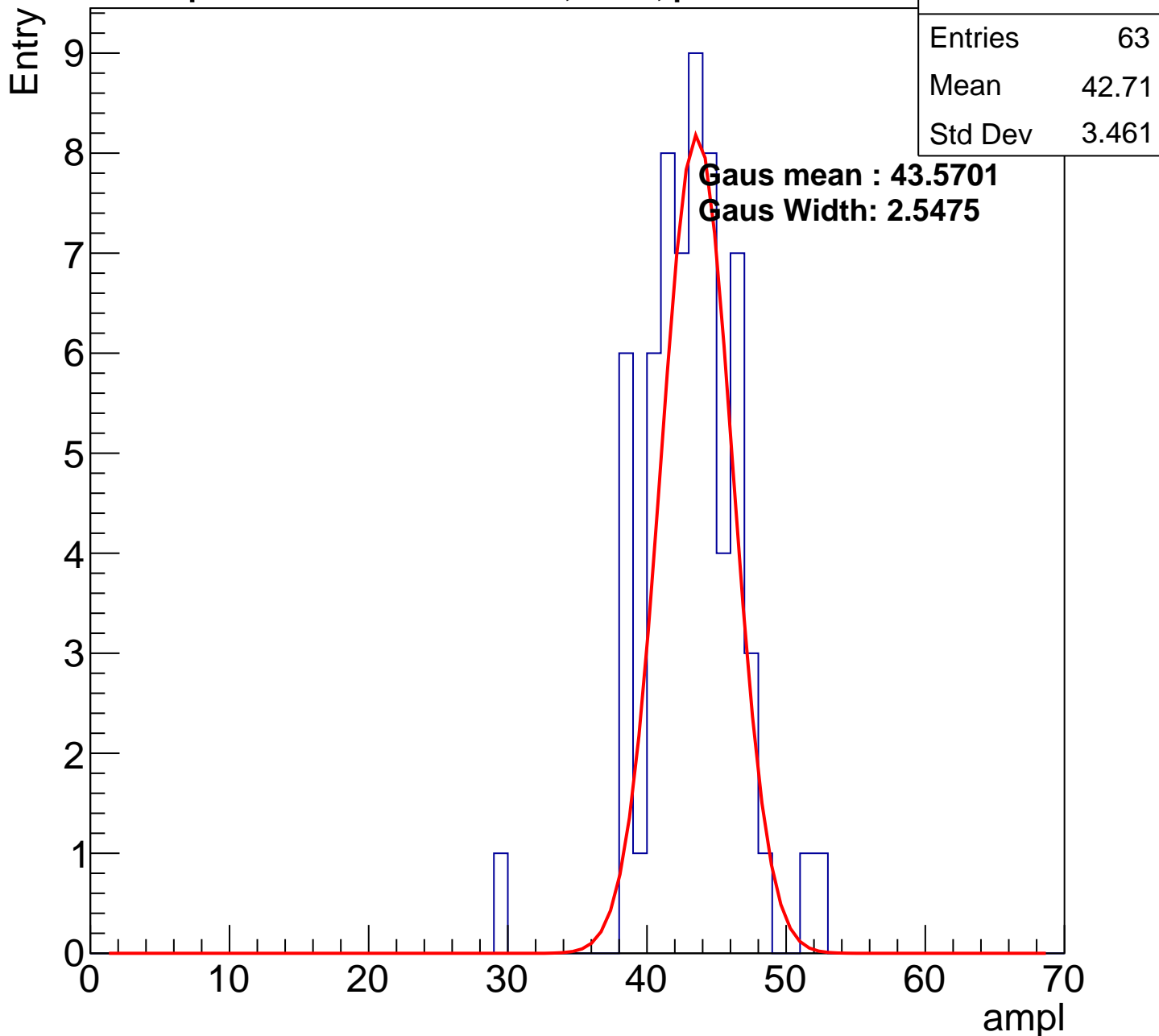
Gaus Width: 2.9230

Entry



B1L100S, U25-ch6, adc2

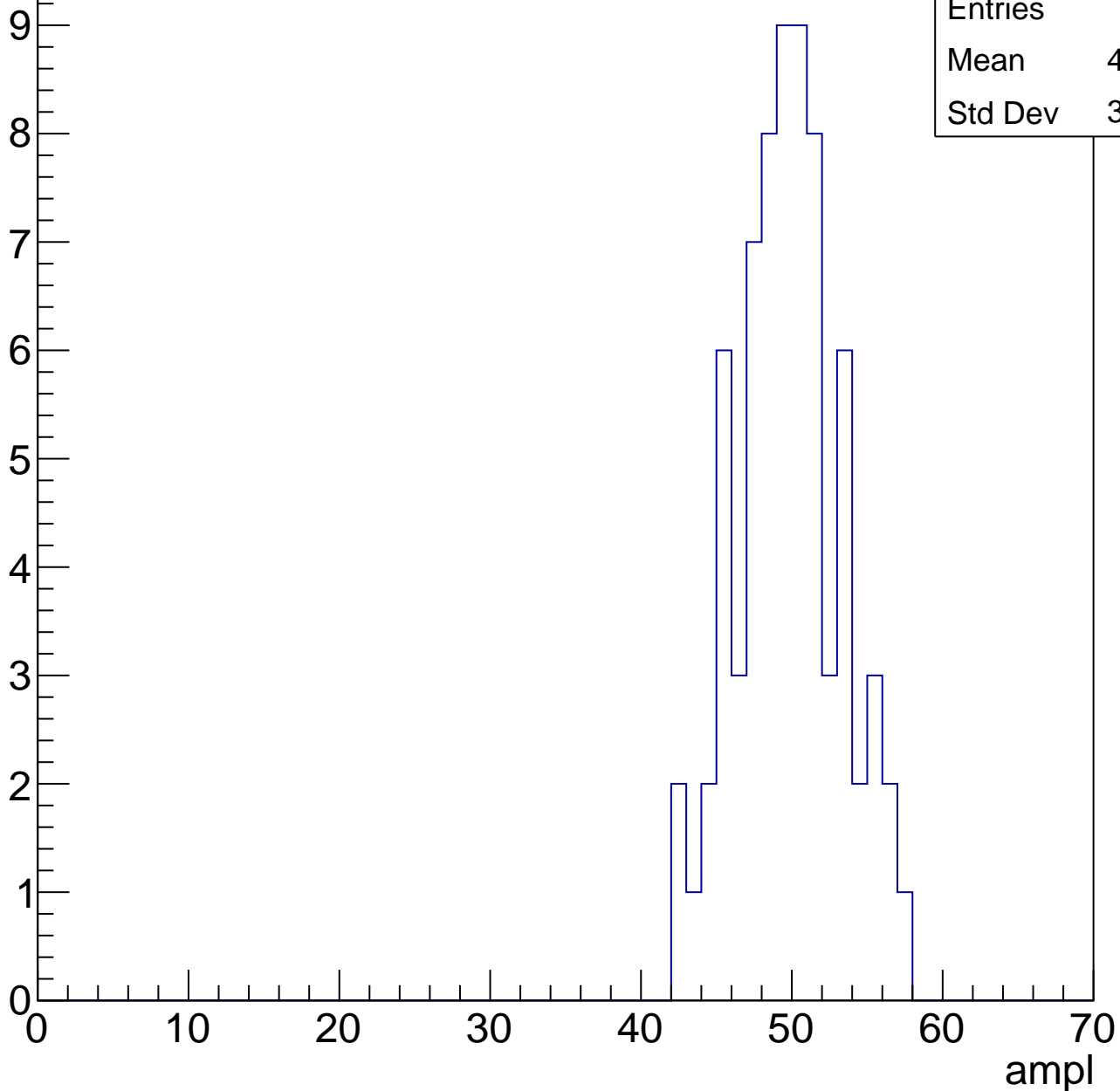
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch6, adc3

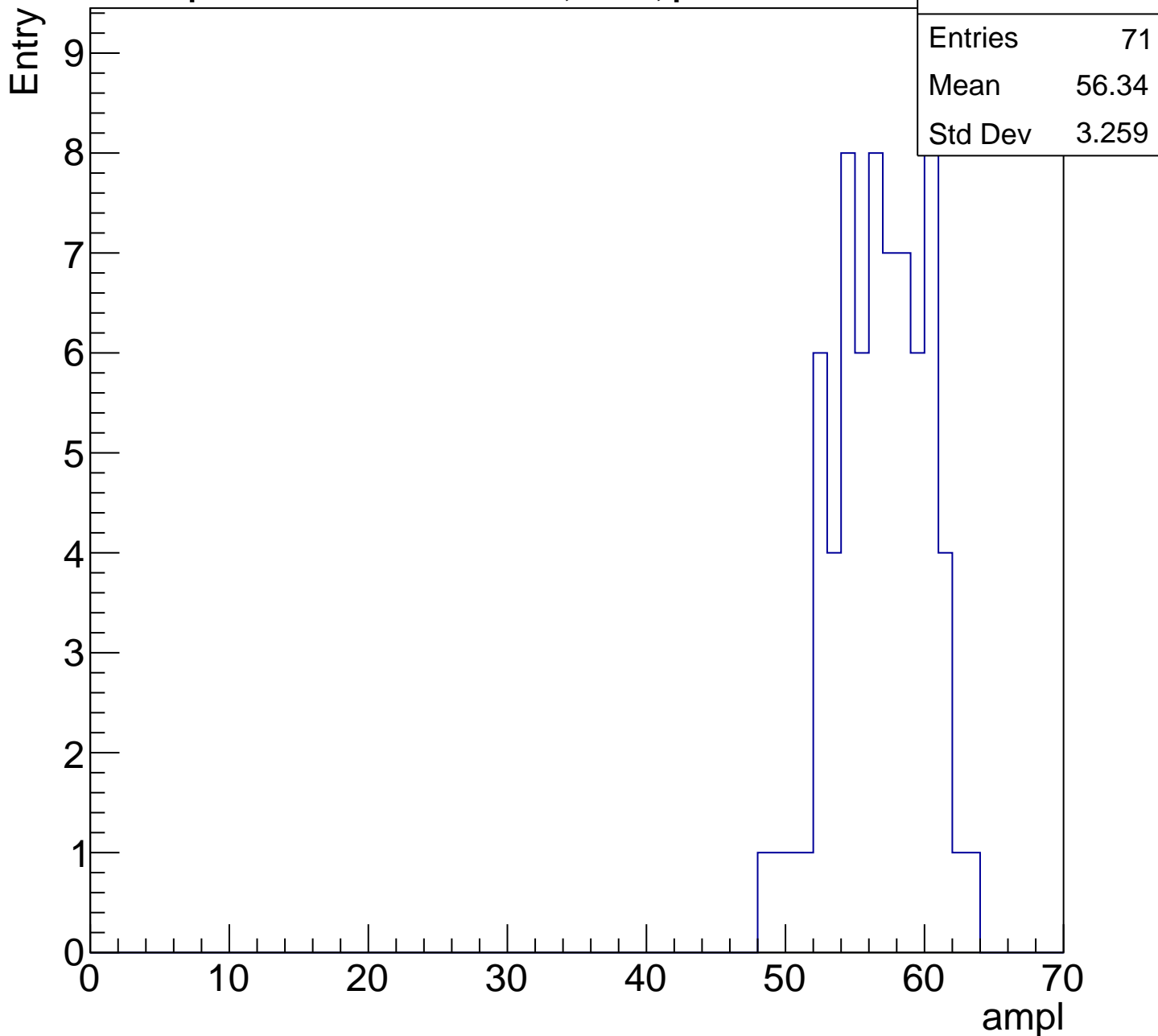
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

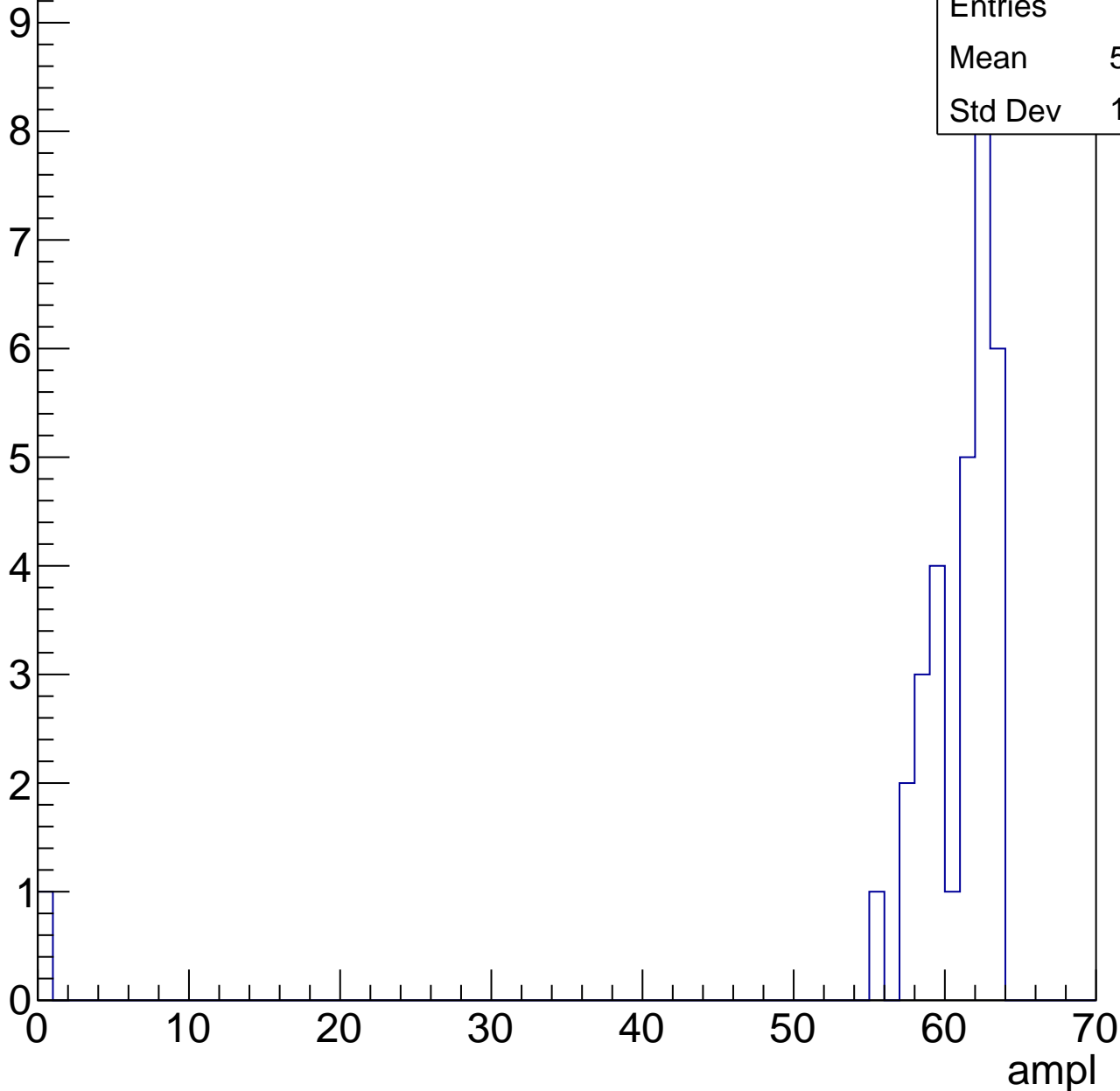


B1L100S, U25-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.75
Std Dev	10.76



B1L100S, U25-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch7, adc0

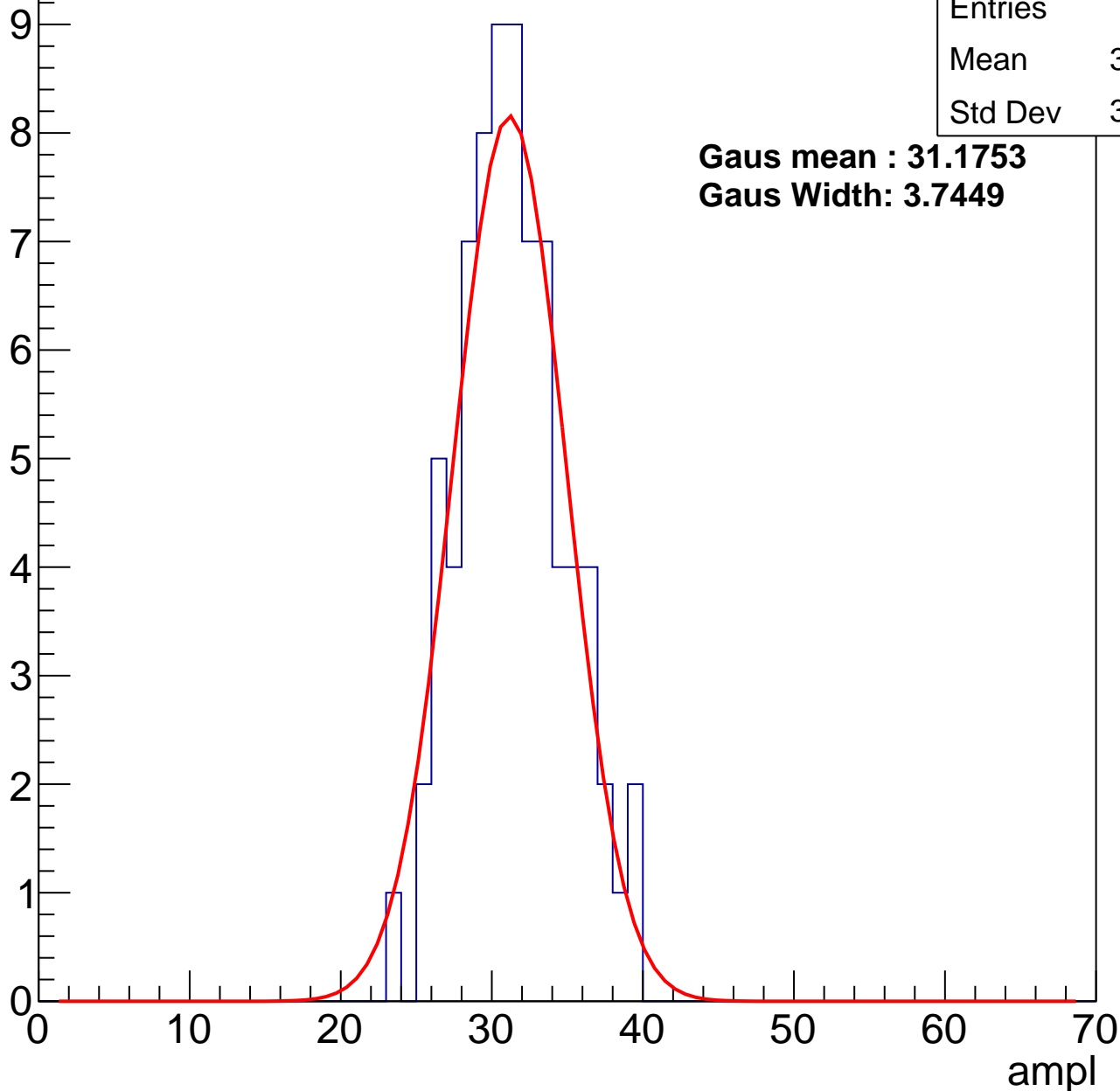
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.96
Std Dev	3.466

Gaus mean : 31.1753

Gaus Width: 3.7449



B1L100S, U25-ch7, adc1

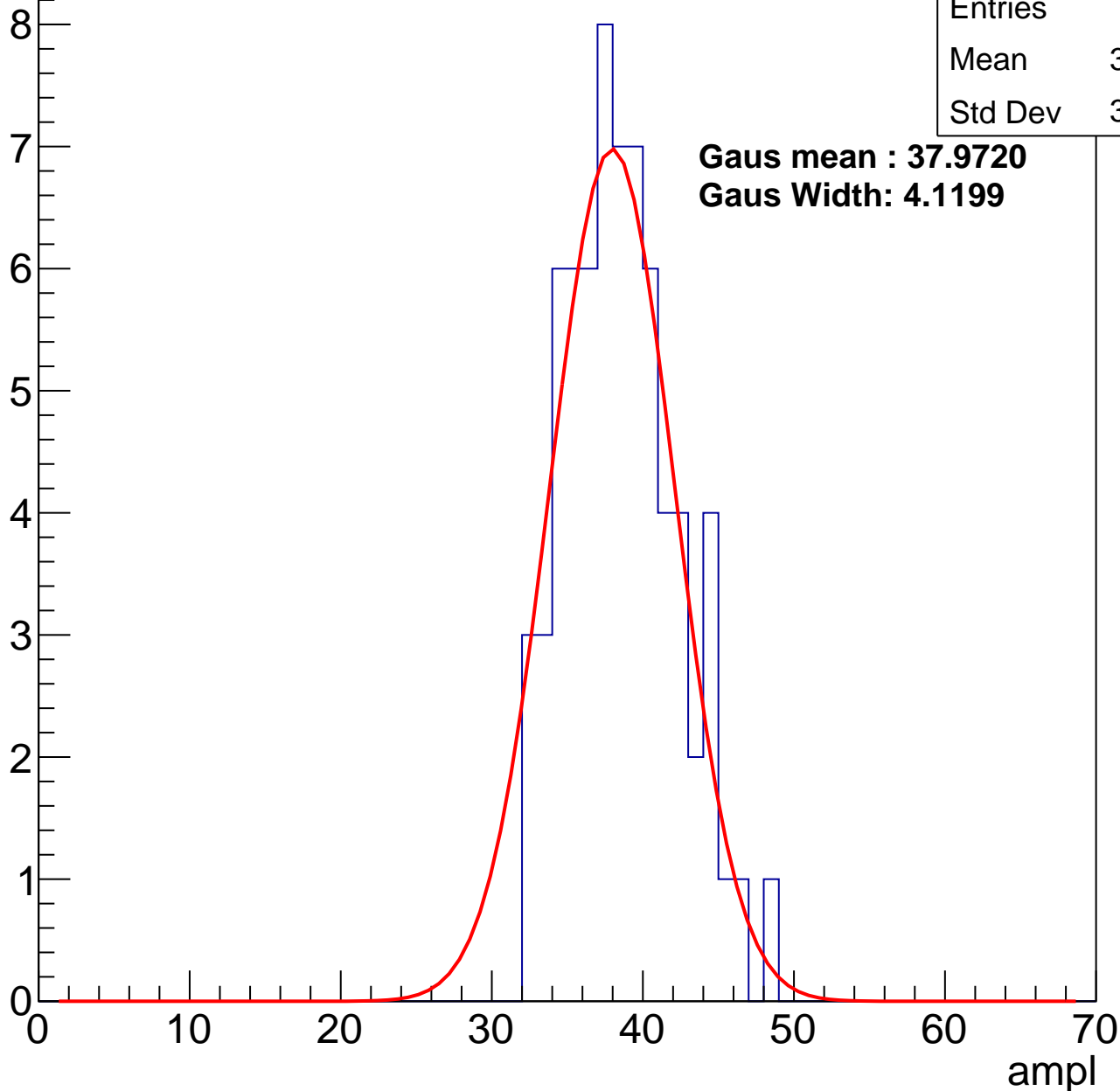
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	38.16
Std Dev	3.614

Gaus mean : 37.9720

Gaus Width: 4.1199



B1L100S, U25-ch7, adc2

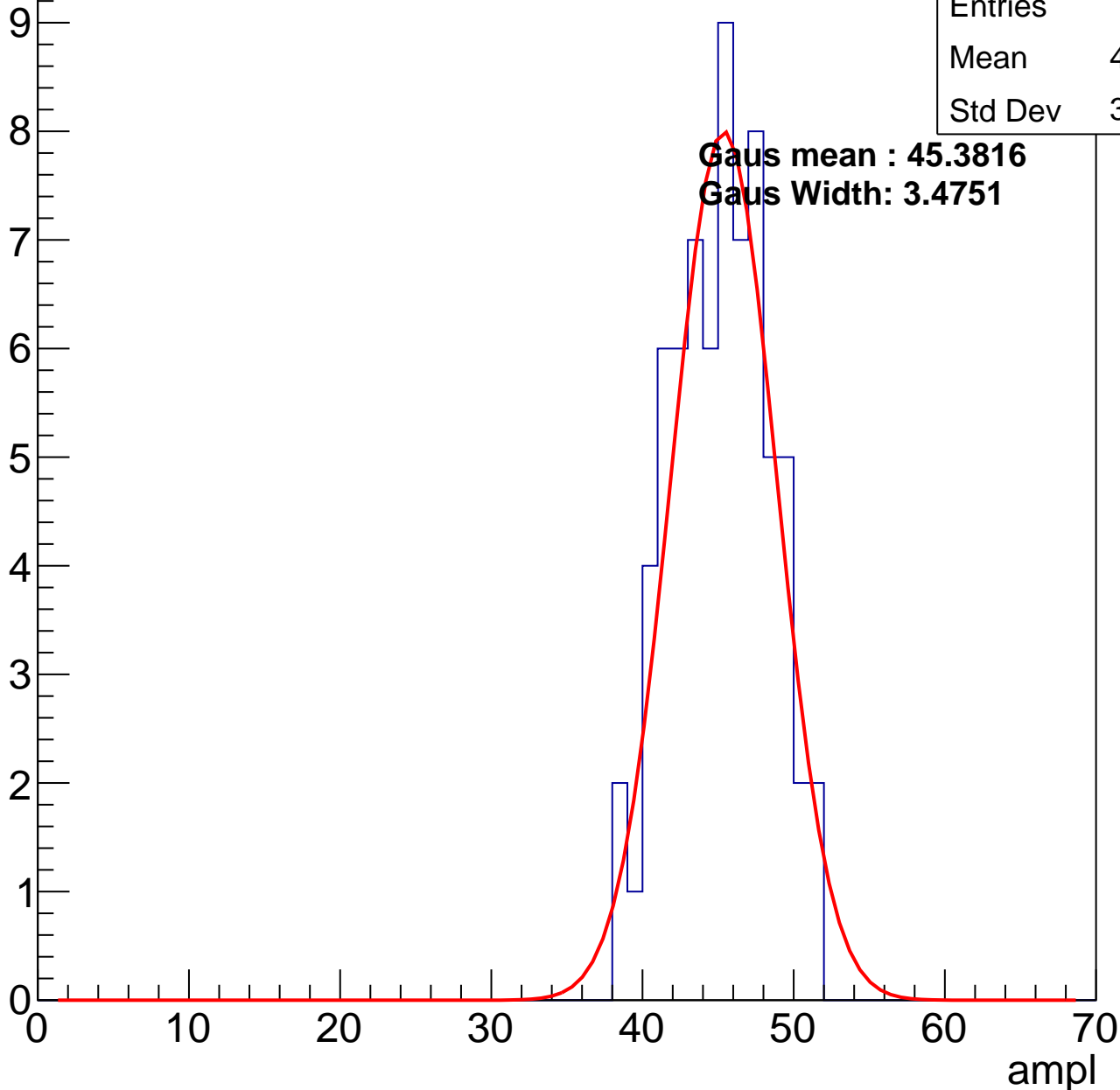
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.69
Std Dev	3.156

Gaus mean : 45.3816

Gaus Width: 3.4751

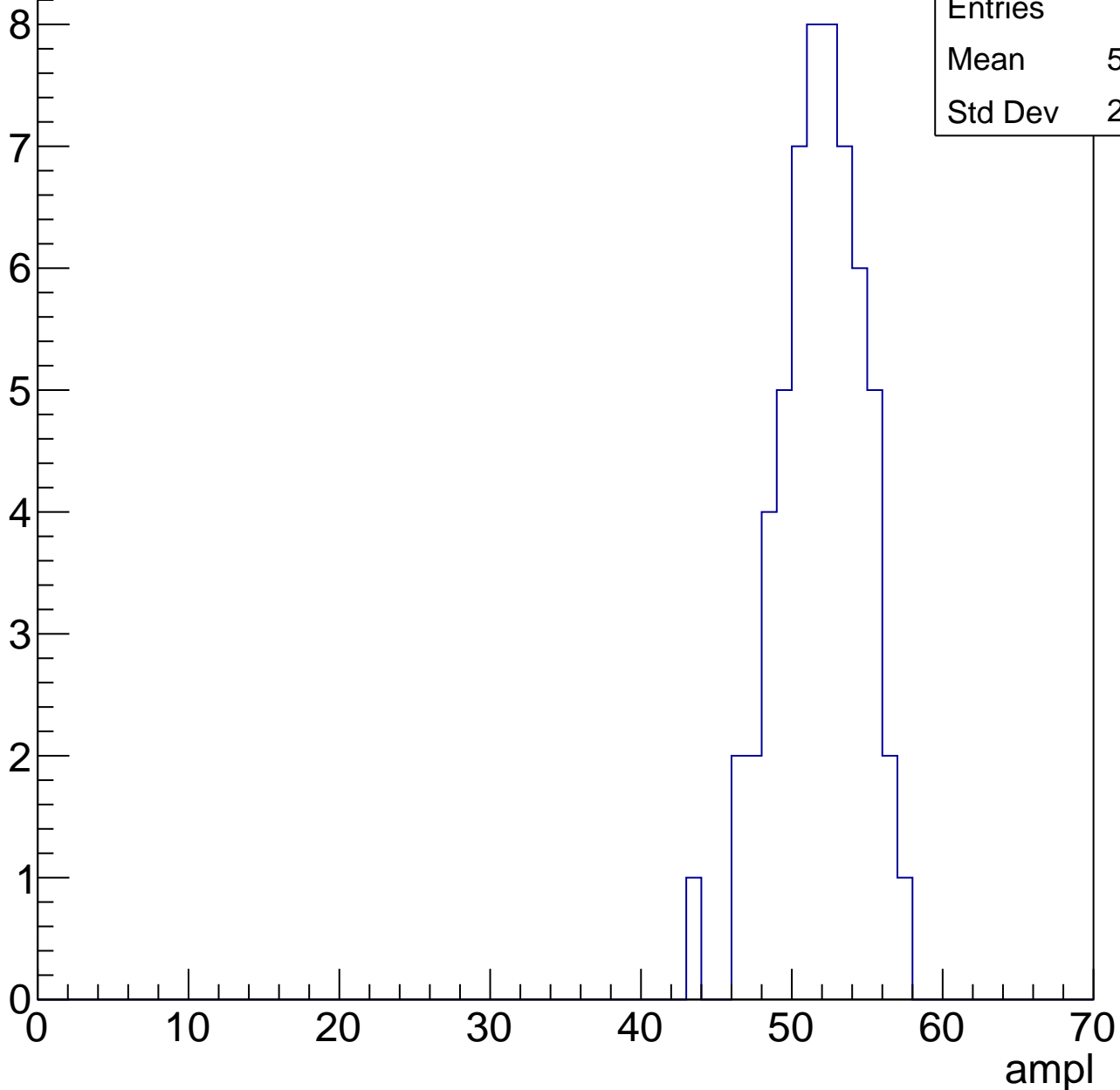


B1L100S, U25-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

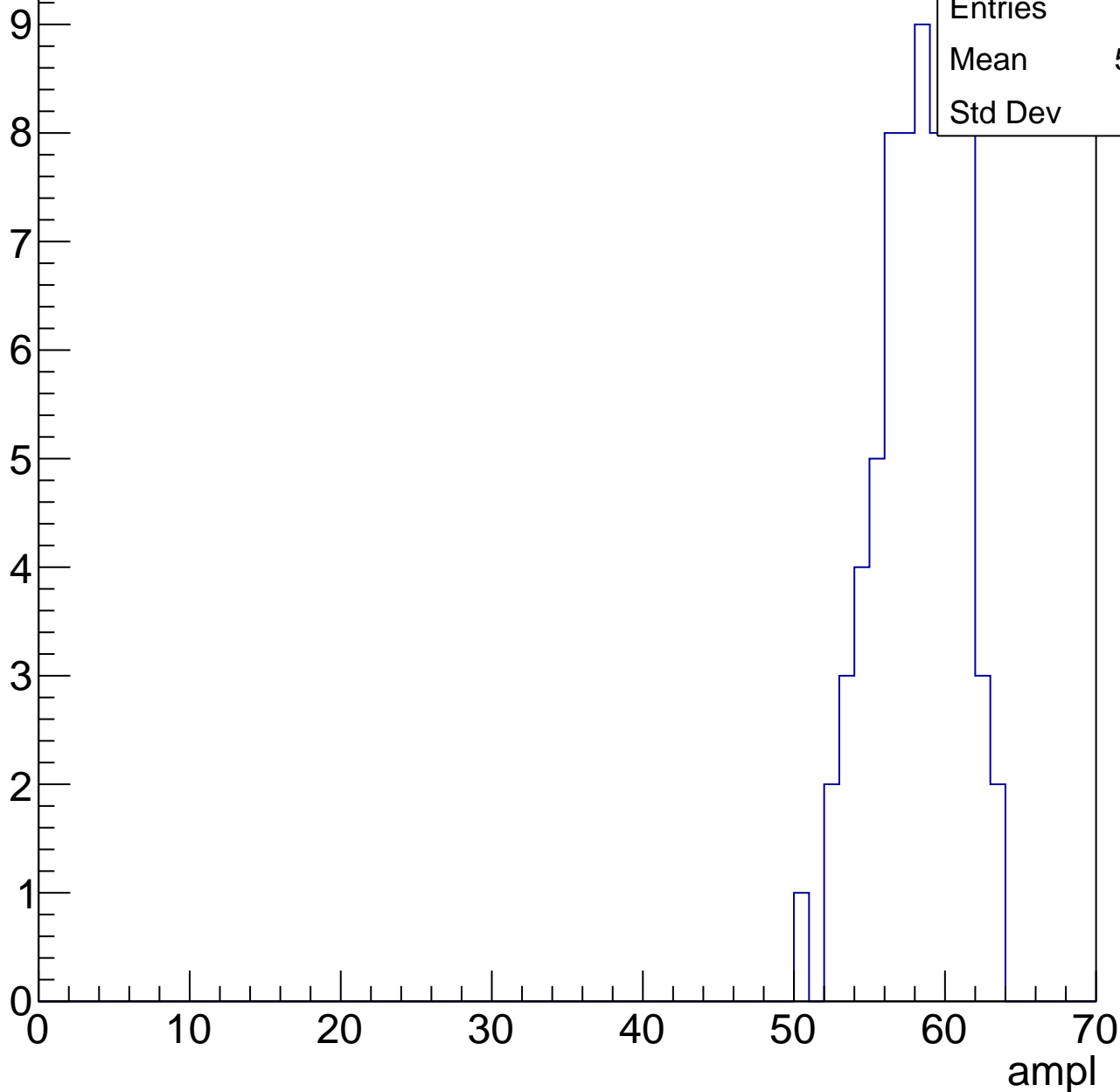
Entries	58
Mean	51.36
Std Dev	2.808



B1L100S, U25-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

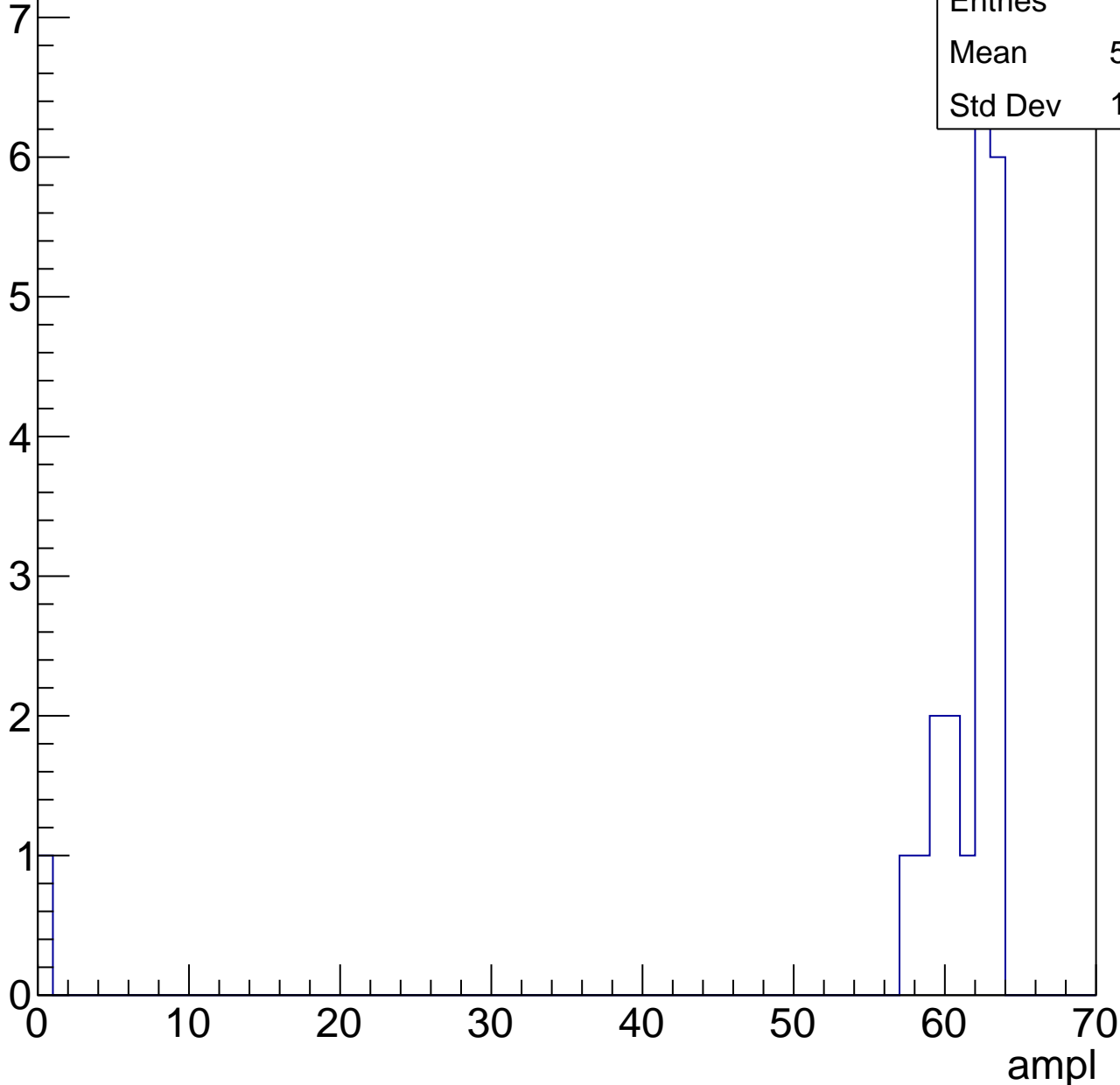


B1L100S, U25-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.38
Std Dev	13.17



B1L100S, U25-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch8, adc0

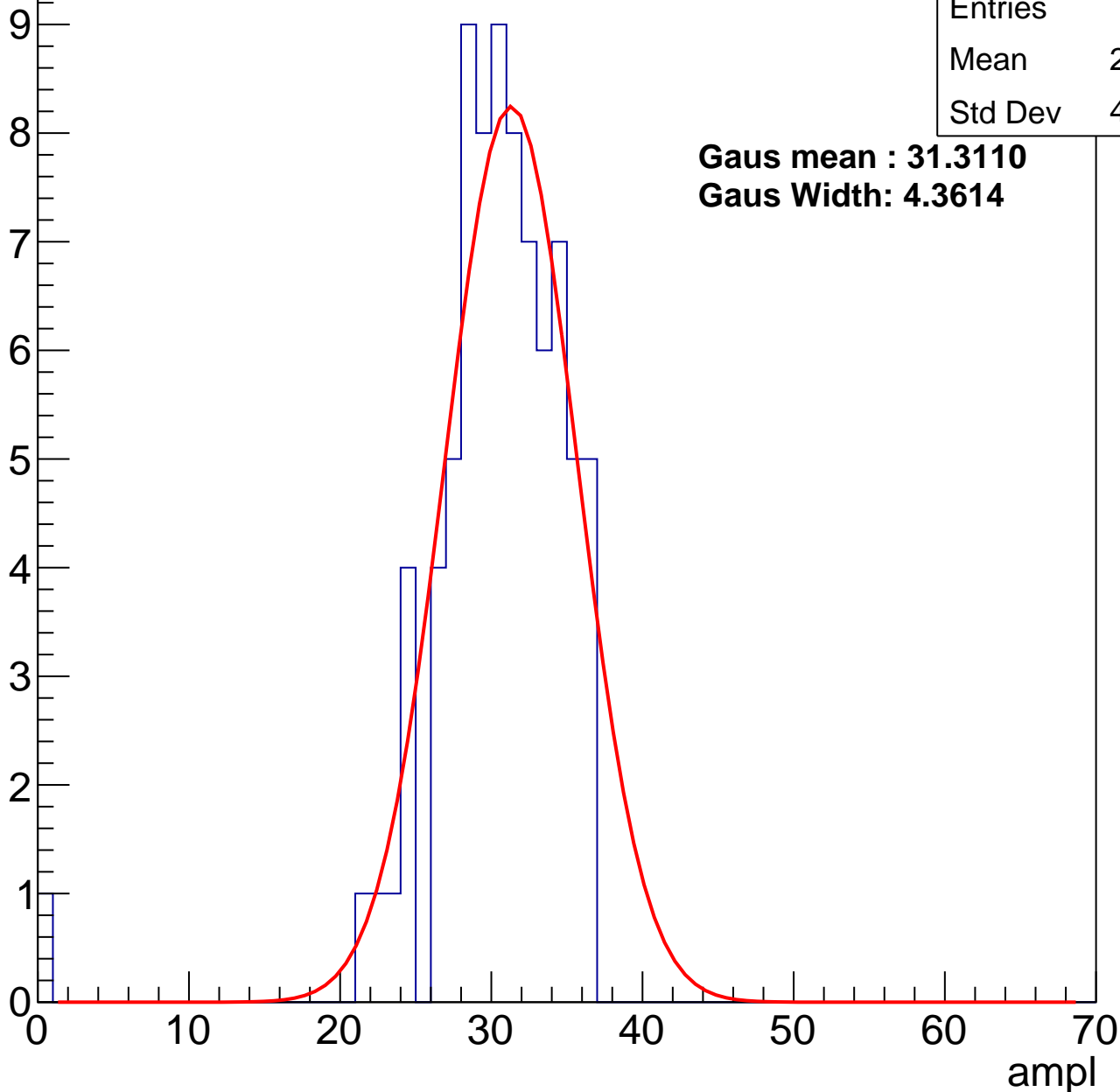
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	29.85
Std Dev	4.838

Gaus mean : 31.3110

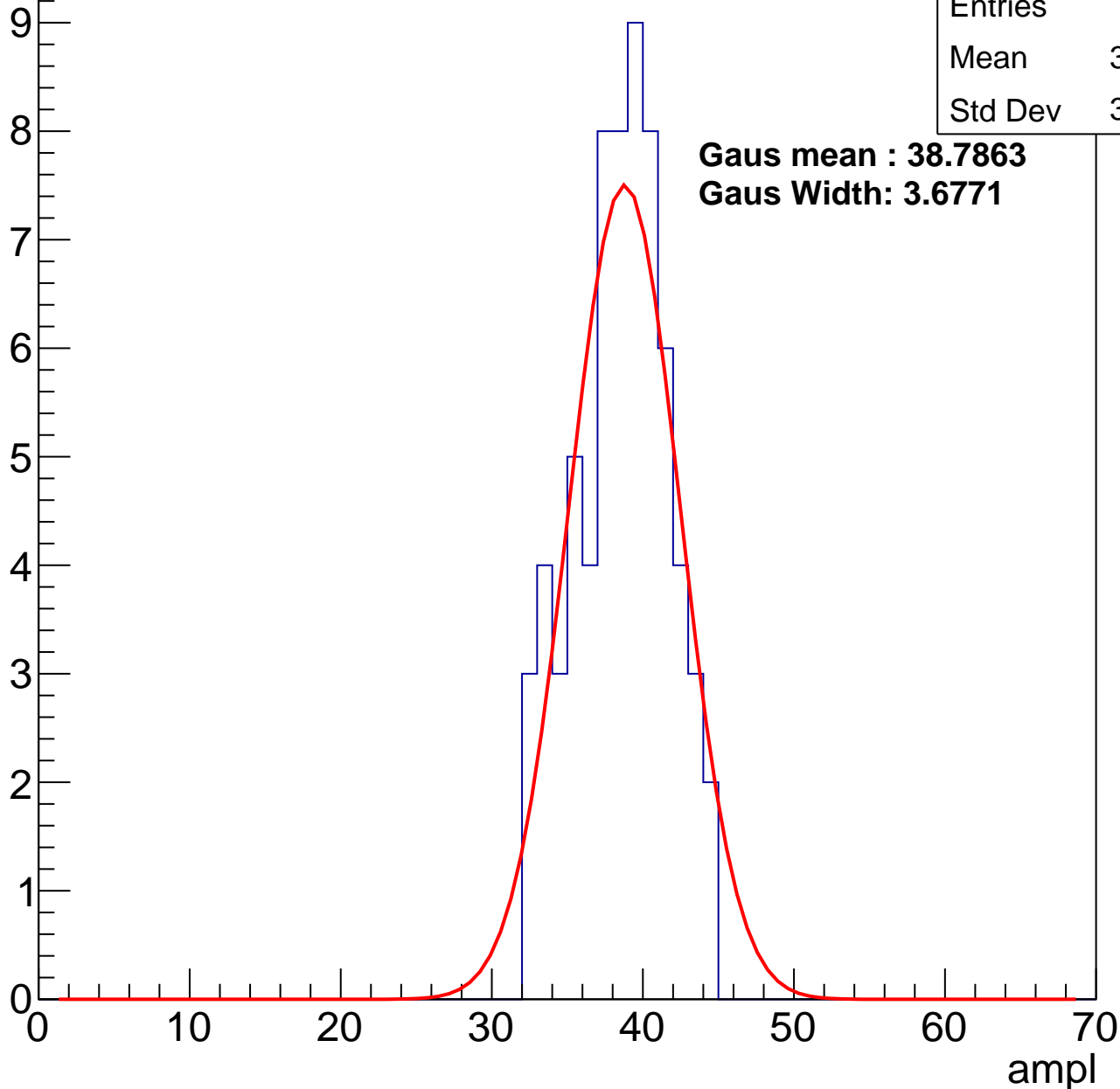
Gaus Width: 4.3614



B1L100S, U25-ch8, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch8, adc2

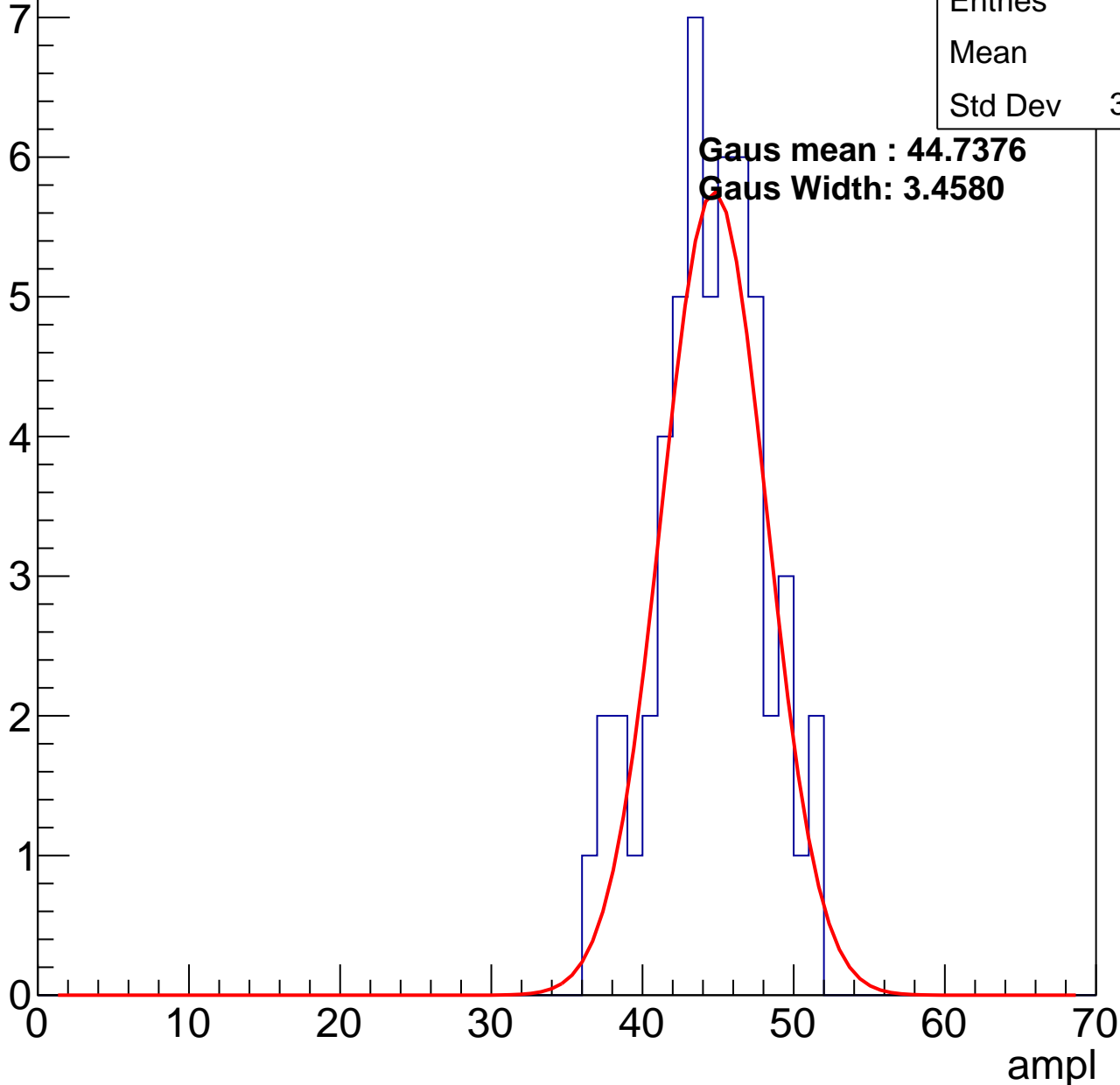
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	44
Std Dev	3.522

Gaus mean : 44.7376

Gaus Width: 3.4580

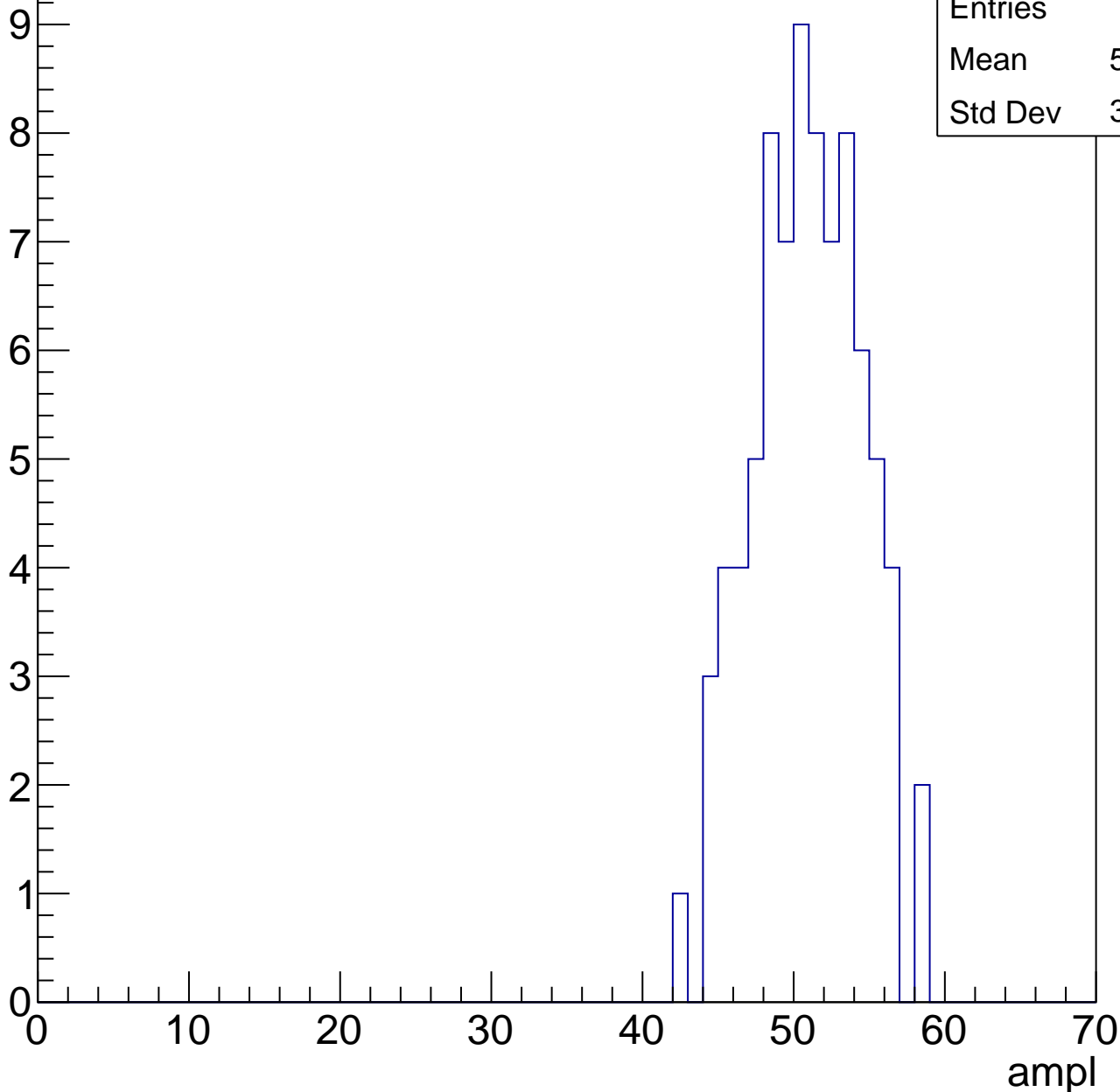


B1L100S, U25-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

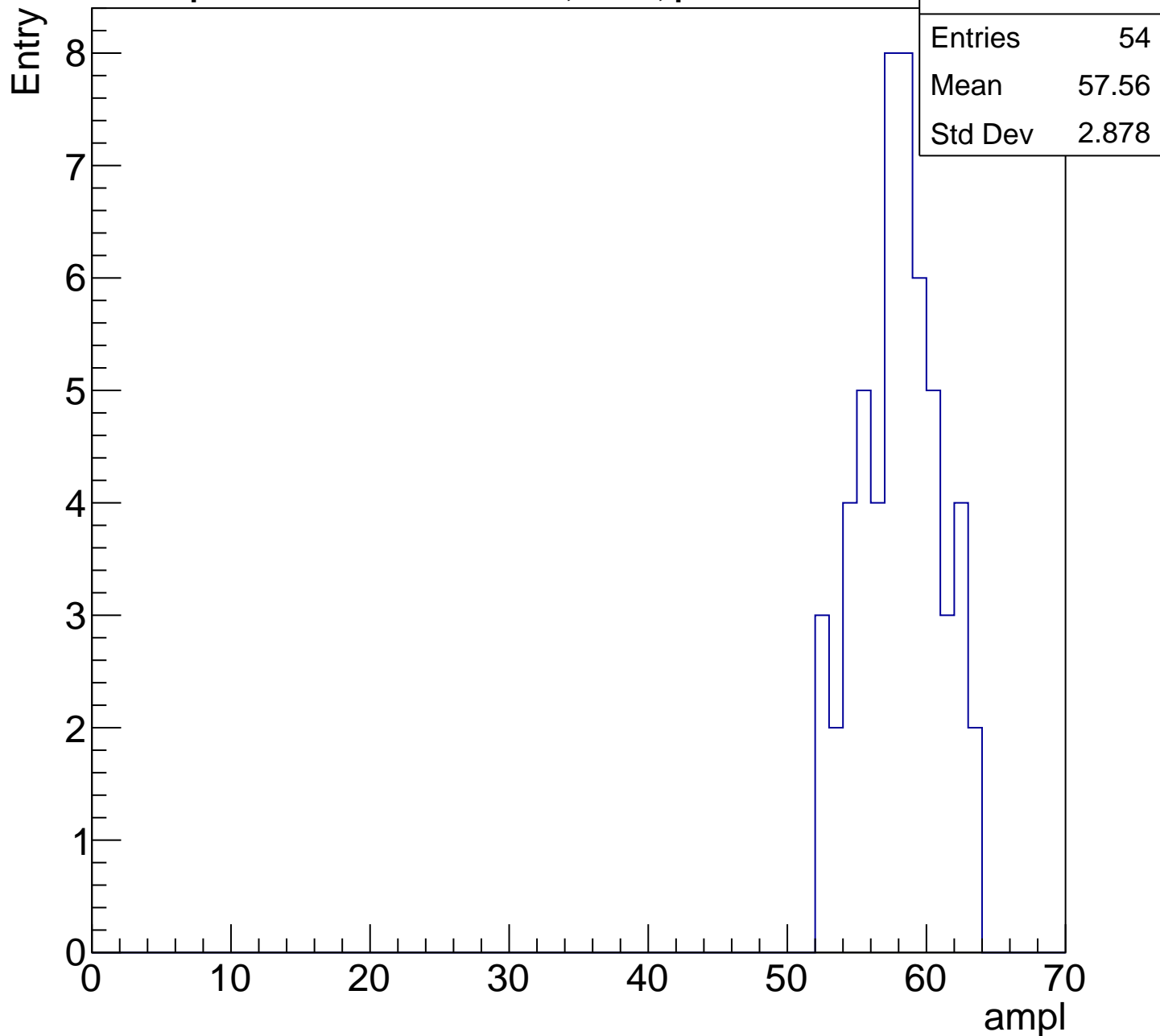
Entry

Entries	81
Mean	50.43
Std Dev	3.524



B1L100S, U25-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

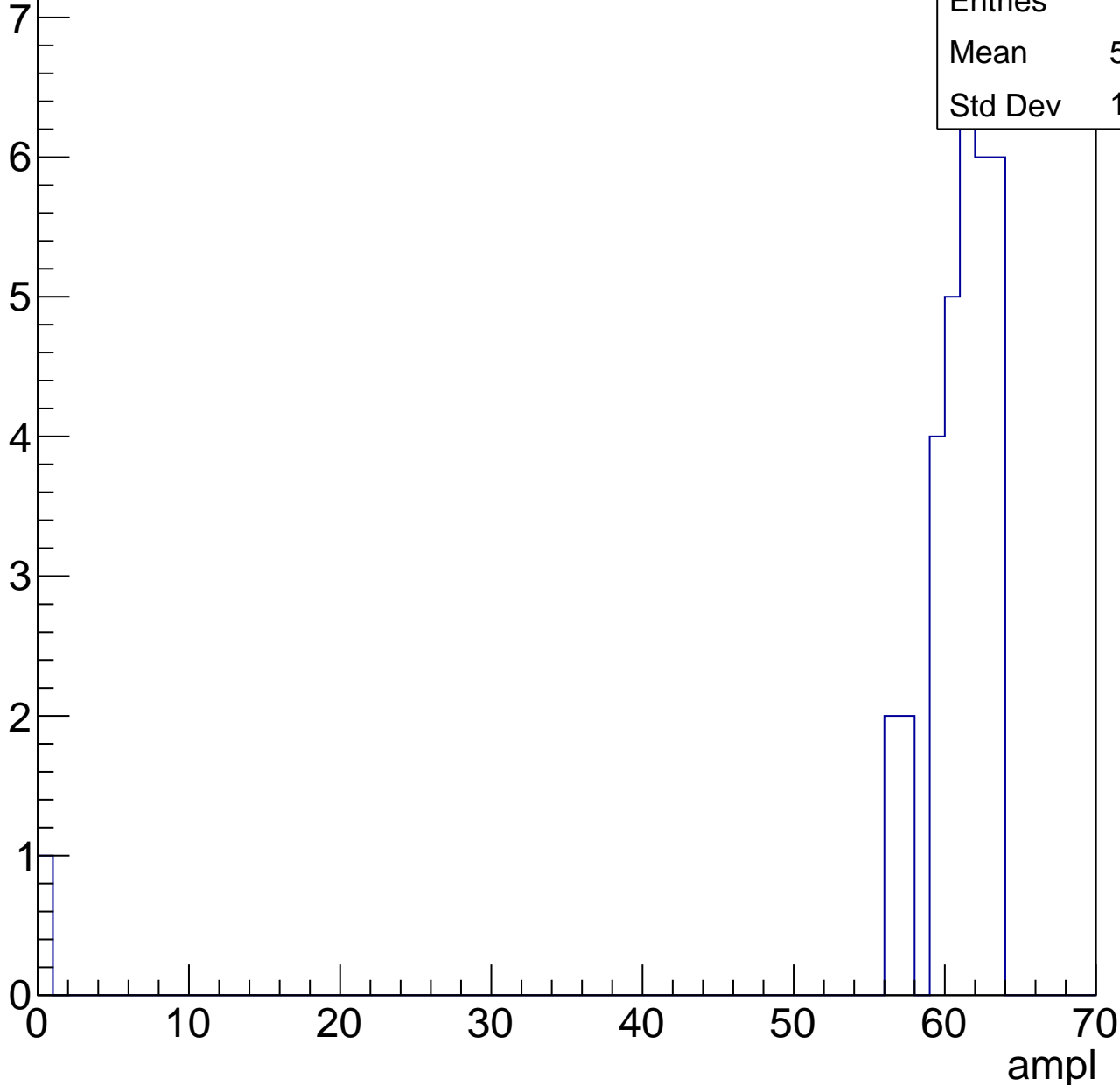


B1L100S, U25-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.76
Std Dev	10.57



B1L100S, U25-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch9, adc0

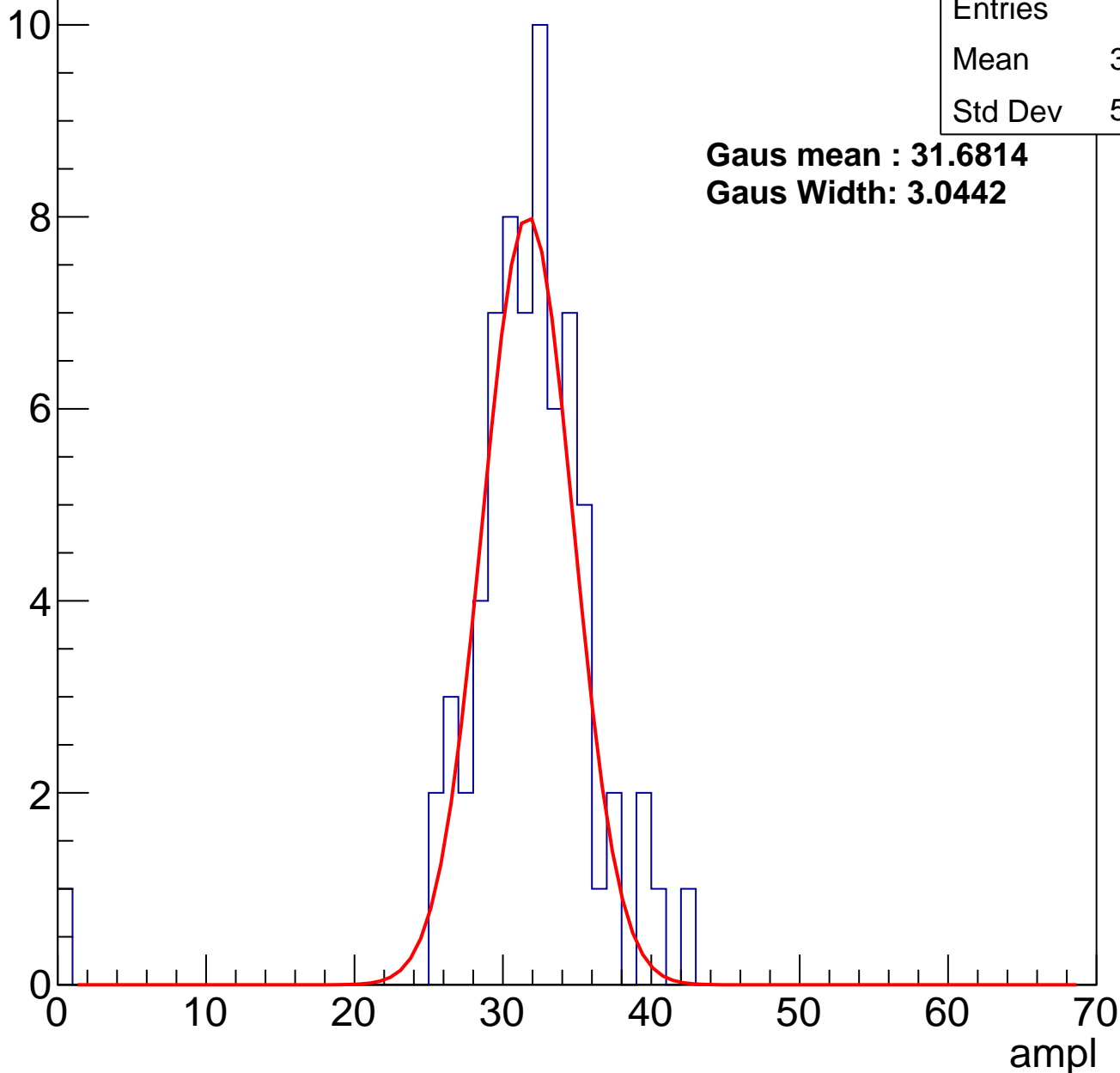
calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	31.23
Std Dev	5.125

Gaus mean : 31.6814

Gaus Width: 3.0442

Entry



B1L100S, U25-ch9, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	38.34
Std Dev	3.206

Gaus mean : 38.9636

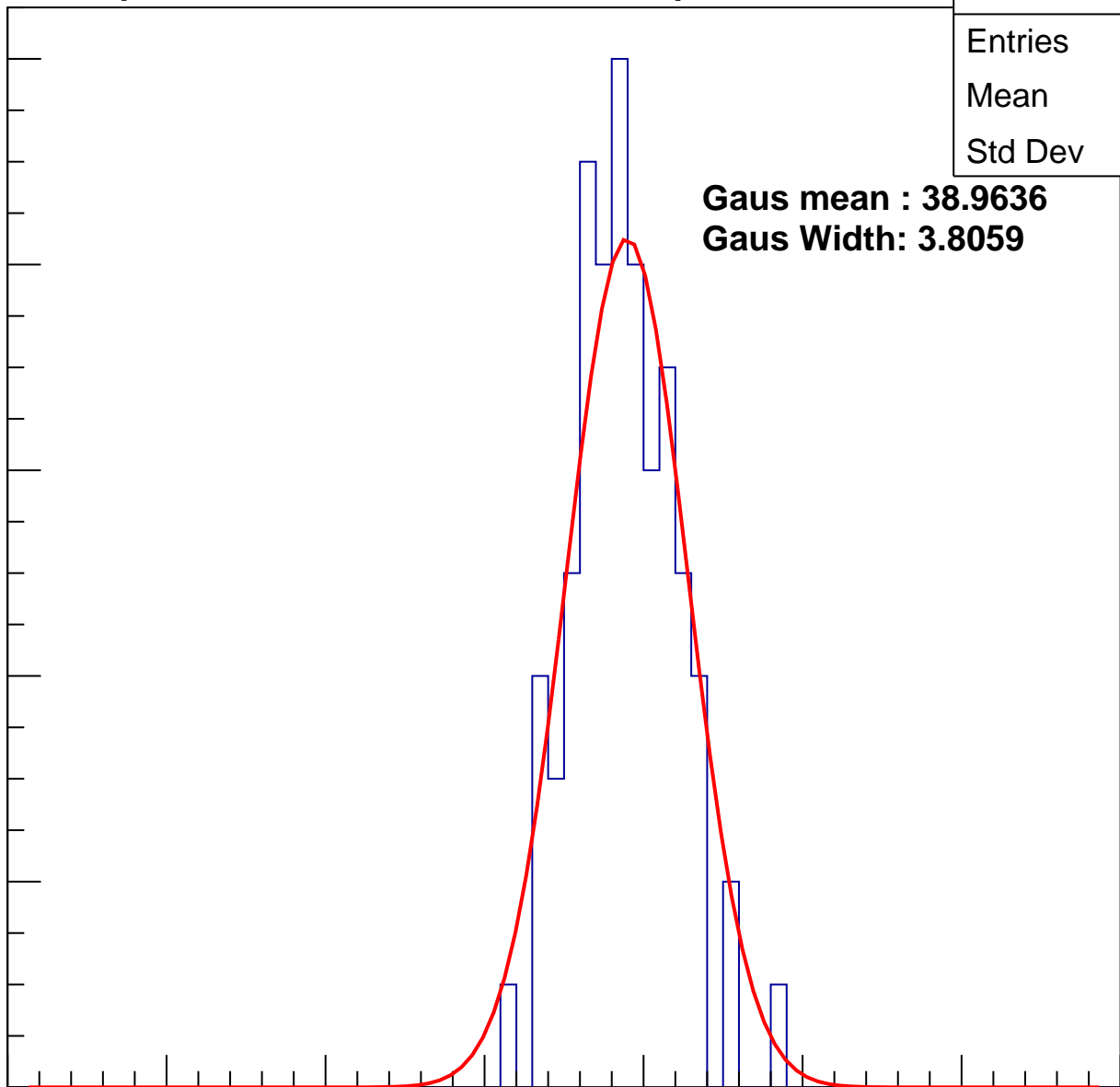
Gaus Width: 3.8059

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch9, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	45.18
Std Dev	3.001

Gaus mean : 45.6122

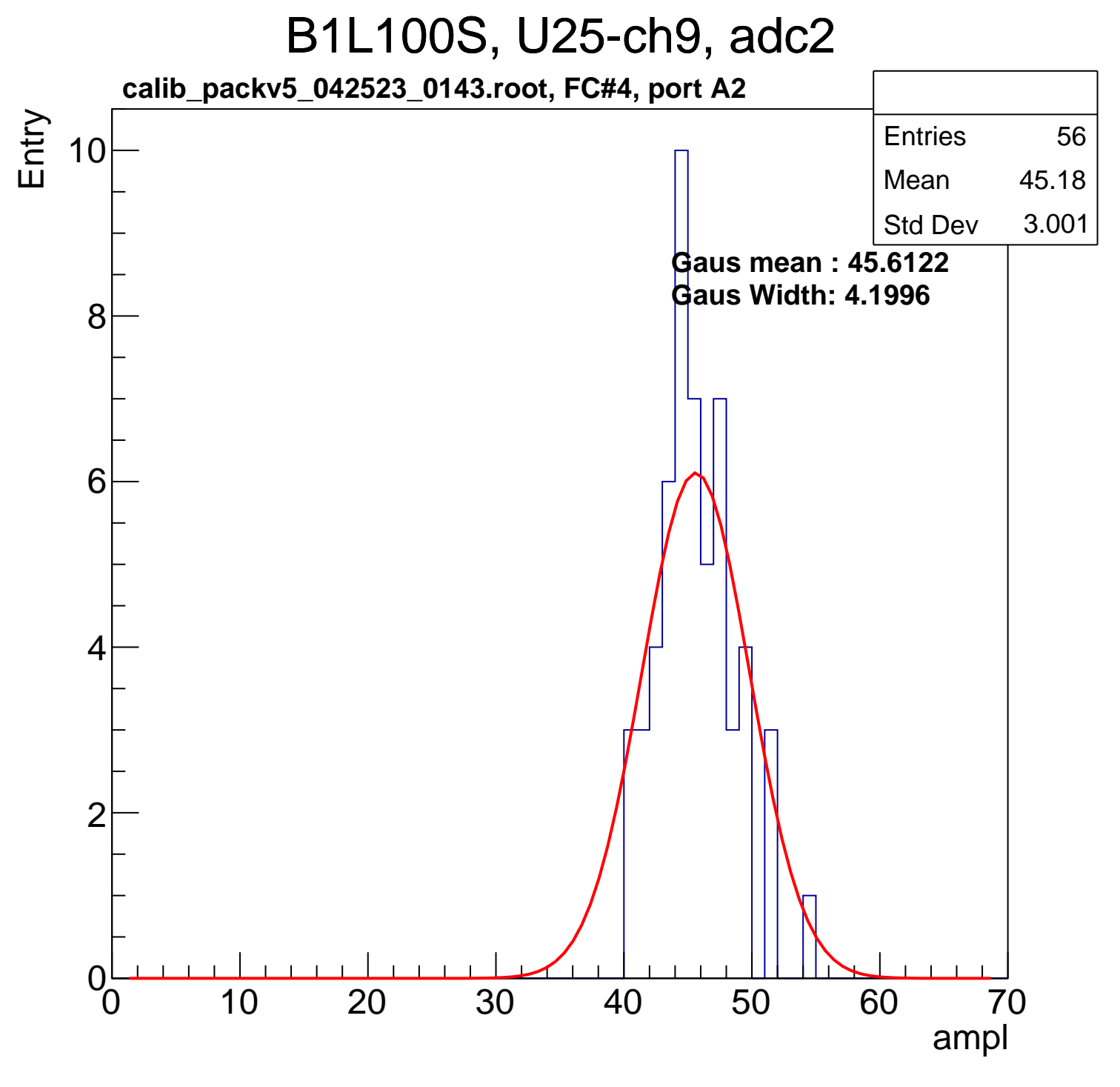
Gaus Width: 4.1996

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

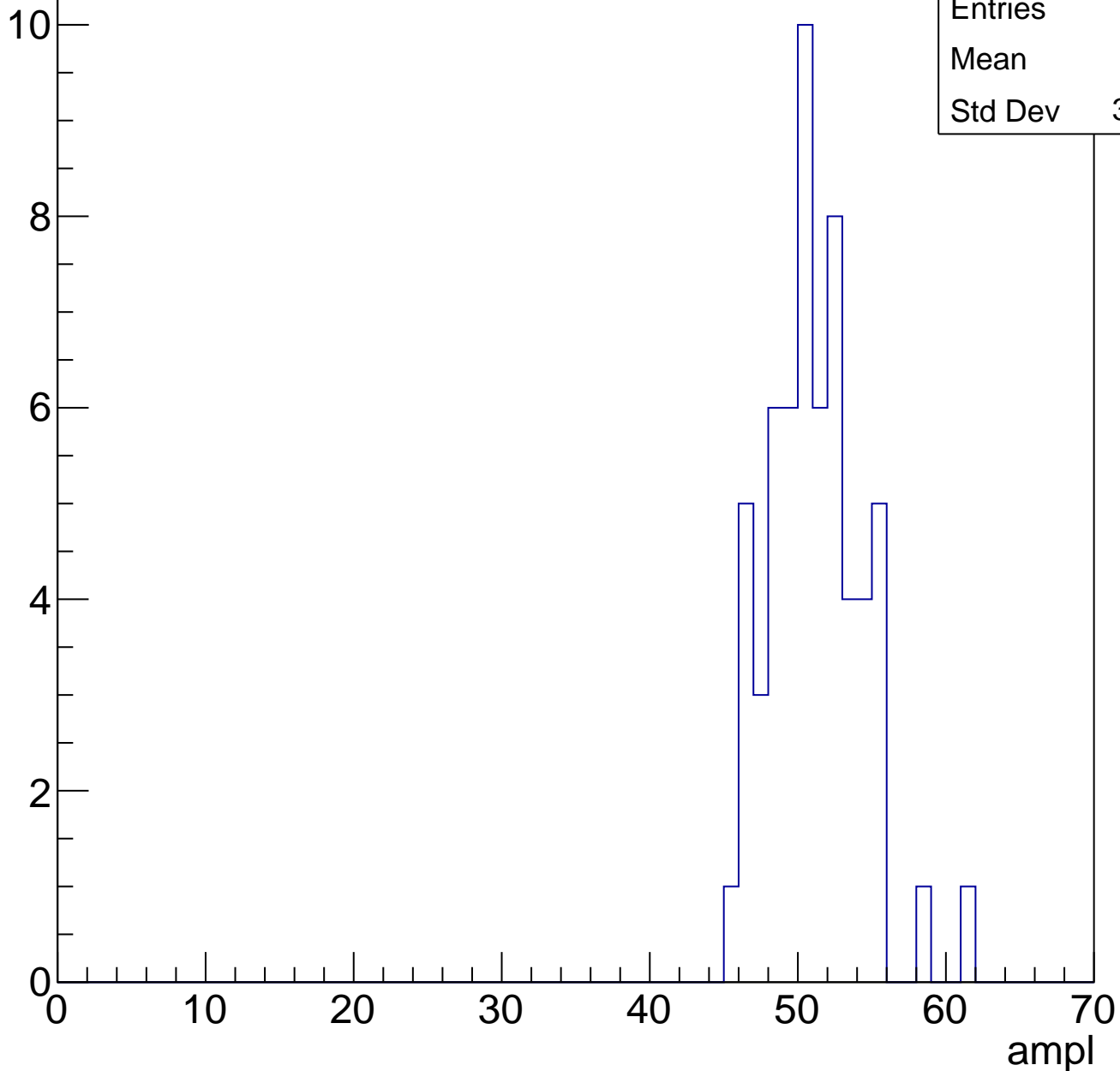


B1L100S, U25-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	50.7
Std Dev	3.111

Entry



B1L100S, U25-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	56.72
Std Dev	2.757

Entry

10

8

6

4

2

0

0

10

20

30

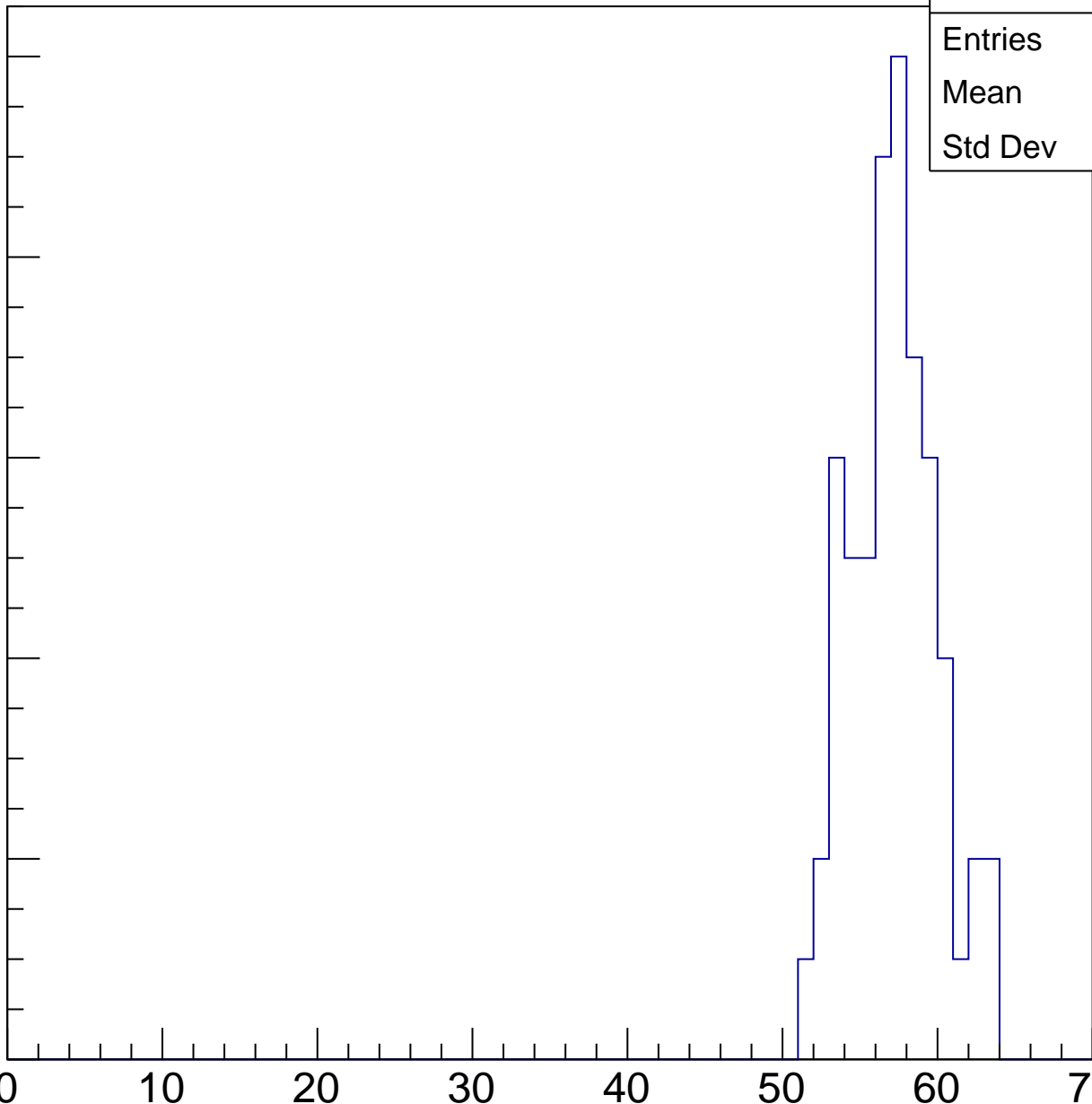
40

50

60

70

ampl

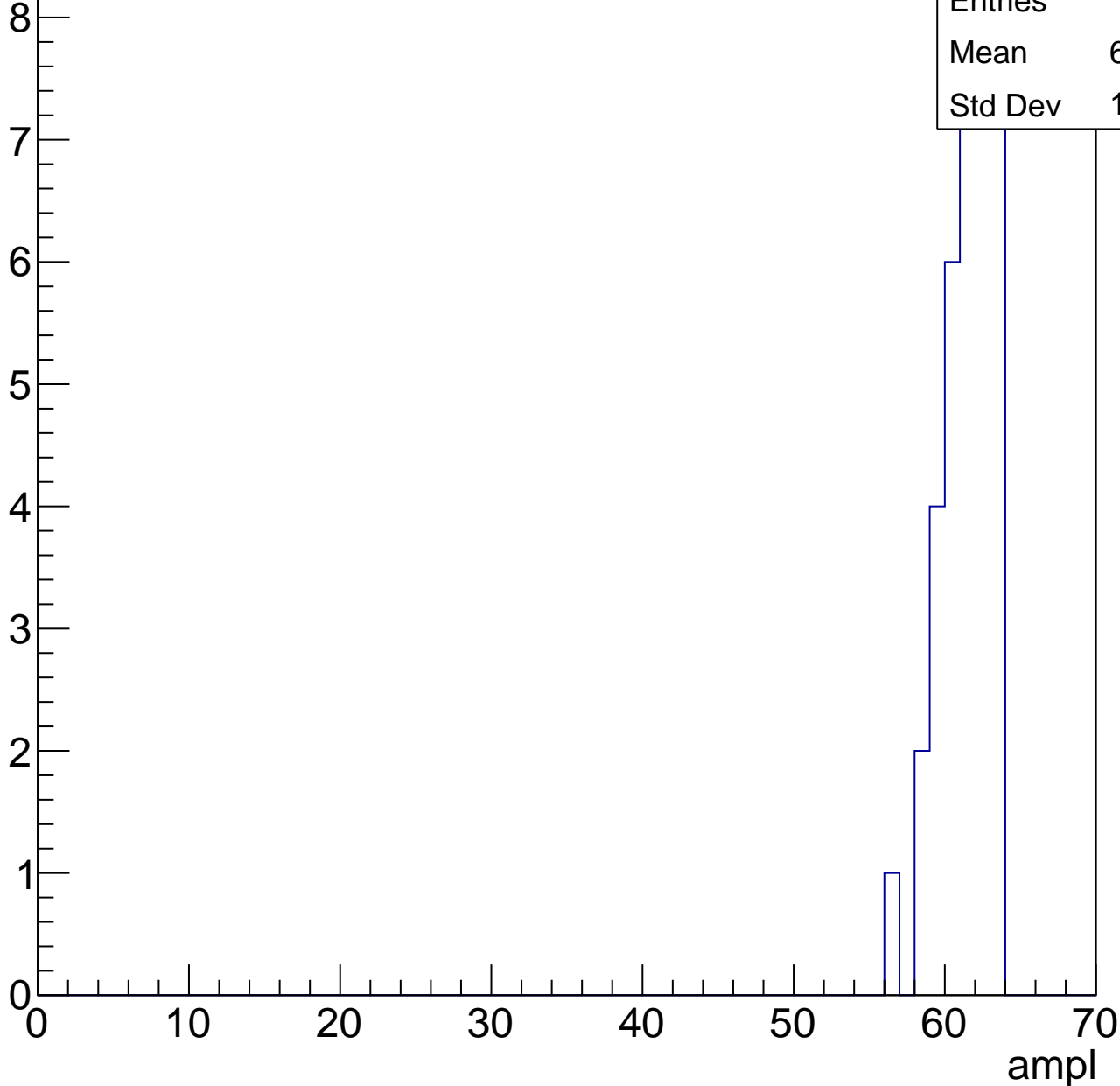


B1L100S, U25-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

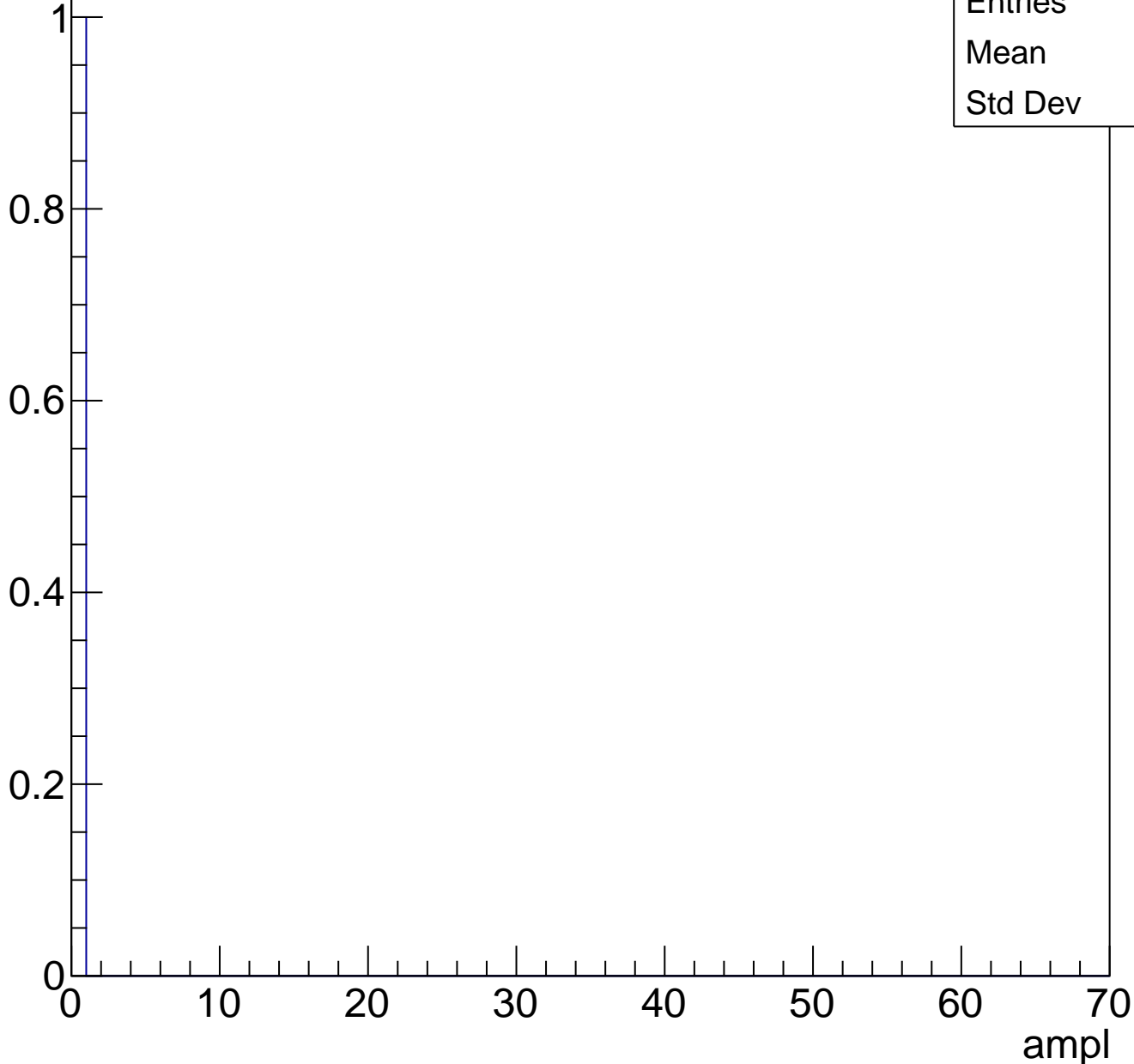
Entries	37
Mean	60.97
Std Dev	1.684



B1L100S, U25-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch10, adc0

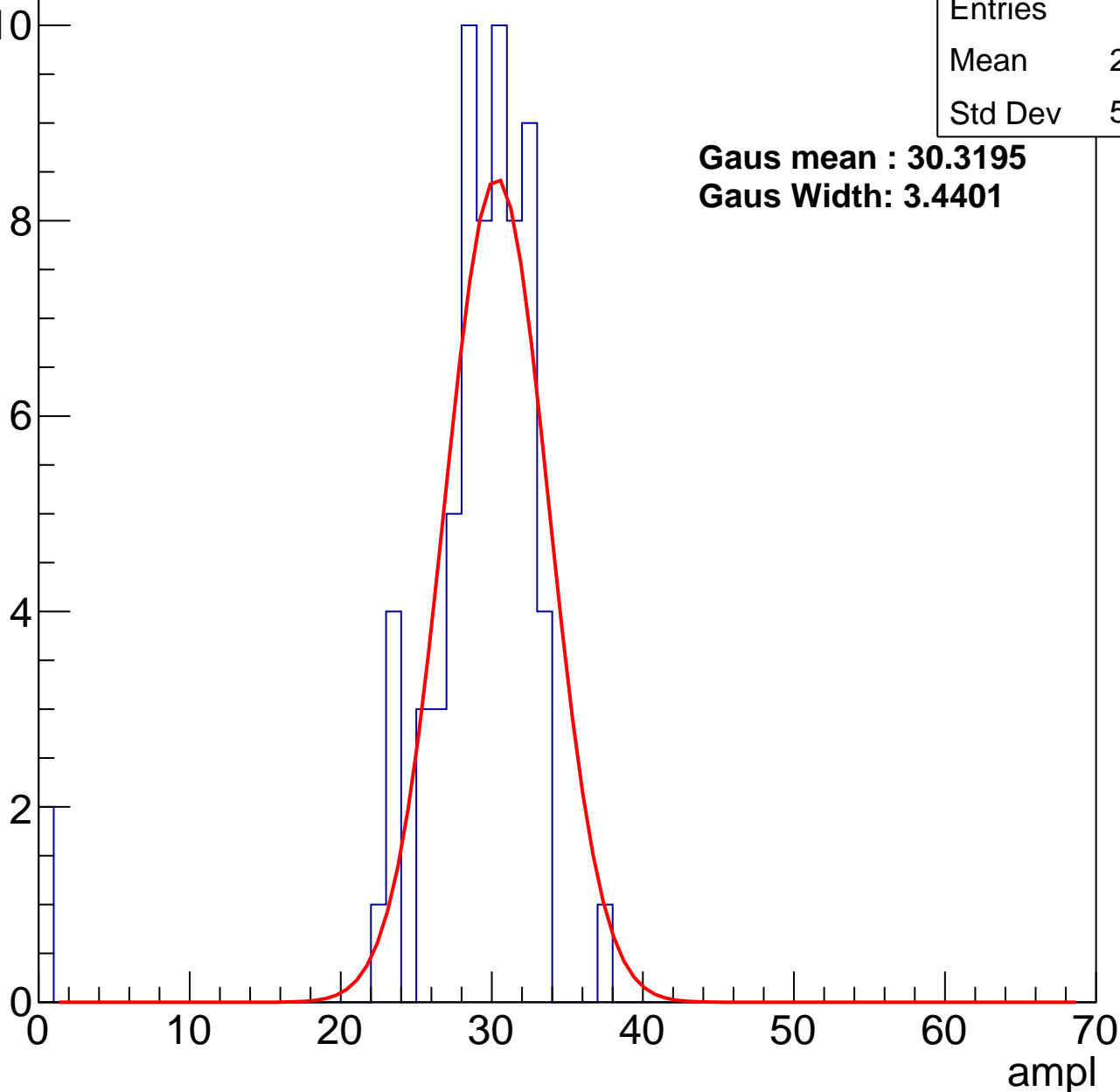
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.22
Std Dev	5.675

Gaus mean : 30.3195

Gaus Width: 3.4401



B1L100S, U25-ch10, adc1

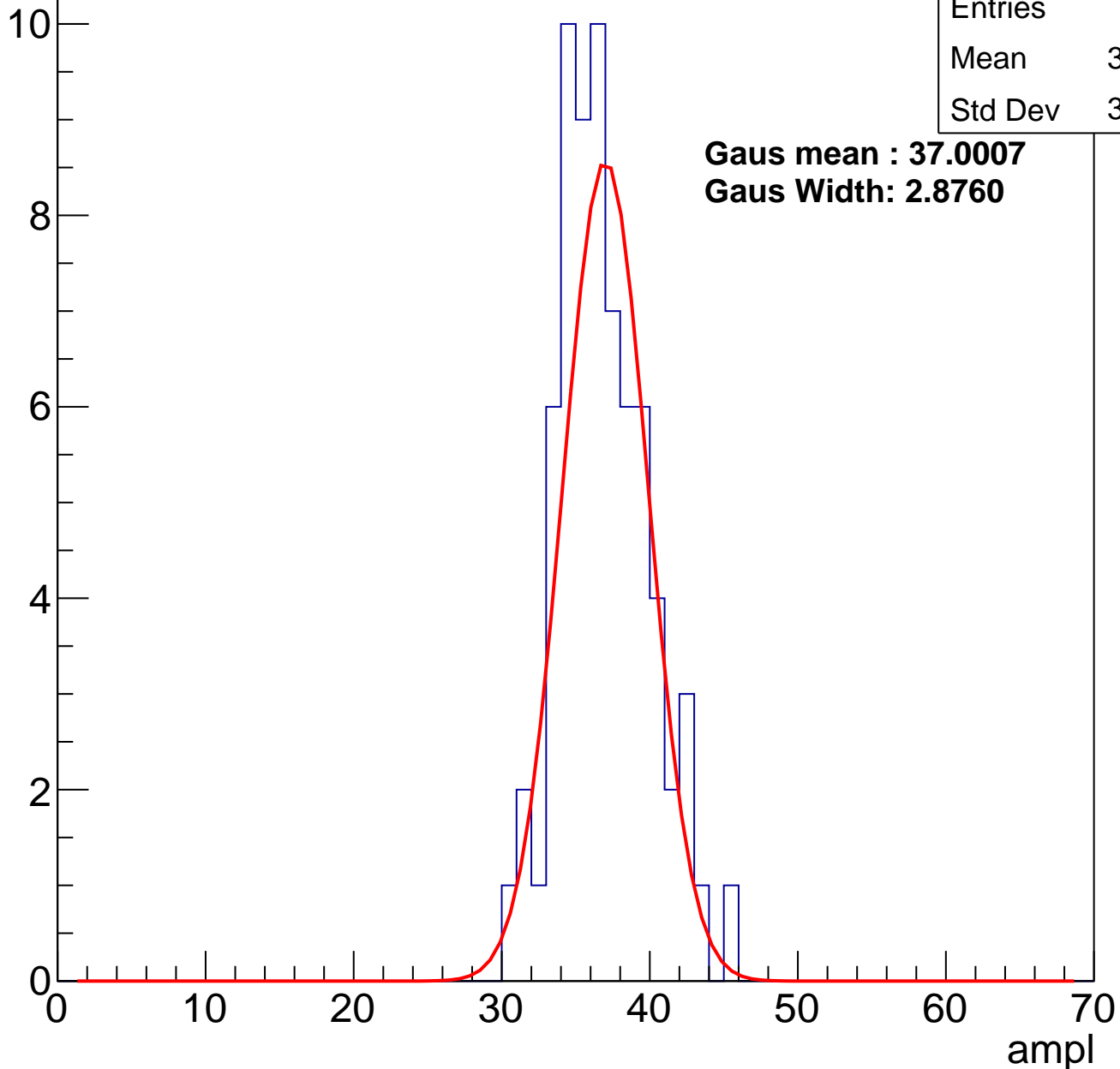
calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	36.43
Std Dev	3.019

Gaus mean : 37.0007

Gaus Width: 2.8760

Entry



B1L100S, U25-ch10, adc2

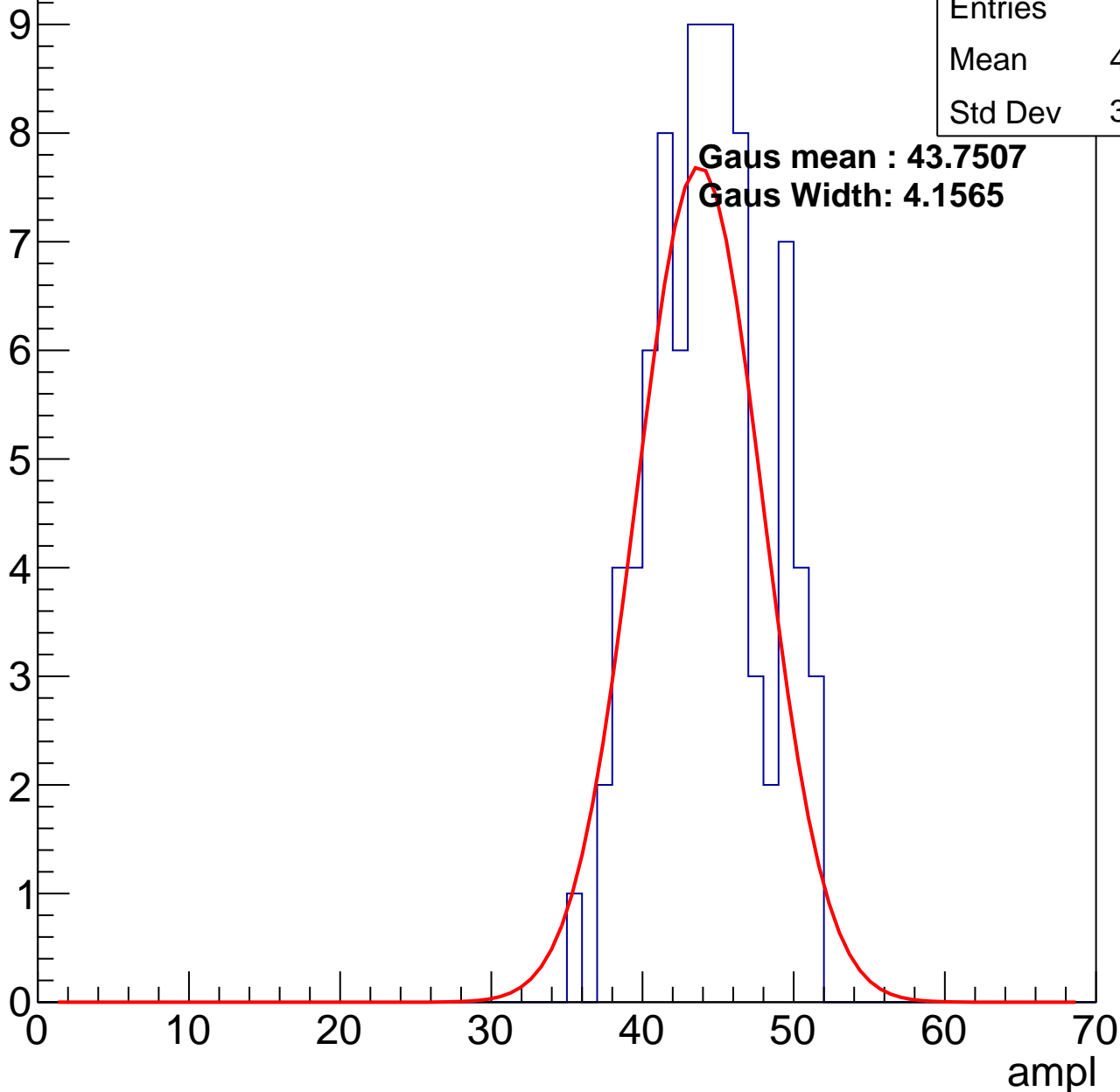
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	43.84
Std Dev	3.738

Gaus mean : 43.7507

Gaus Width: 4.1565

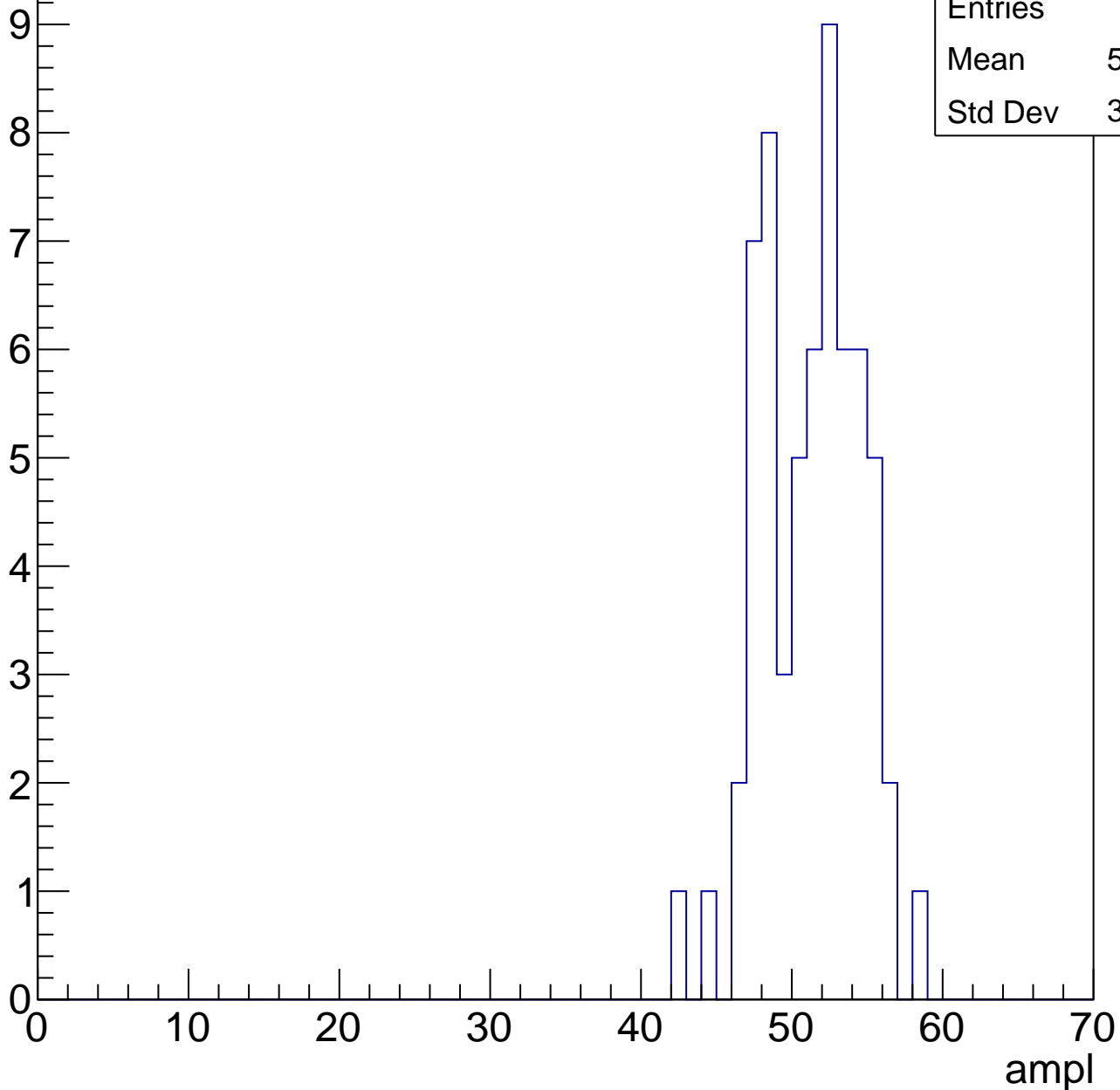


B1L100S, U25-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

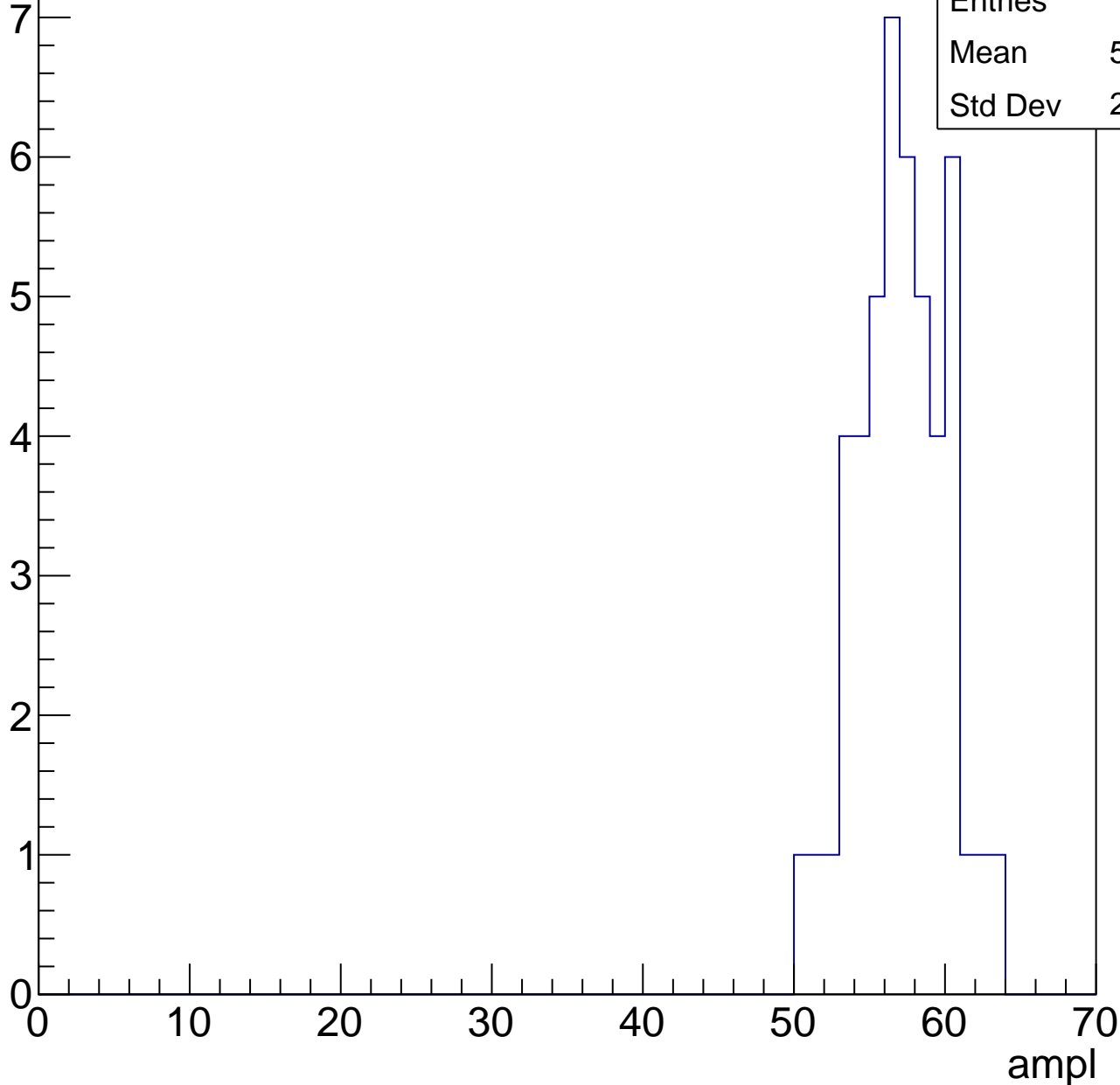
Entries	62
Mean	50.79
Std Dev	3.224



B1L100S, U25-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

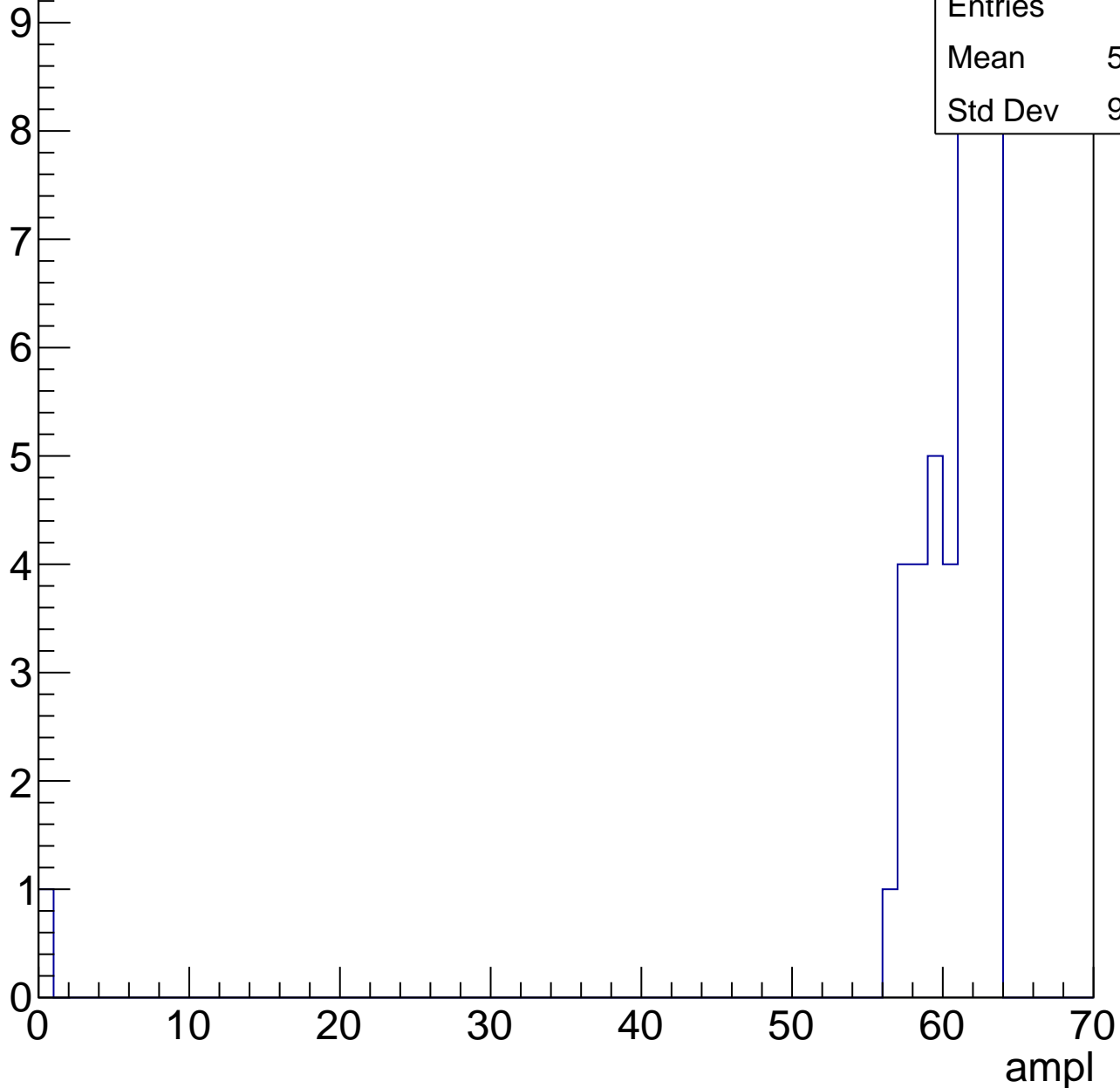
Entry



B1L100S, U25-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch11, adc0

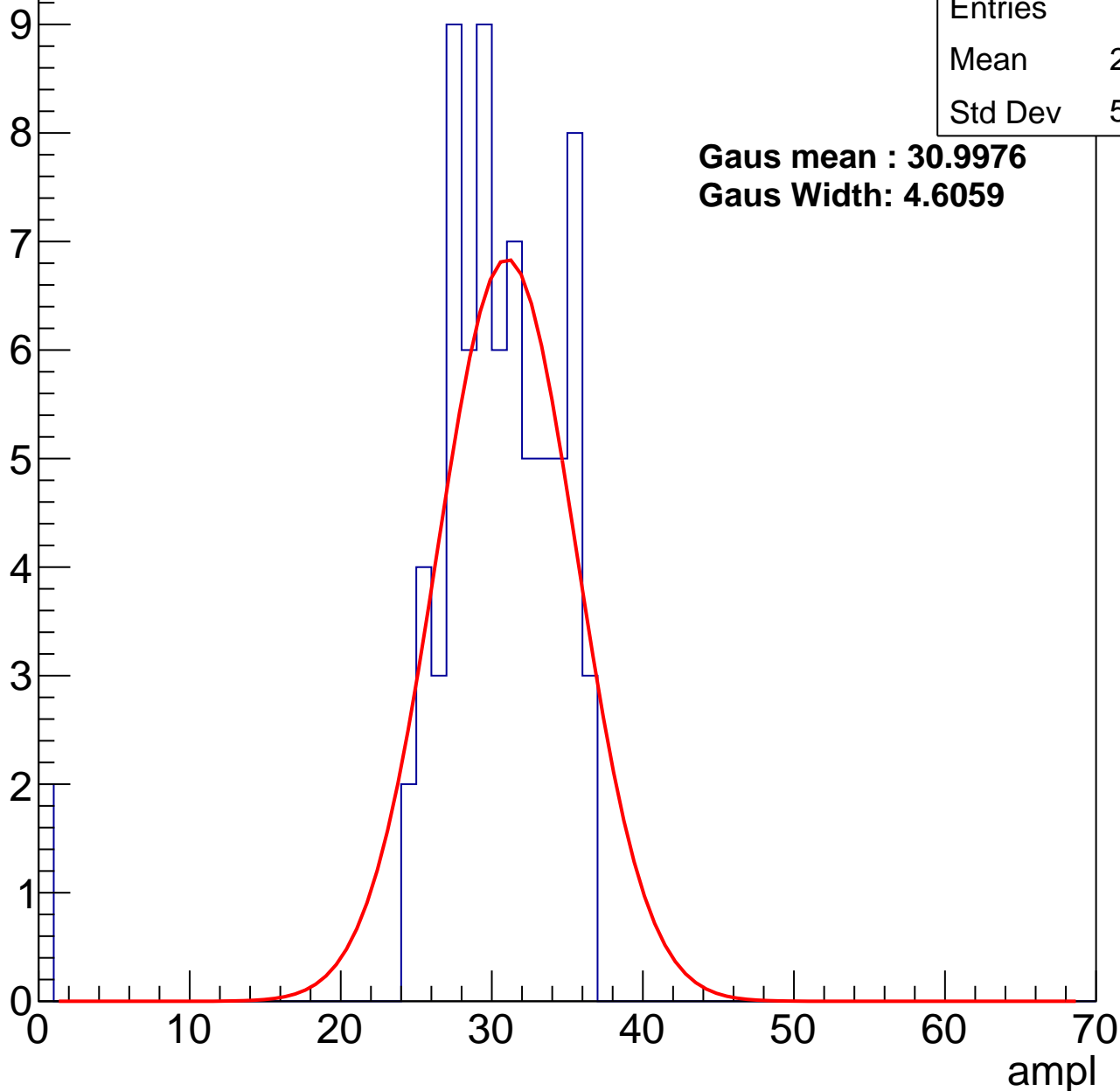
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.43
Std Dev	5.893

Gaus mean : 30.9976

Gaus Width: 4.6059



B1L100S, U25-ch11, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	36.74
Std Dev	3.677

Gaus mean : 37.6002

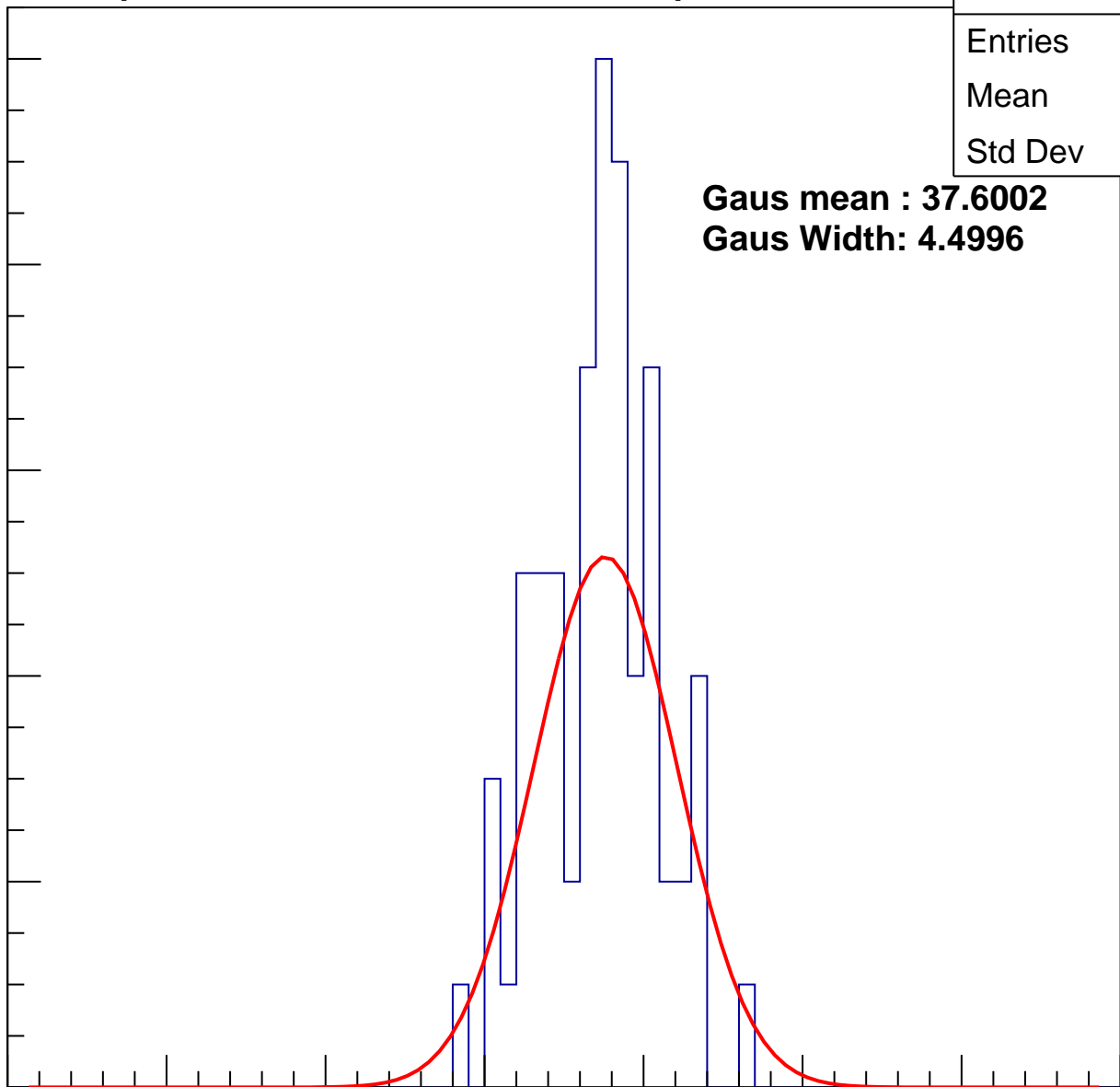
Gaus Width: 4.4996

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch11, adc2

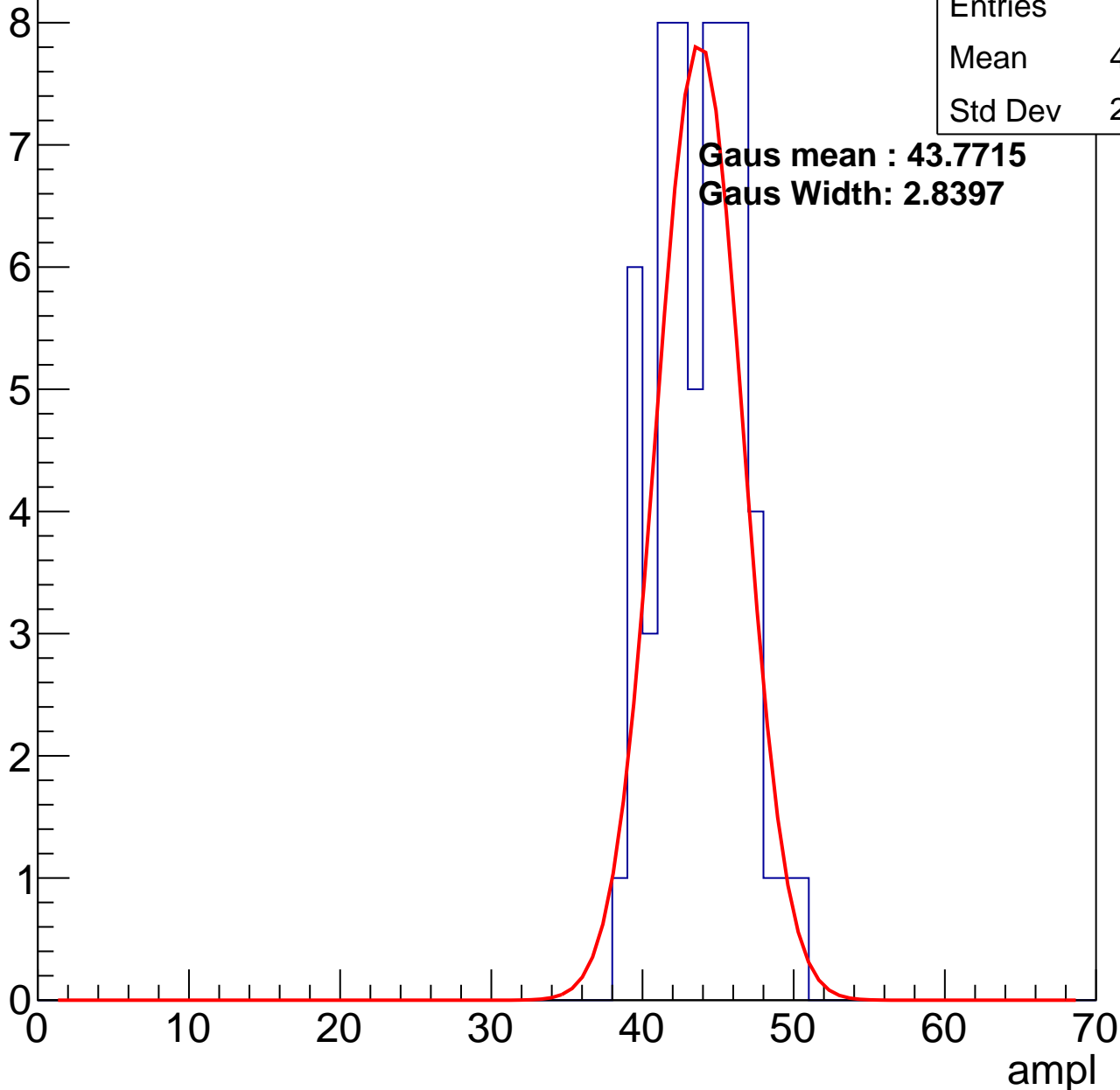
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.32
Std Dev	2.746

Gaus mean : 43.7715

Gaus Width: 2.8397

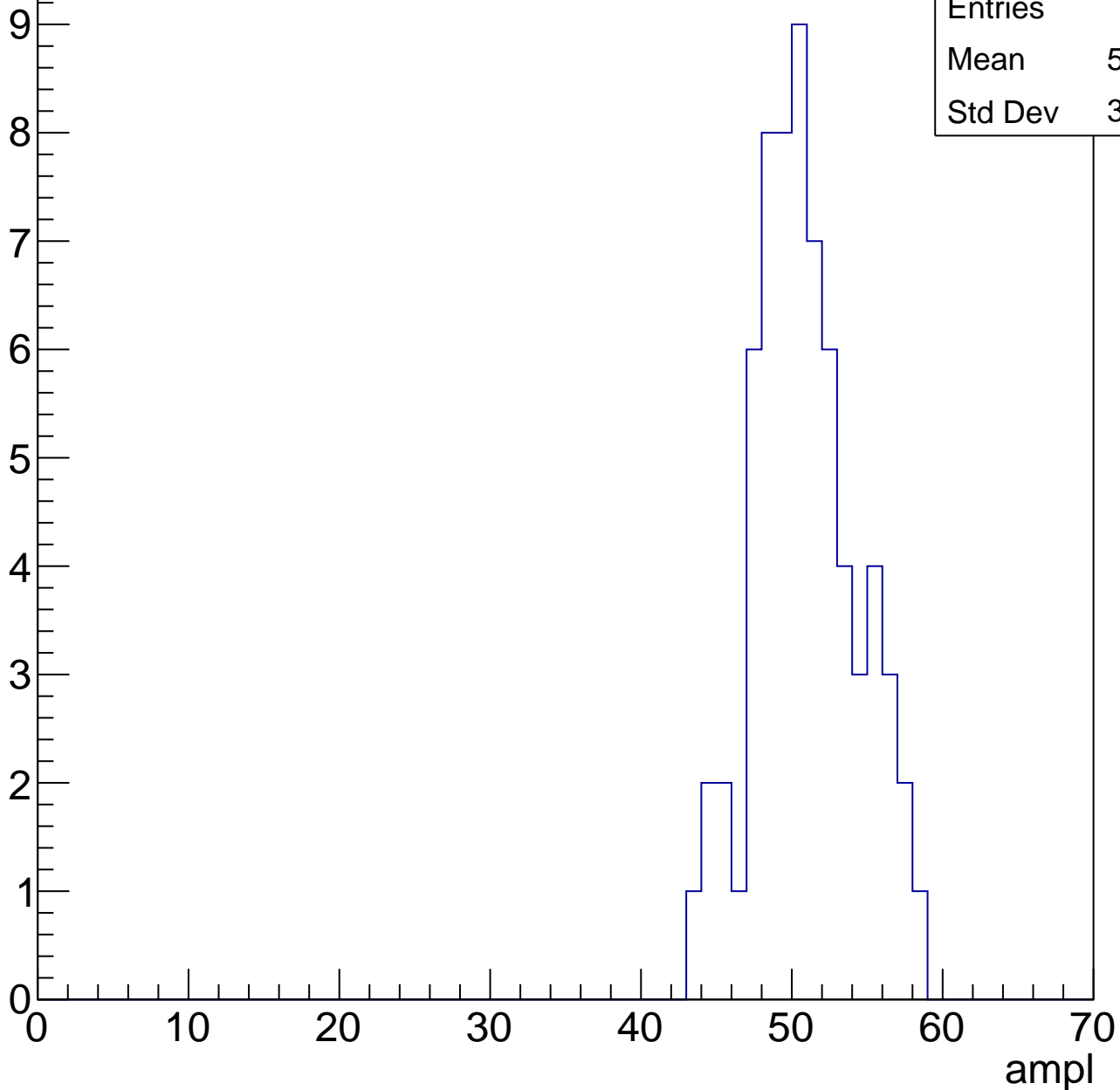


B1L100S, U25-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	50.42
Std Dev	3.355

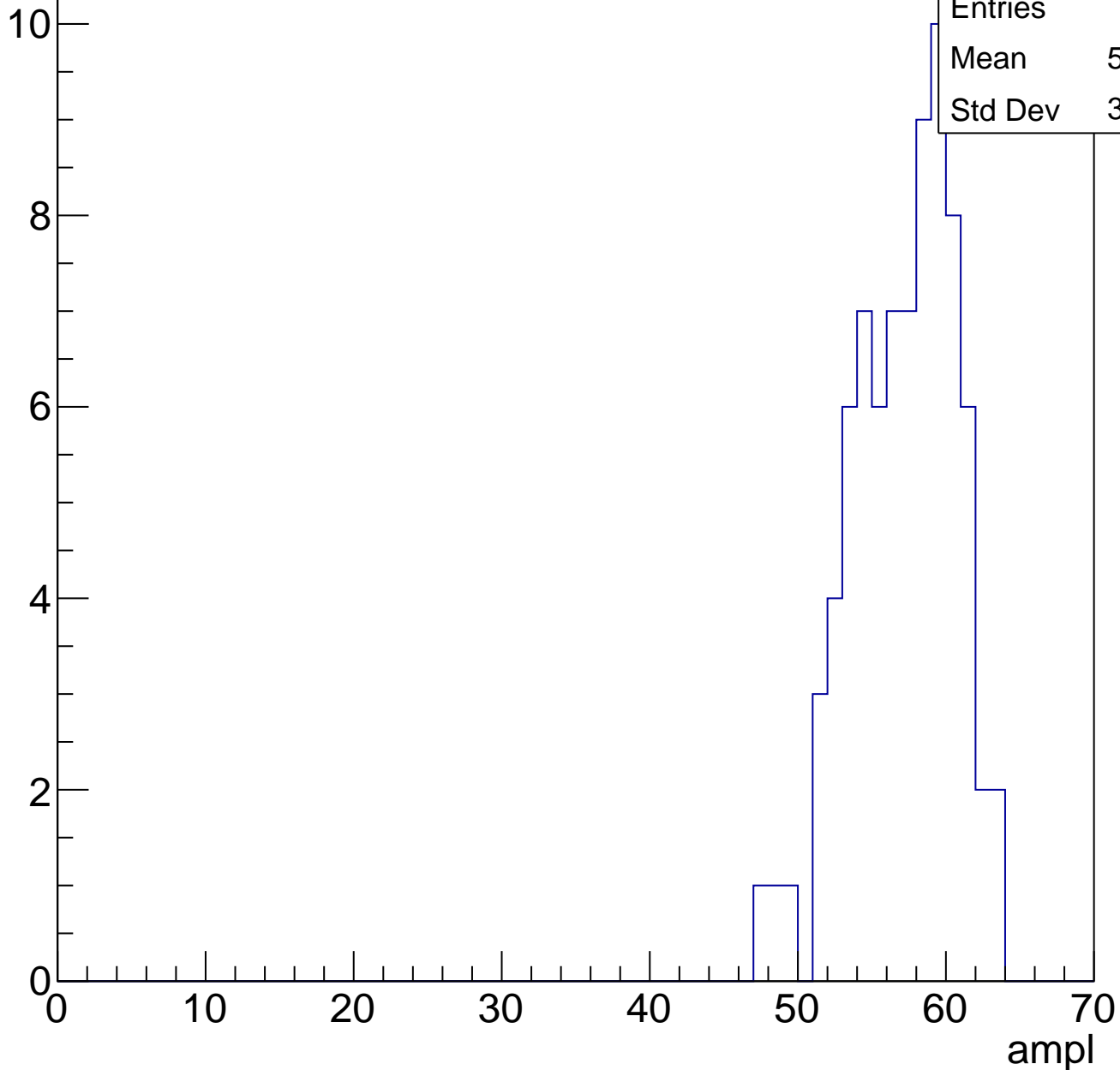


B1L100S, U25-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	56.62
Std Dev	3.483

Entry

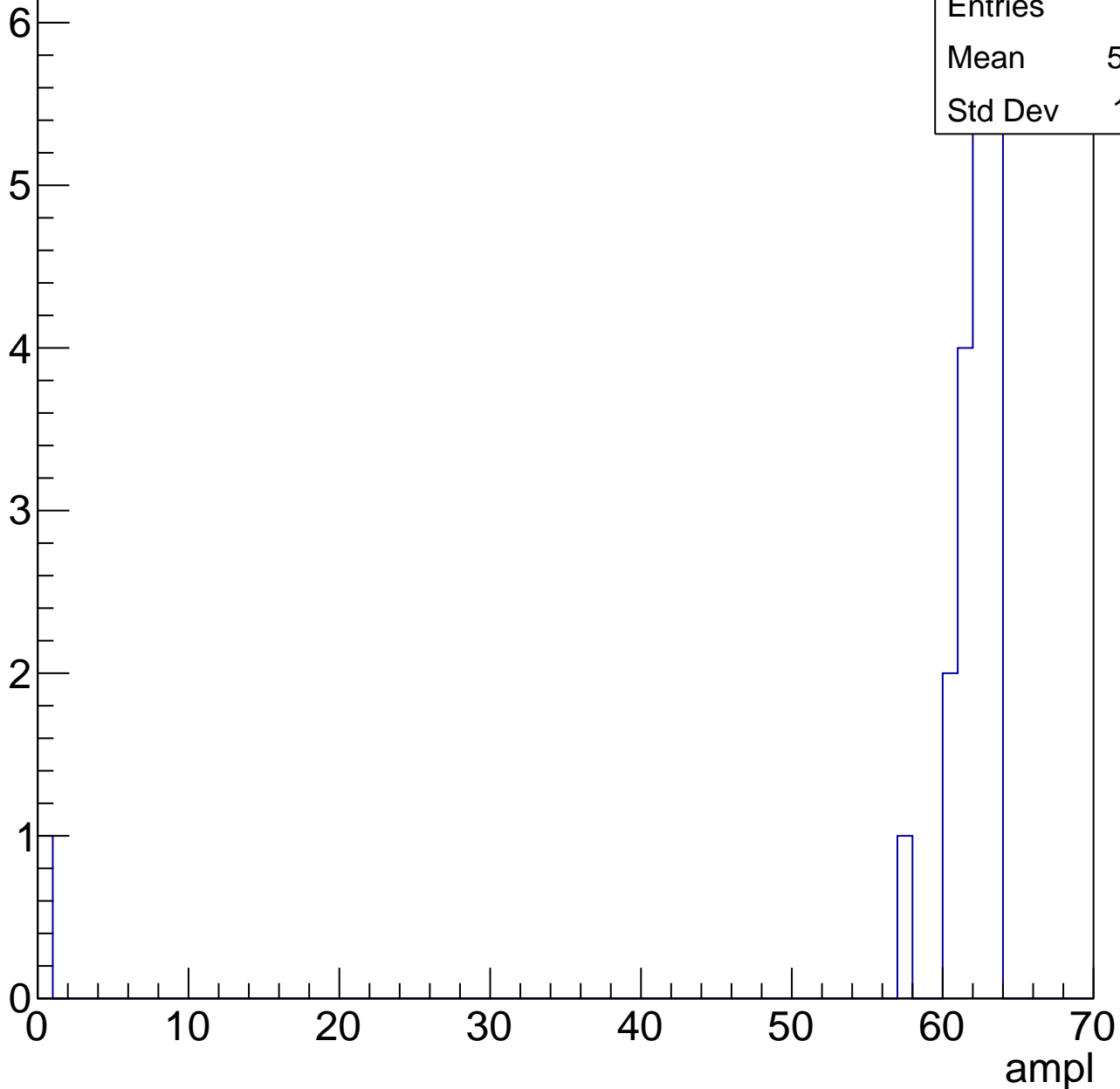


B1L100S, U25-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	58.55
Std Dev	13.51



B1L100S, U25-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch12, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

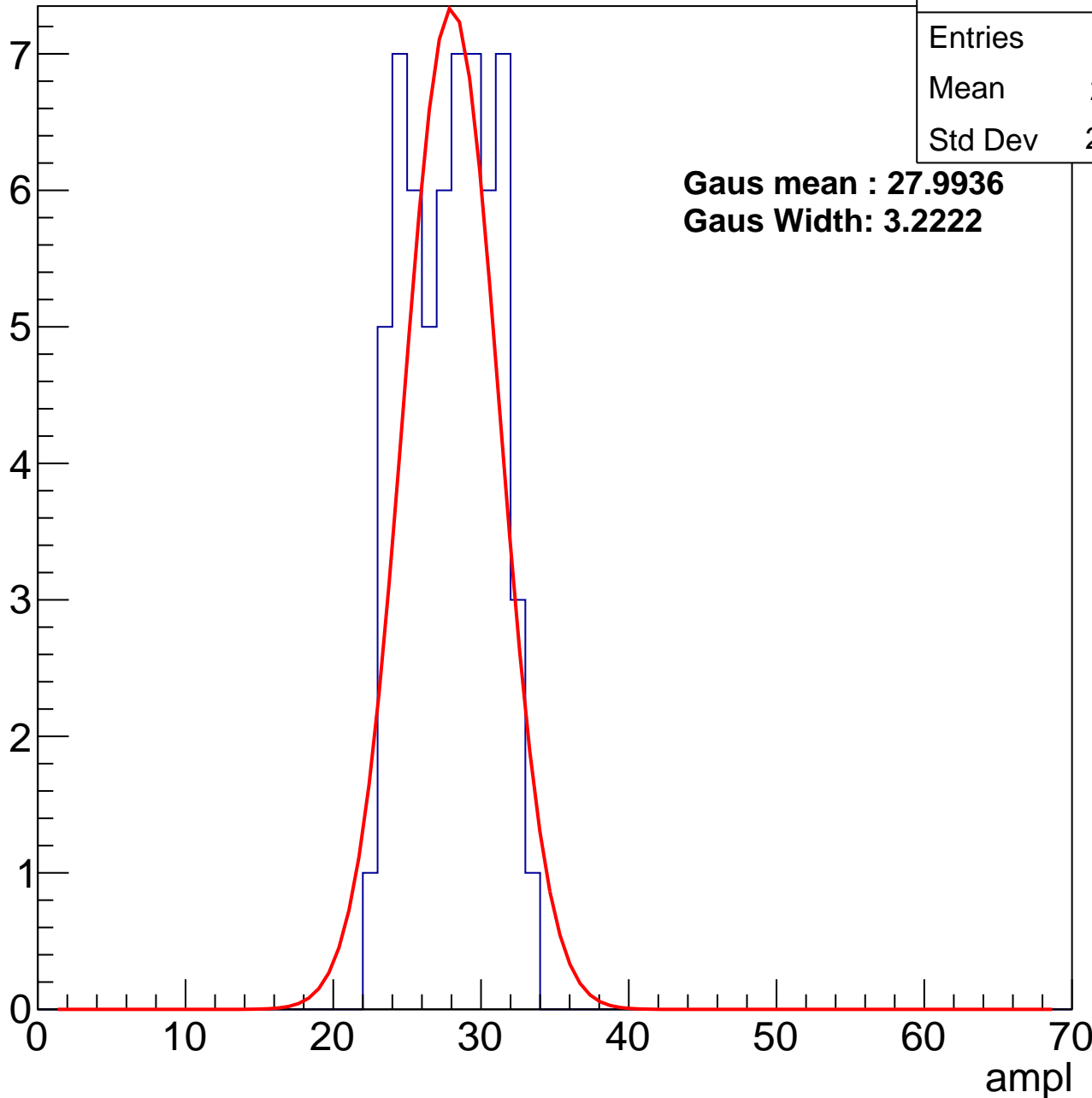
70

ampl

Entries	61
Mean	27.41
Std Dev	2.859

Gaus mean : 27.9936

Gaus Width: 3.2222



B1L100S, U25-ch12, adc1

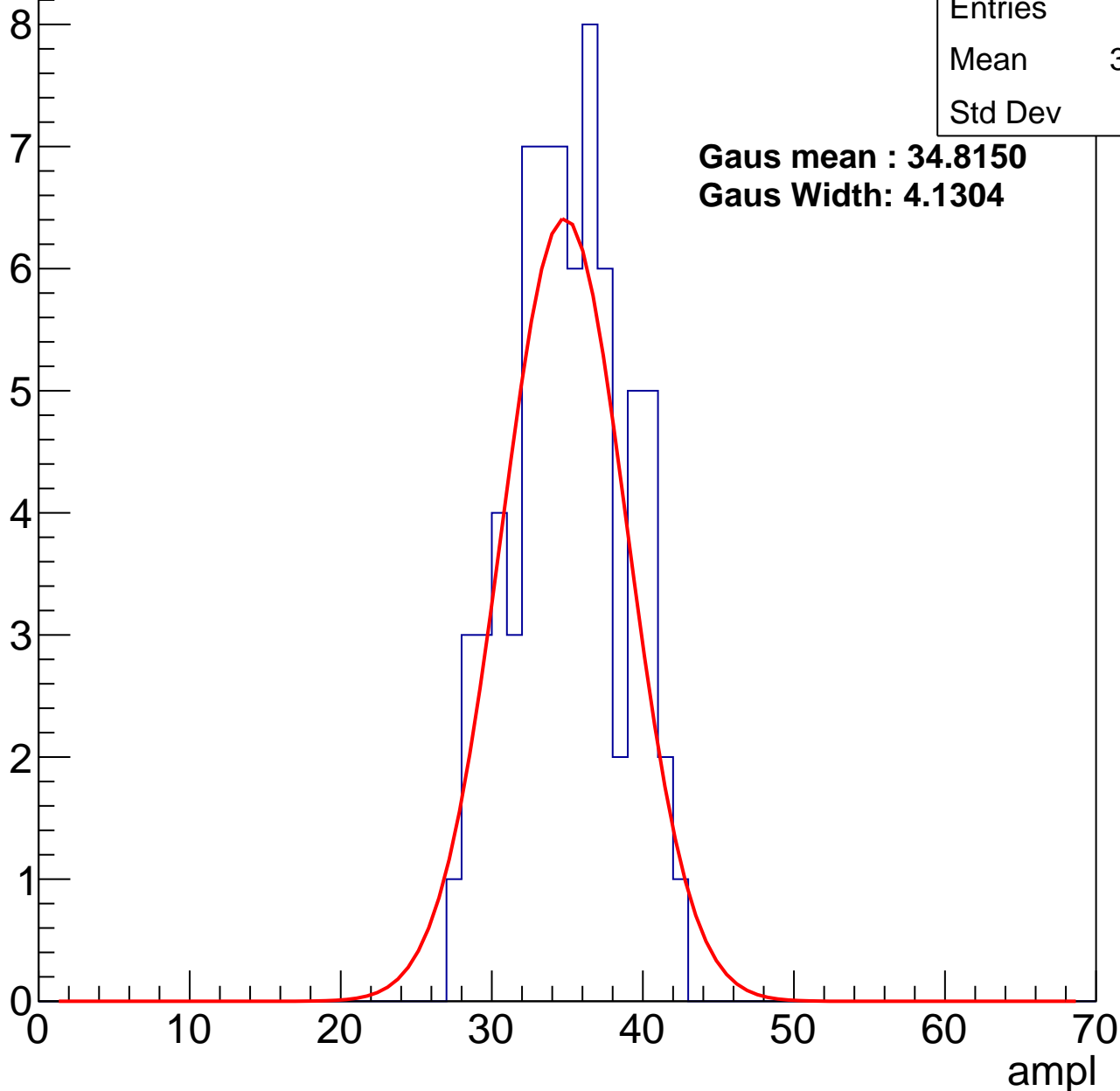
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.56
Std Dev	3.64

Gaus mean : 34.8150

Gaus Width: 4.1304



B1L100S, U25-ch12, adc2

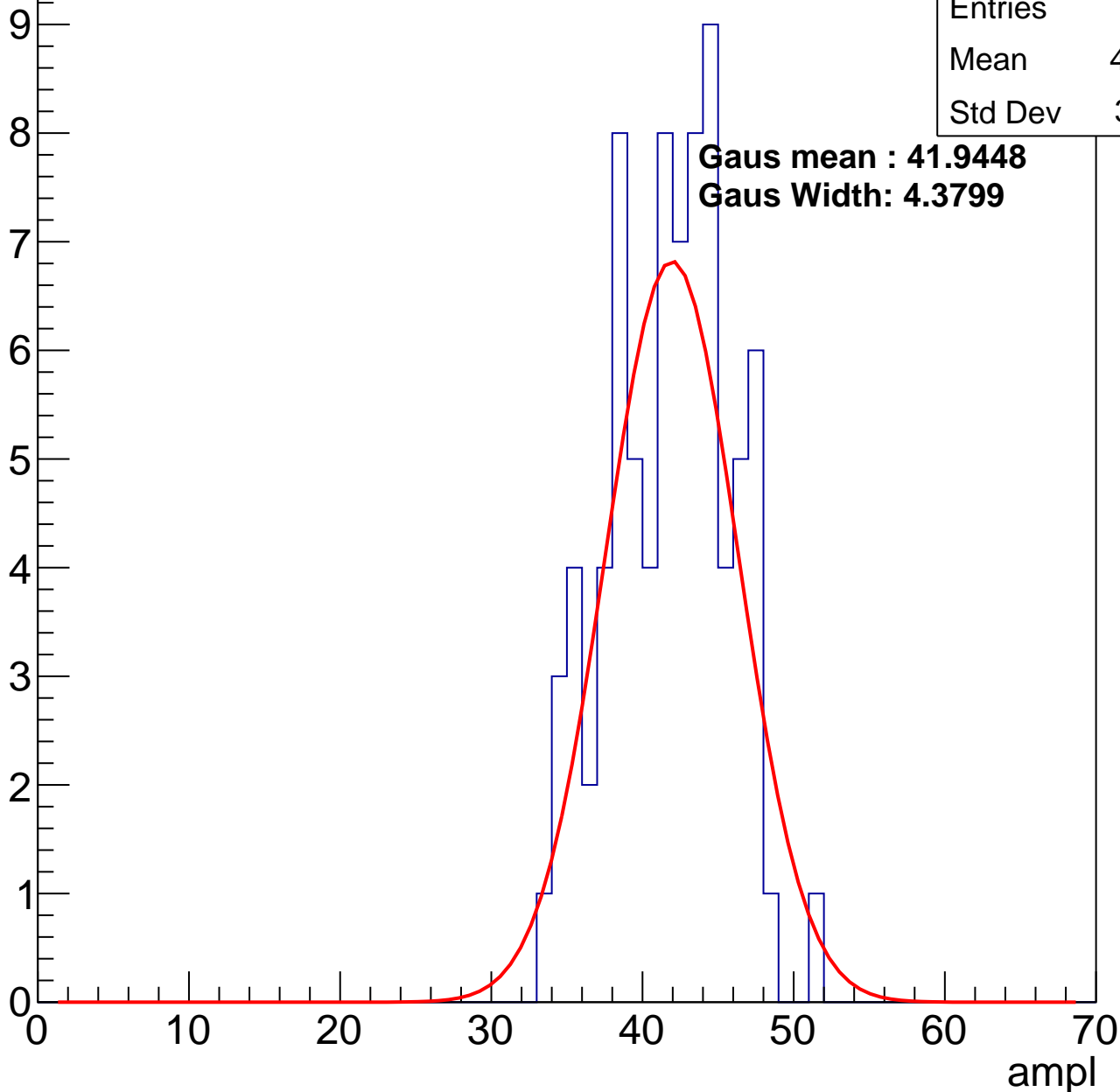
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	41.34
Std Dev	3.921

Gaus mean : 41.9448

Gaus Width: 4.3799

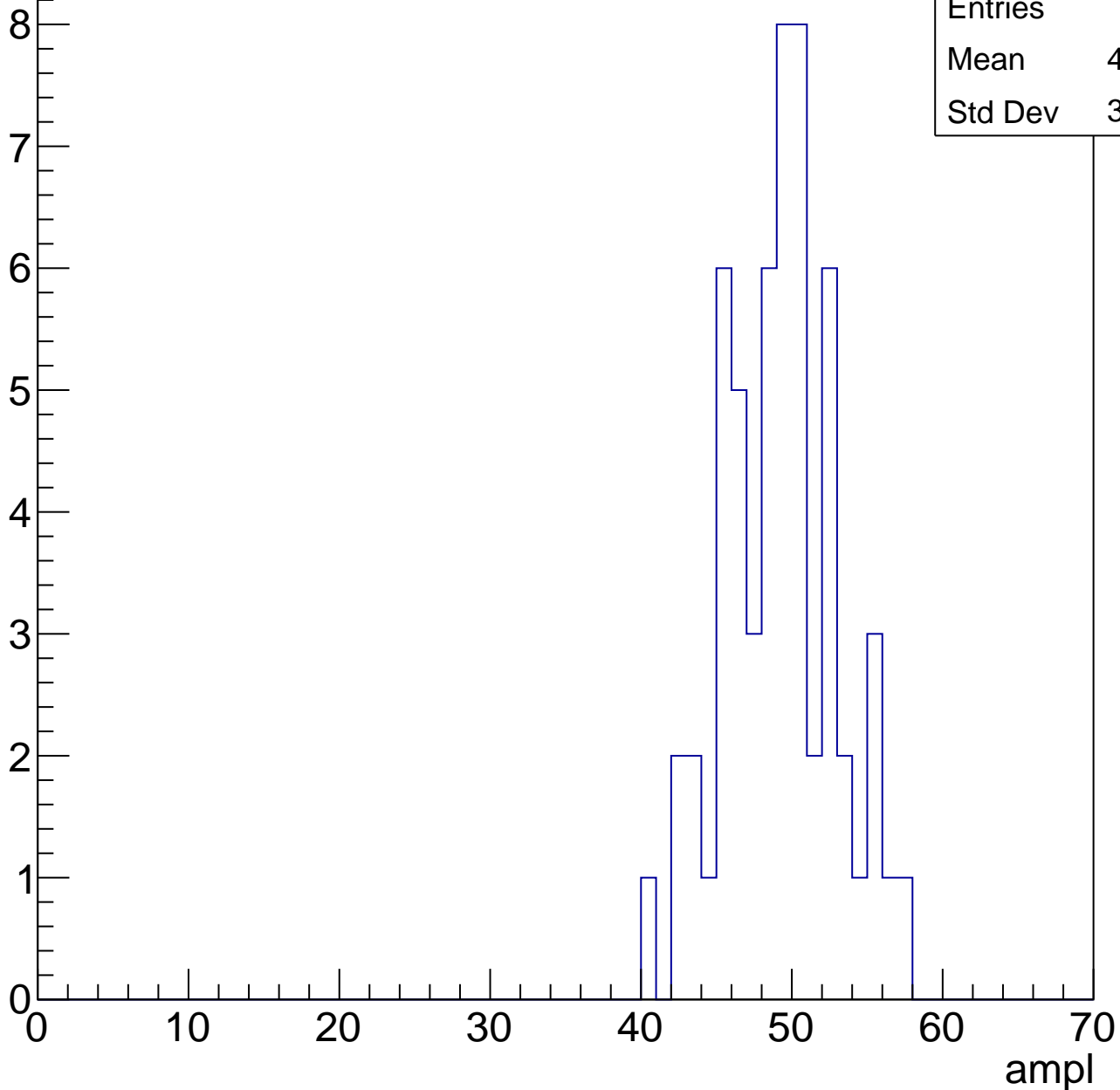


B1L100S, U25-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.74
Std Dev	3.665

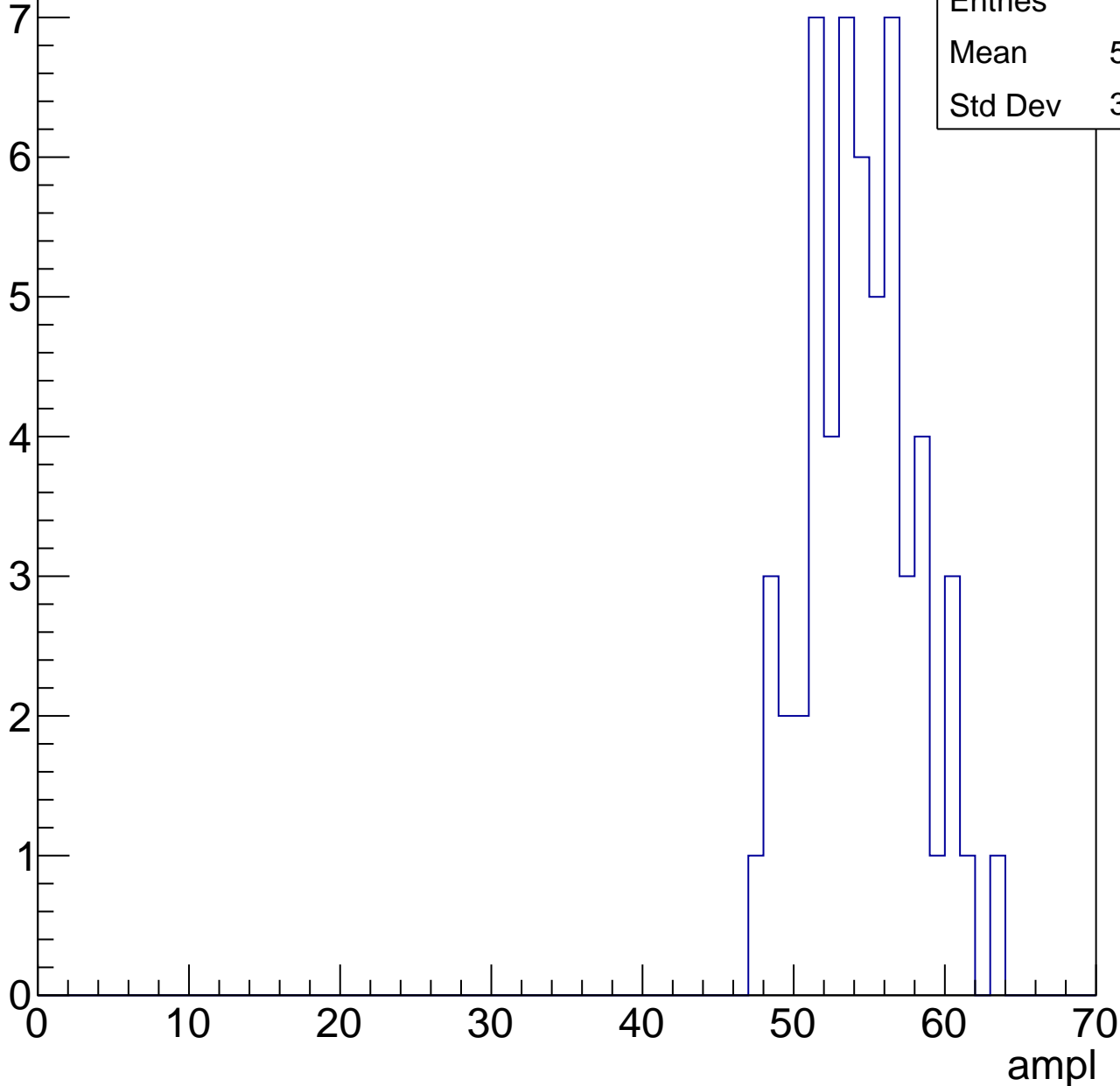


B1L100S, U25-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

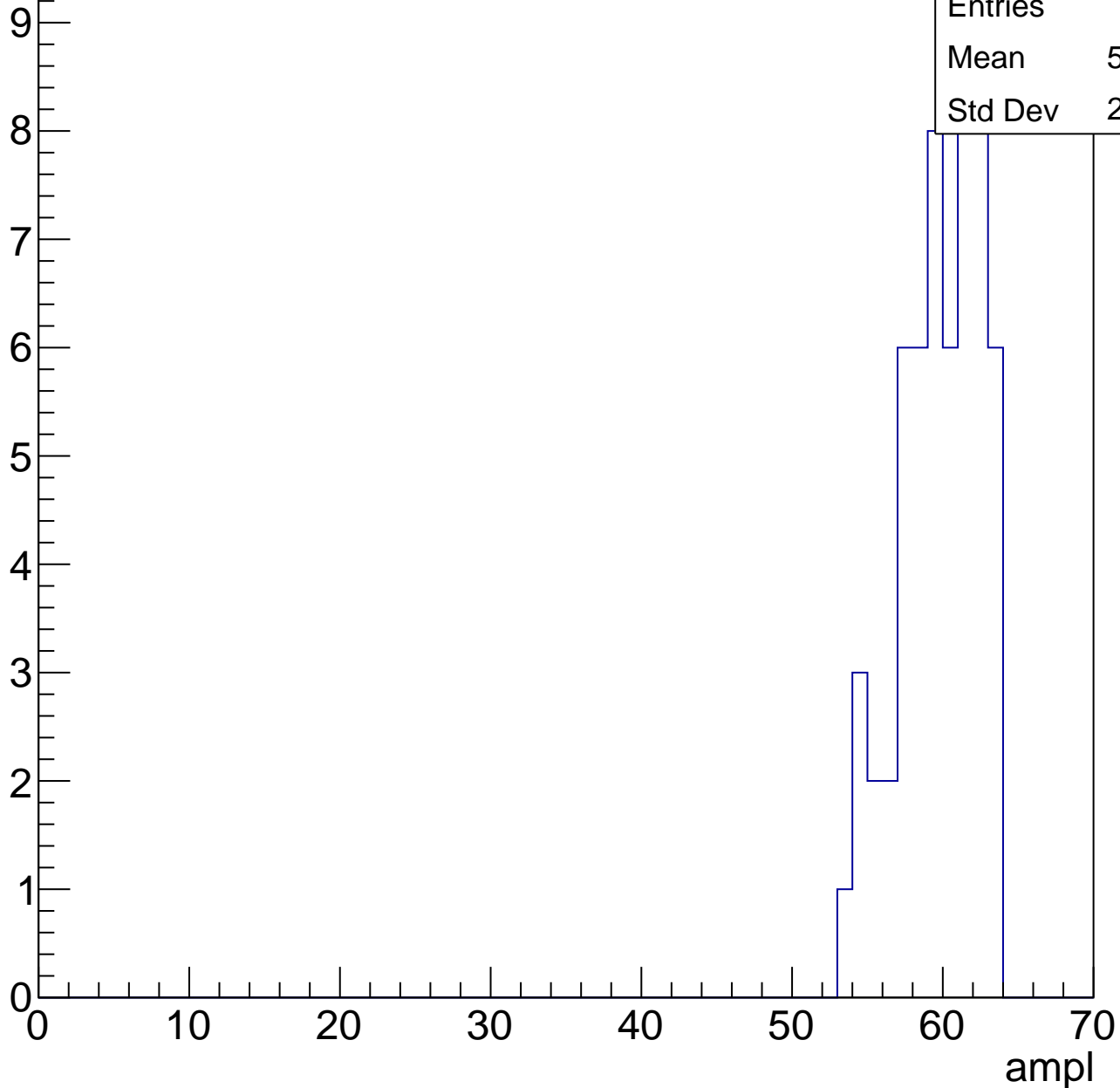
Entries	57
Mean	54.07
Std Dev	3.534



B1L100S, U25-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	57
Mean	59.35
Std Dev	2.646

B1L100S, U25-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

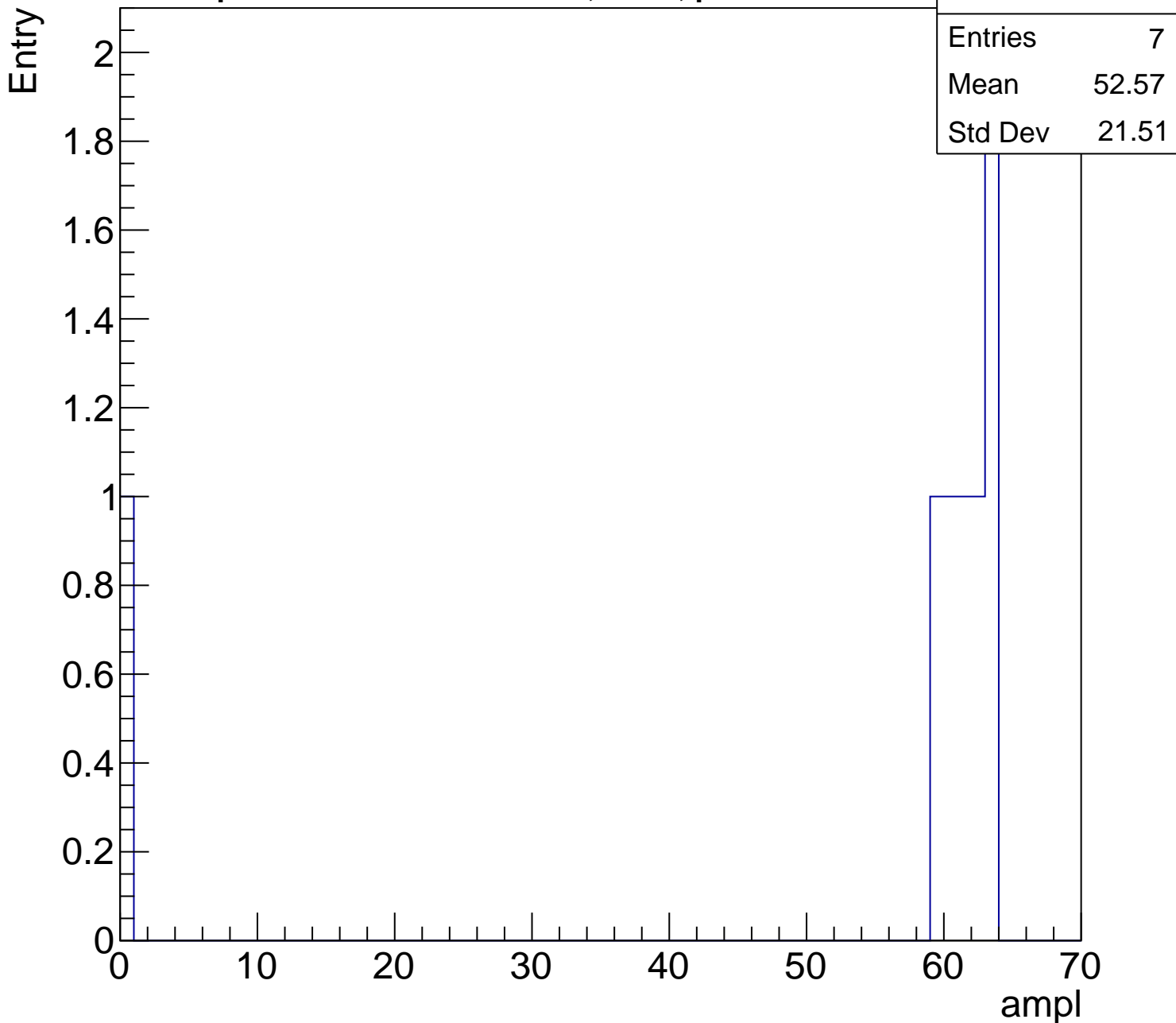
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.57
Std Dev	21.51

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch13, adc0

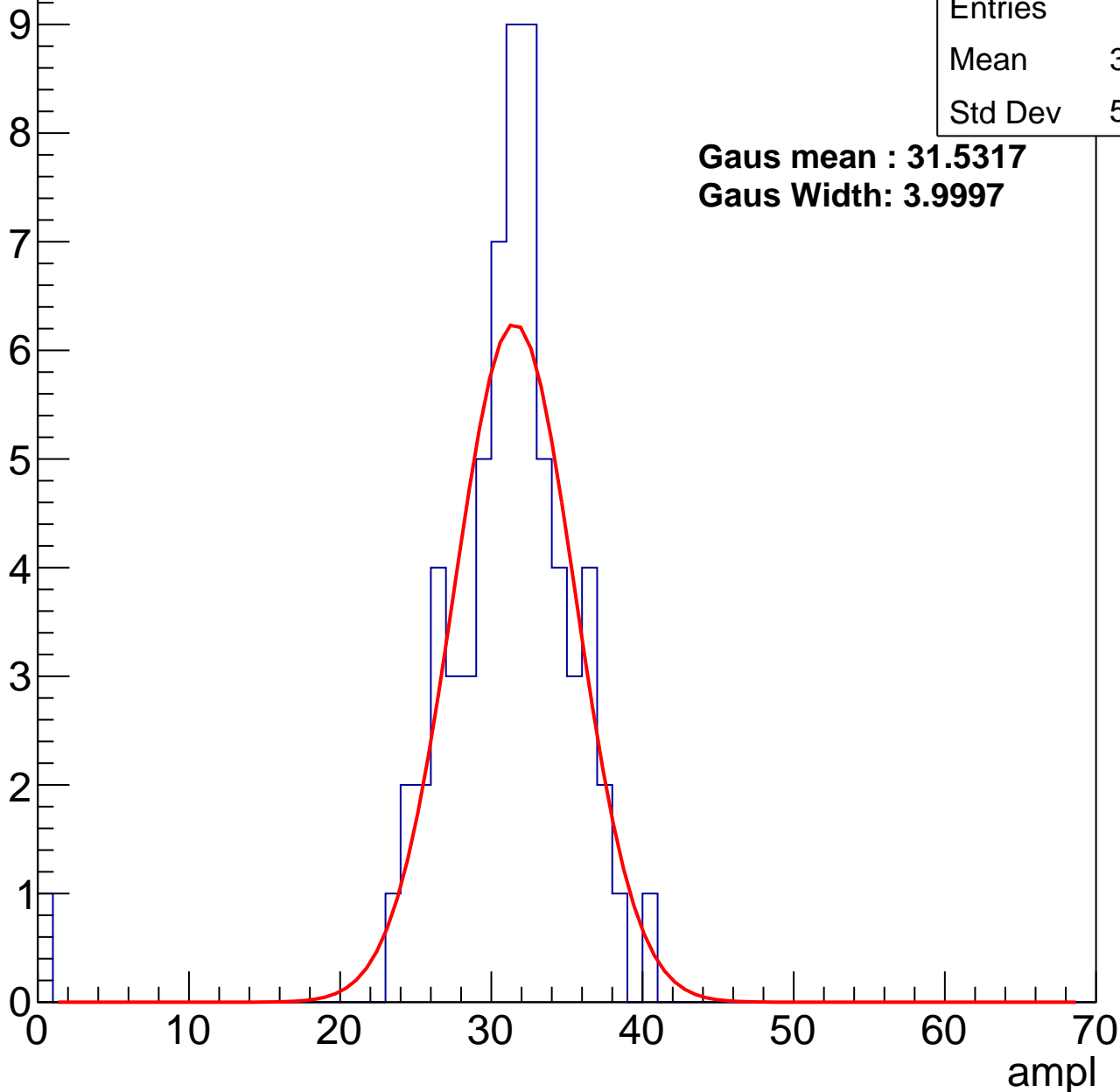
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.52
Std Dev	5.217

Gaus mean : 31.5317

Gaus Width: 3.9997



B1L100S, U25-ch13, adc1

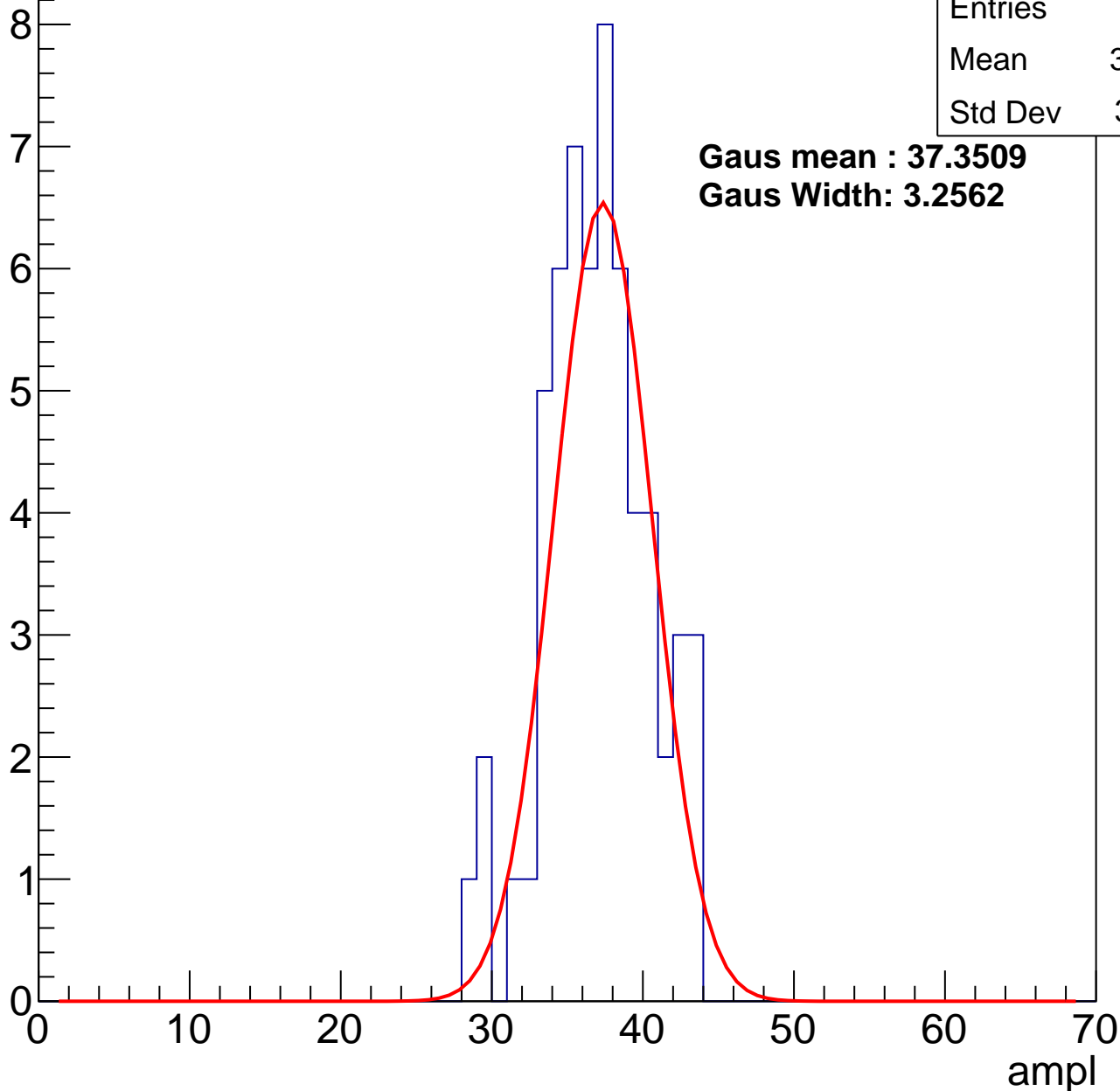
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.54
Std Dev	3.441

Gaus mean : 37.3509

Gaus Width: 3.2562



B1L100S, U25-ch13, adc2

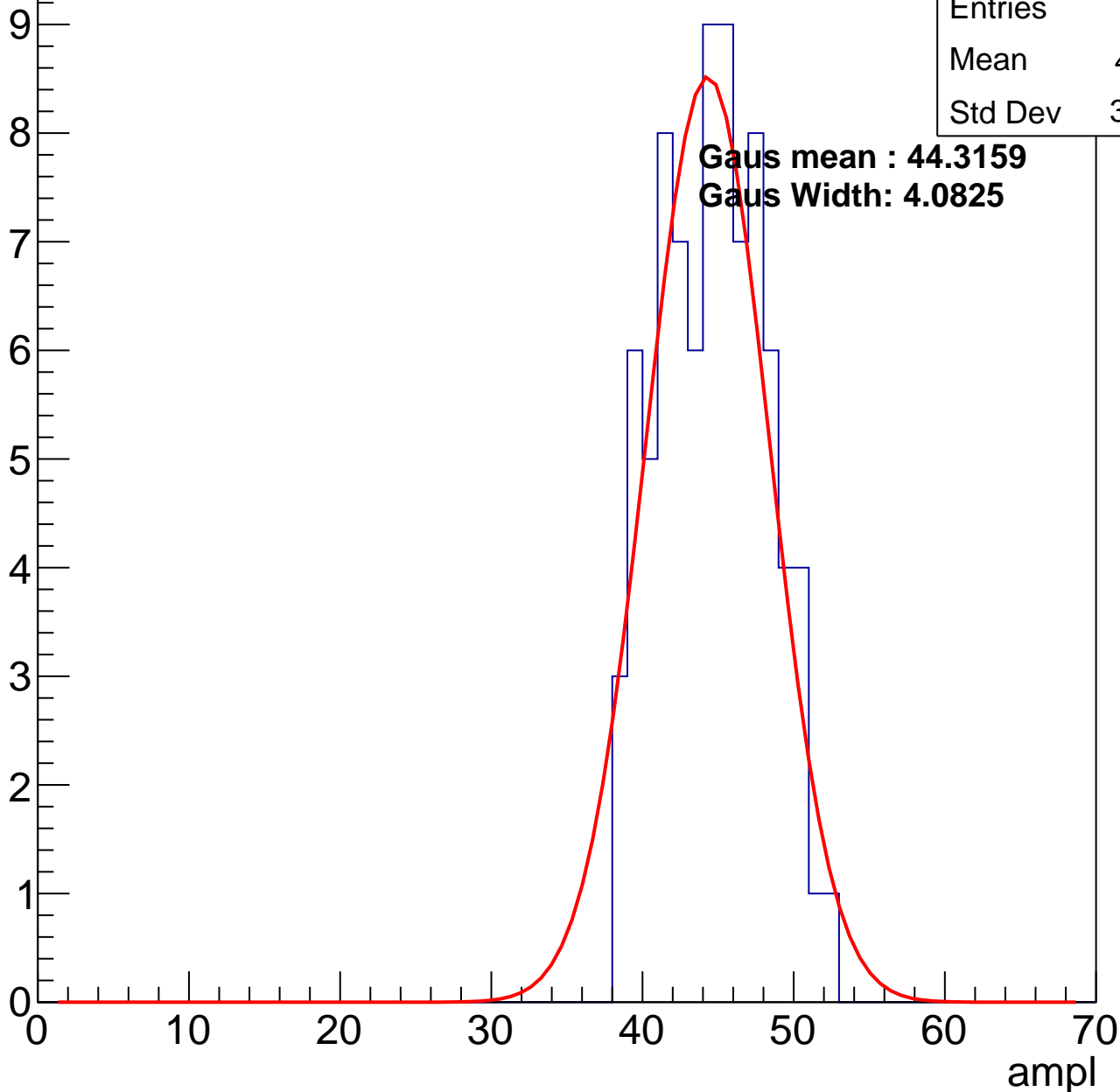
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	44.21
Std Dev	3.454

Gaus mean : 44.3159

Gaus Width: 4.0825

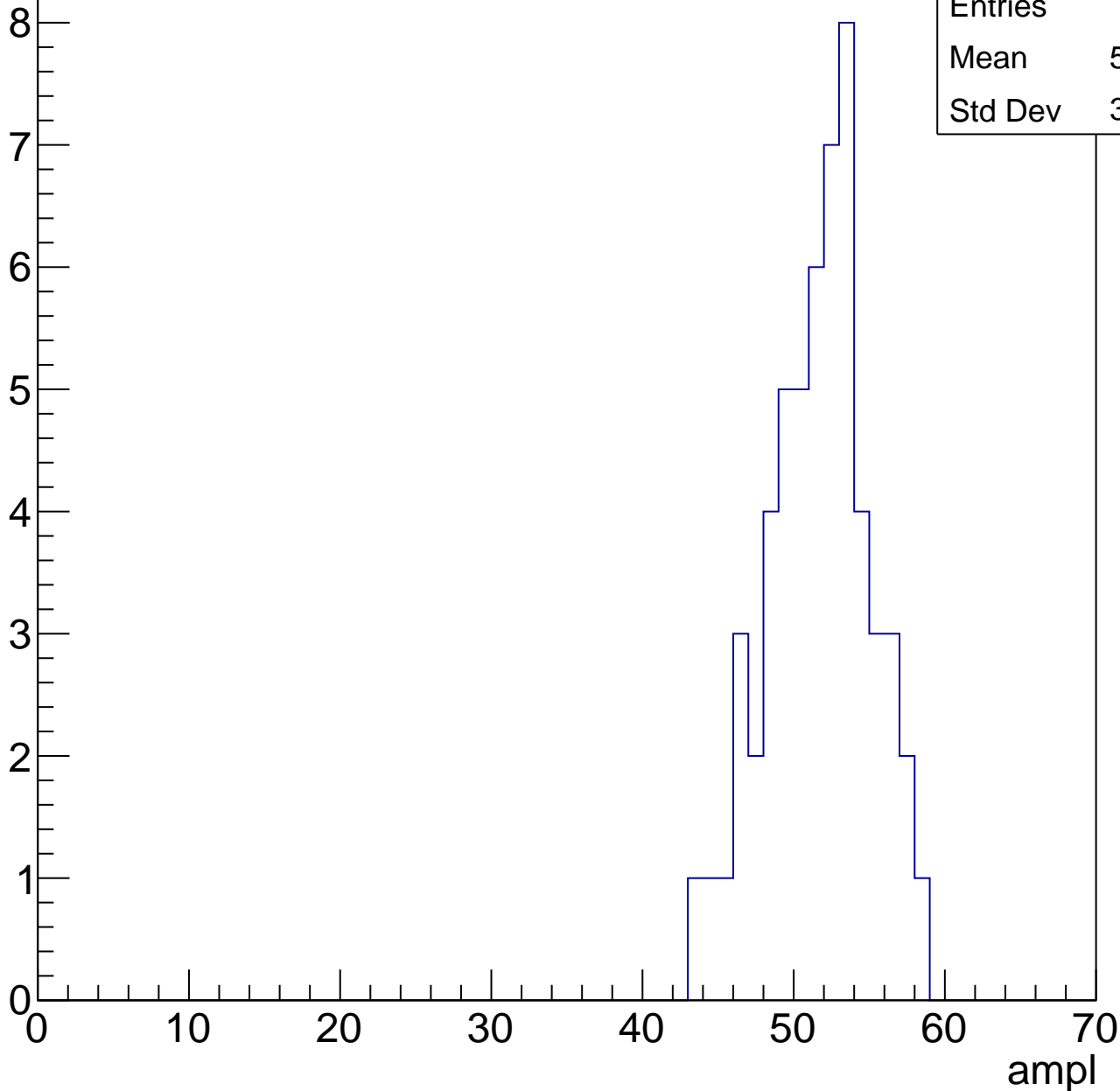


B1L100S, U25-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

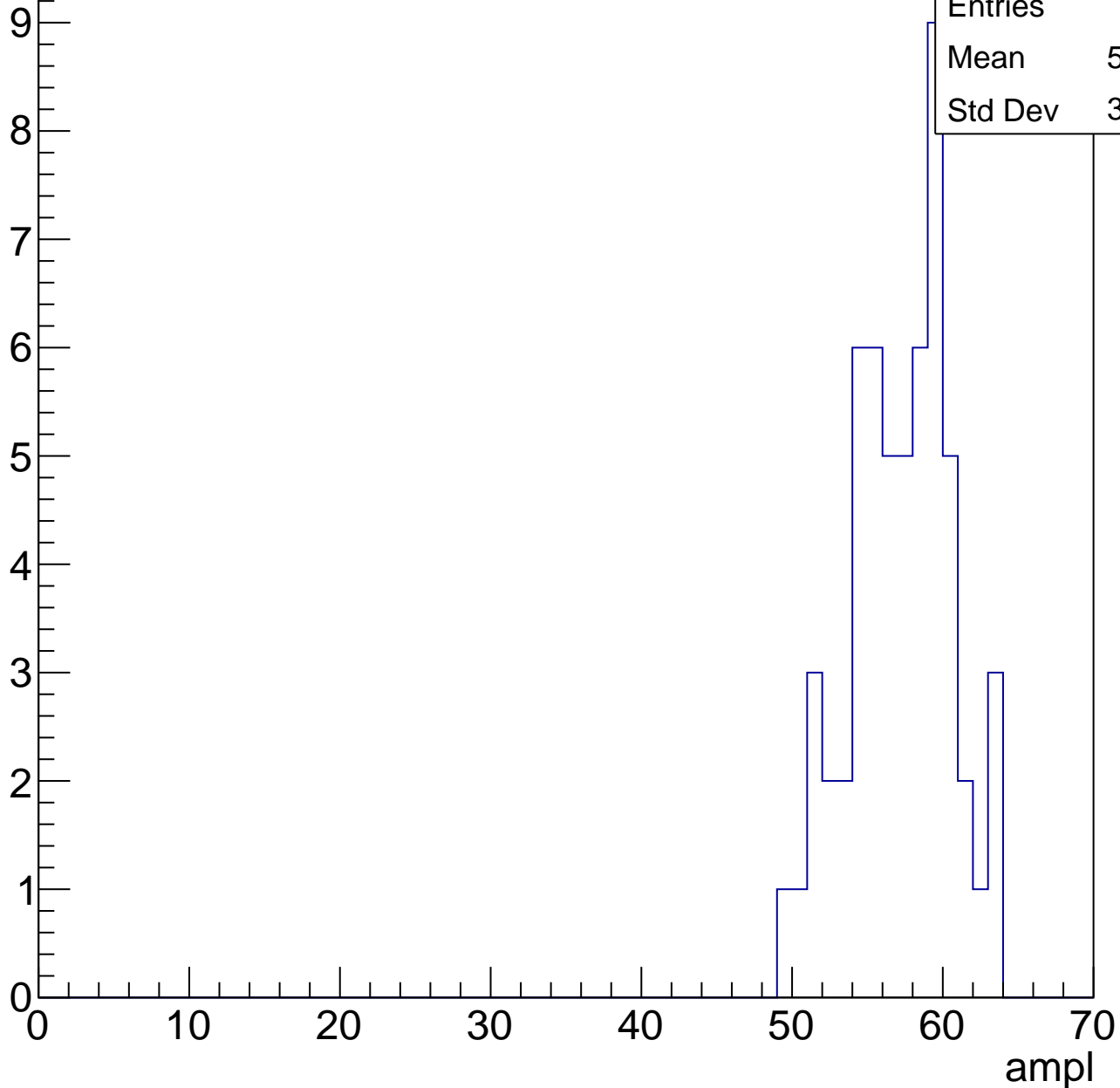
Entries	56
Mean	51.18
Std Dev	3.365



B1L100S, U25-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

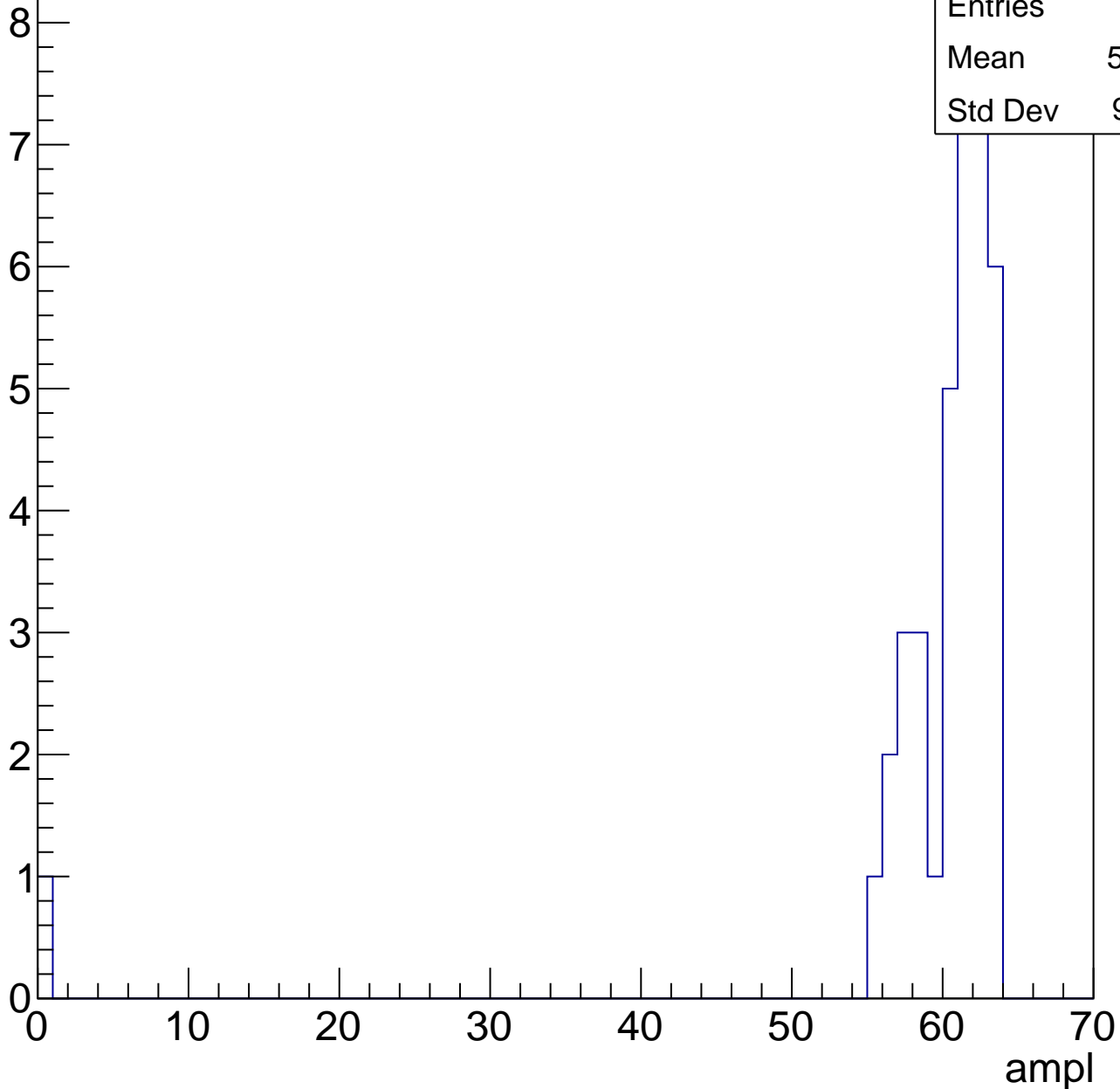


B1L100S, U25-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.76
Std Dev	9.911



B1L100S, U25-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch14, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	29.78
Std Dev	4.896

Gaus mean : 30.4984

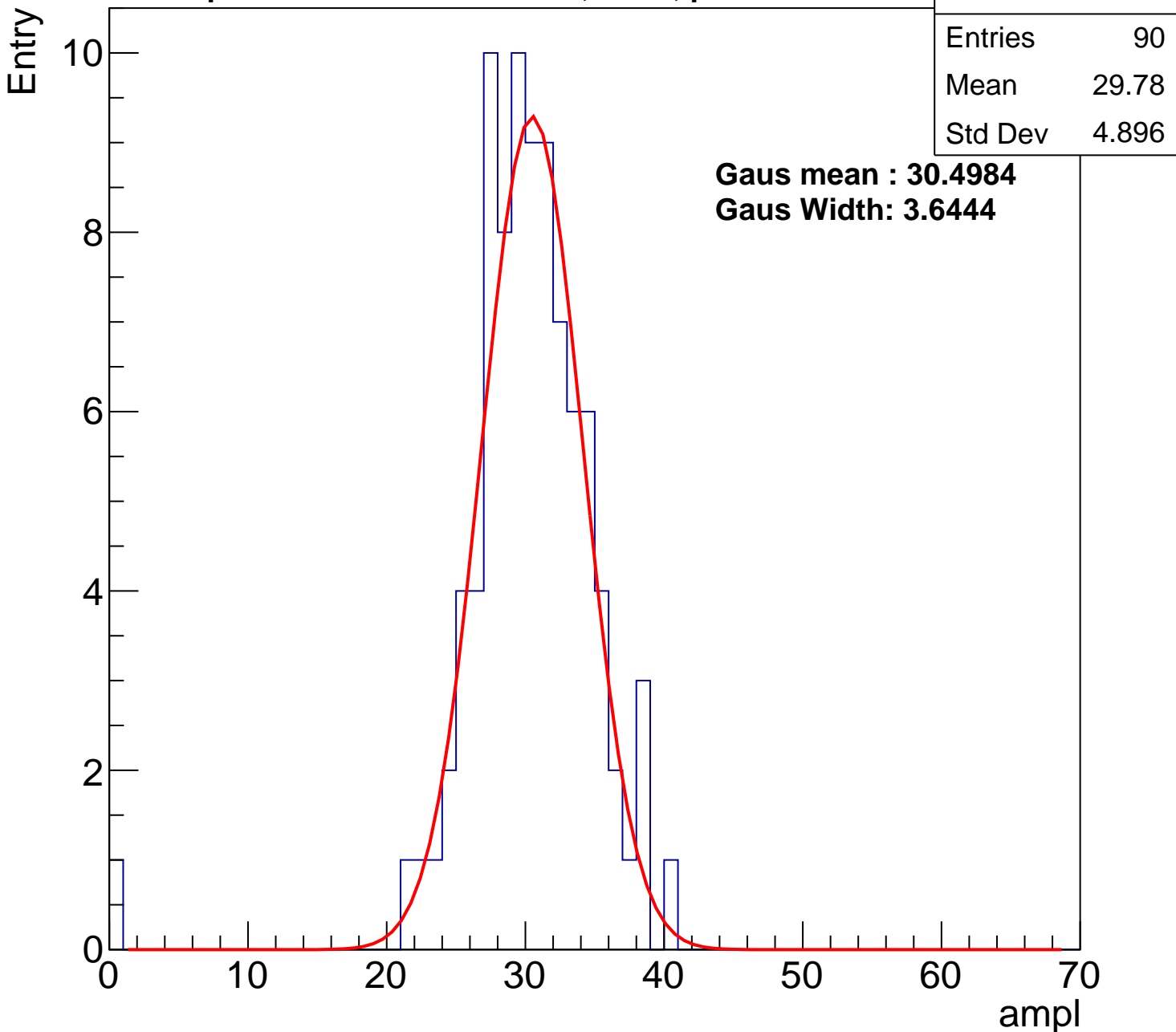
Gaus Width: 3.6444

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch14, adc1

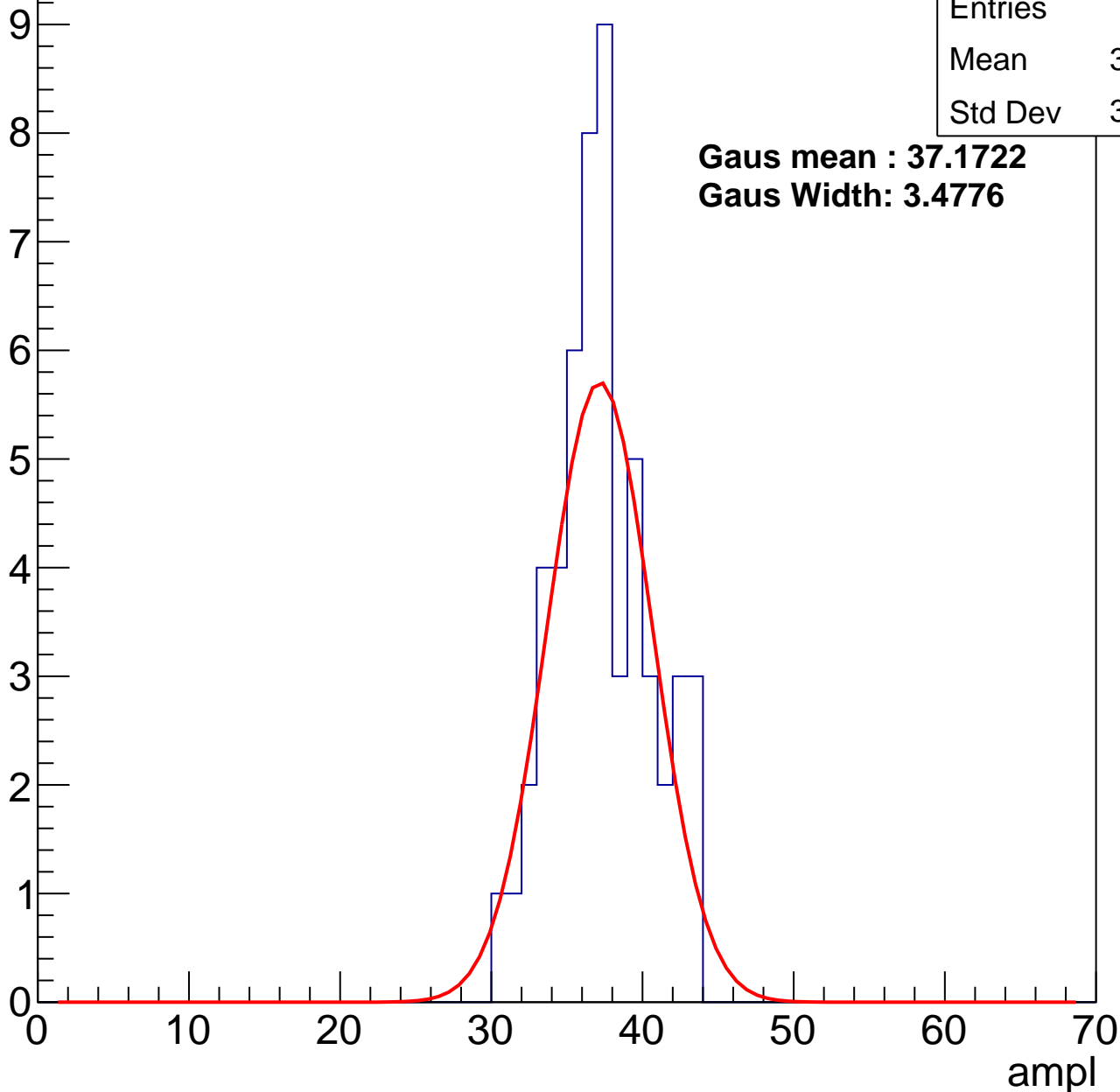
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.85
Std Dev	3.135

Gaus mean : 37.1722

Gaus Width: 3.4776



B1L100S, U25-ch14, adc2

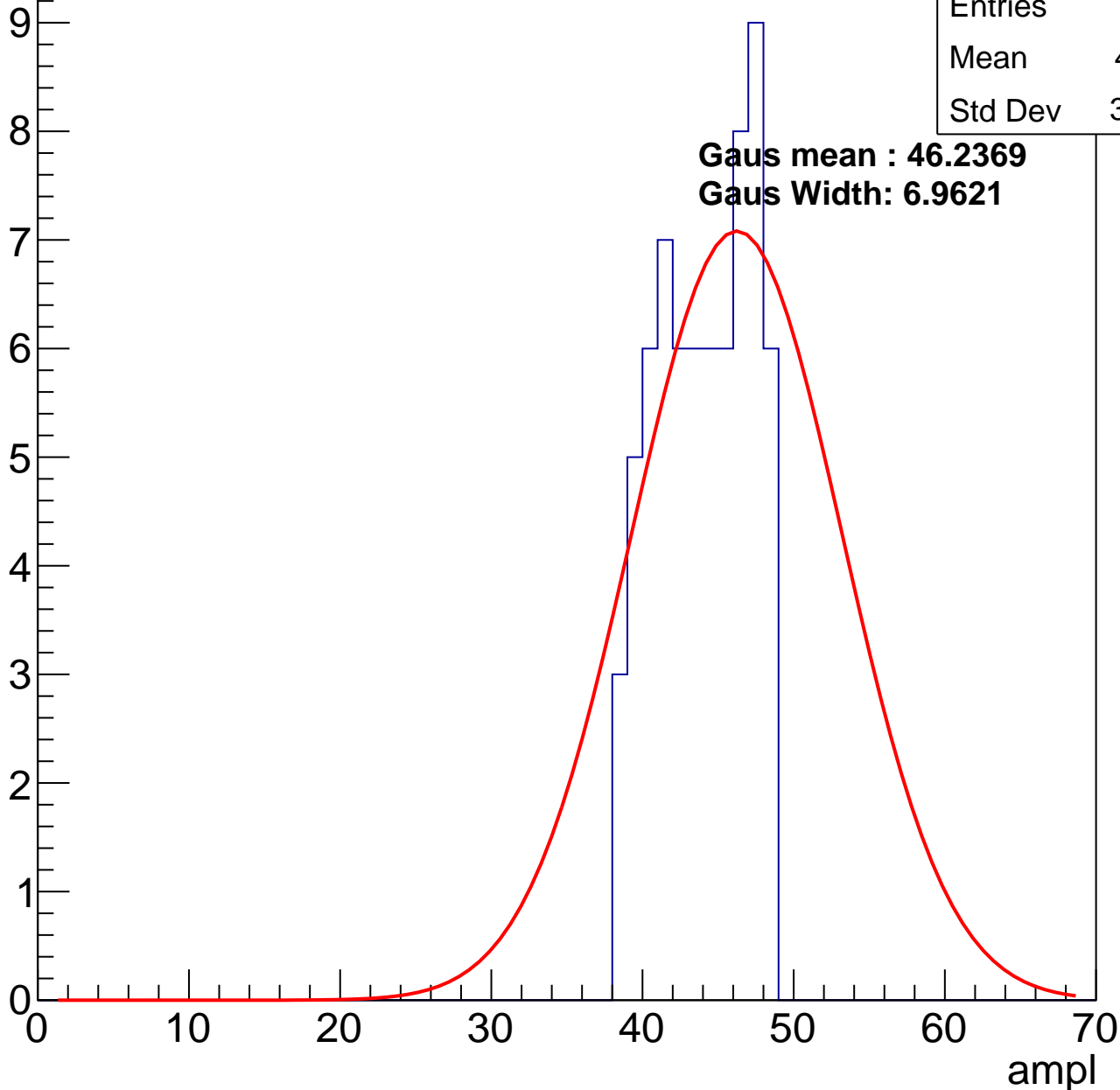
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.51
Std Dev	3.022

Gaus mean : 46.2369

Gaus Width: 6.9621



B1L100S, U25-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

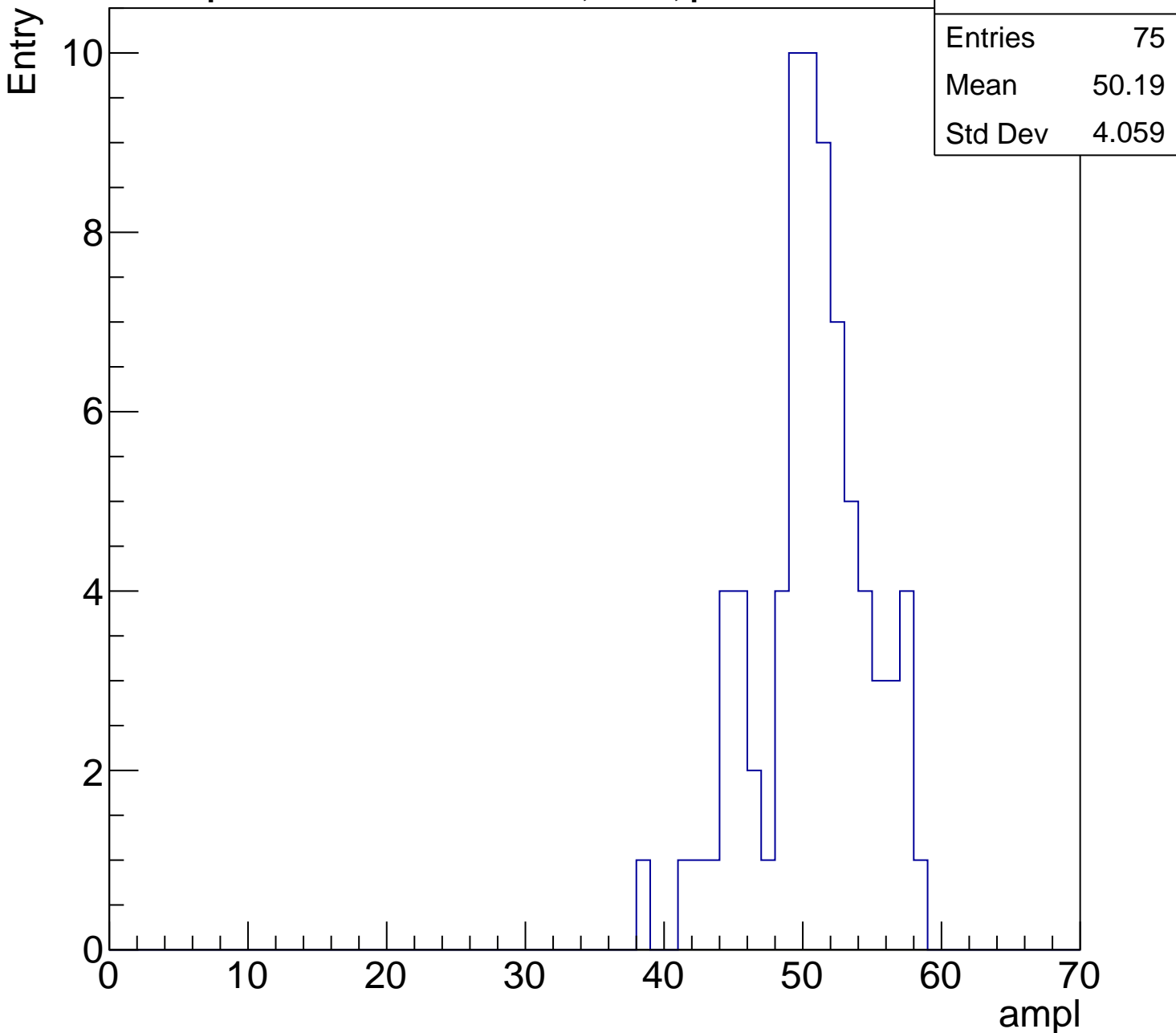
Entries	75
Mean	50.19
Std Dev	4.059

Entry

10
8
6
4
2
0

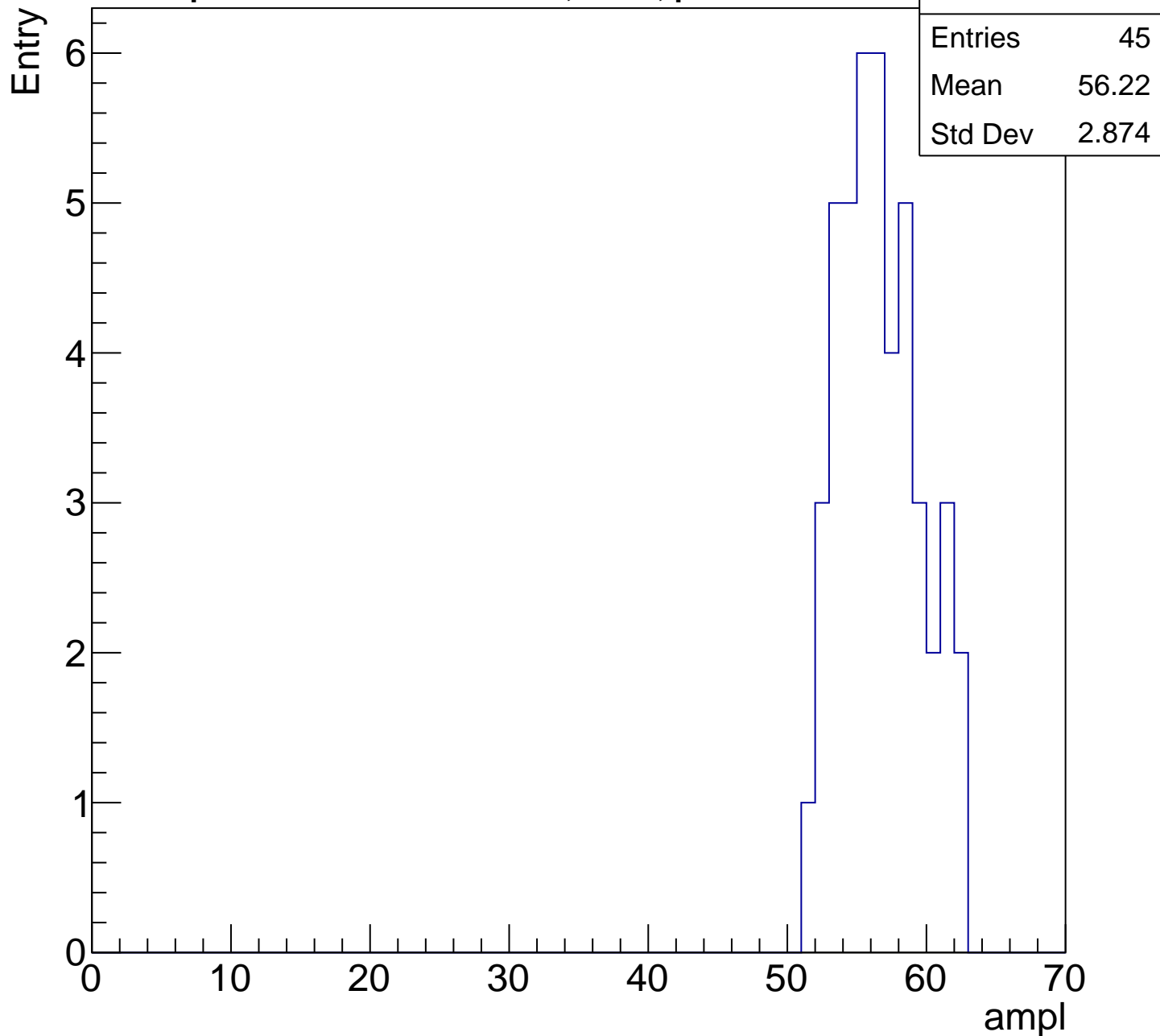
0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch14, adc5

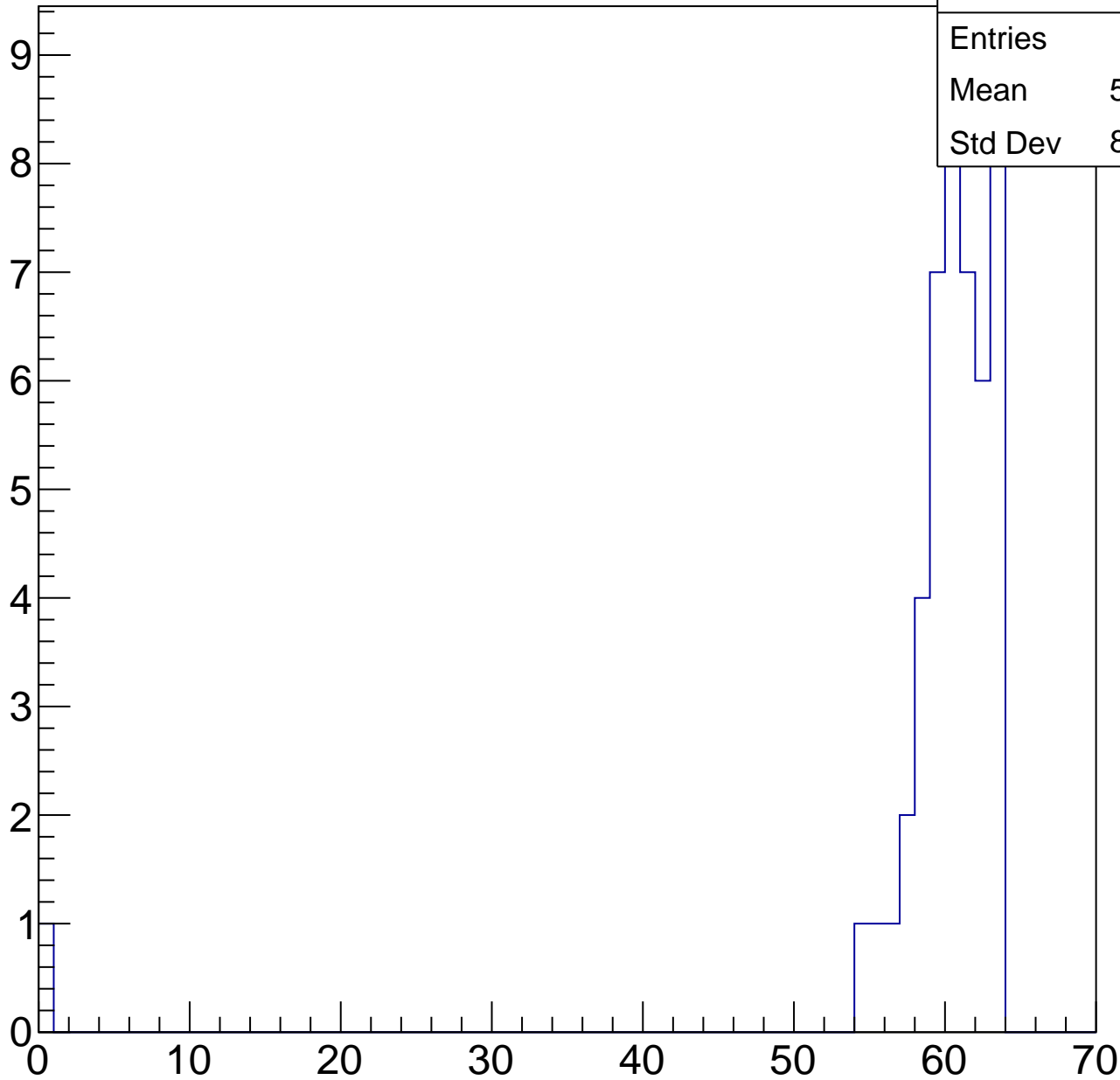
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.94
Std Dev	8.964

ampl



B1L100S, U25-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch15, adc0

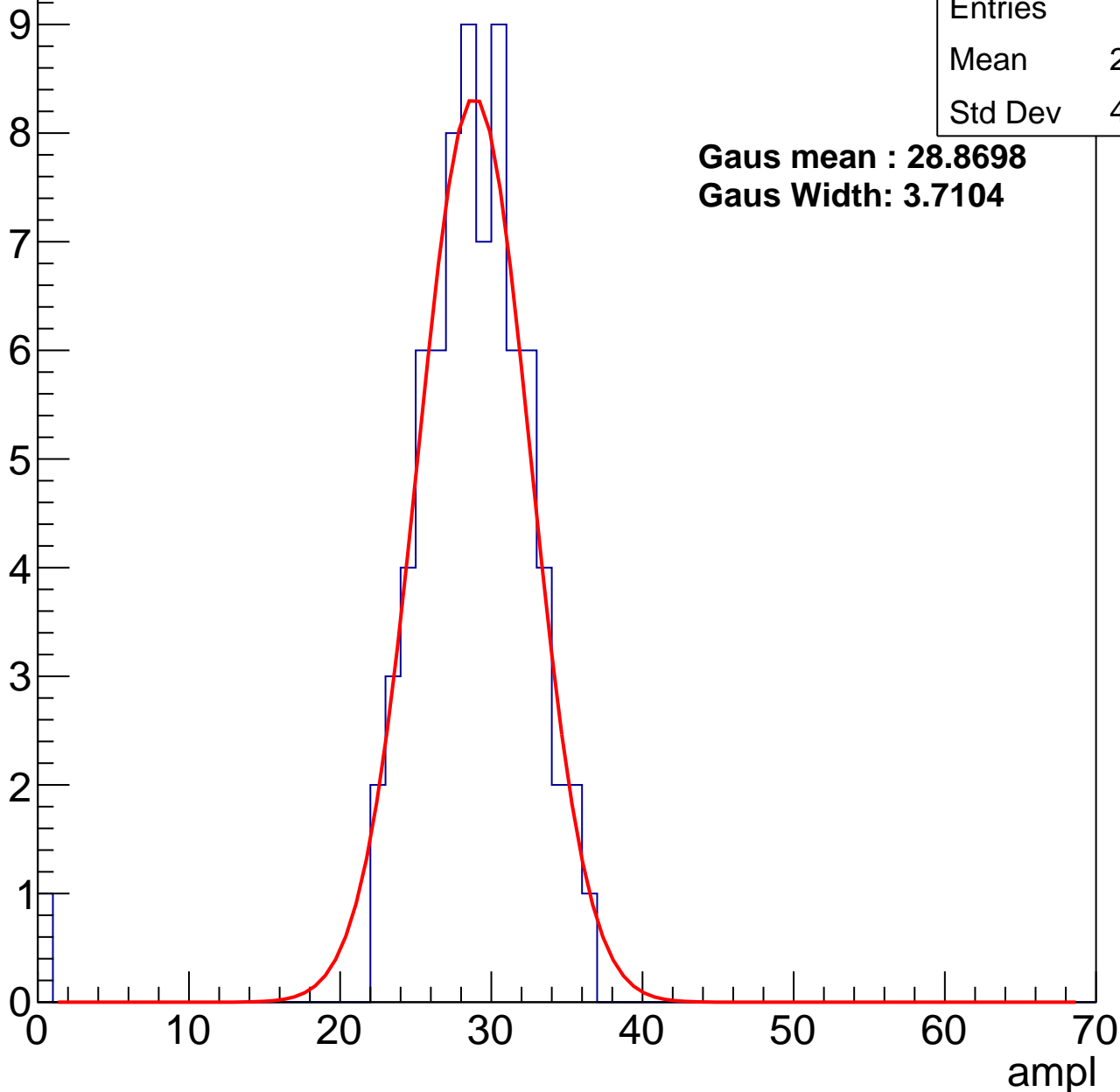
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	28.16
Std Dev	4.597

Gaus mean : 28.8698

Gaus Width: 3.7104



B1L100S, U25-ch15, adc1

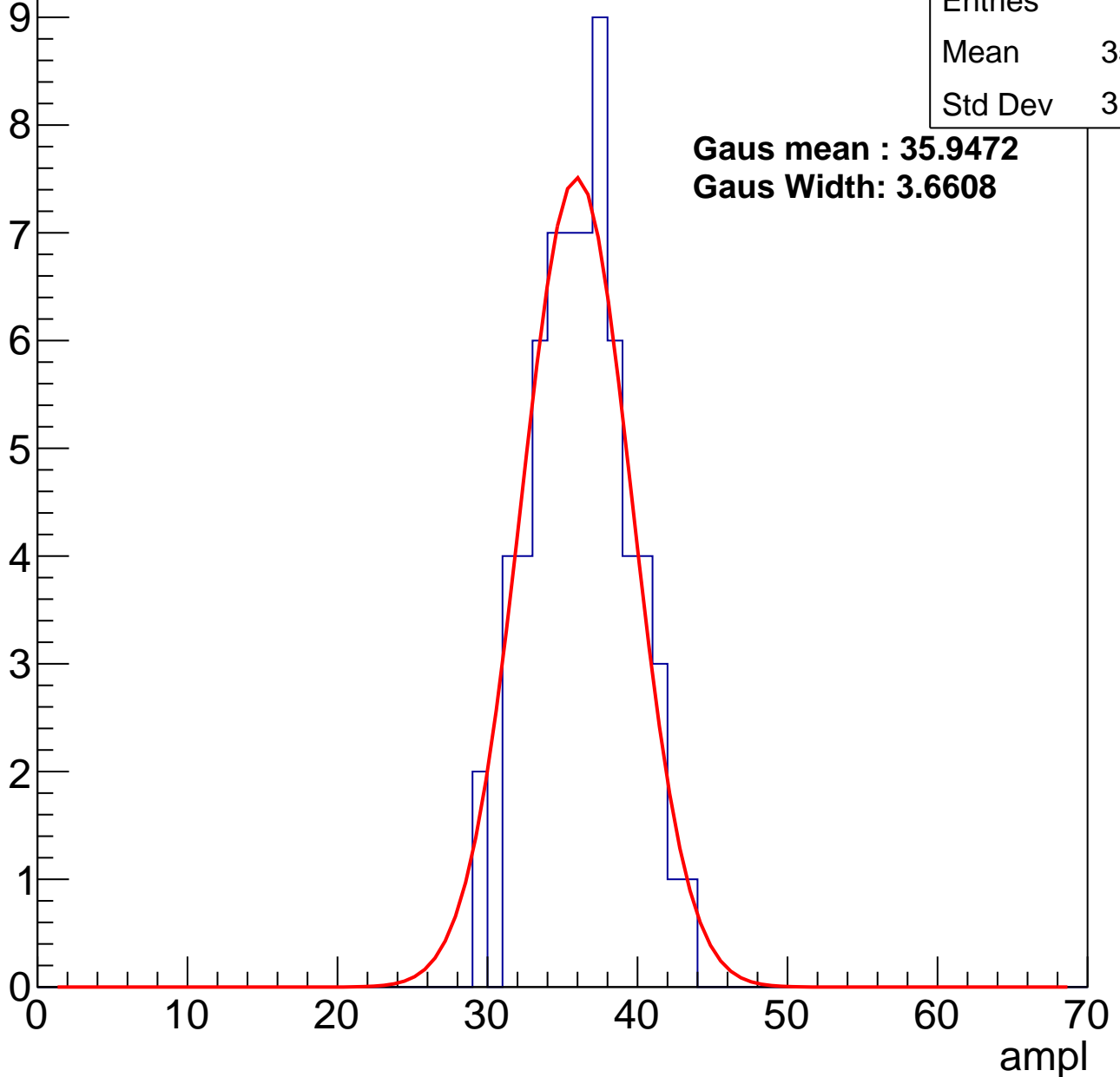
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.82
Std Dev	3.142

Gaus mean : 35.9472

Gaus Width: 3.6608



B1L100S, U25-ch15, adc2

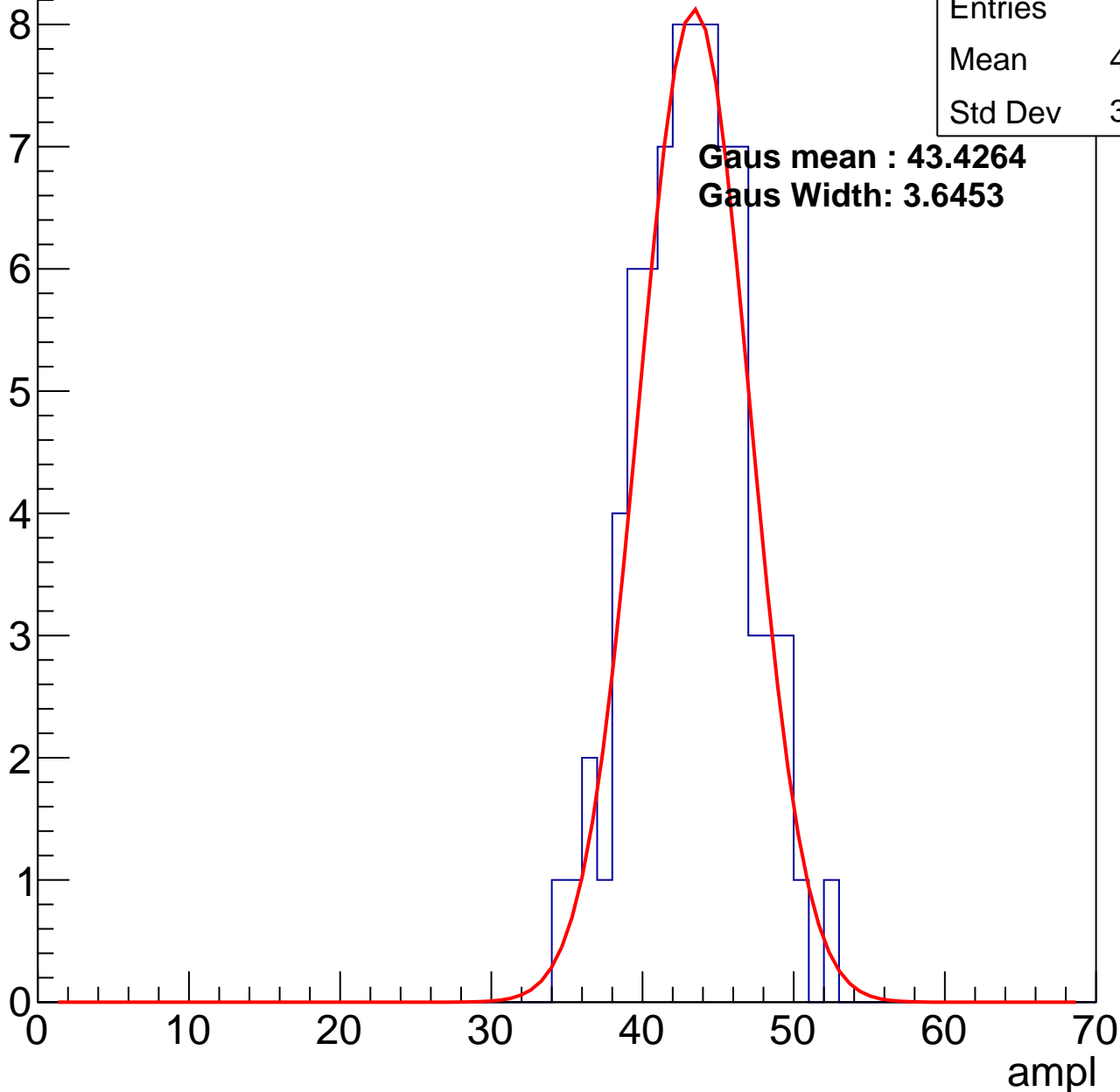
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.78
Std Dev	3.642

Gaus mean : 43.4264

Gaus Width: 3.6453

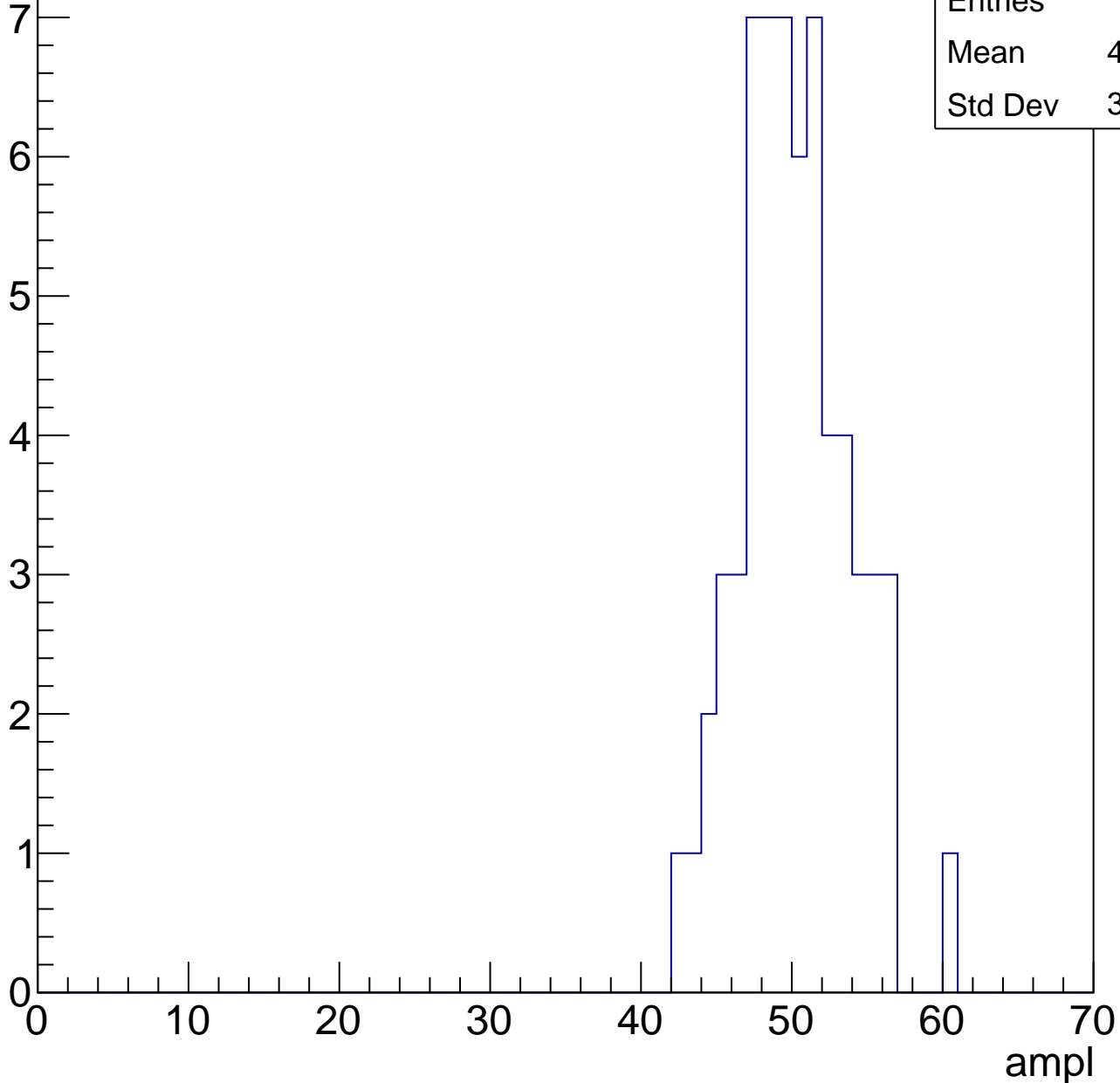


B1L100S, U25-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	49.77
Std Dev	3.576

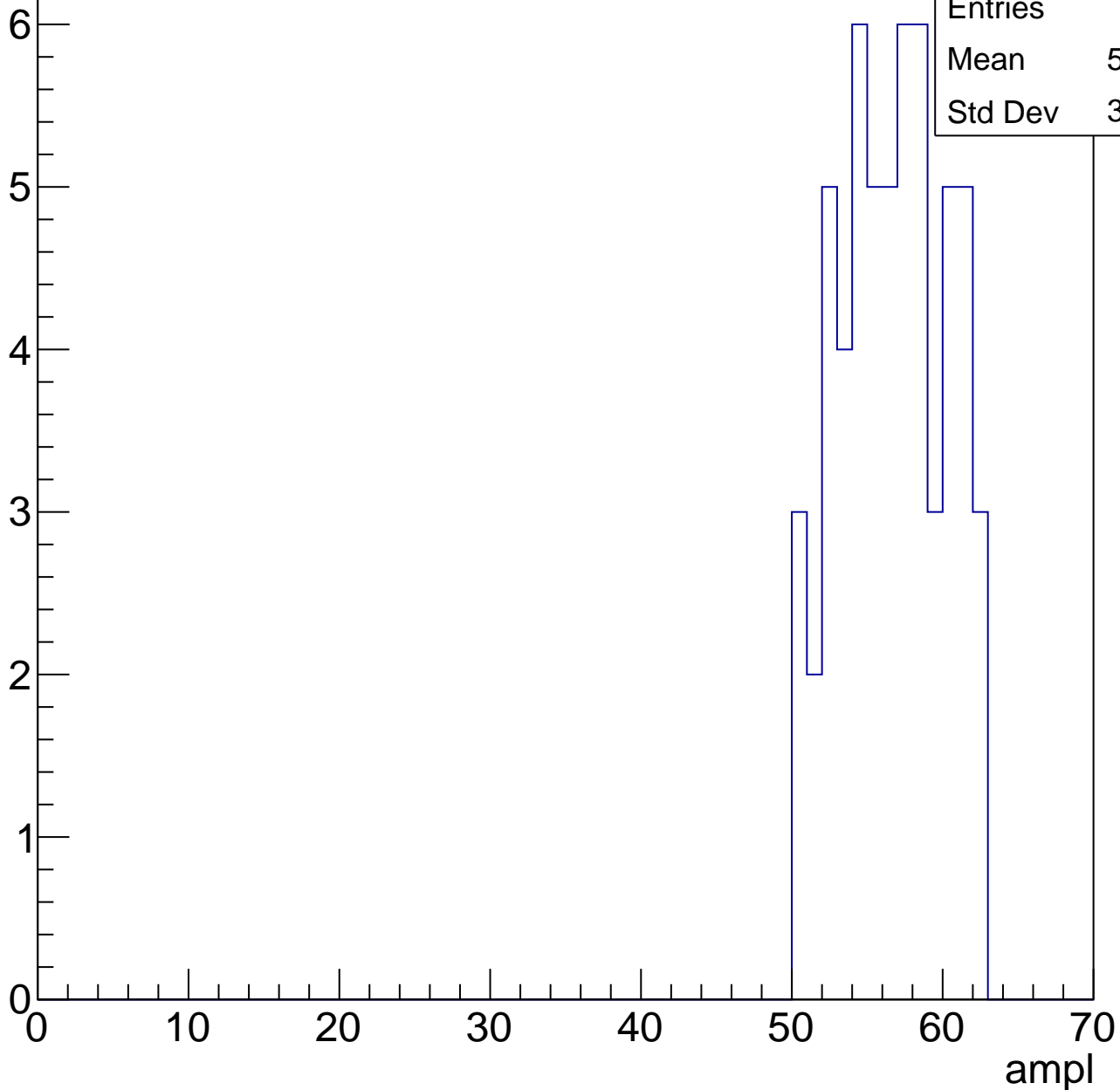


B1L100S, U25-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.22
Std Dev	3.399

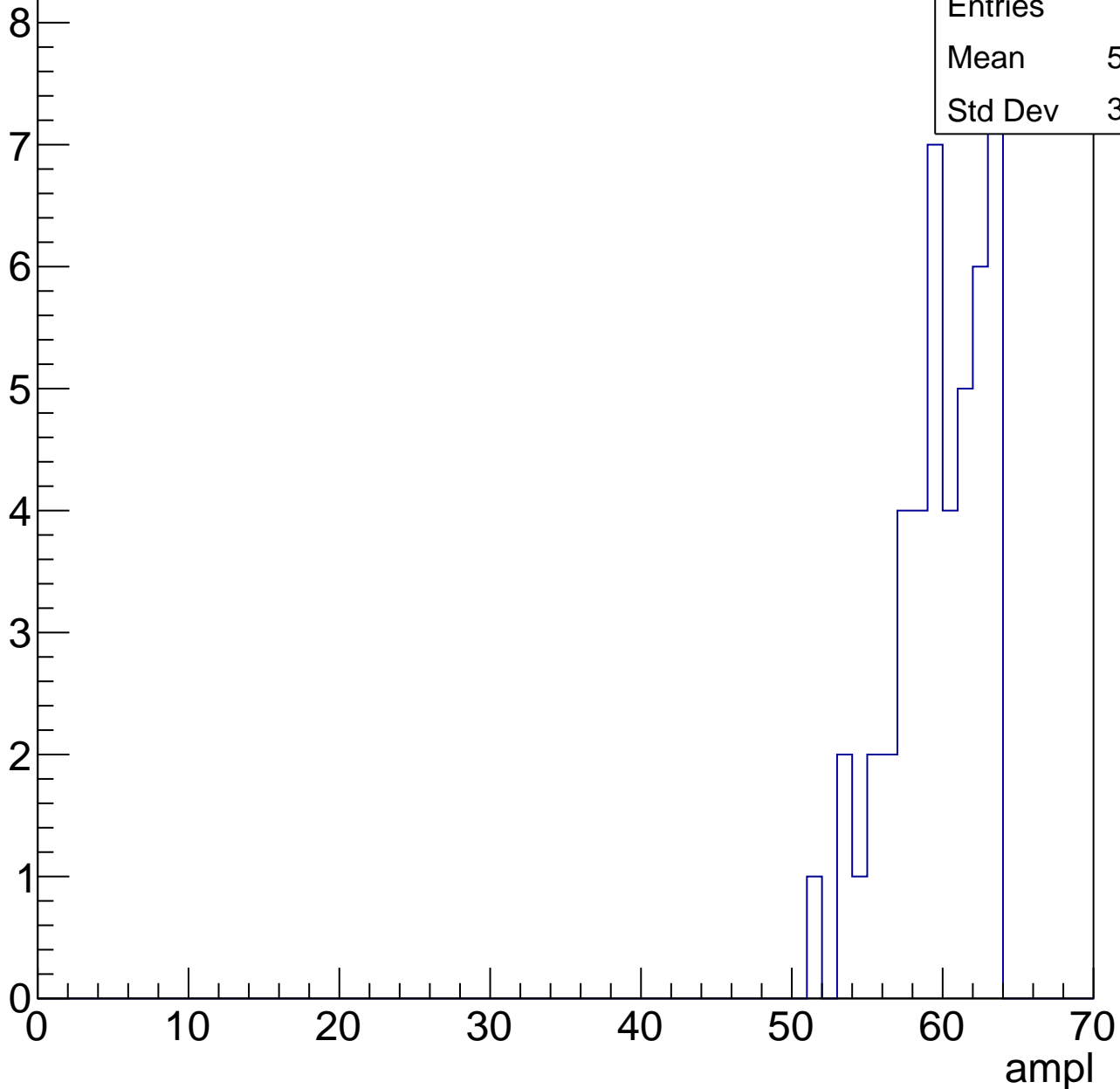


B1L100S, U25-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.28
Std Dev	3.069



B1L100S, U25-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch16, adc0

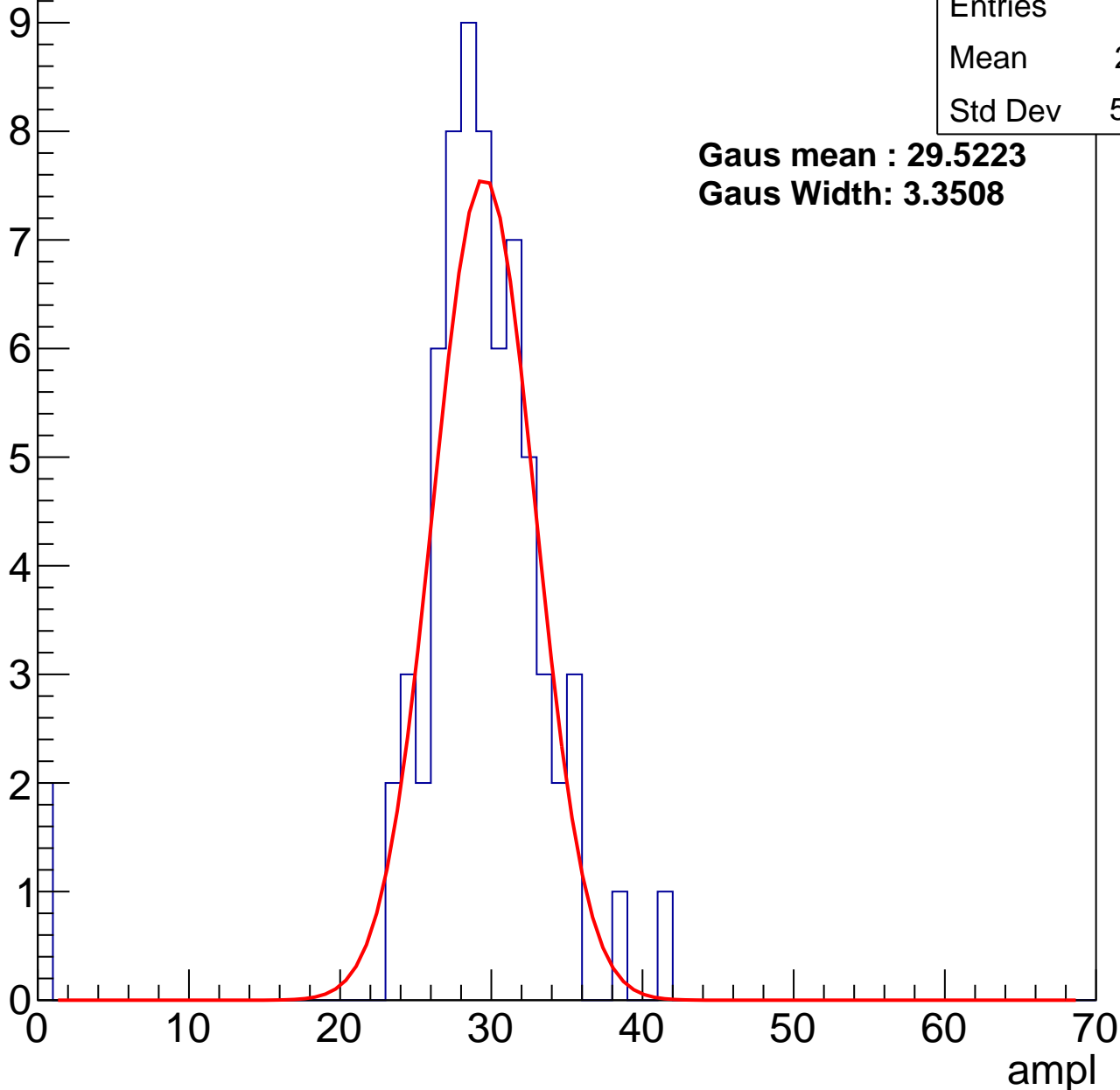
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.41
Std Dev	5.993

Gaus mean : 29.5223

Gaus Width: 3.3508



B1L100S, U25-ch16, adc1

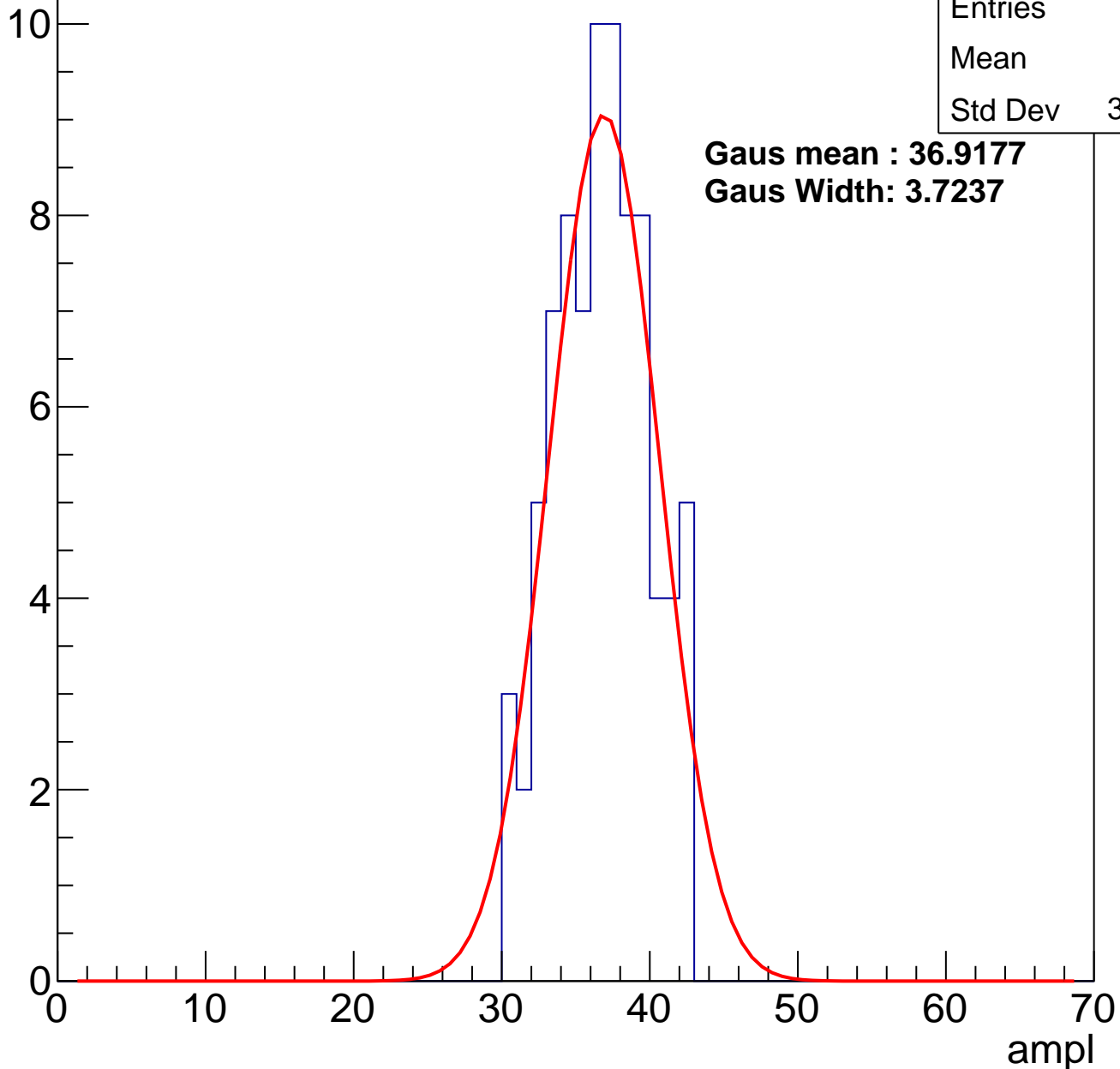
calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	36.3
Std Dev	3.125

Gaus mean : 36.9177

Gaus Width: 3.7237

Entry



B1L100S, U25-ch16, adc2

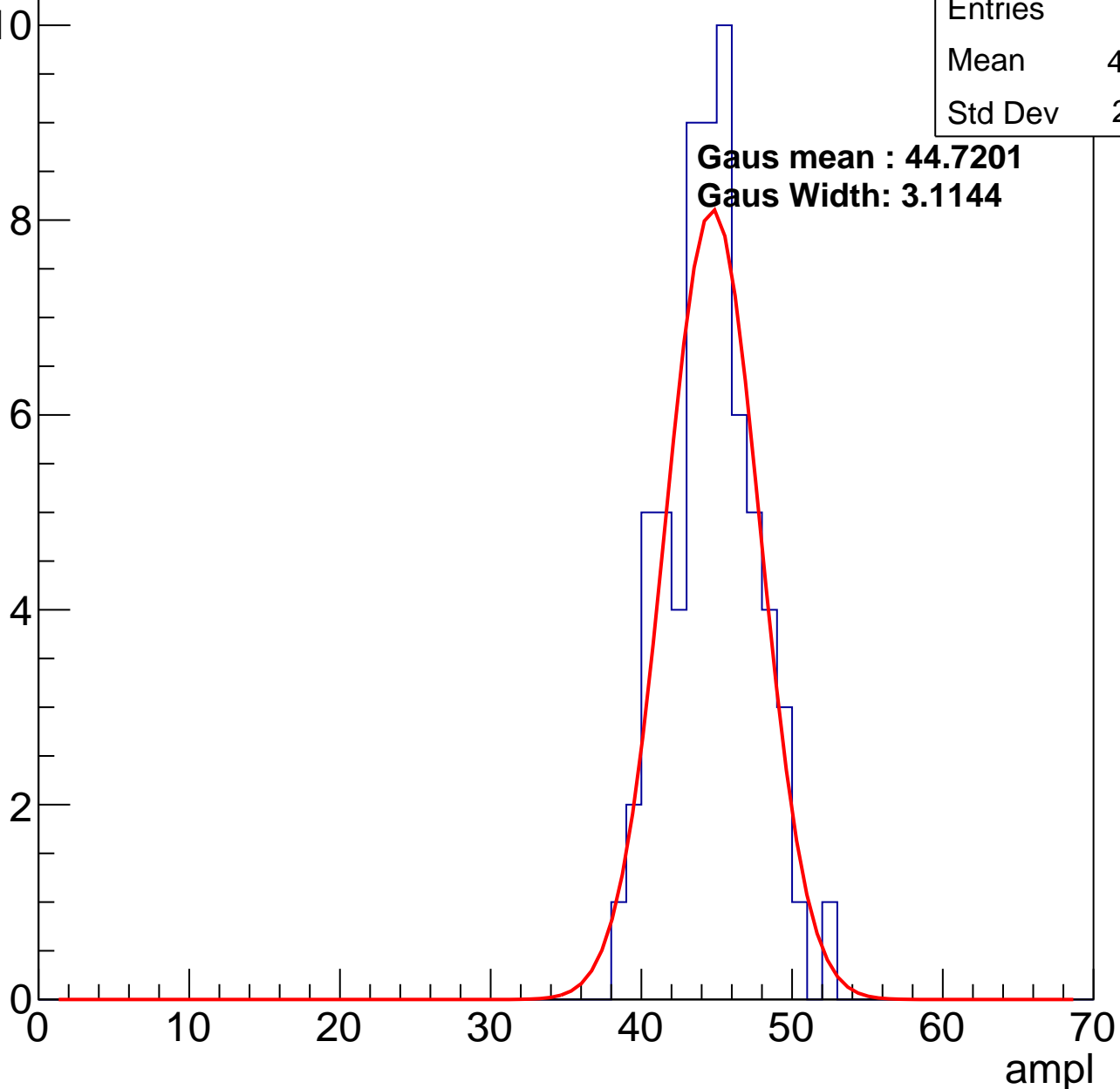
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.22
Std Dev	2.911

Gaus mean : 44.7201

Gaus Width: 3.1144

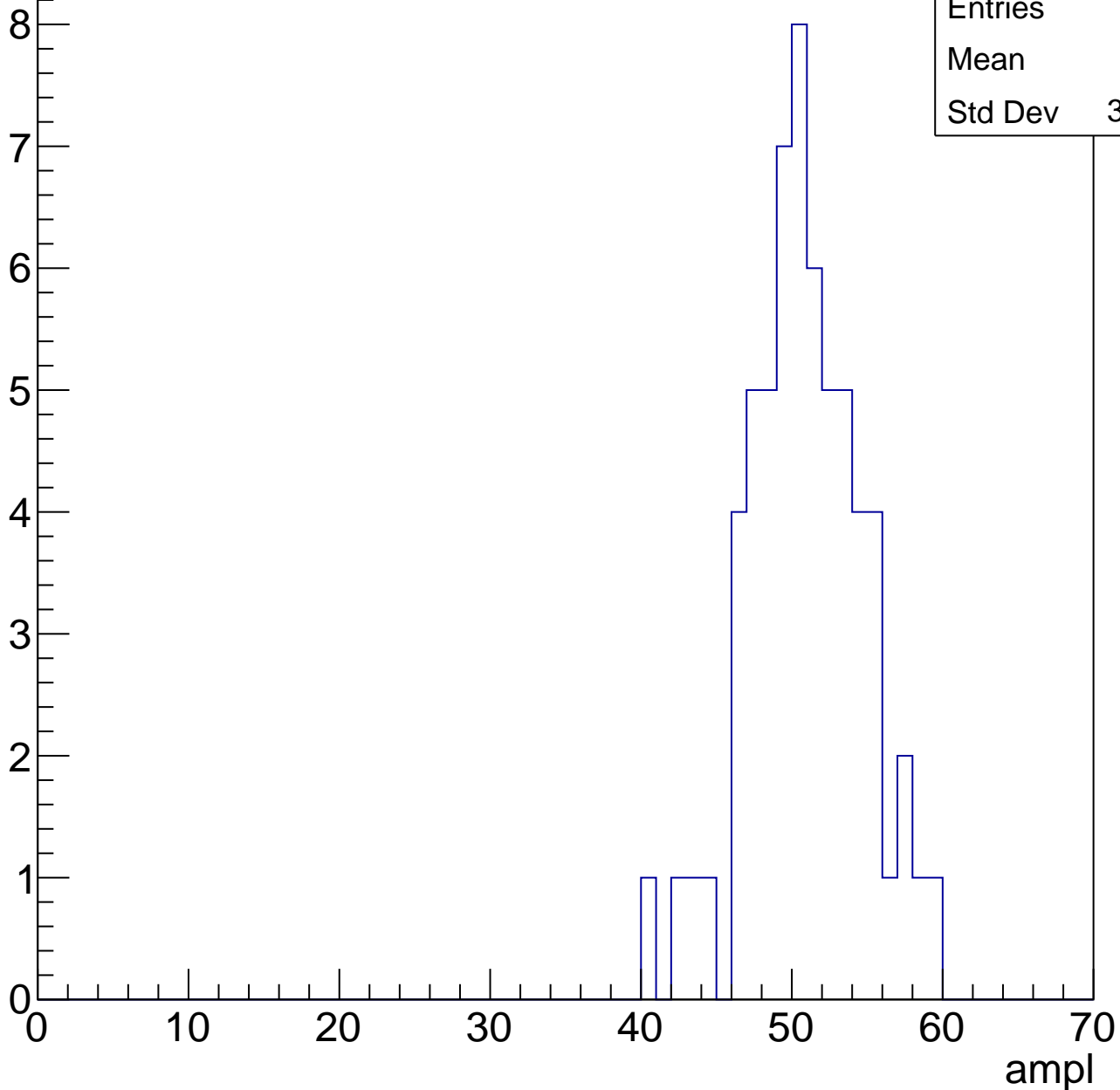


B1L100S, U25-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.4
Std Dev	3.786

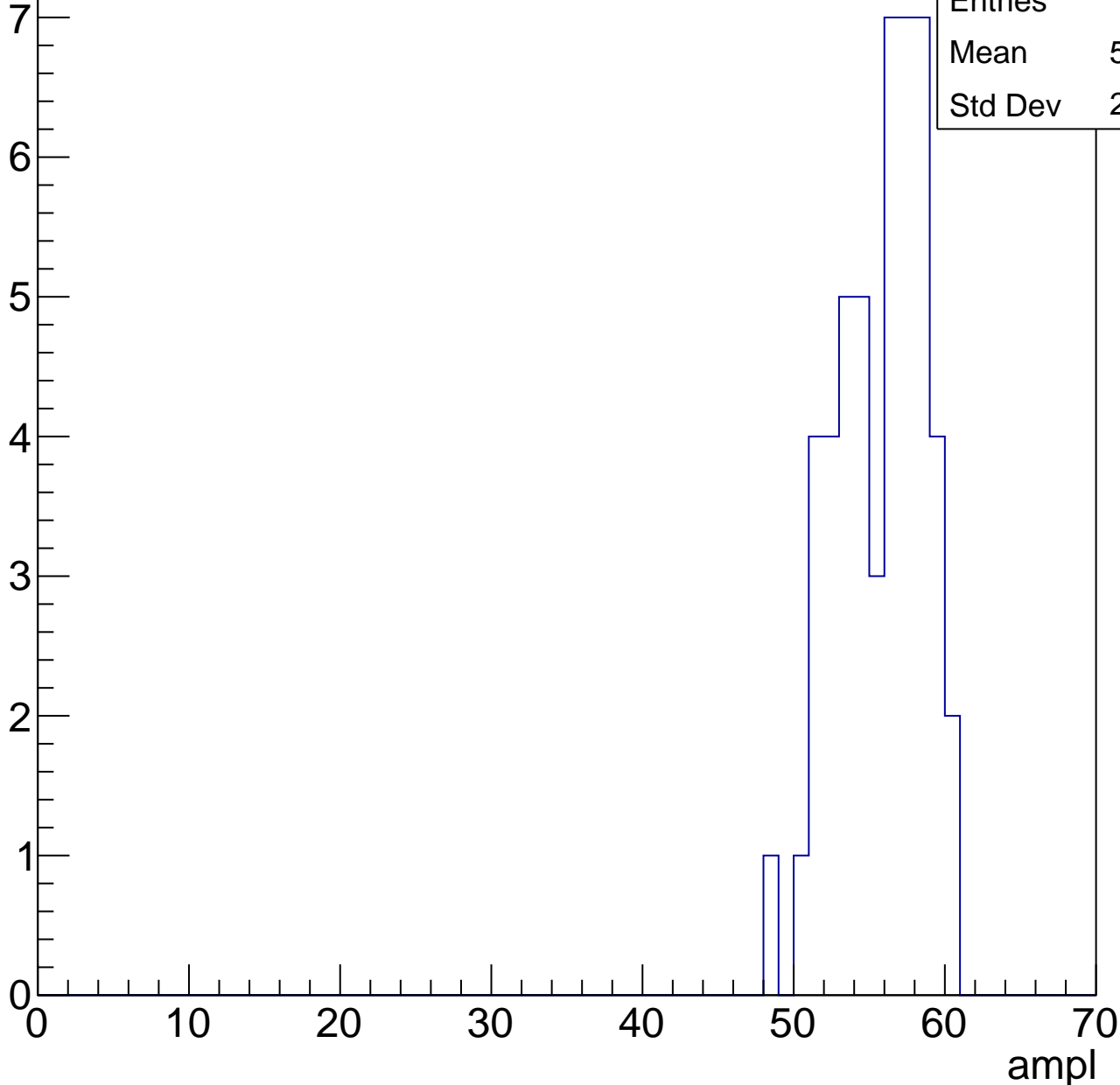


B1L100S, U25-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

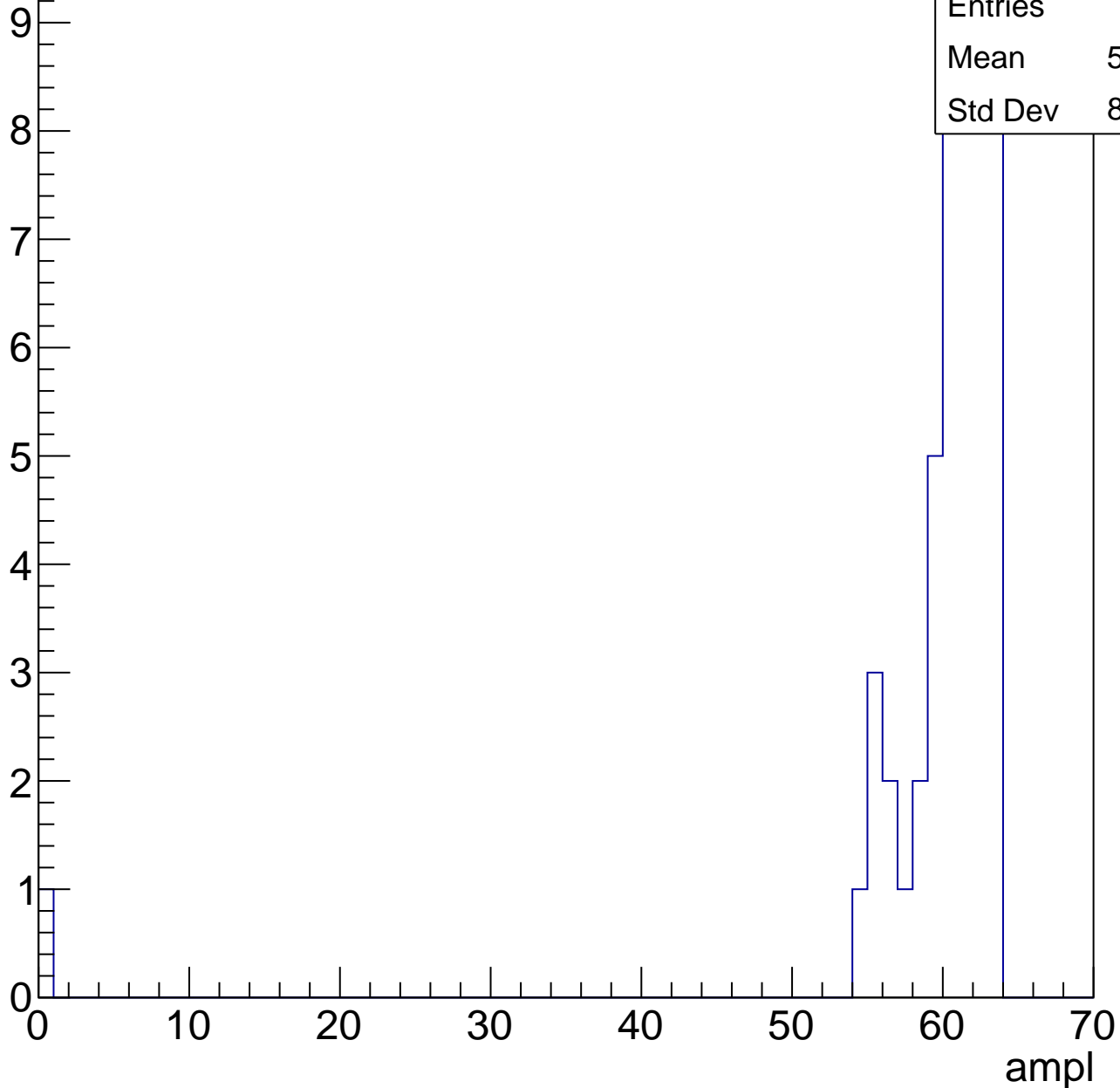
Entries	50
Mean	55.26
Std Dev	2.855



B1L100S, U25-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch17, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	30.82
Std Dev	4.944

Gaus mean : 31.1654

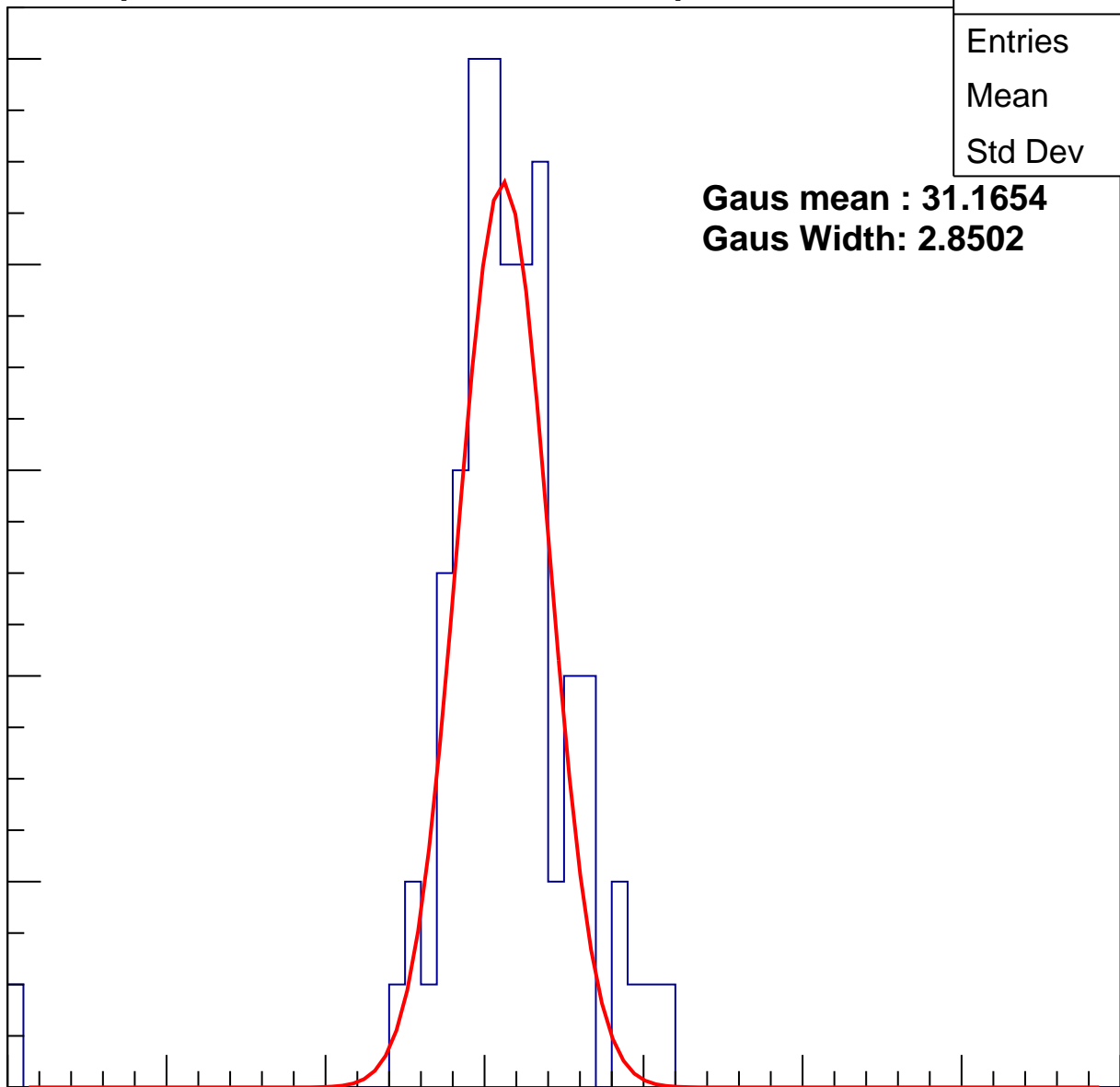
Gaus Width: 2.8502

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch17, adc1

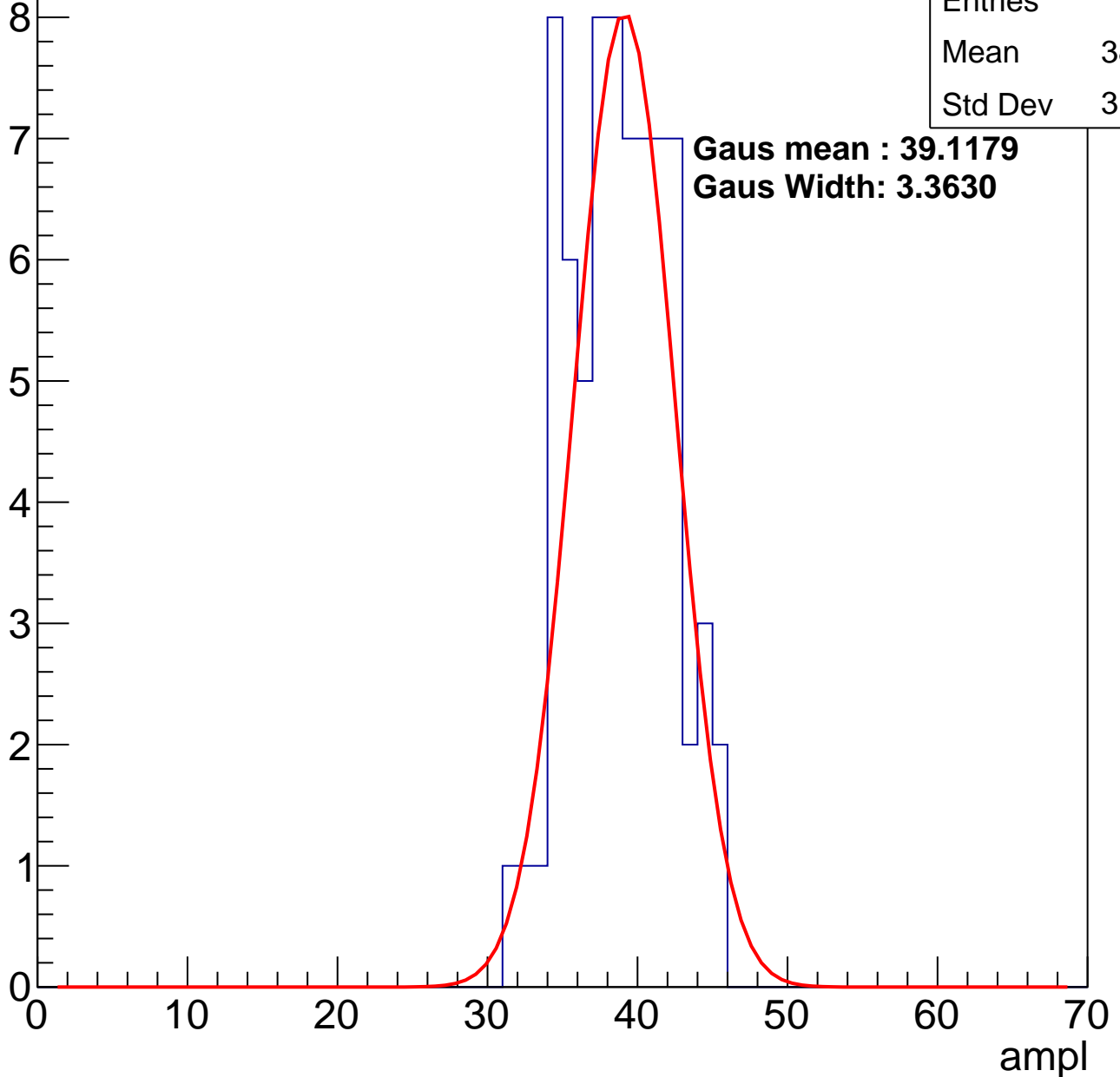
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	38.36
Std Dev	3.262

Gaus mean : 39.1179

Gaus Width: 3.3630



B1L100S, U25-ch17, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	45.6
Std Dev	3.465

Gaus mean : 46.5244

Gaus Width: 3.2375

10

8

6

4

2

0

0

10

20

30

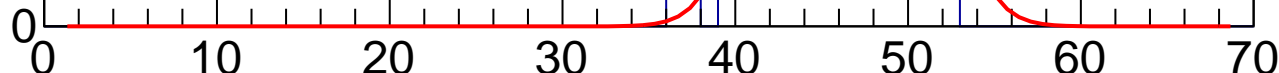
40

50

60

70

ampl

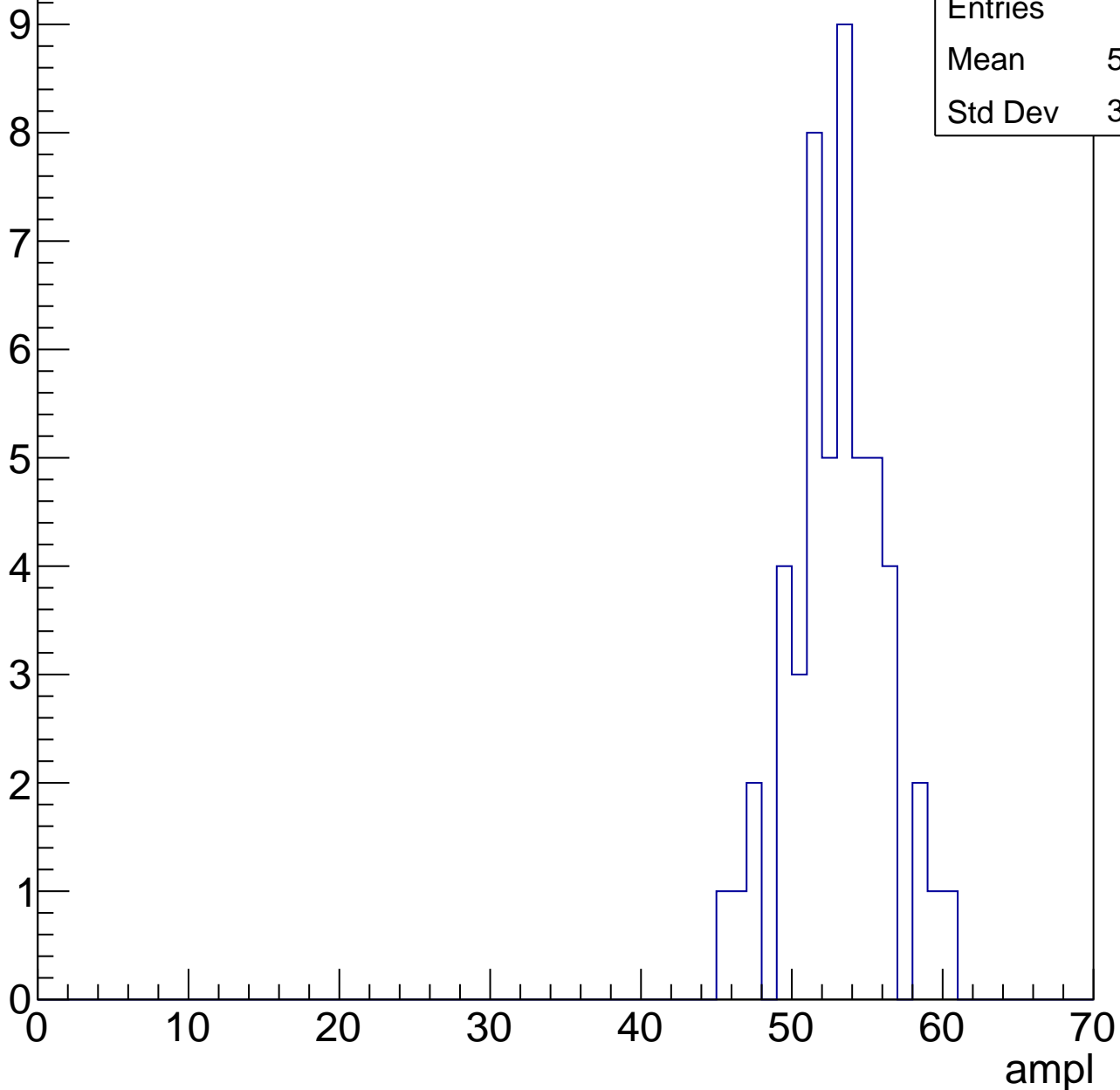


B1L100S, U25-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

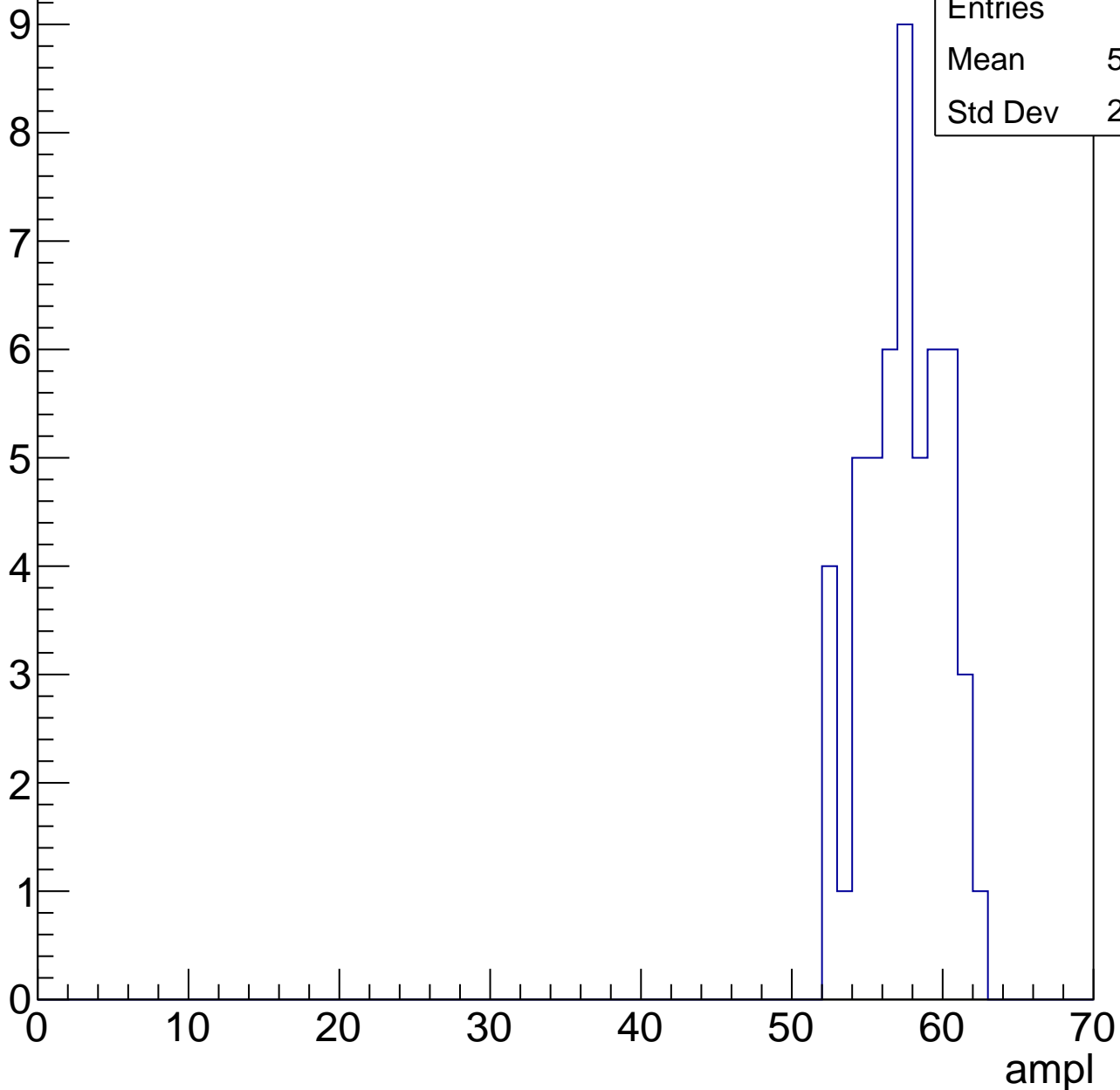
Entries	51
Mean	52.55
Std Dev	3.127



B1L100S, U25-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	37
Mean	59.54
Std Dev	10.07

Entry

10

8

6

4

2

0

0

10

20

30

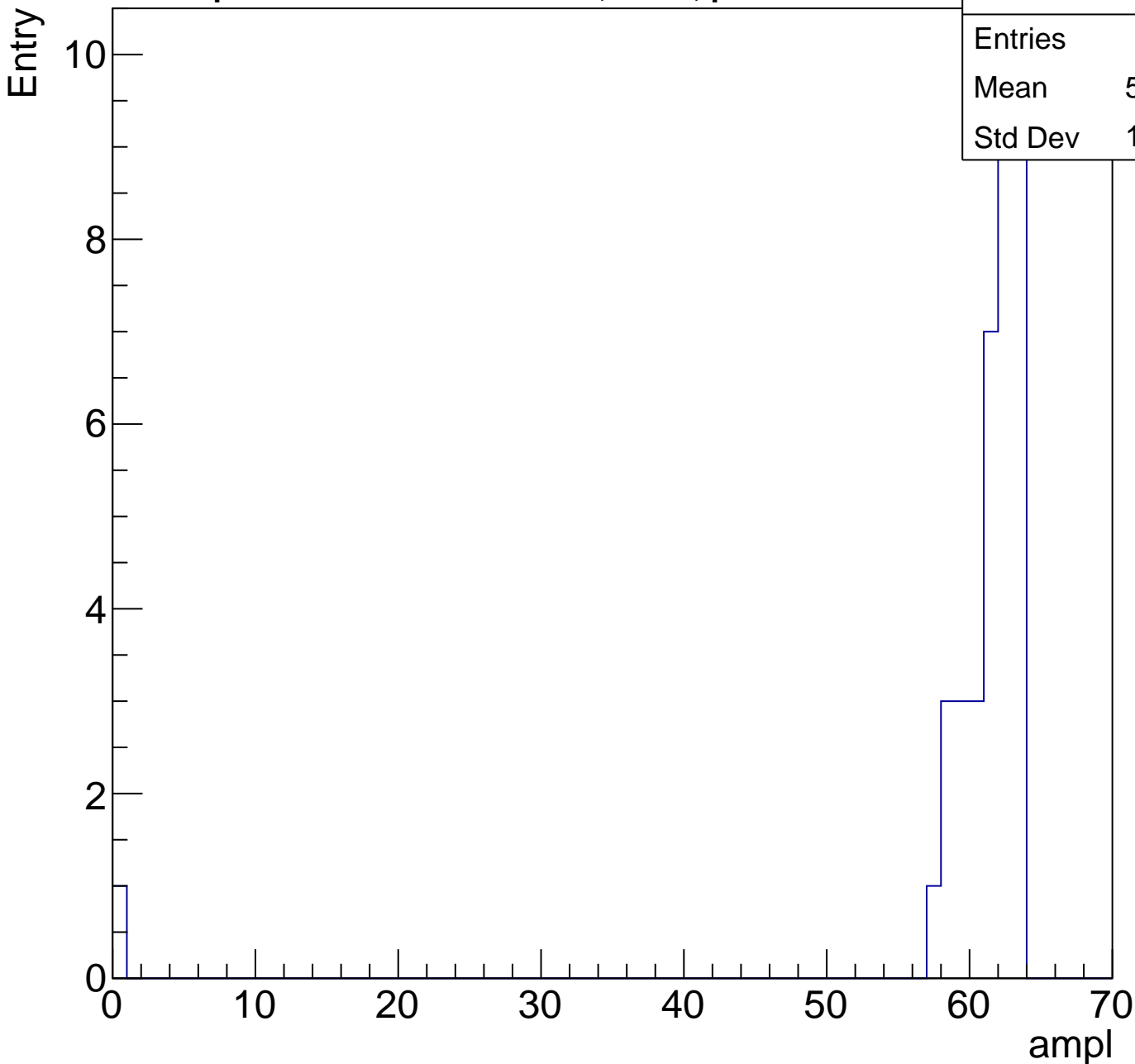
40

50

60

70

ampl



B1L100S, U25-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch18, adc0

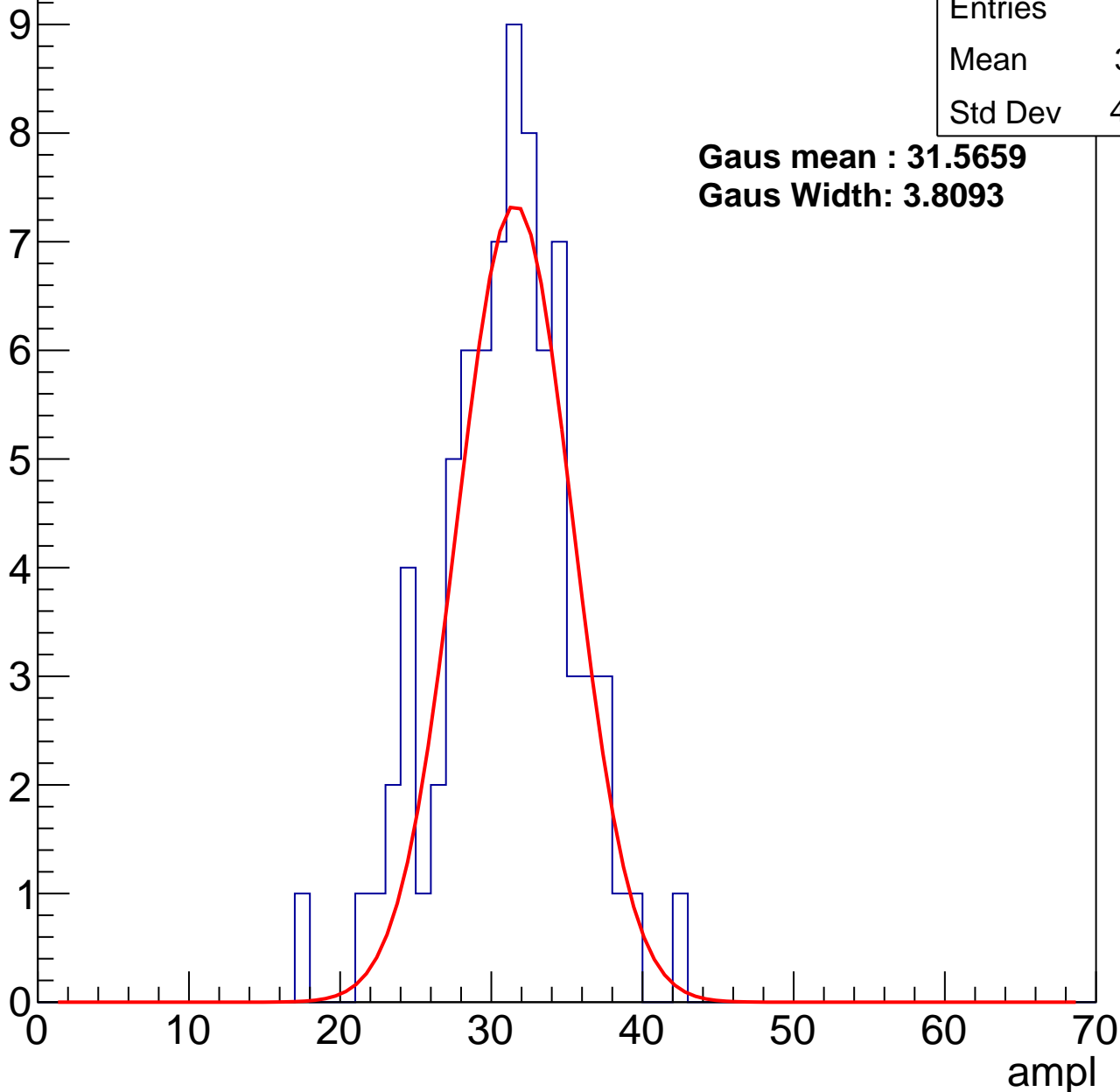
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.51
Std Dev	4.376

Gaus mean : 31.5659

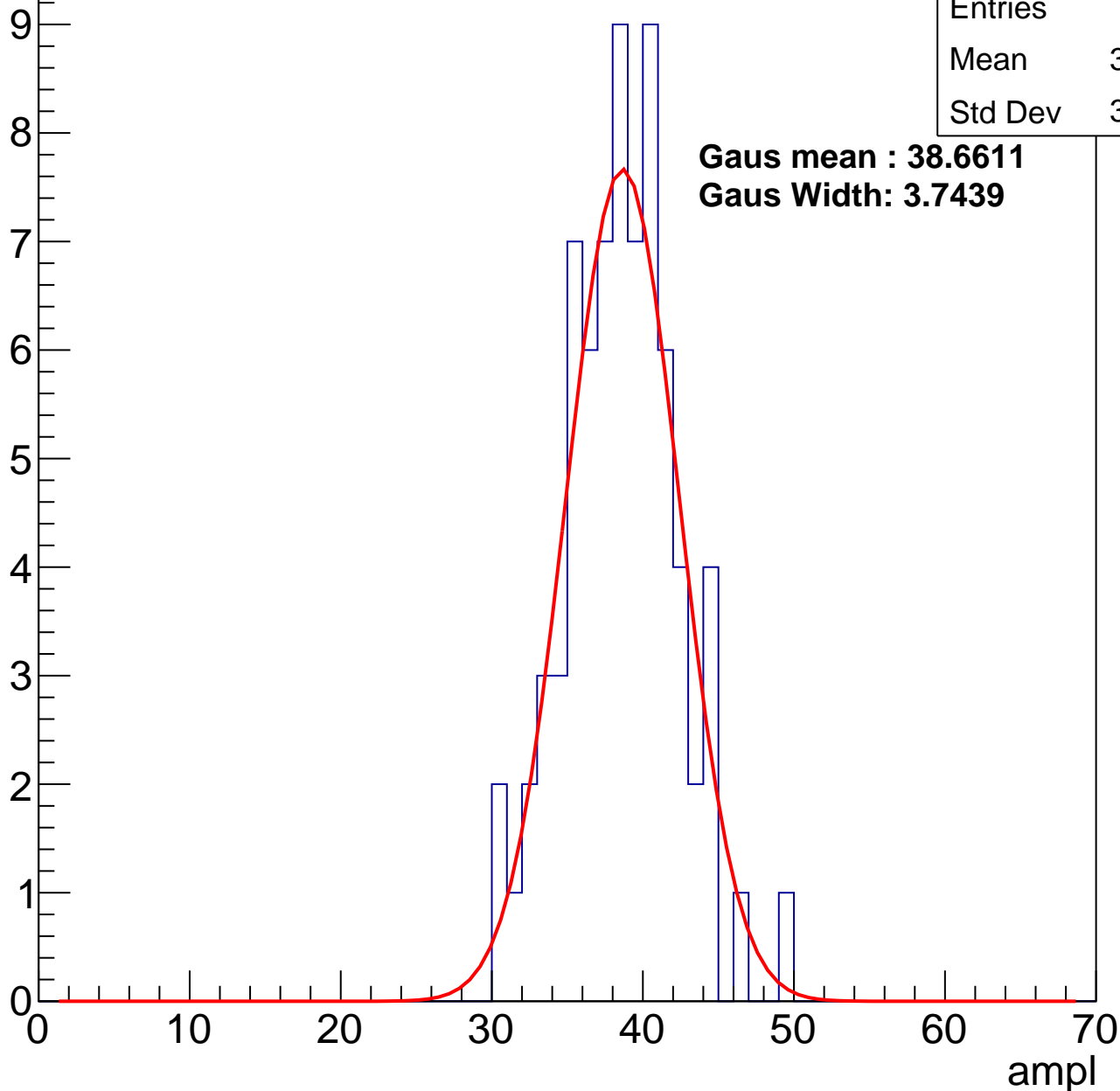
Gaus Width: 3.8093



B1L100S, U25-ch18, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch18, adc2

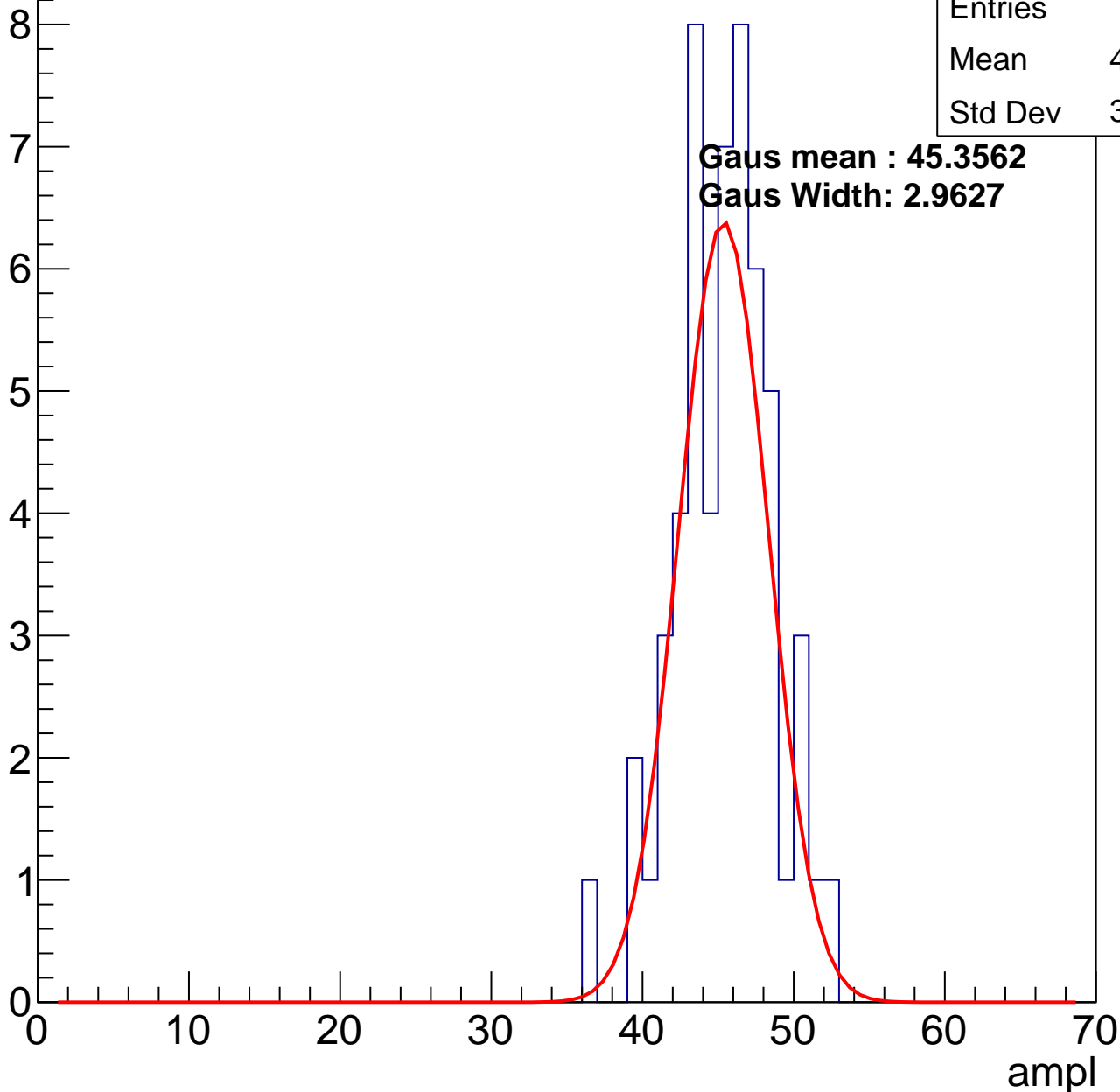
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	44.95
Std Dev	3.165

Gaus mean : 45.3562

Gaus Width: 2.9627

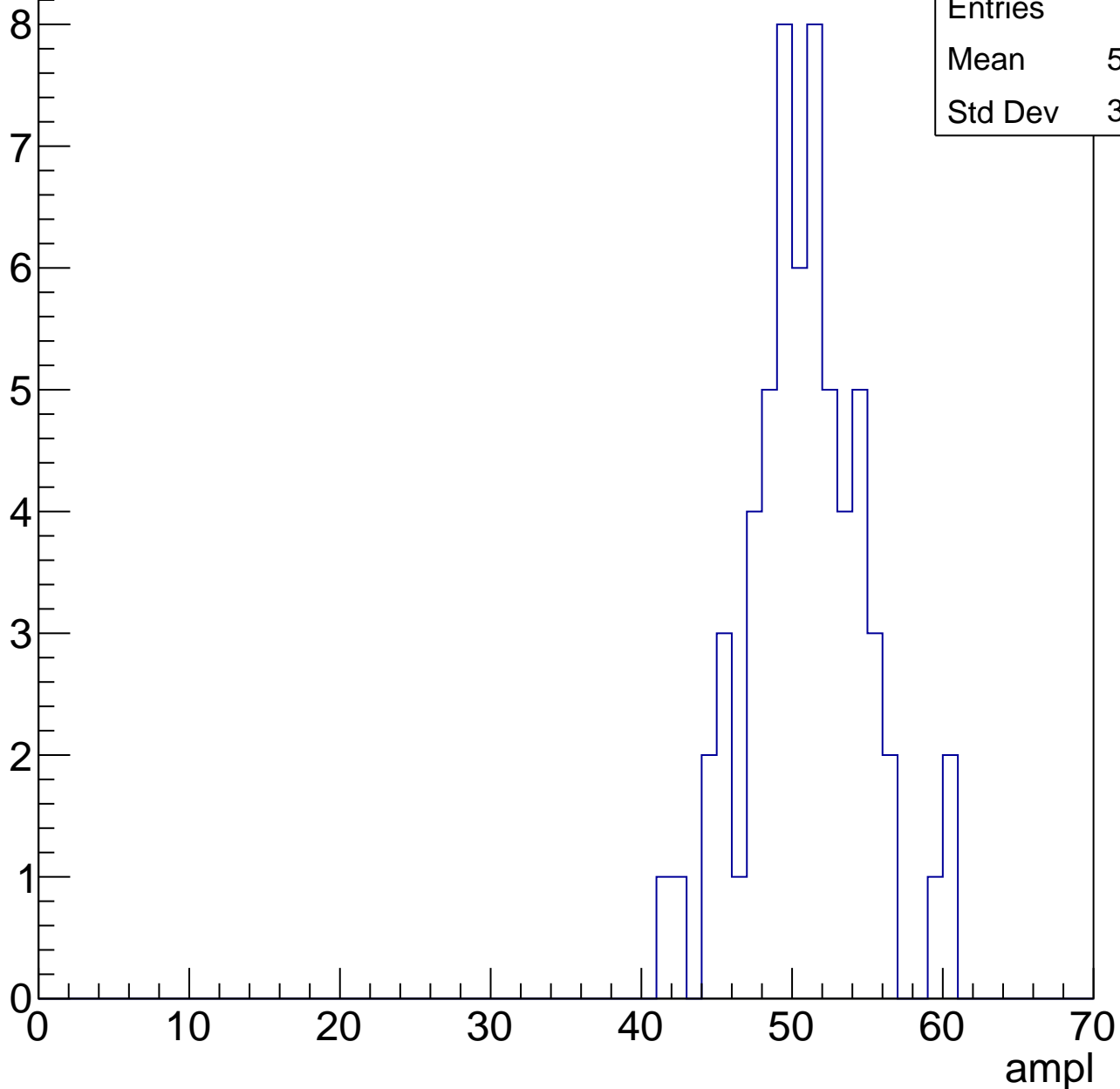


B1L100S, U25-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.46
Std Dev	3.907

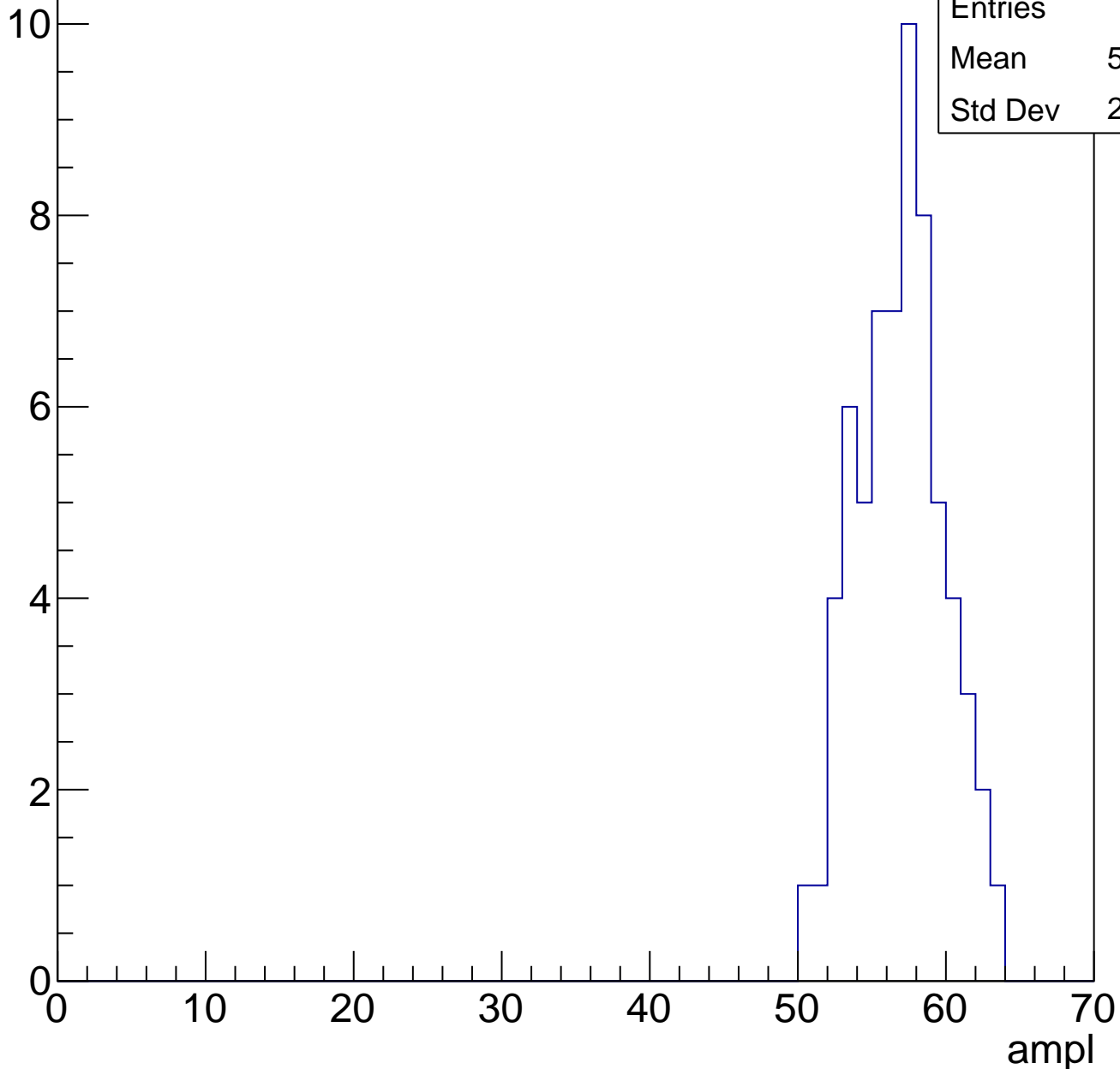


B1L100S, U25-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	56.45
Std Dev	2.904

Entry

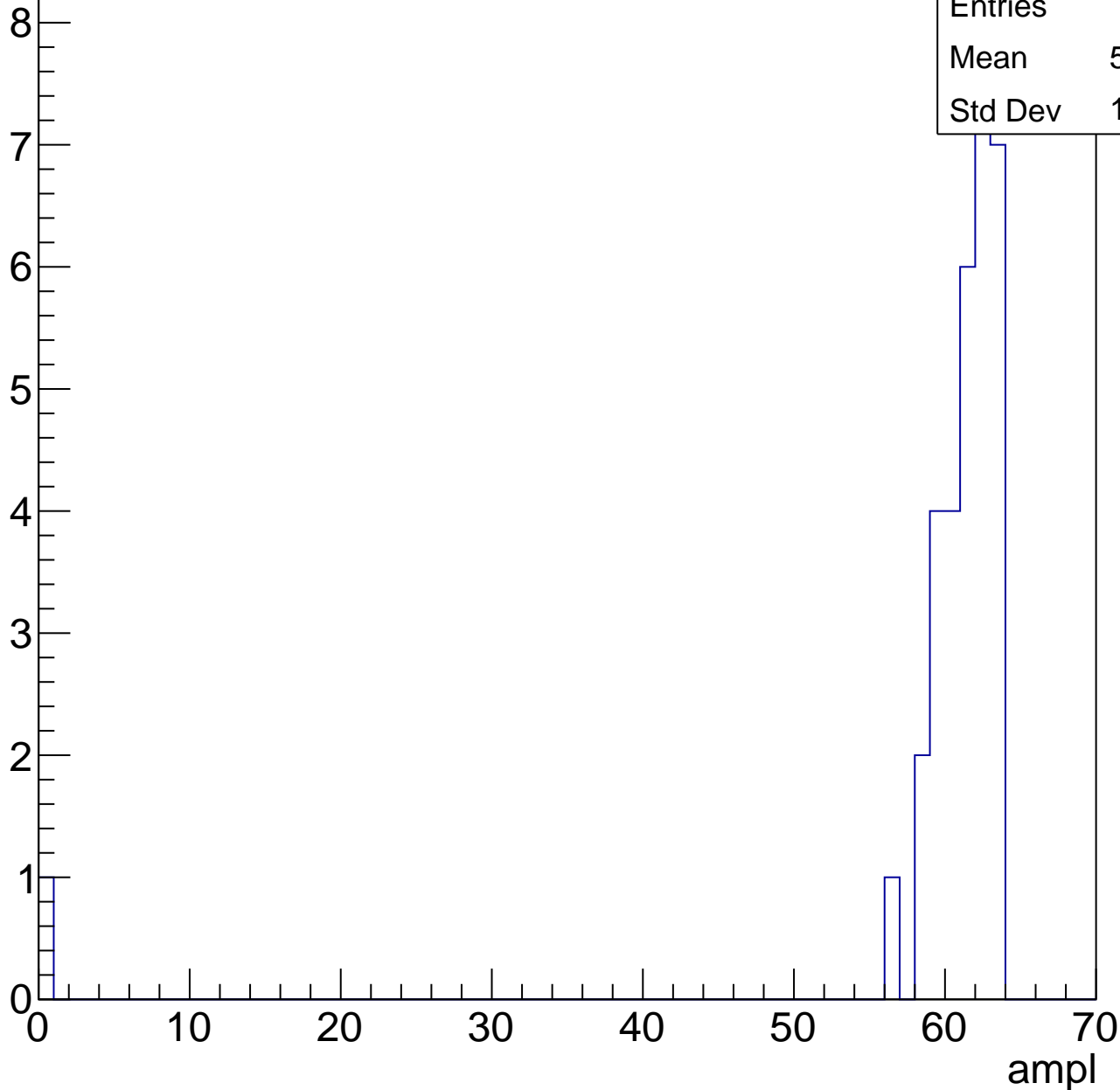


B1L100S, U25-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	59.12
Std Dev	10.59



B1L100S, U25-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

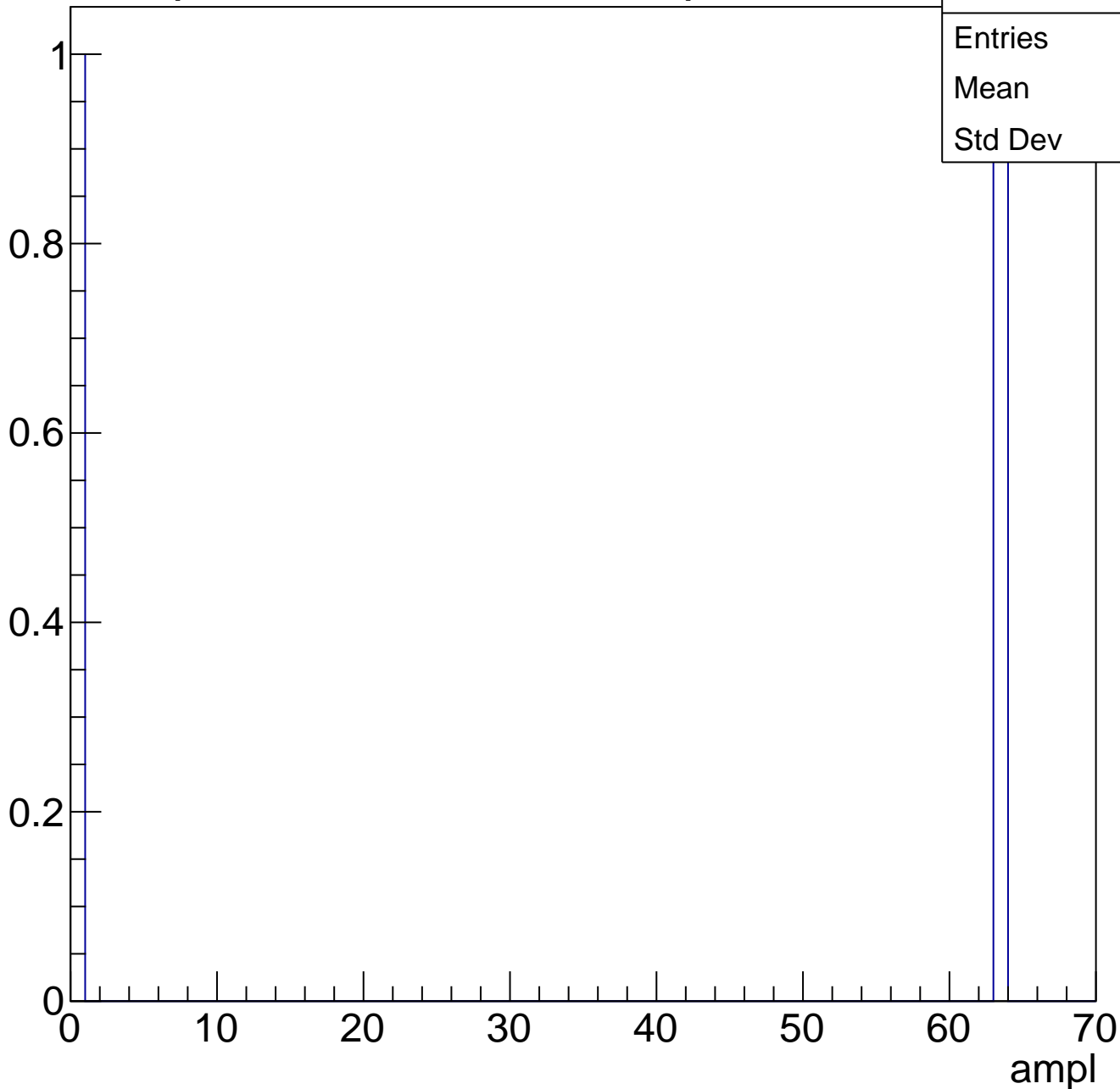
Entry



B1L100S, U25-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch19, adc0

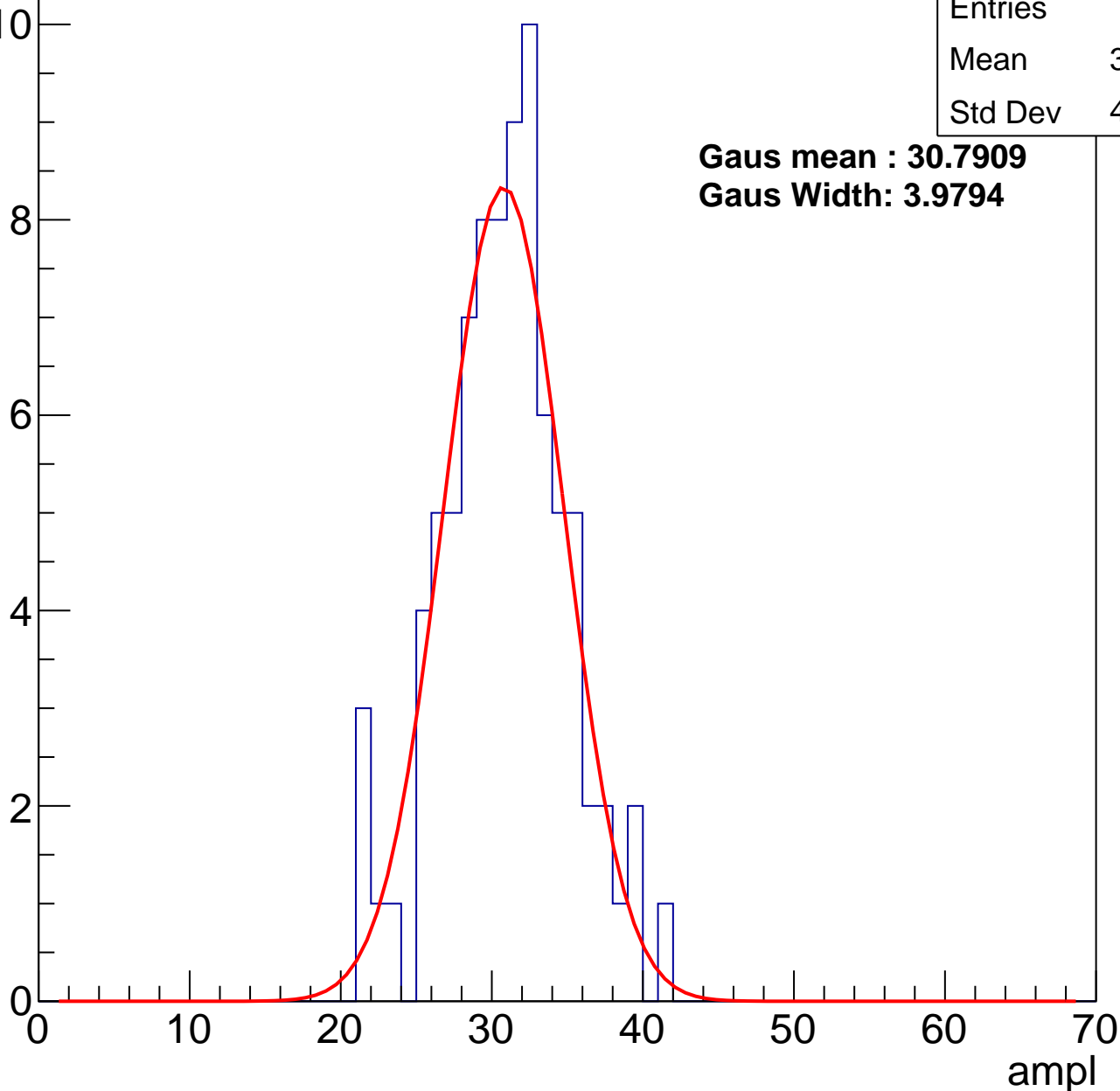
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.42
Std Dev	4.097

Gaus mean : 30.7909

Gaus Width: 3.9794



B1L100S, U25-ch19, adc1

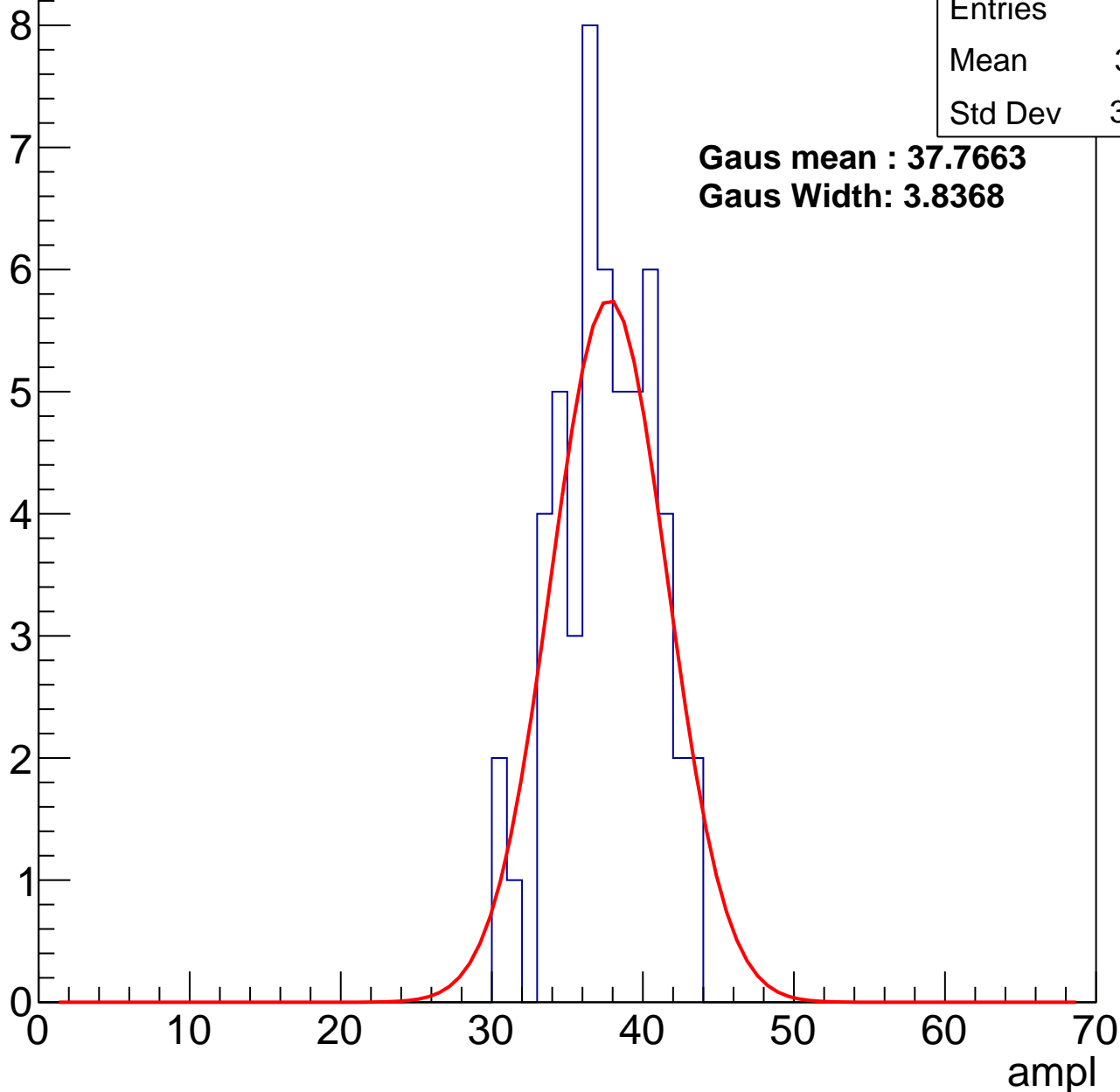
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	37.11
Std Dev	3.154

Gaus mean : 37.7663

Gaus Width: 3.8368



B1L100S, U25-ch19, adc2

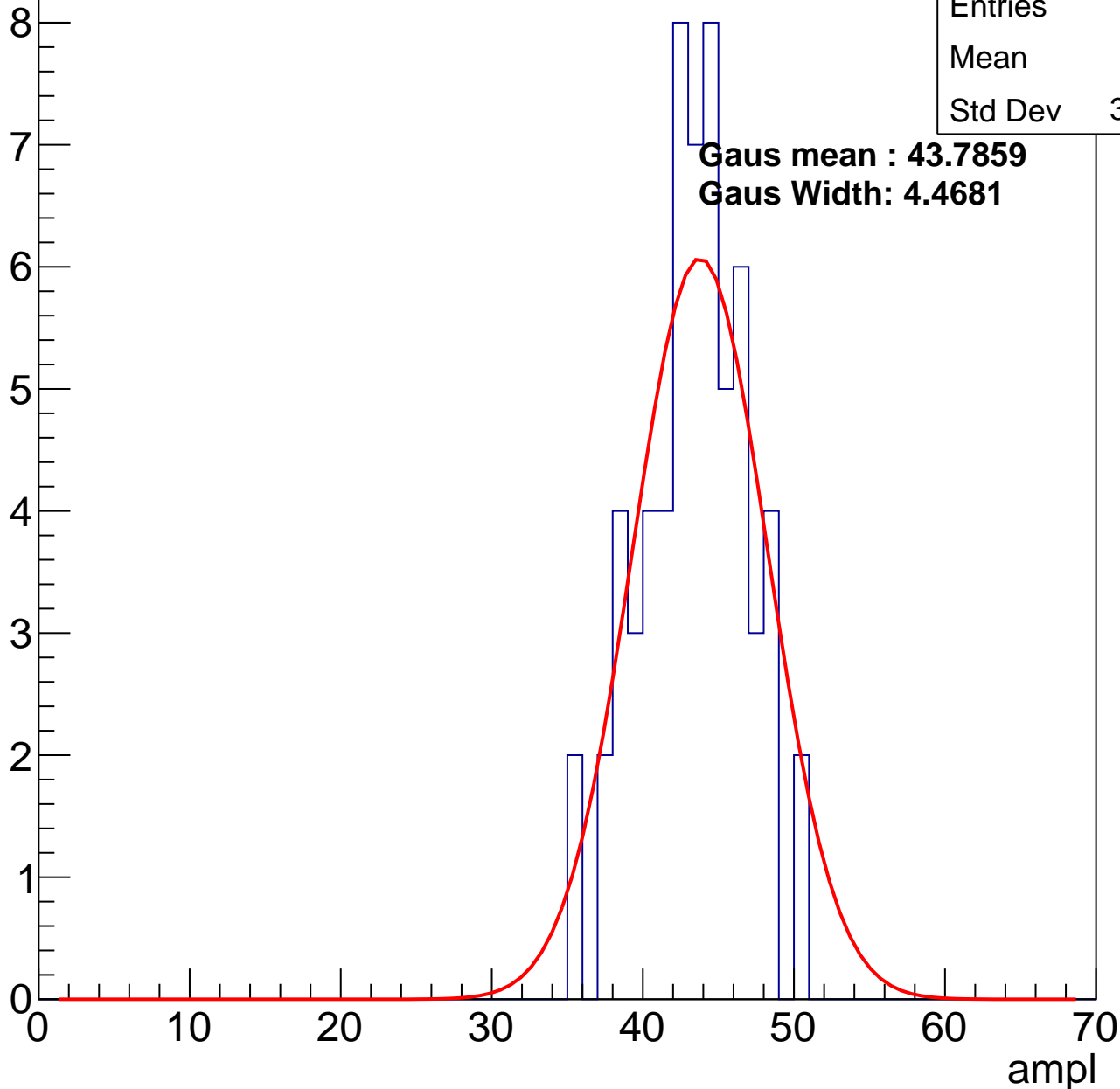
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.9
Std Dev	3.444

Gaus mean : 43.7859

Gaus Width: 4.4681

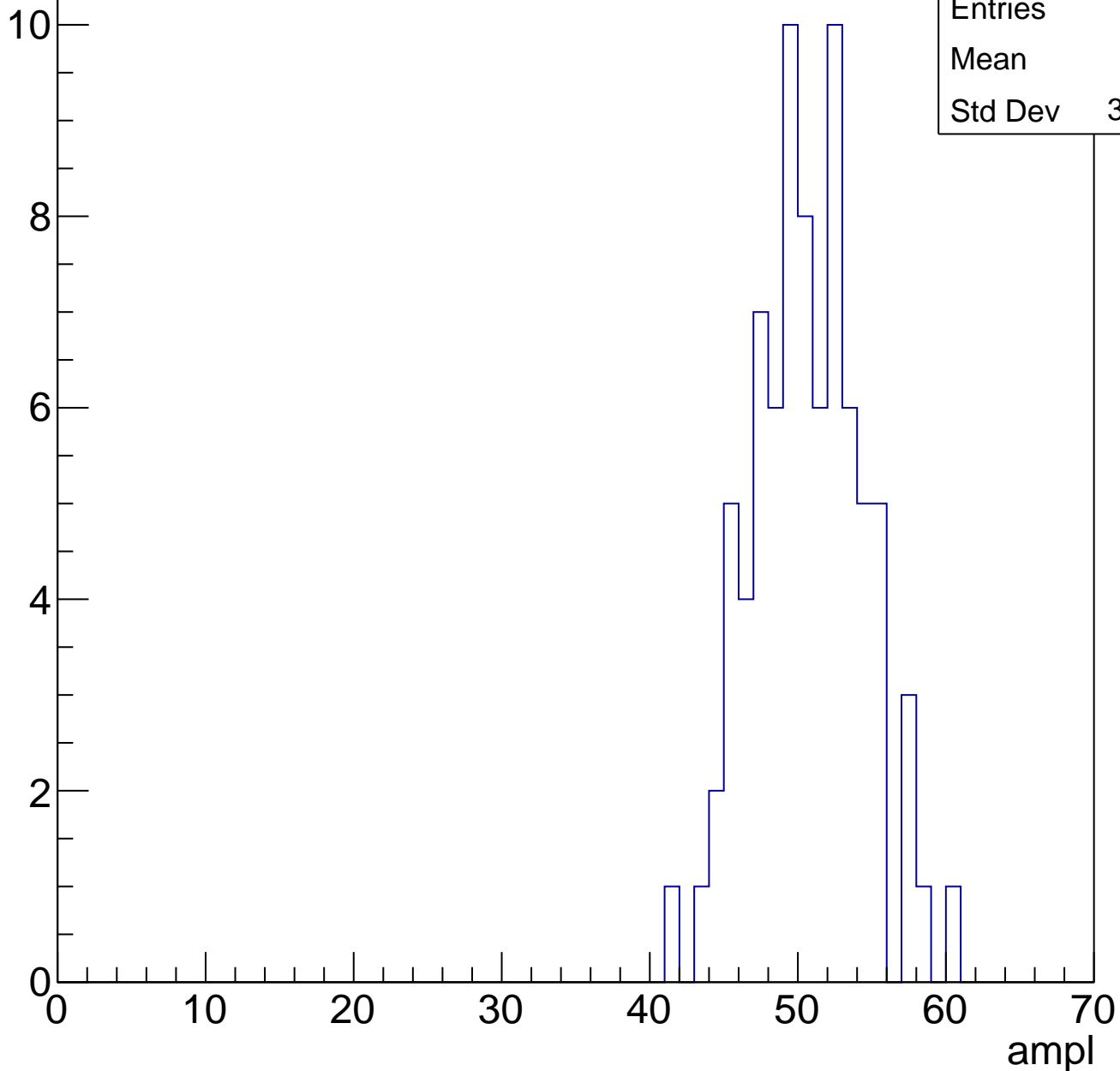


B1L100S, U25-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	50.2
Std Dev	3.687

Entry

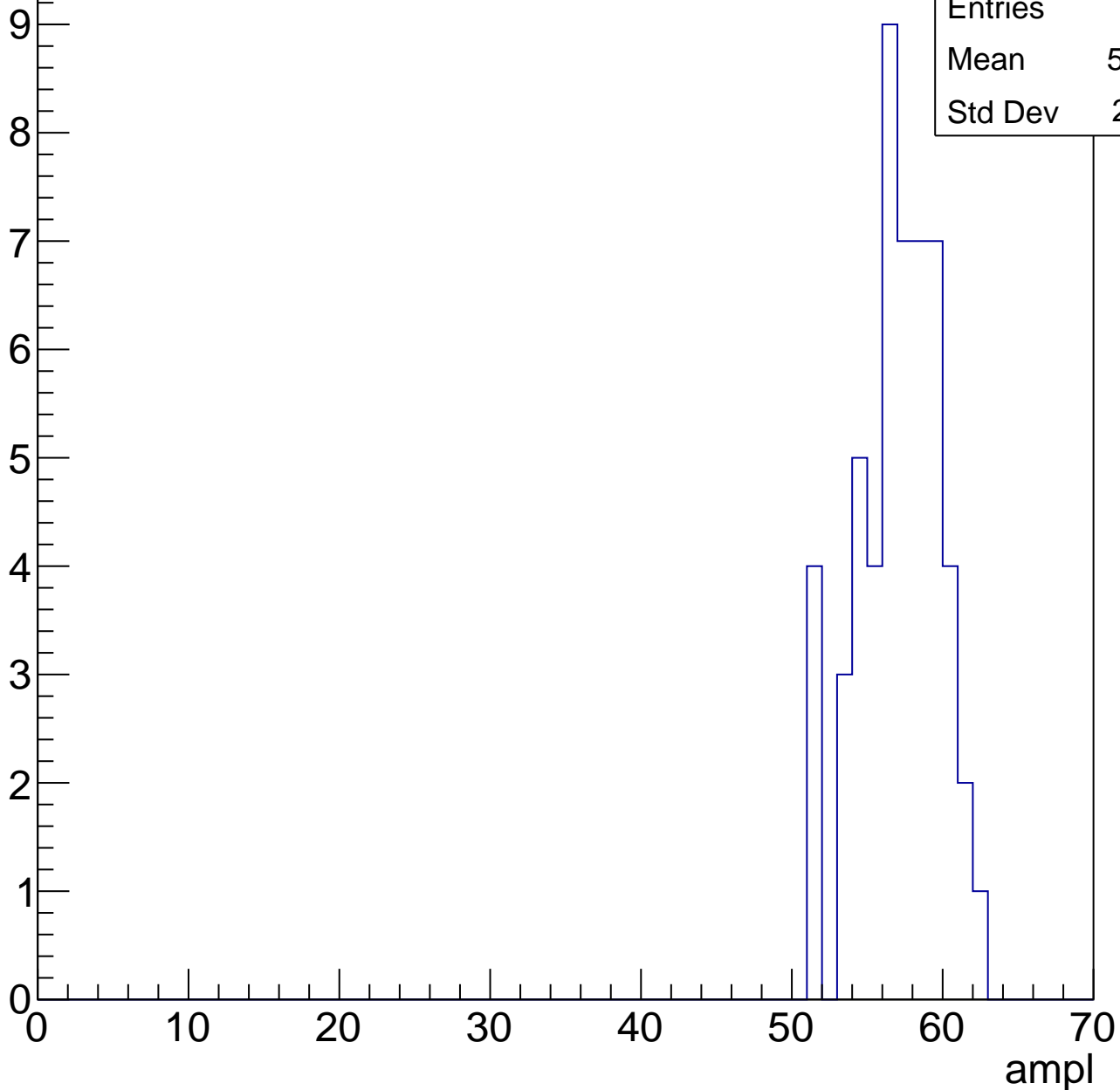


B1L100S, U25-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	56.58
Std Dev	2.681

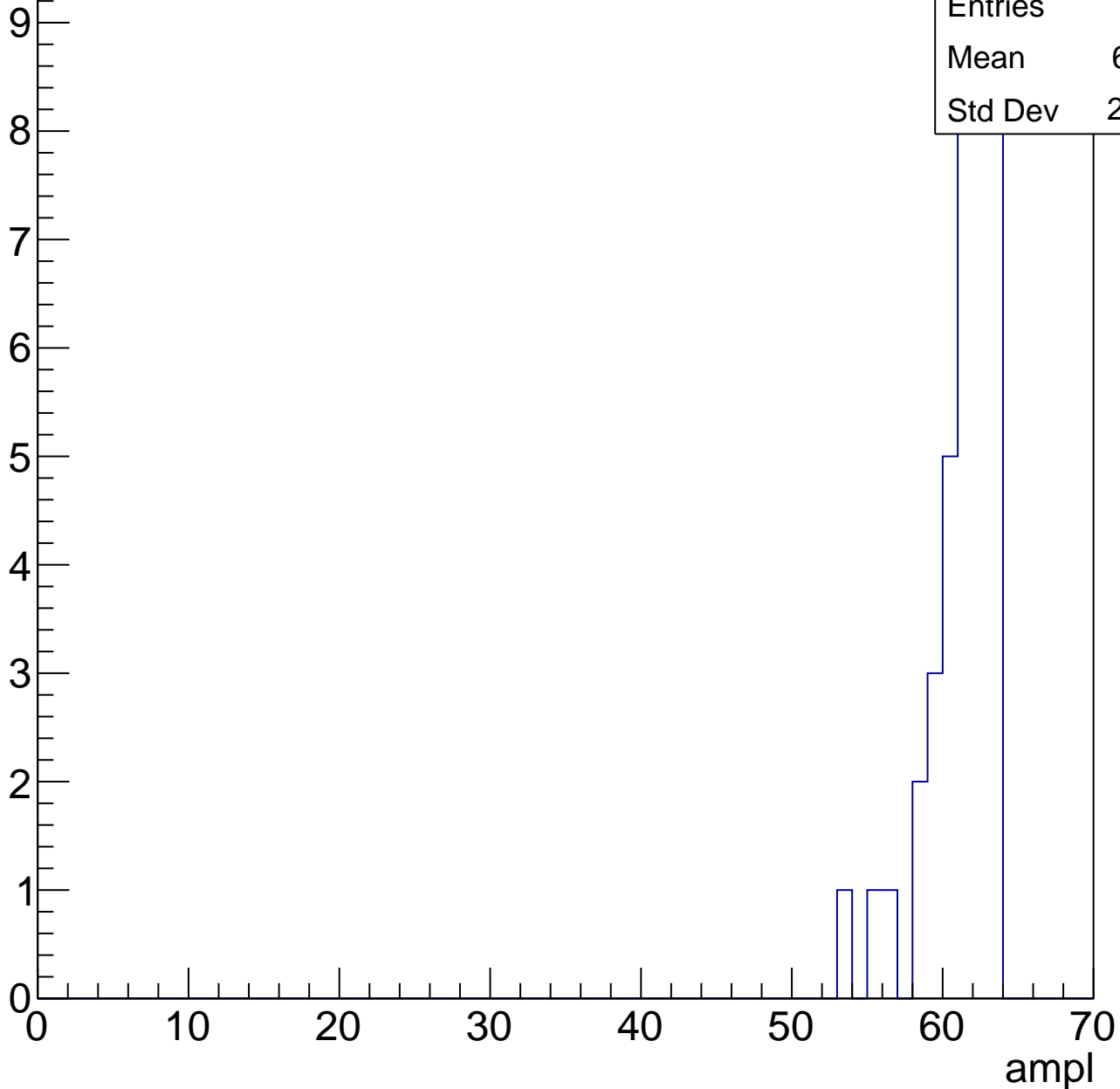


B1L100S, U25-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.71
Std Dev	2.282



B1L100S, U25-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch20, adc0

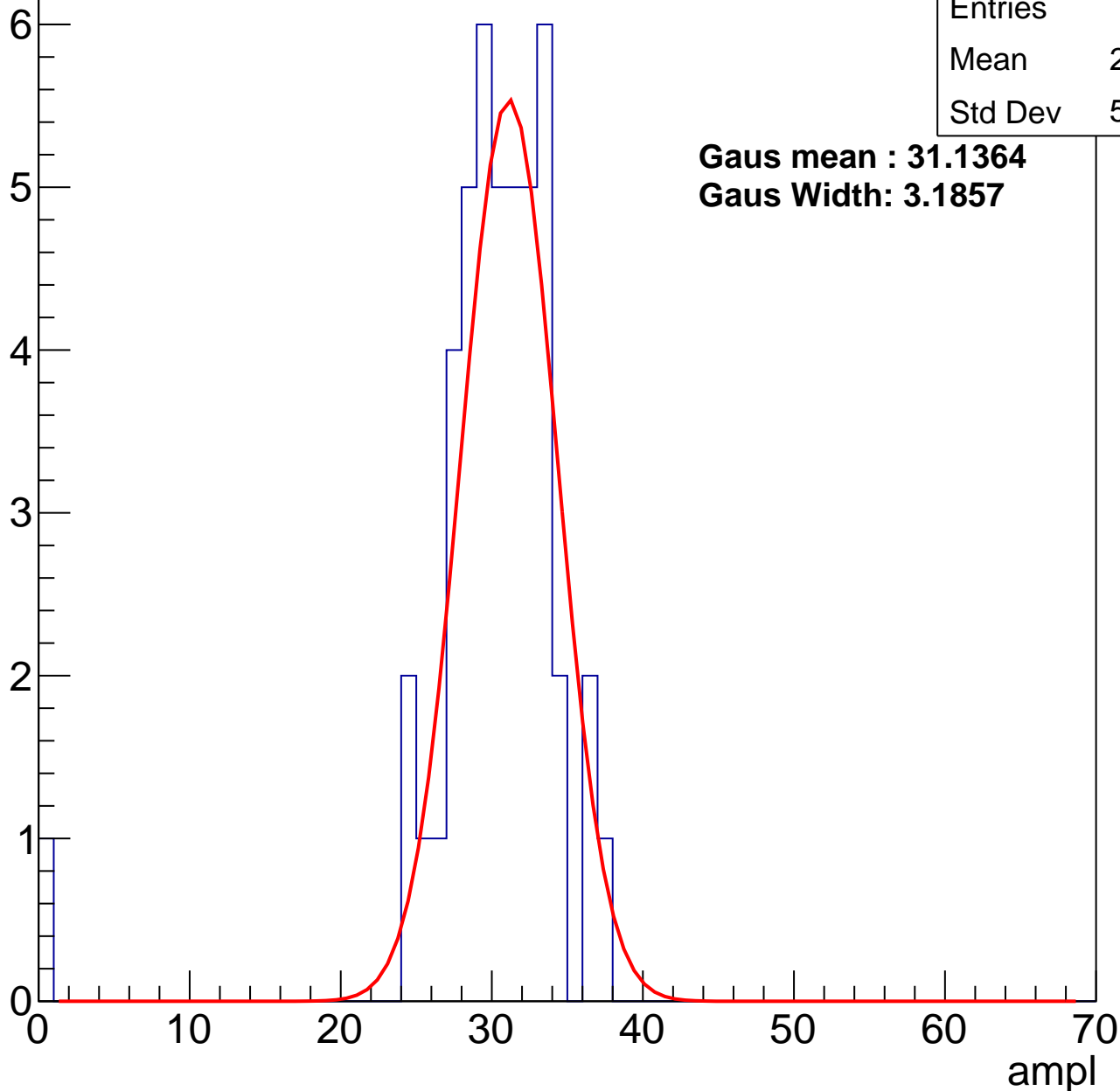
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	29.59
Std Dev	5.314

Gaus mean : 31.1364

Gaus Width: 3.1857



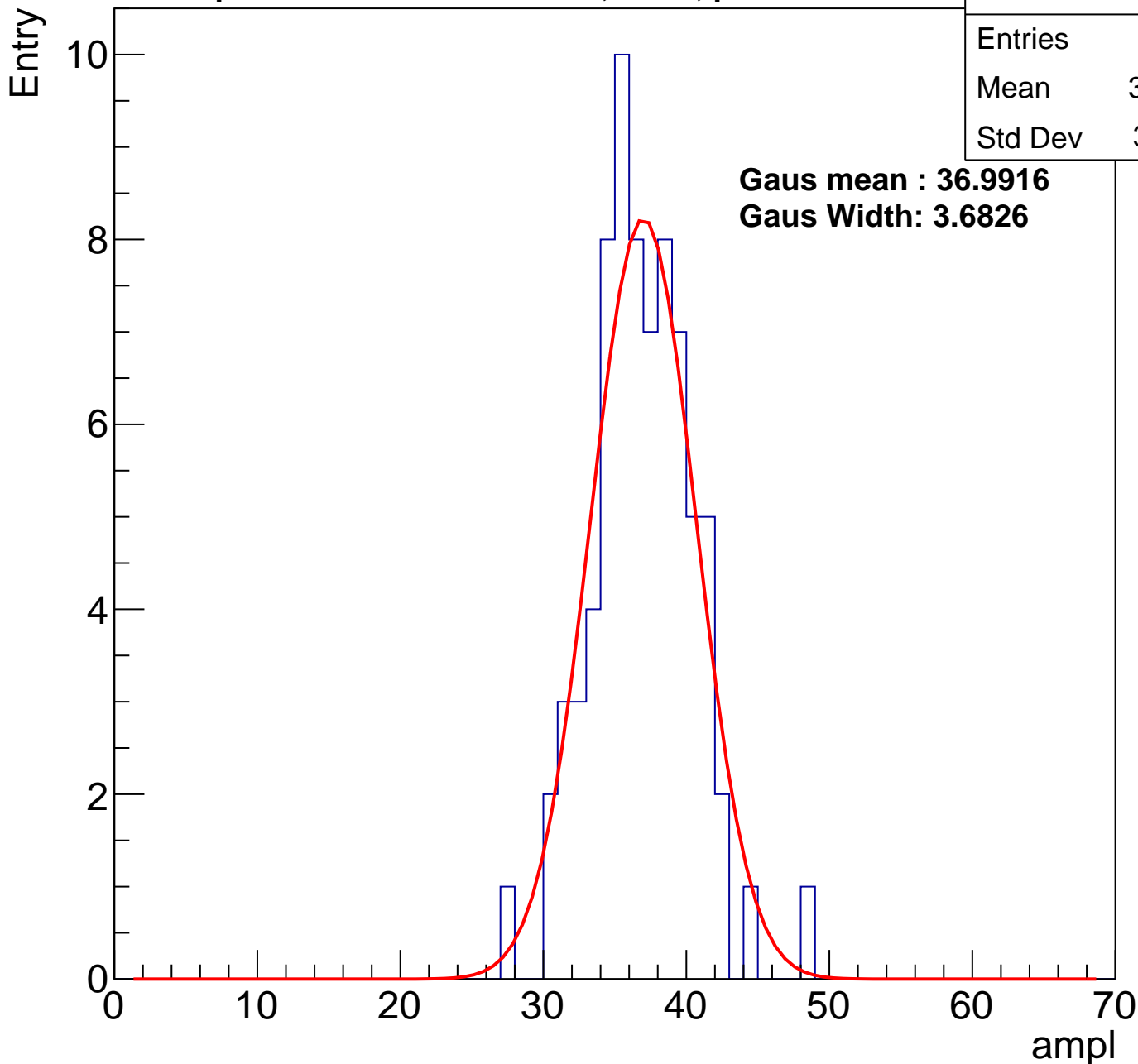
B1L100S, U25-ch20, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	36.47
Std Dev	3.511

Gaus mean : 36.9916

Gaus Width: 3.6826



B1L100S, U25-ch20, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	43.82
Std Dev	3.194

Gaus mean : 44.2575

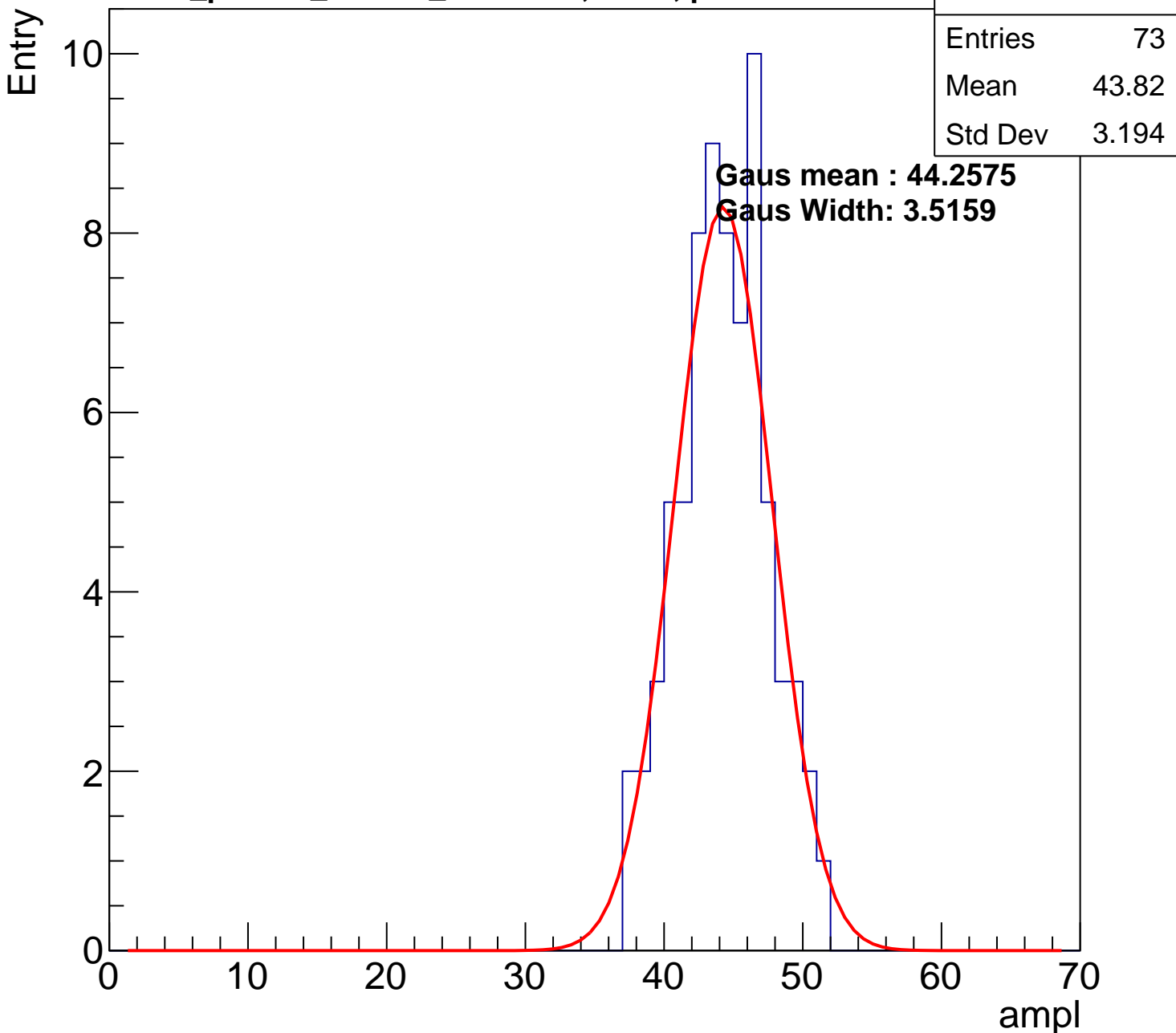
Gaus Width: 3.5159

Entry

10
8
6
4
2
0

ampl

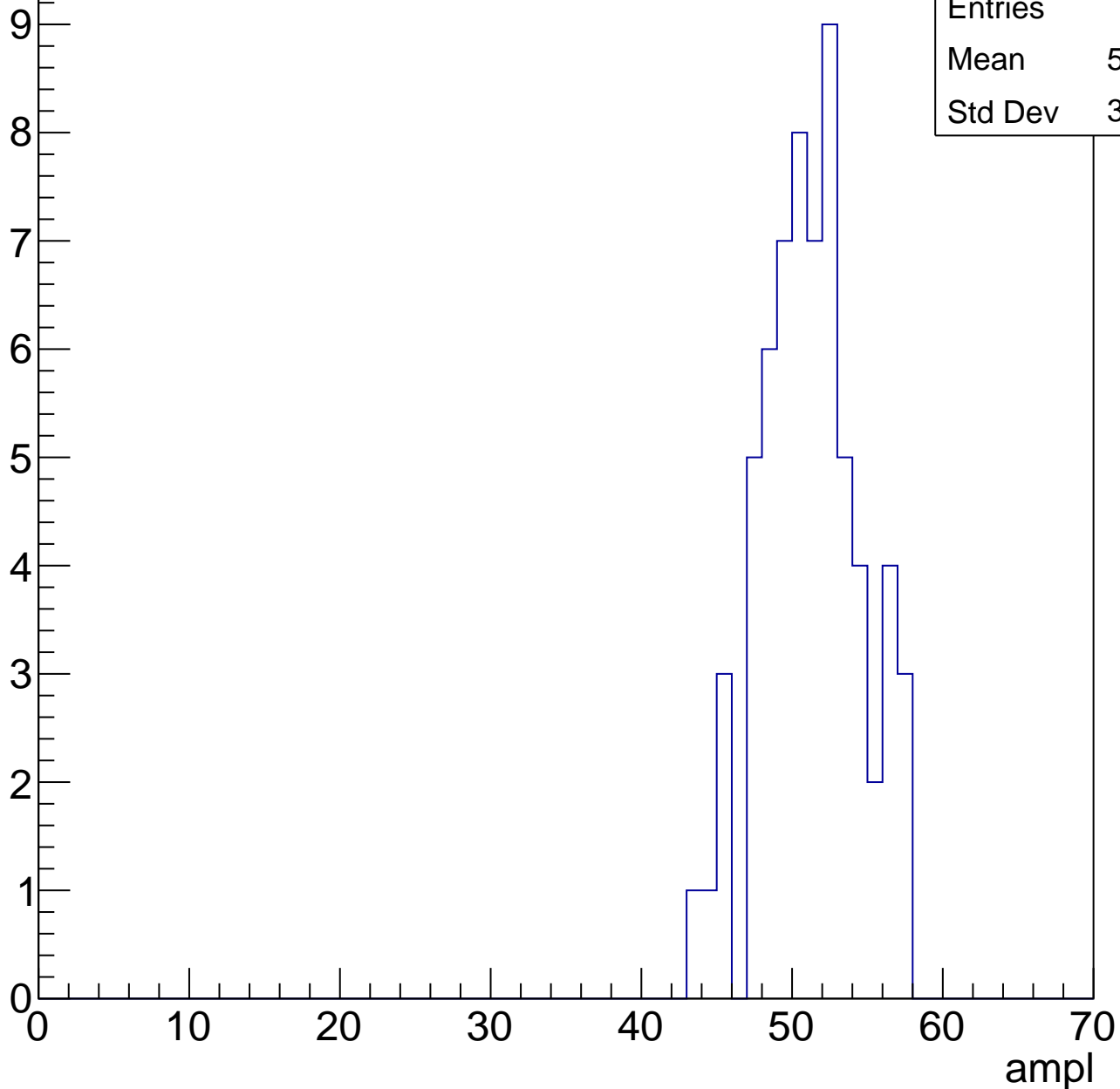
0 10 20 30 40 50 60 70



B1L100S, U25-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

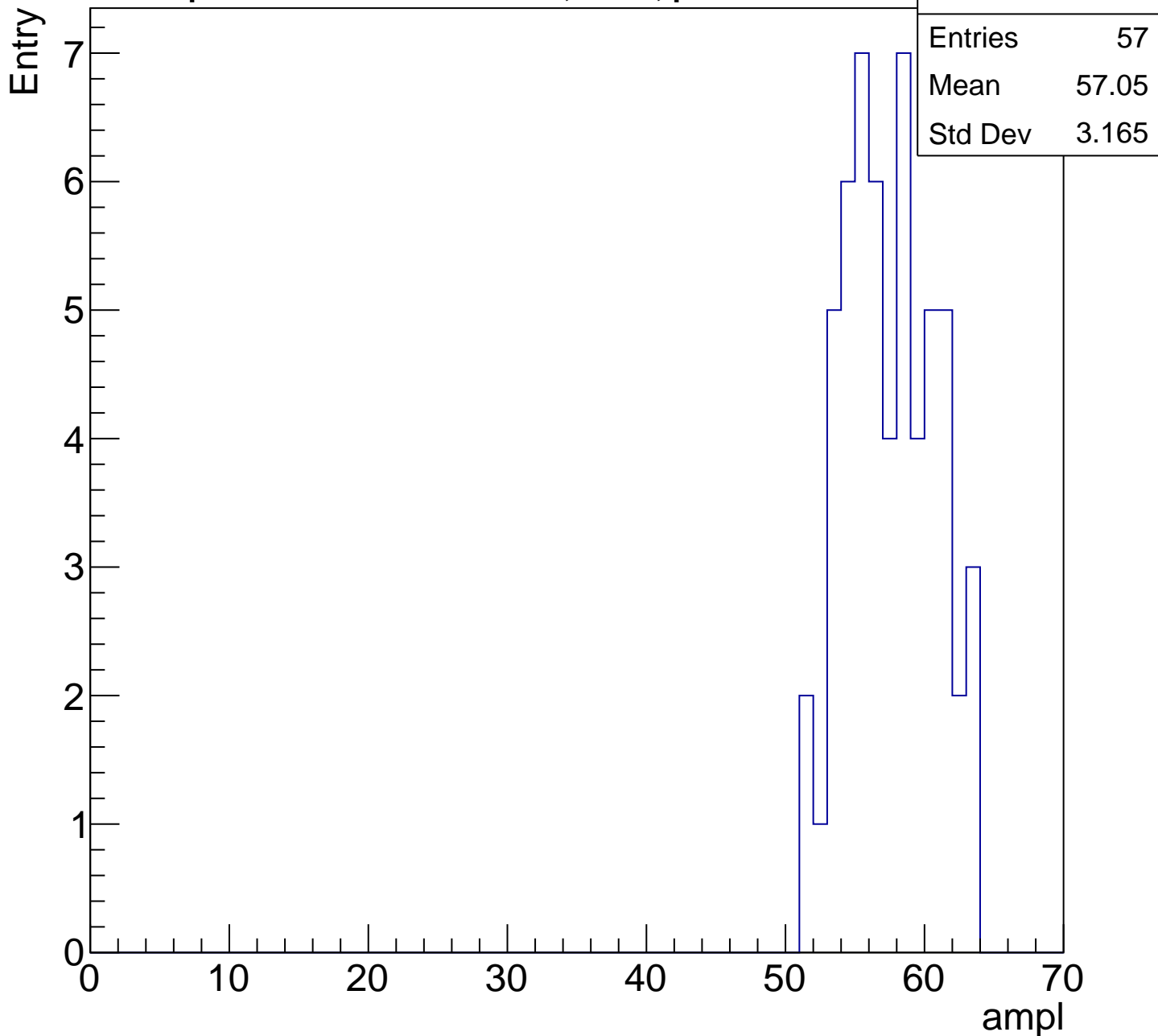
Entry



Entries	65
Mean	50.75
Std Dev	3.263

B1L100S, U25-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

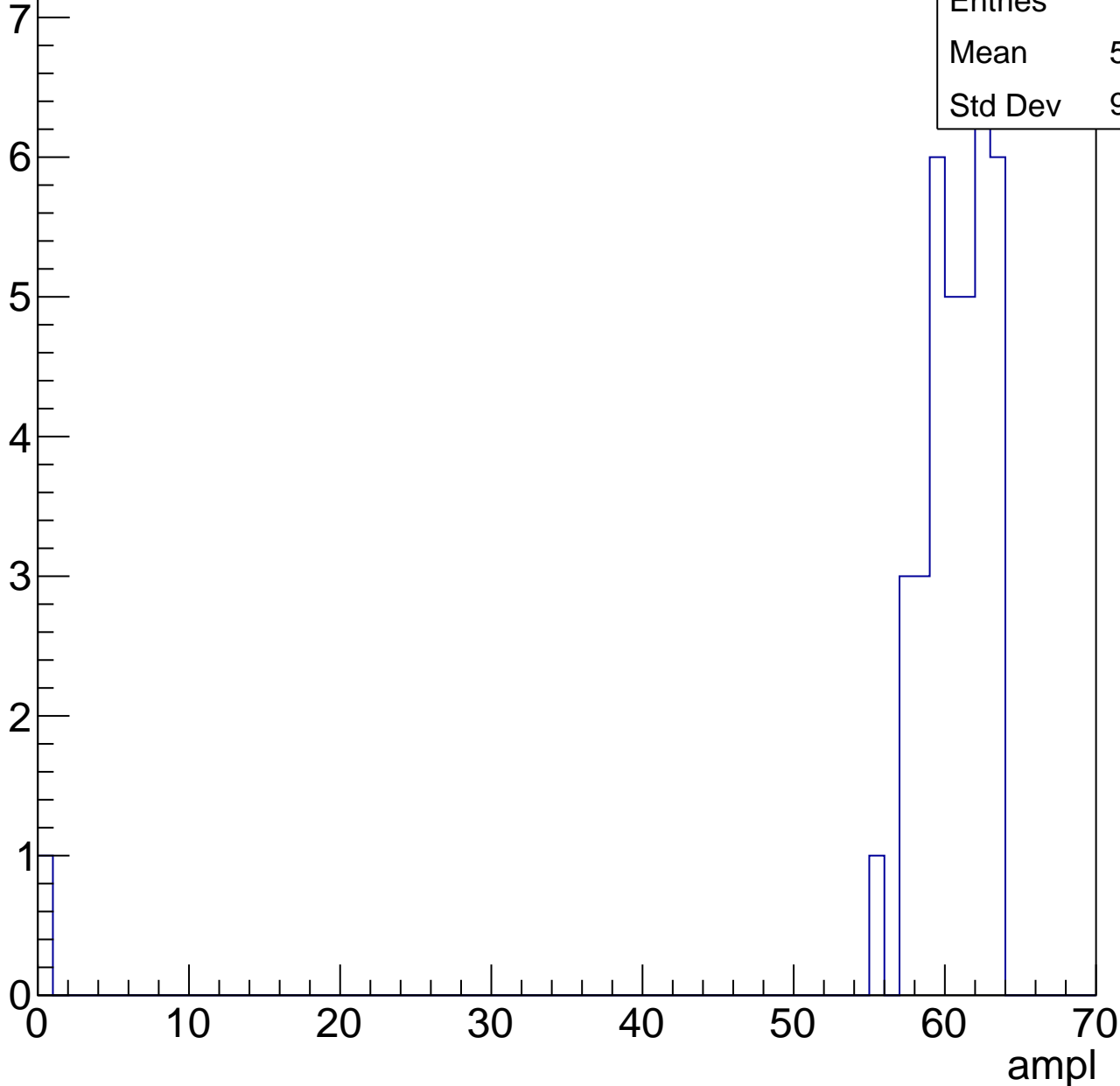


B1L100S, U25-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.68
Std Dev	9.989



B1L100S, U25-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L100S, U25-ch21, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	31.01
Std Dev	6.11

Gaus mean : 32.0861

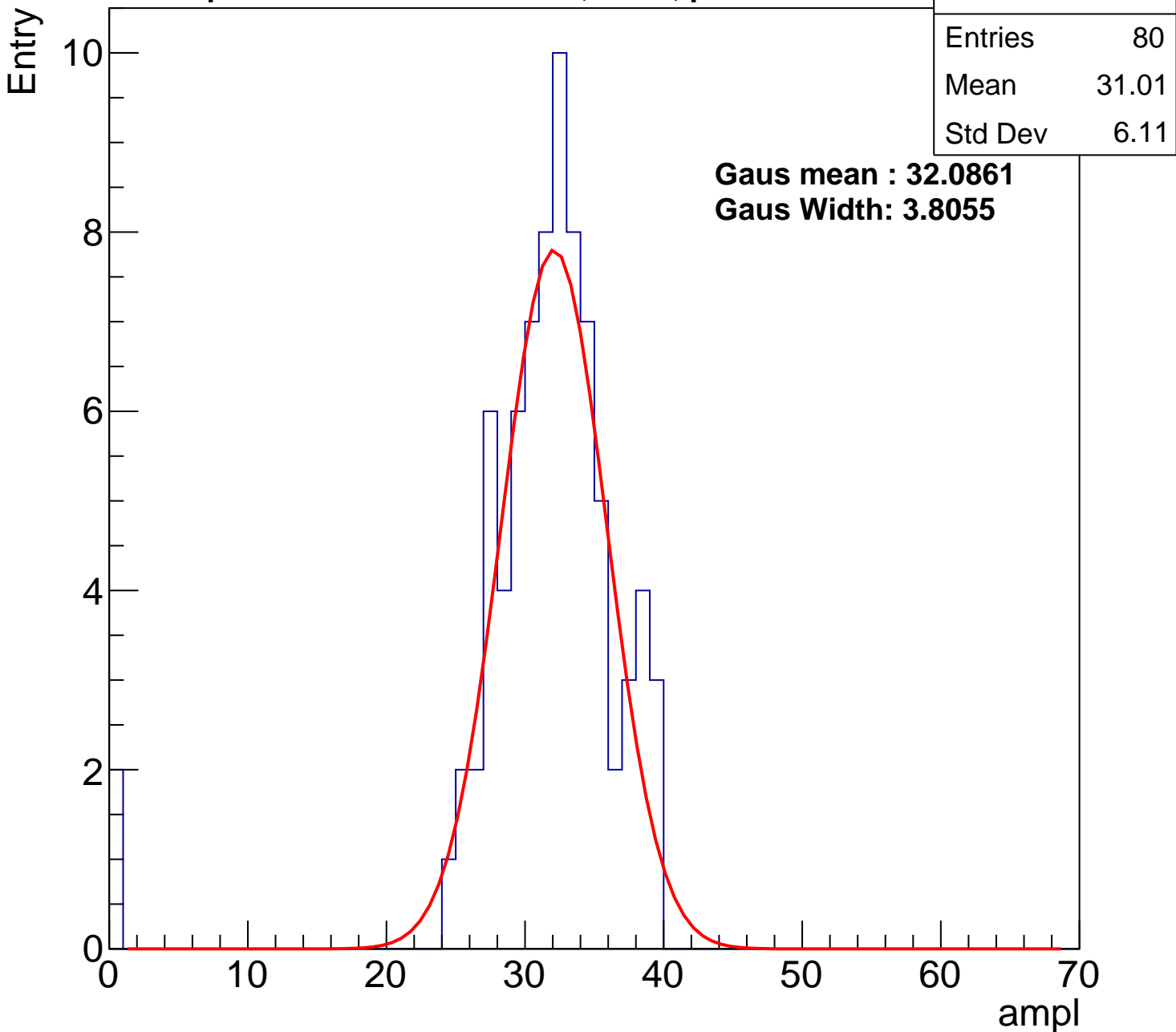
Gaus Width: 3.8055

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch21, adc1

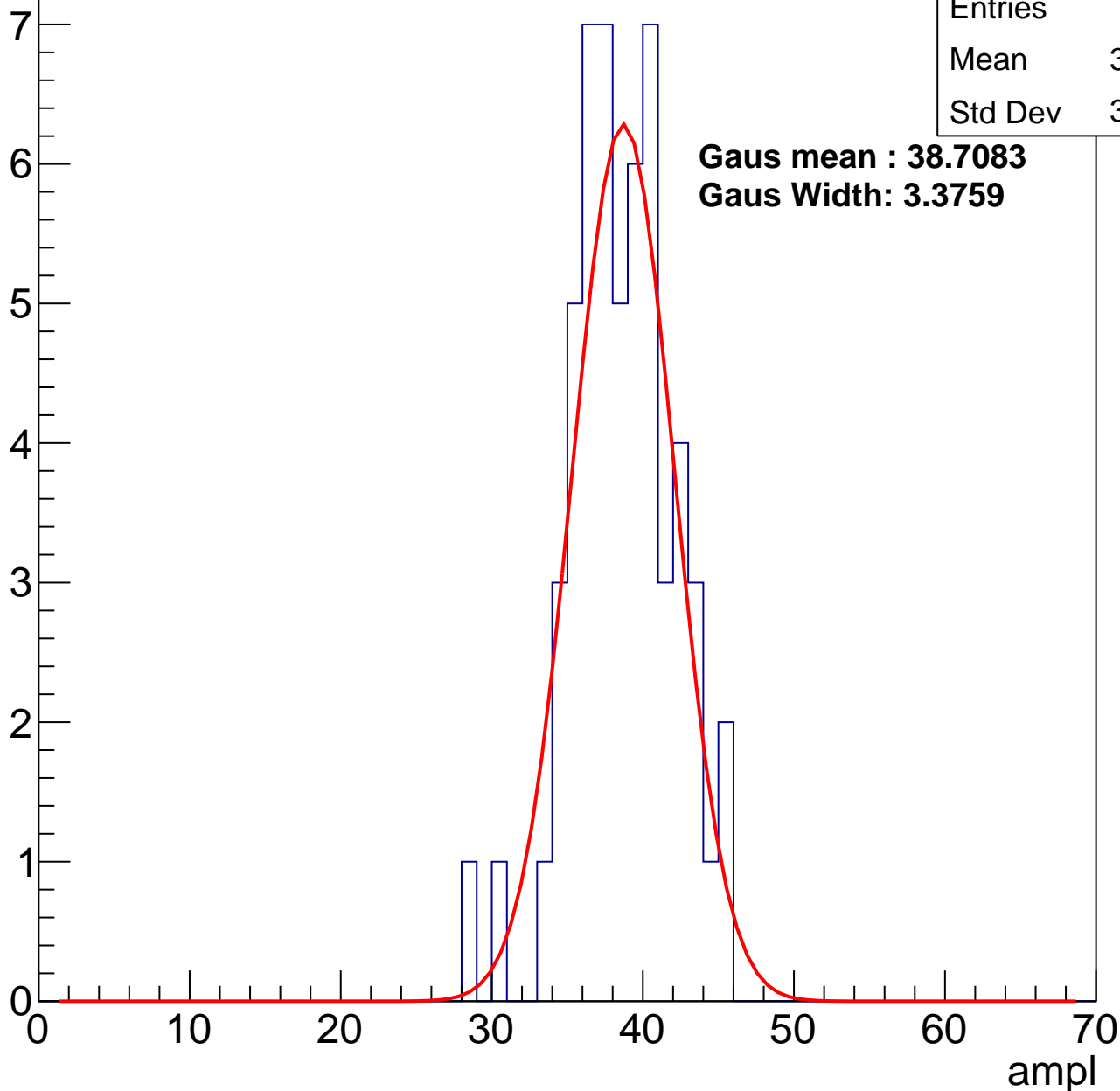
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	38.16
Std Dev	3.406

Gaus mean : 38.7083

Gaus Width: 3.3759



B1L100S, U25-ch21, adc2

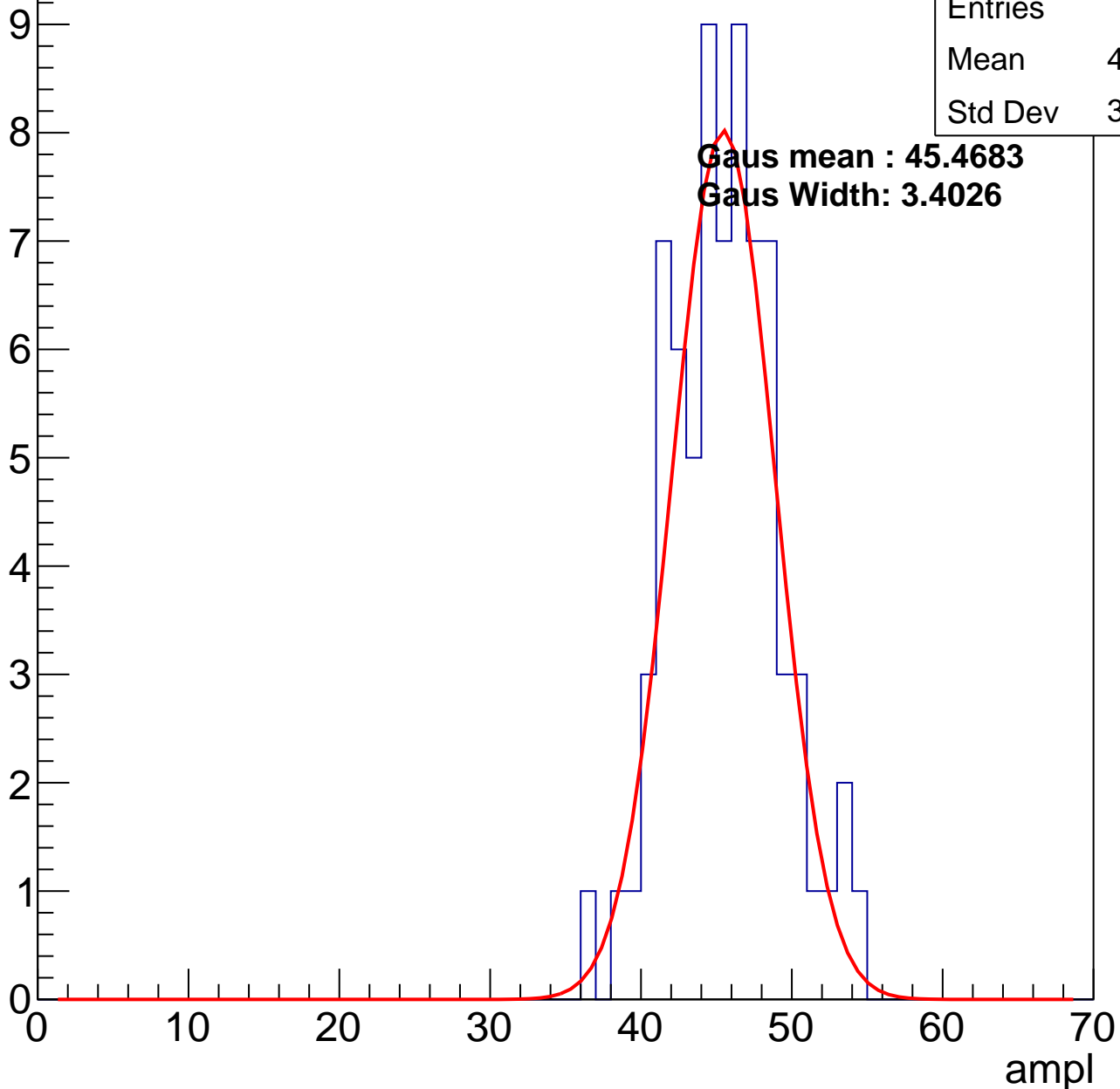
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	45.09
Std Dev	3.587

Gaus mean : 45.4683

Gaus Width: 3.4026

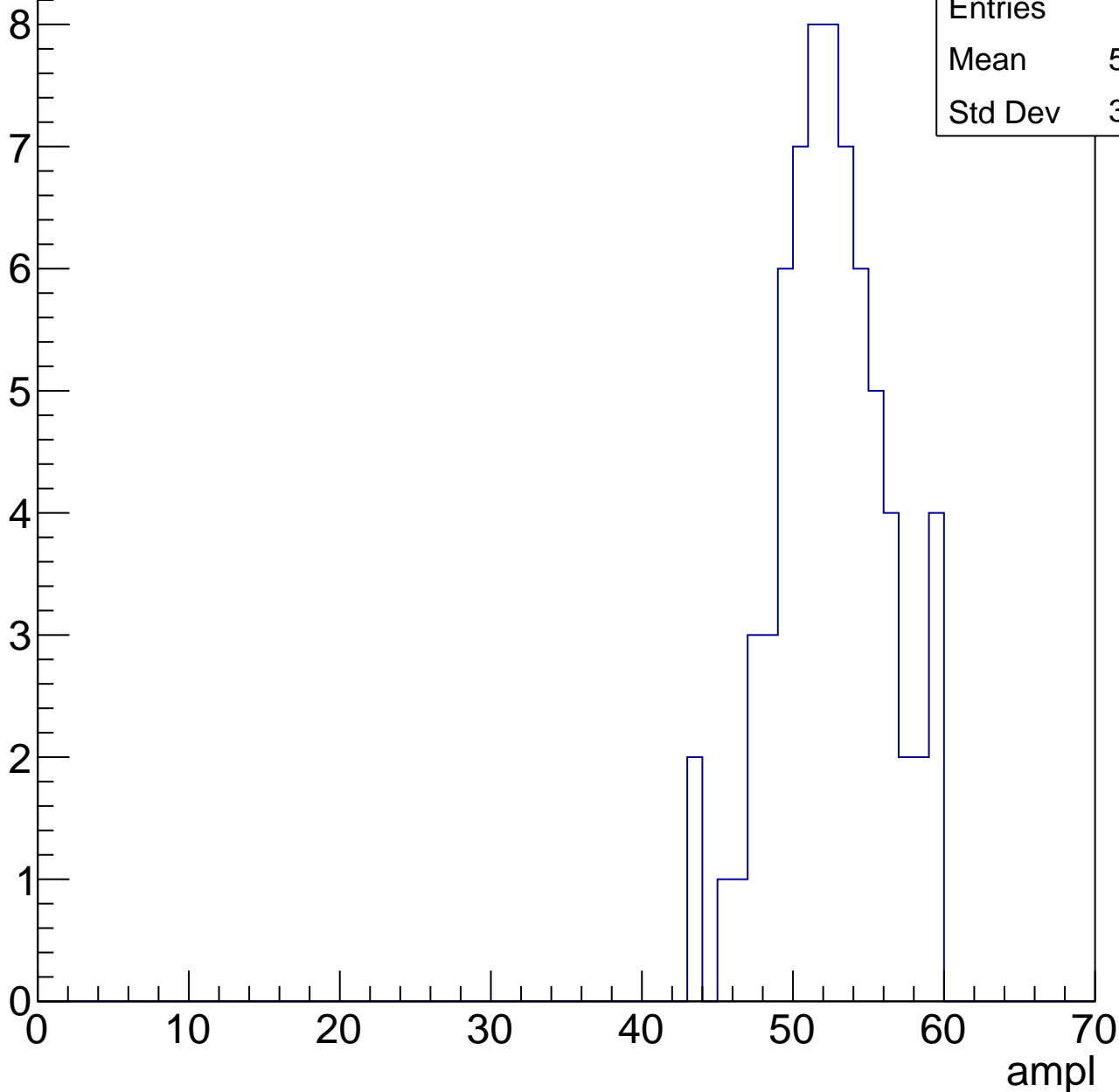


B1L100S, U25-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	52.03
Std Dev	3.647

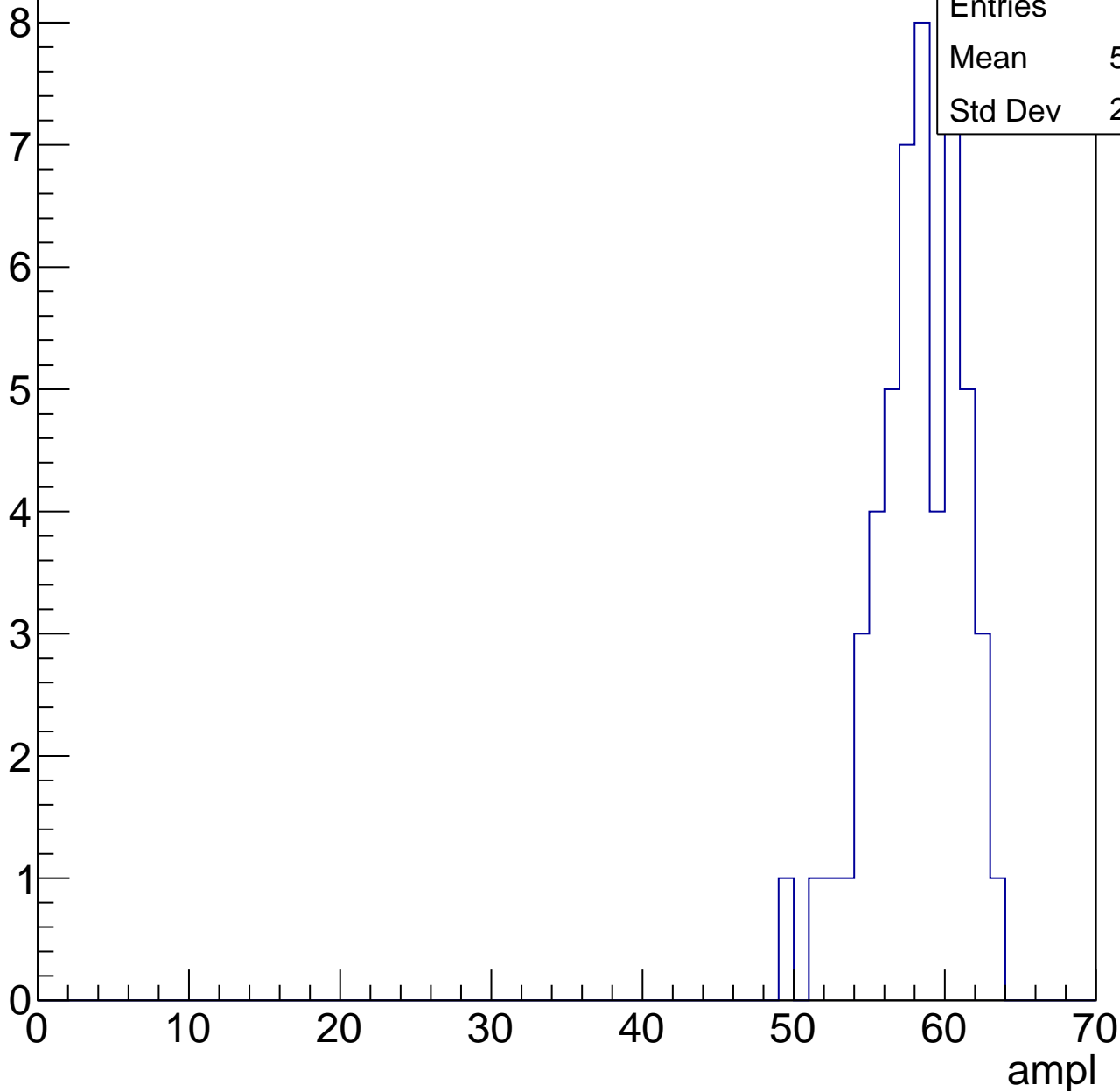


B1L100S, U25-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	57.69
Std Dev	2.945

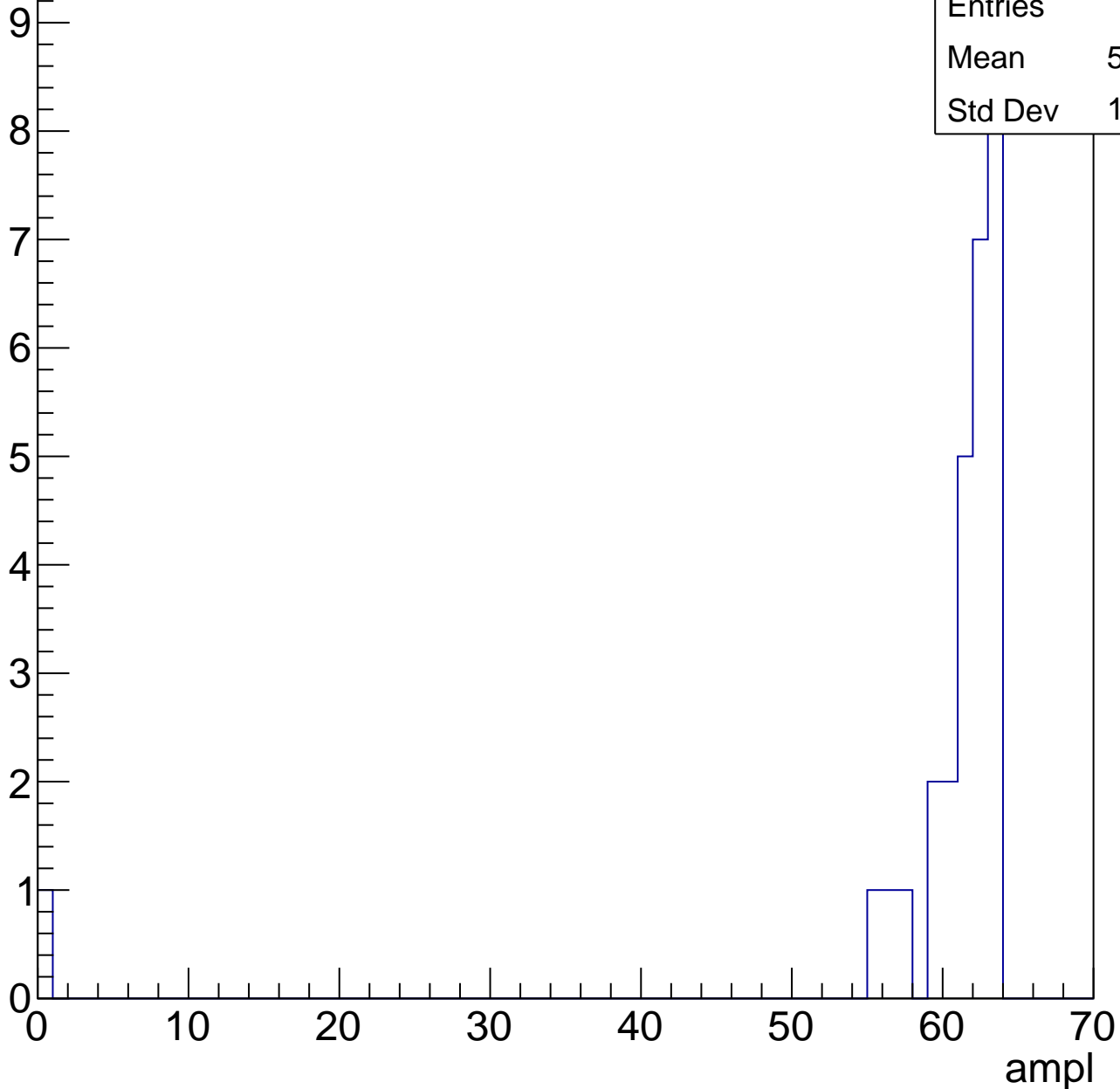


B1L100S, U25-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	59.03
Std Dev	11.35



B1L100S, U25-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch22, adc0

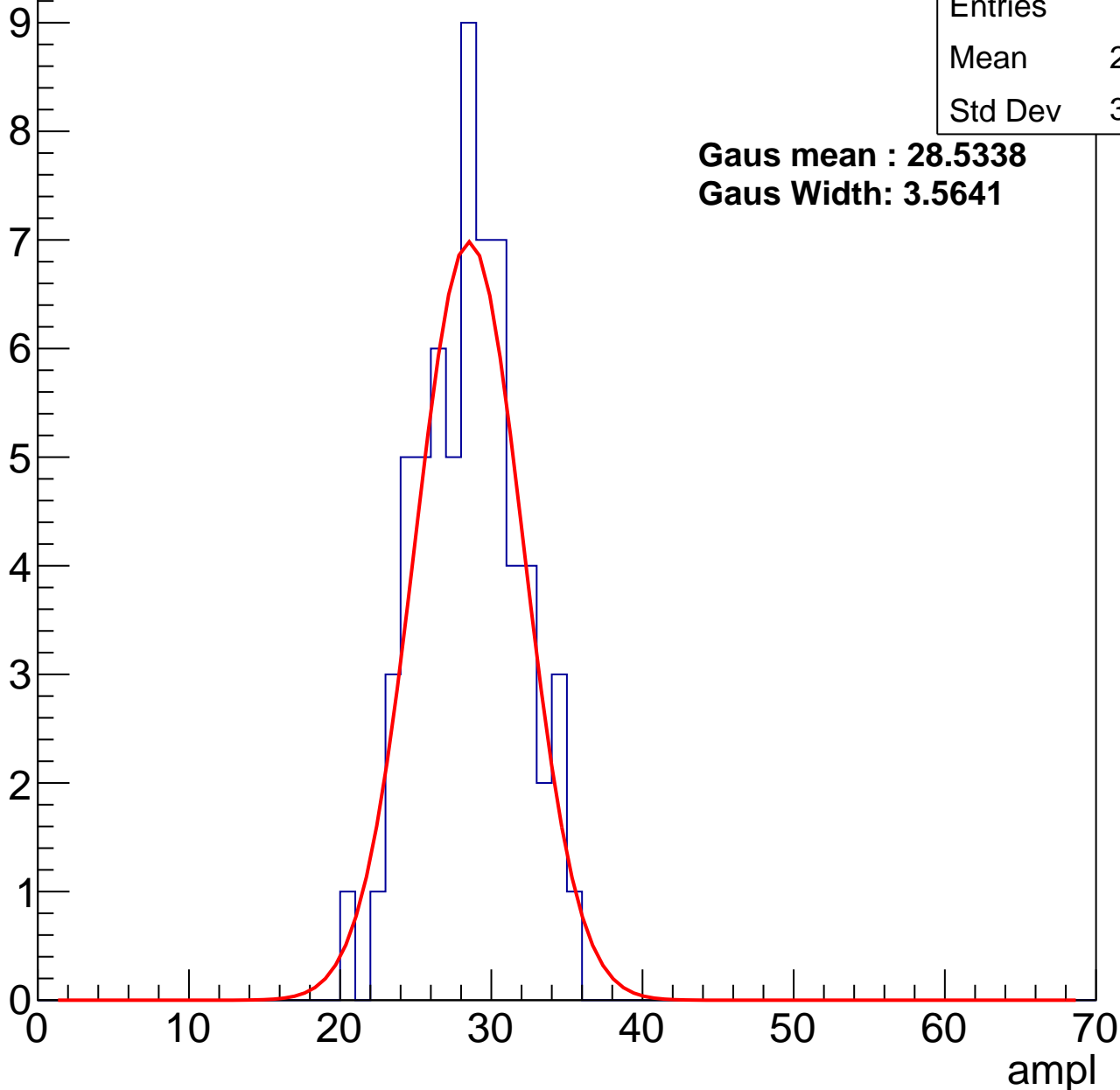
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.05
Std Dev	3.263

Gaus mean : 28.5338

Gaus Width: 3.5641



B1L100S, U25-ch22, adc1

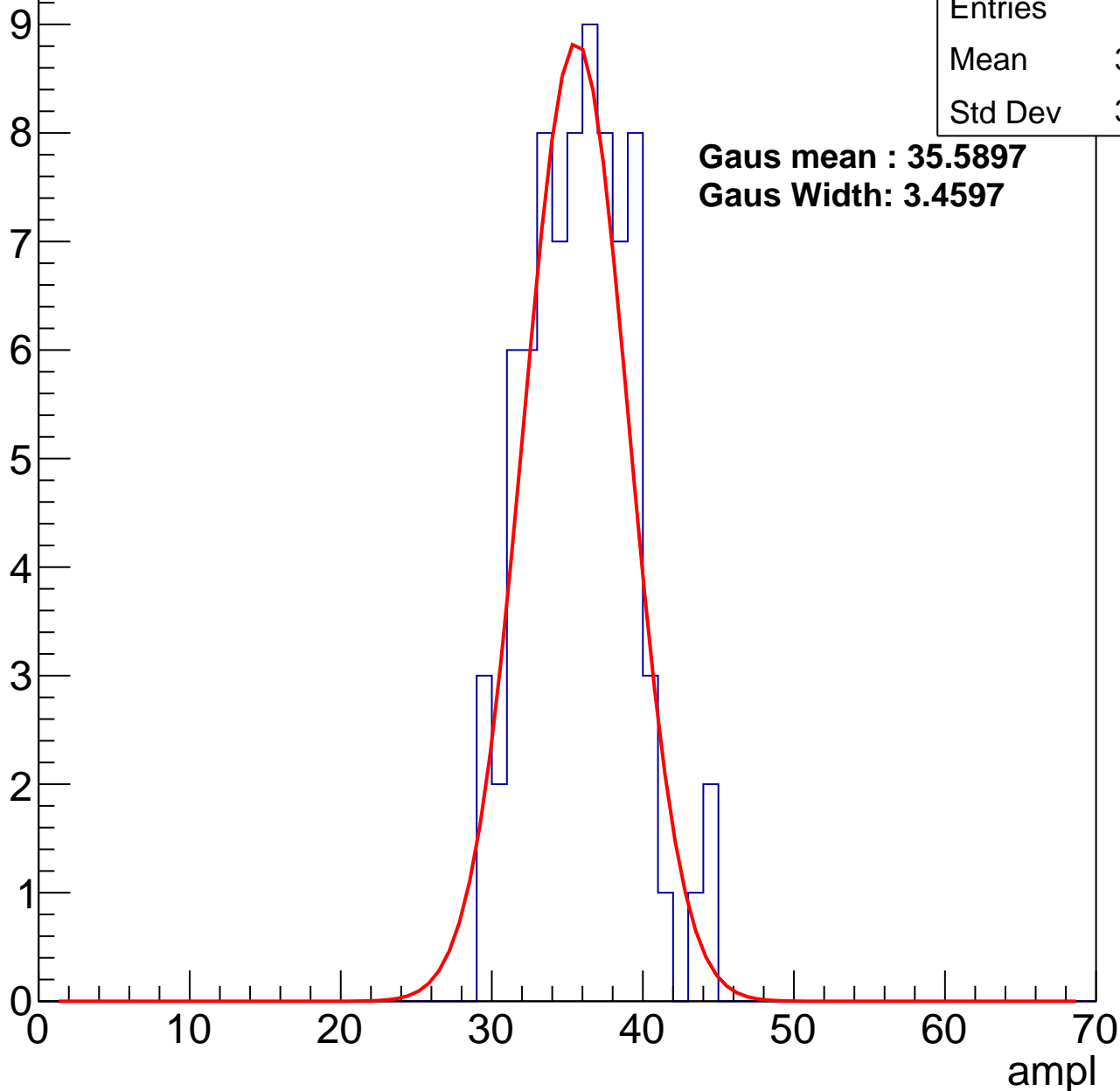
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.41
Std Dev	3.381

Gaus mean : 35.5897

Gaus Width: 3.4597



B1L100S, U25-ch22, adc2

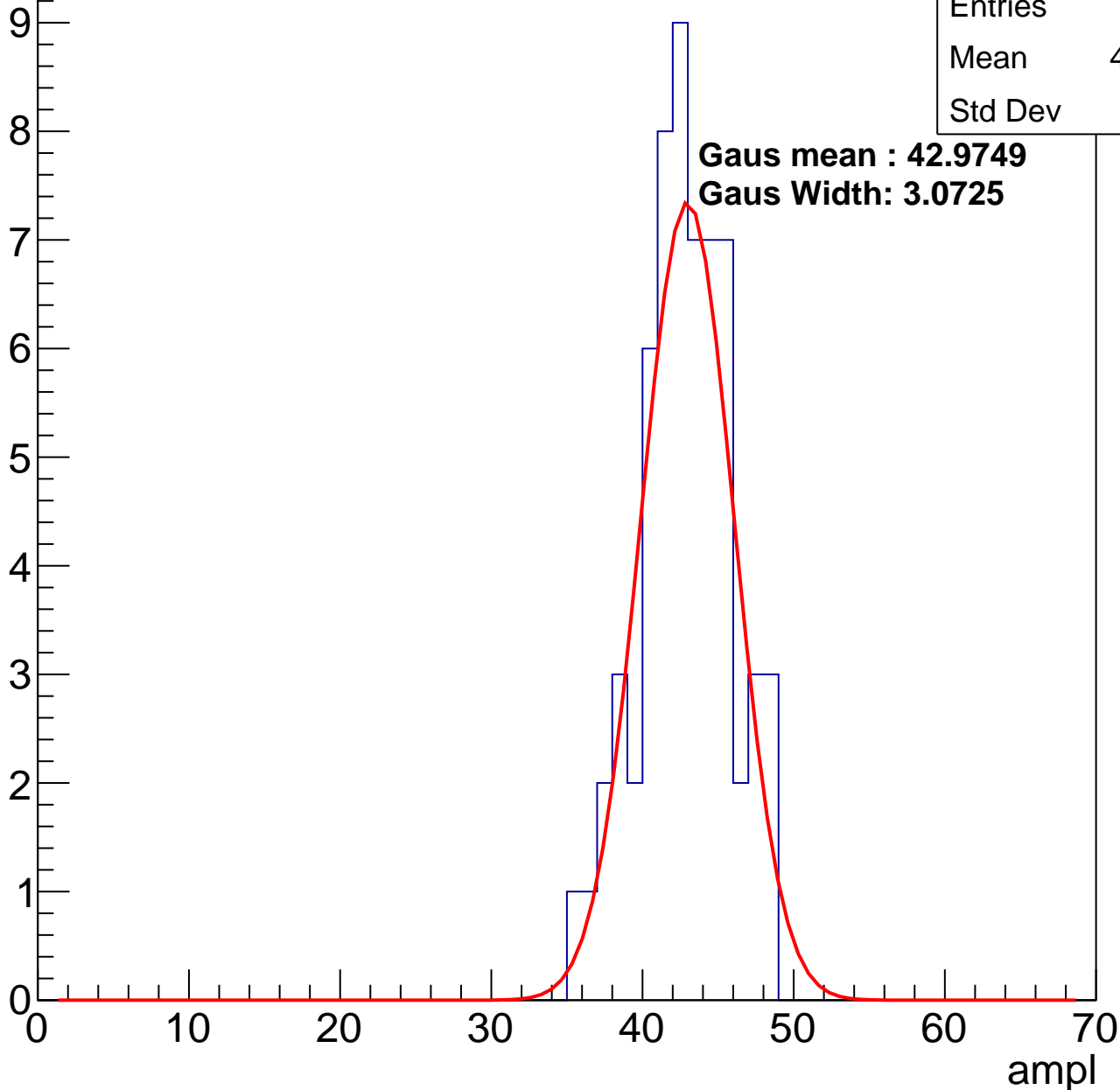
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.36
Std Dev	2.97

Gaus mean : 42.9749

Gaus Width: 3.0725

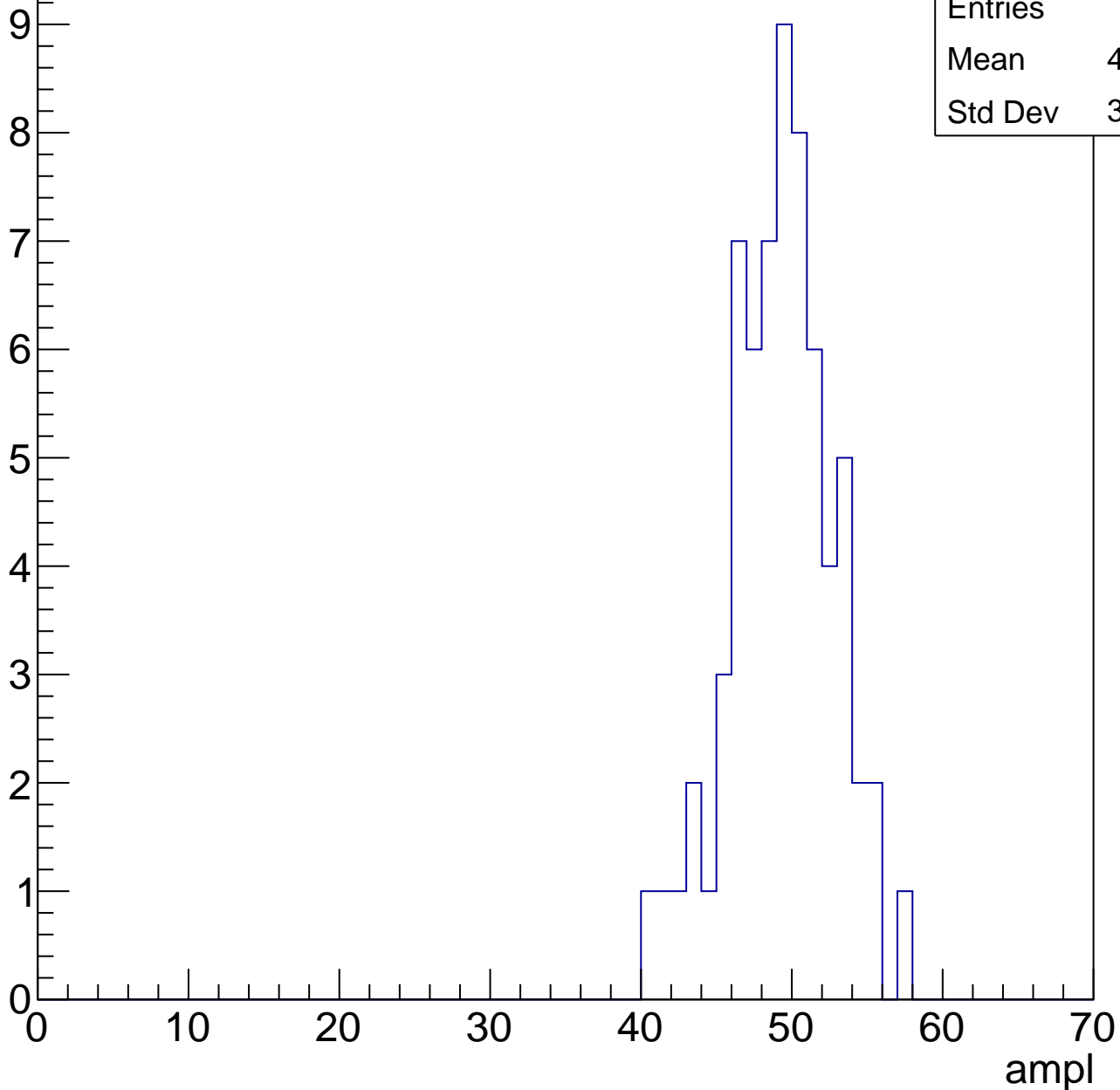


B1L100S, U25-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	48.83
Std Dev	3.405

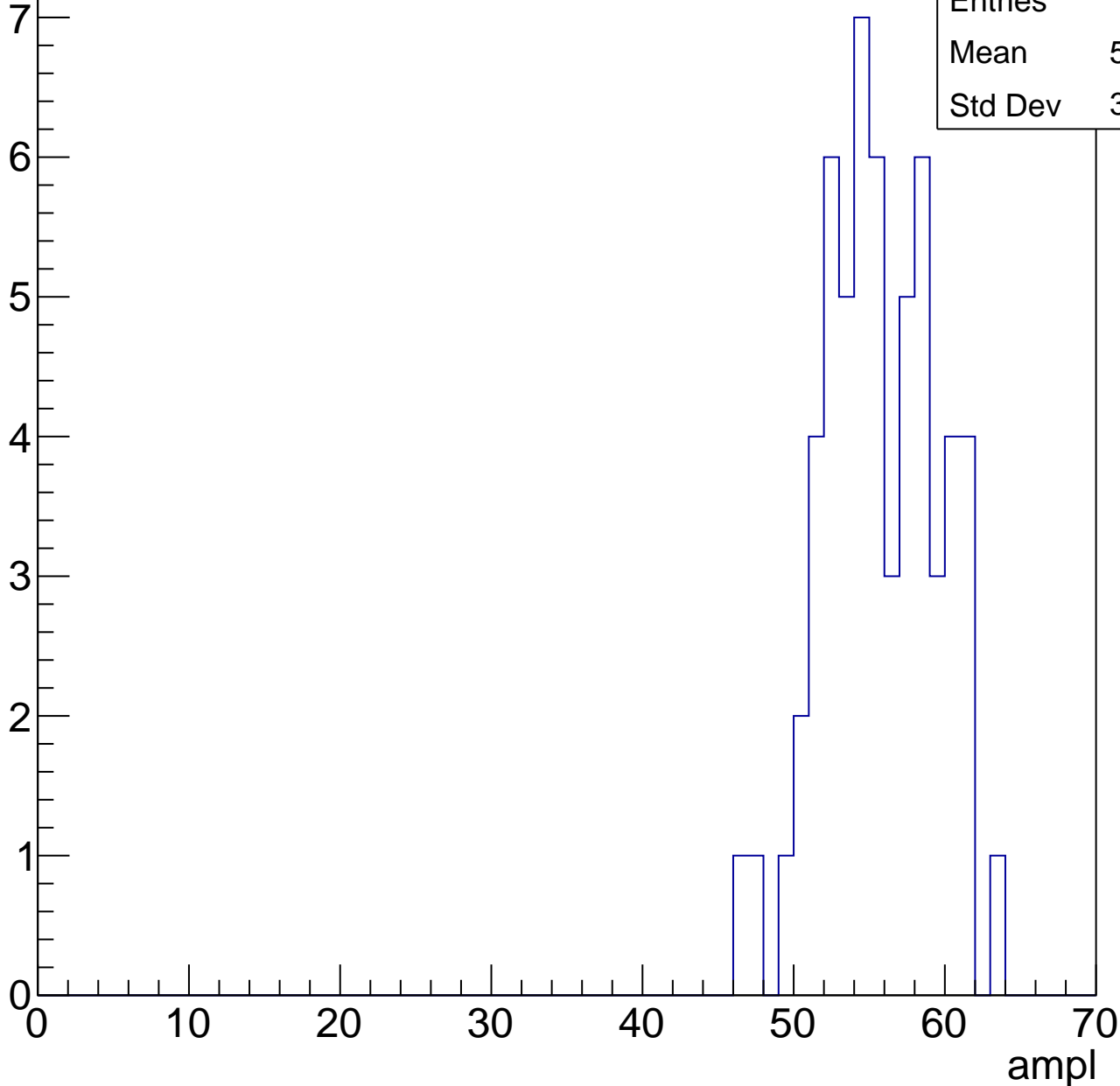


B1L100S, U25-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

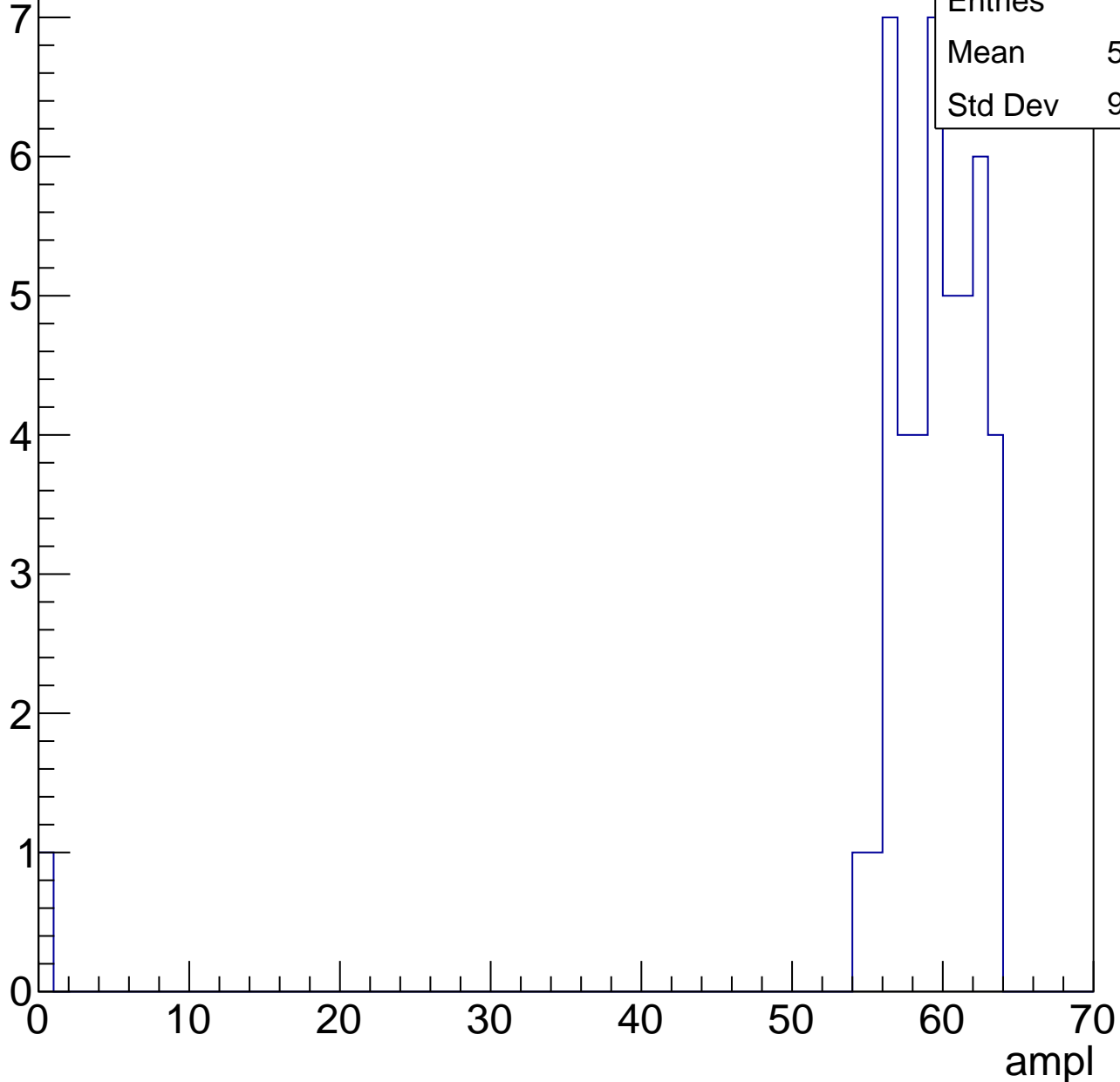
Entries	59
Mean	55.19
Std Dev	3.698



B1L100S, U25-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

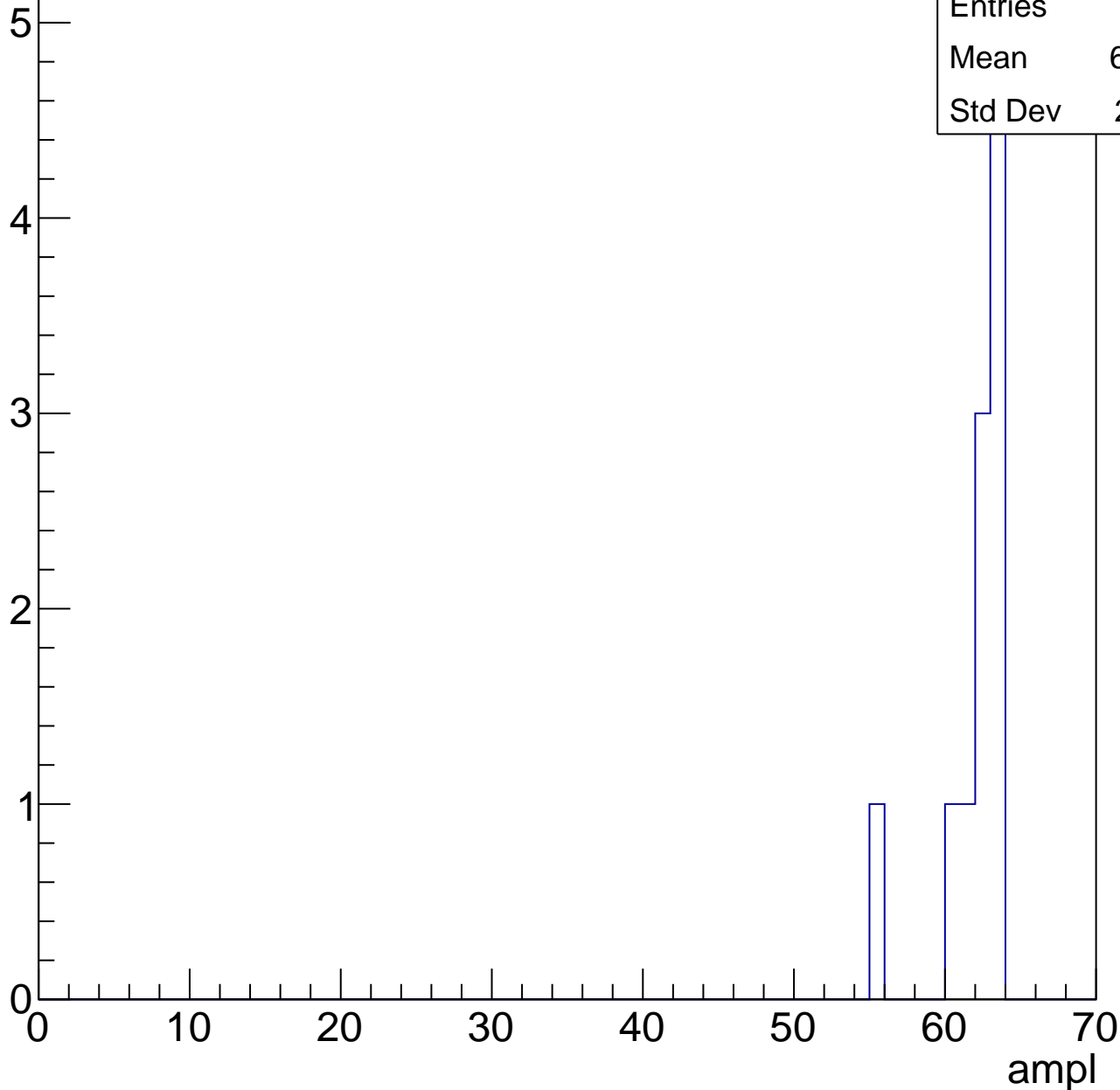


B1L100S, U25-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	61.55
Std Dev	2.271



B1L100S, U25-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch23, adc0

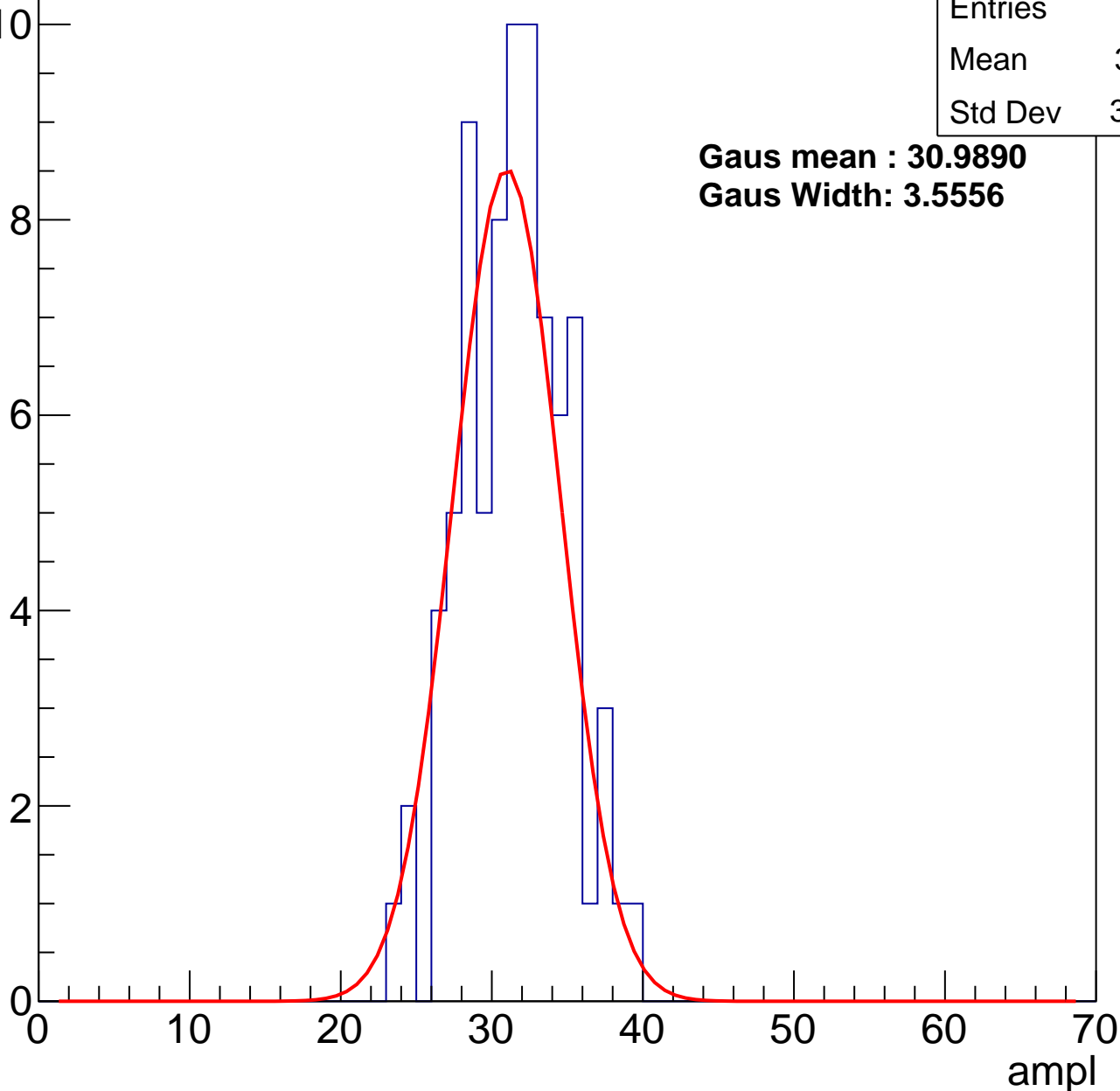
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	31.01
Std Dev	3.356

Gaus mean : 30.9890

Gaus Width: 3.5556



B1L100S, U25-ch23, adc1

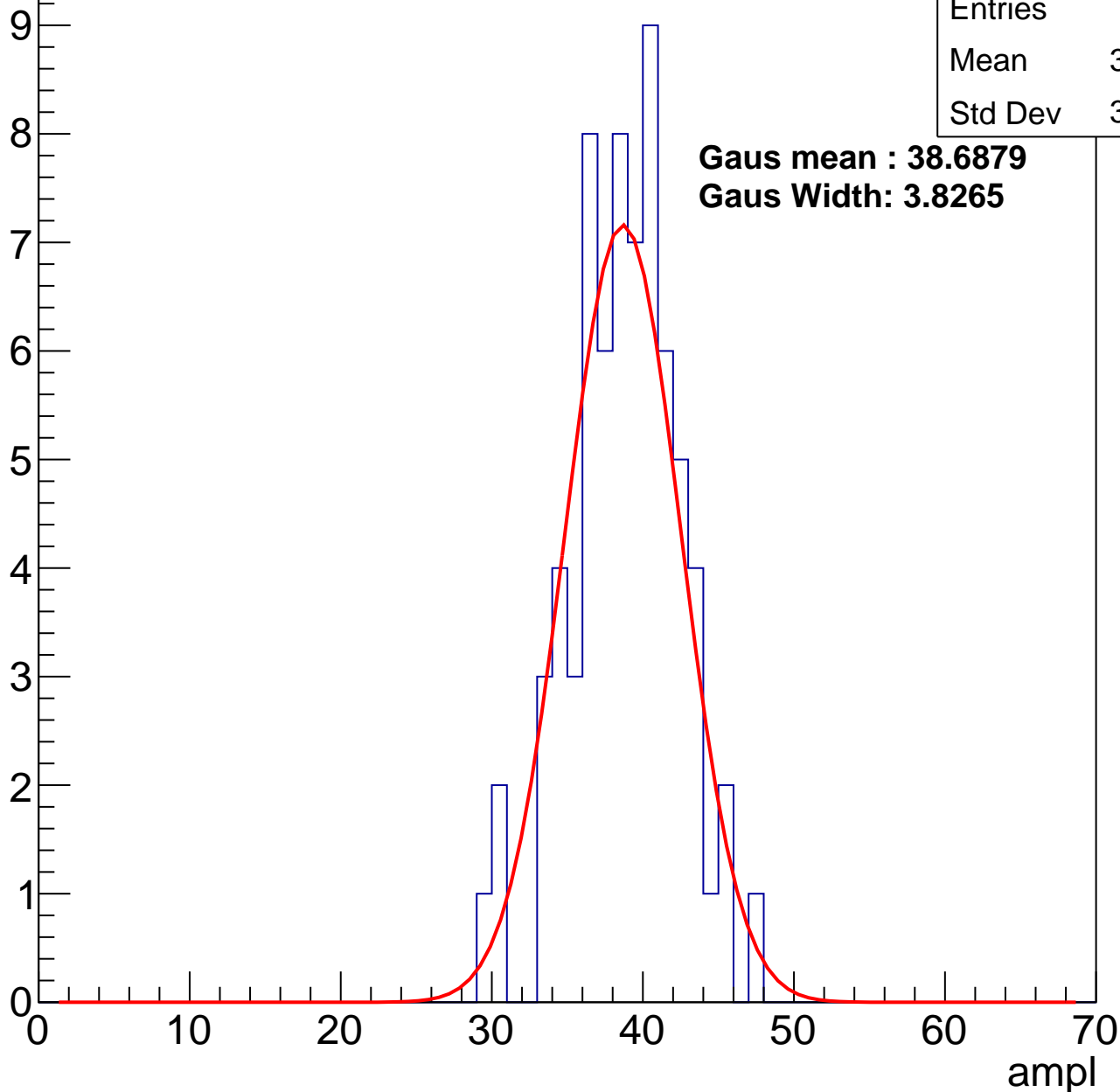
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	38.36
Std Dev	3.578

Gaus mean : 38.6879

Gaus Width: 3.8265



B1L100S, U25-ch23, adc2

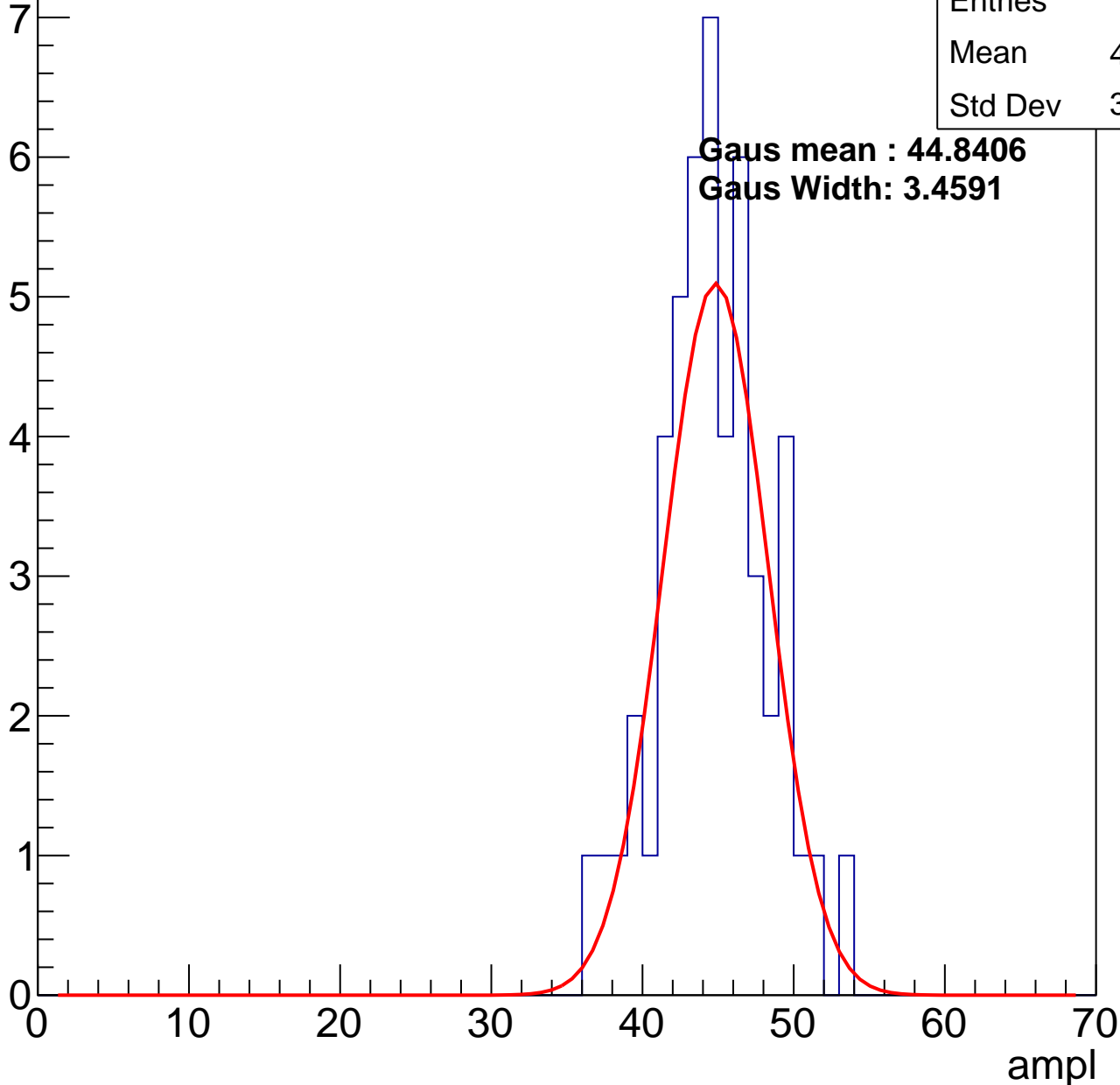
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	44.24
Std Dev	3.542

Gaus mean : 44.8406

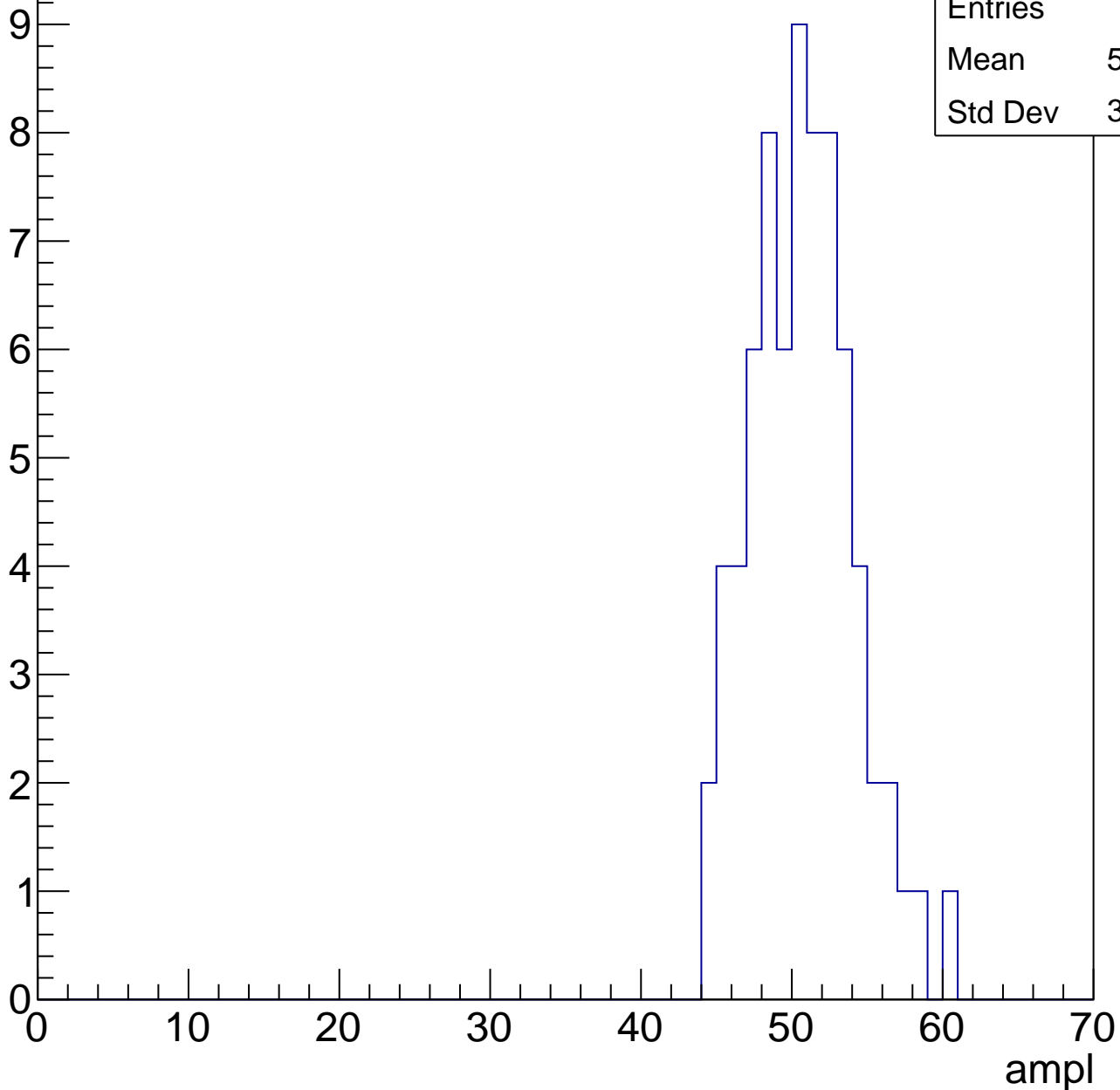
Gaus Width: 3.4591



B1L100S, U25-ch23, adc3

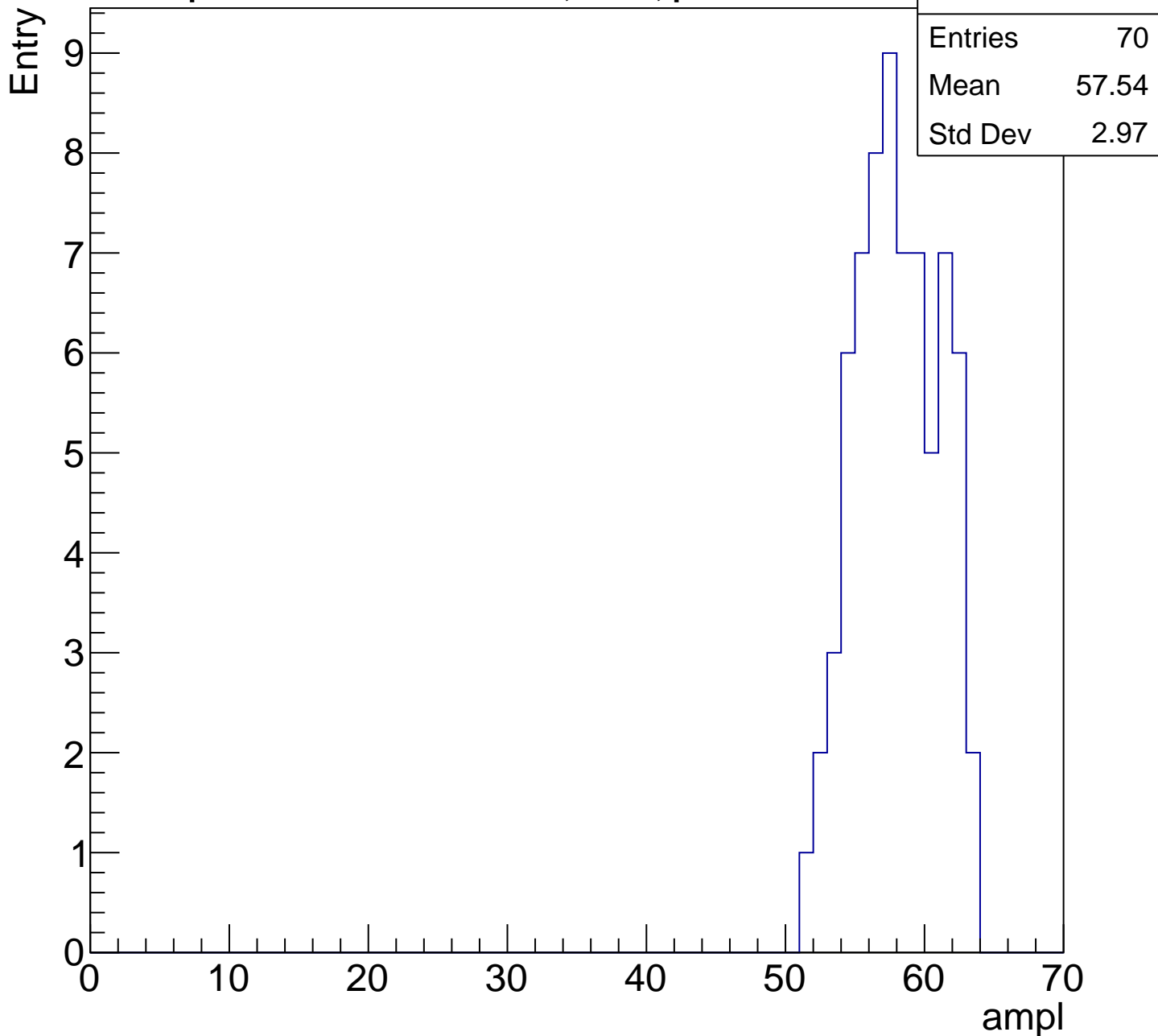
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

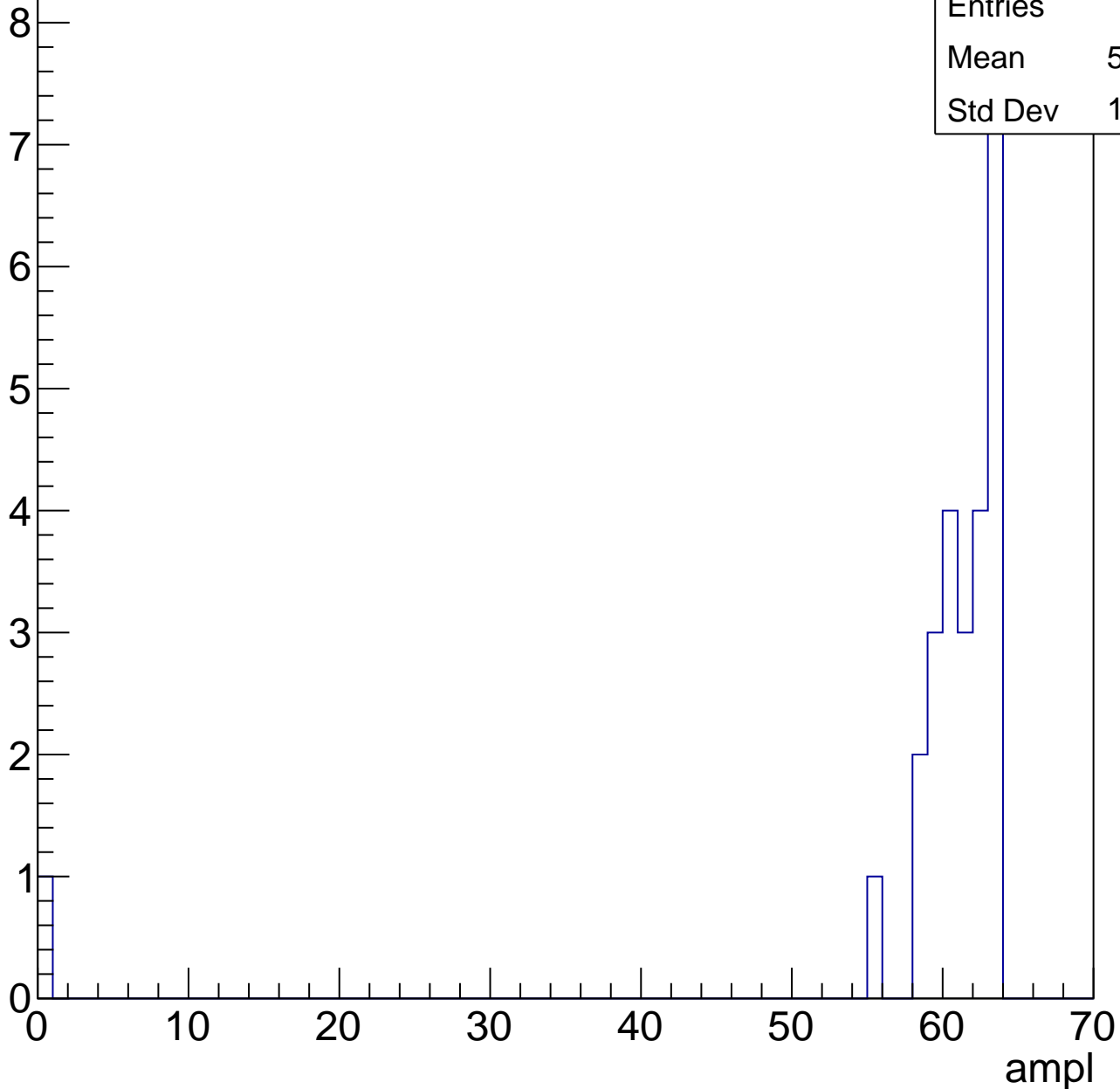


B1L100S, U25-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.58
Std Dev	11.89



B1L100S, U25-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U25-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

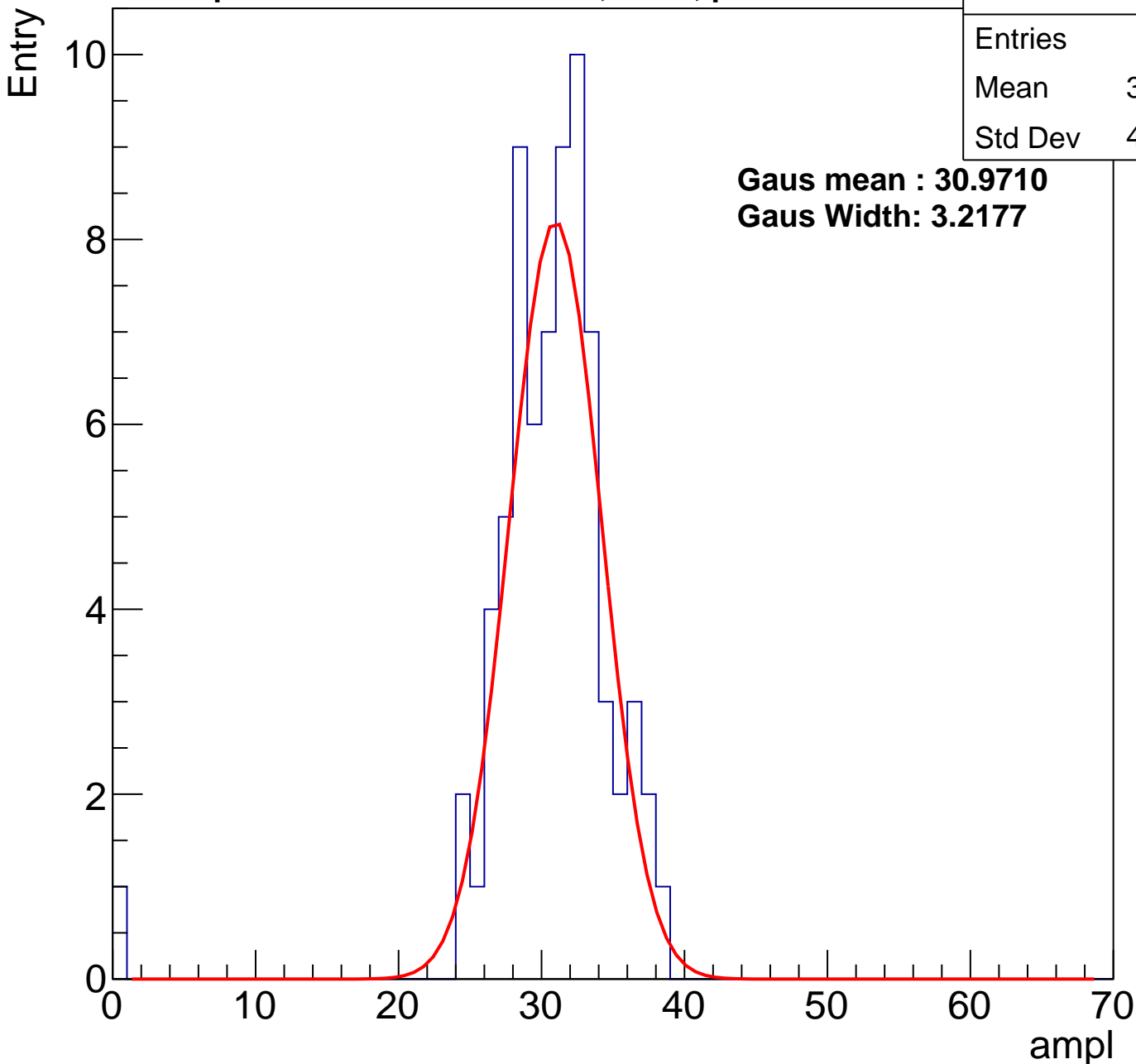
B1L100S, U25-ch24, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	30.14
Std Dev	4.753

Gaus mean : 30.9710

Gaus Width: 3.2177



B1L100S, U25-ch24, adc1

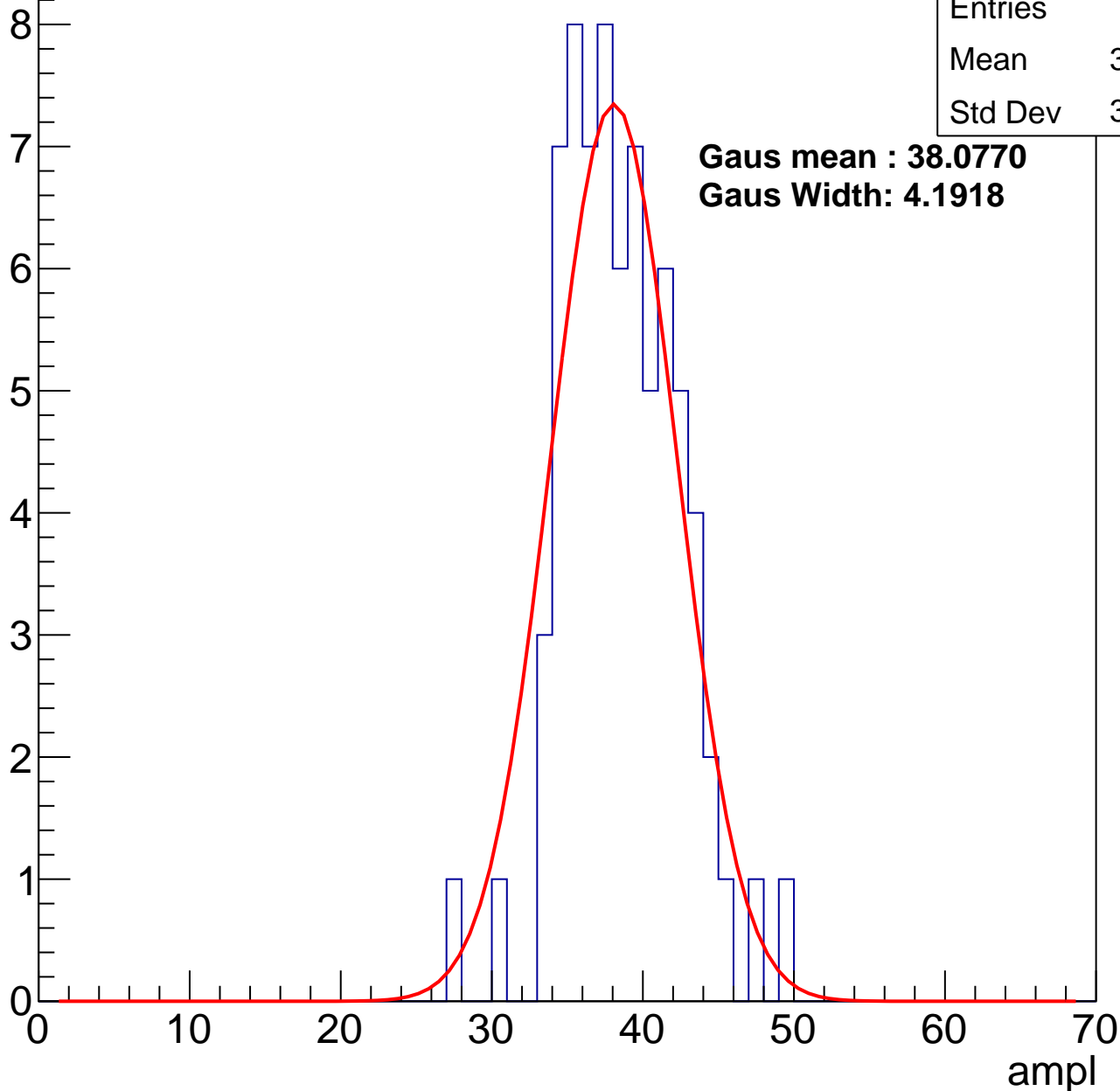
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	38.08
Std Dev	3.817

Gaus mean : 38.0770

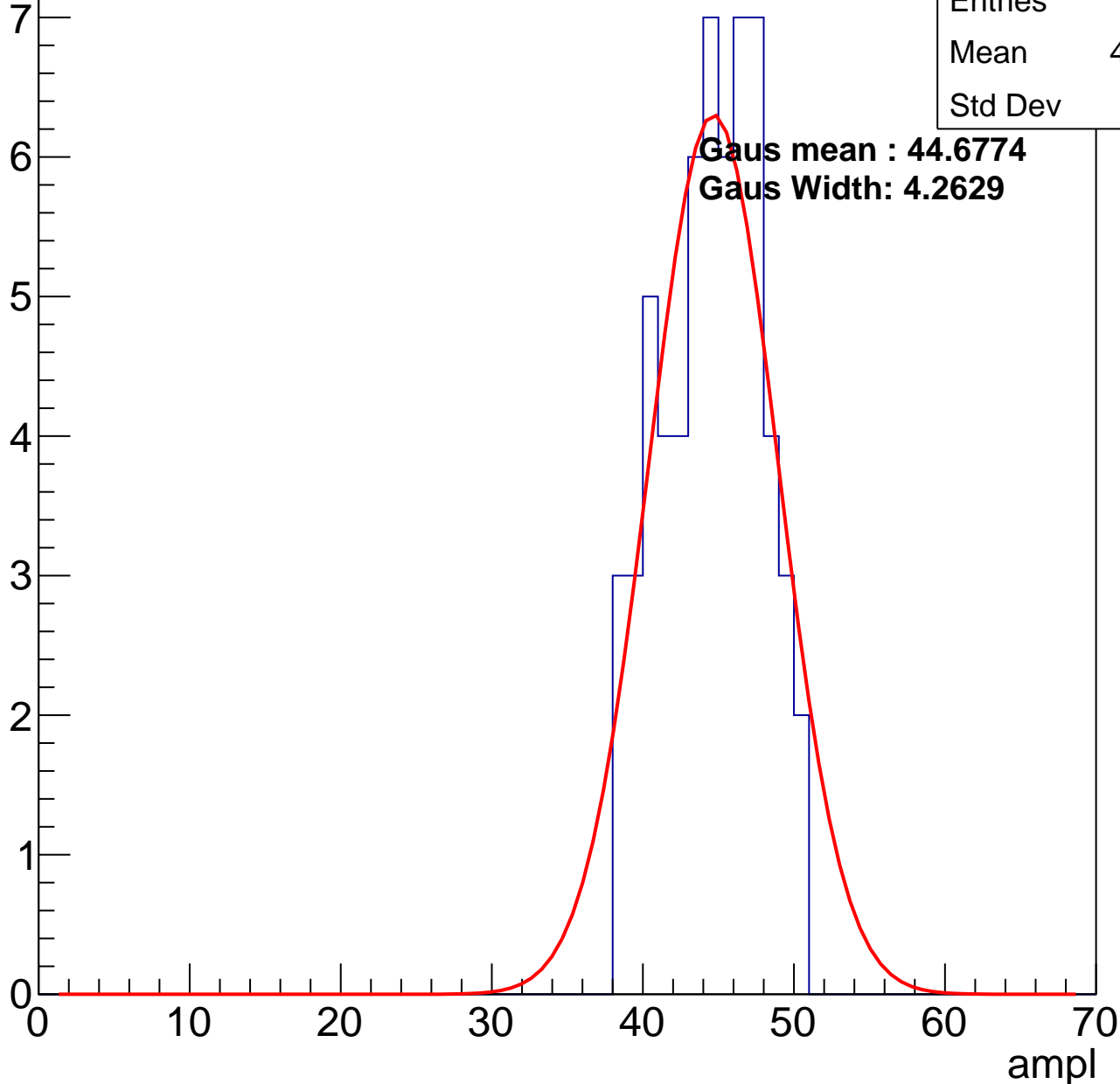
Gaus Width: 4.1918



B1L100S, U25-ch24, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

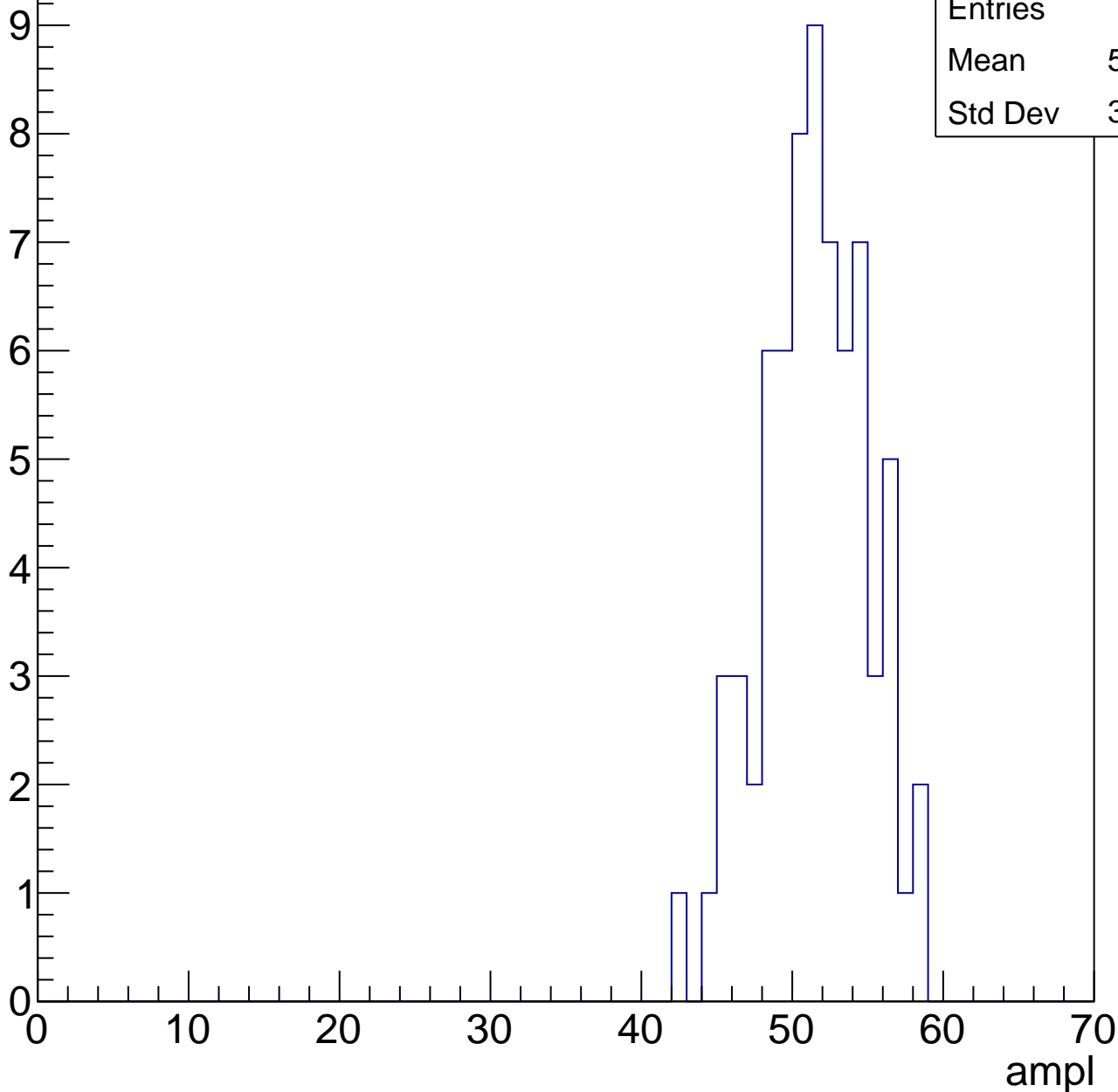


B1L100S, U25-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	51.03
Std Dev	3.447

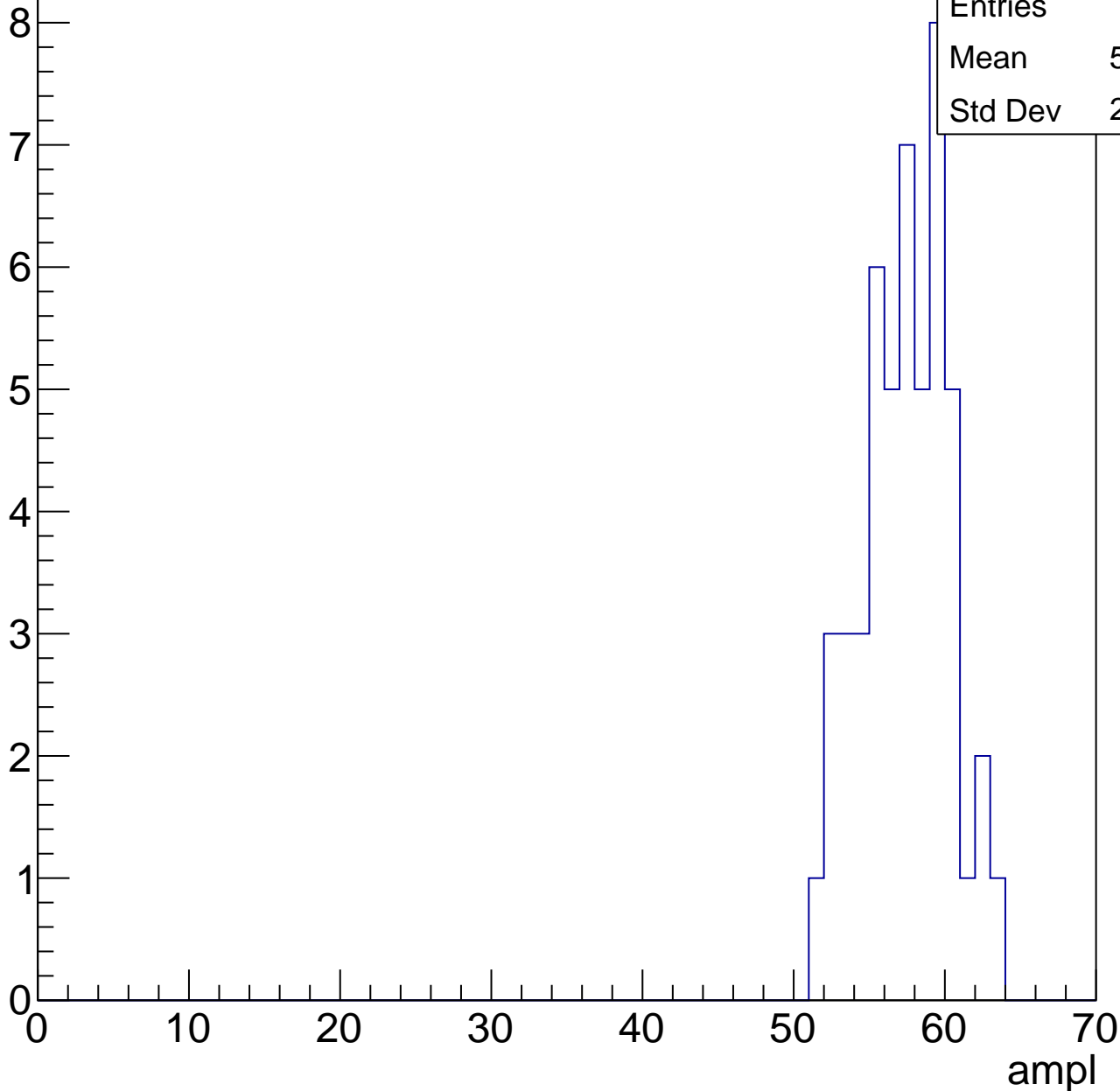


B1L100S, U25-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

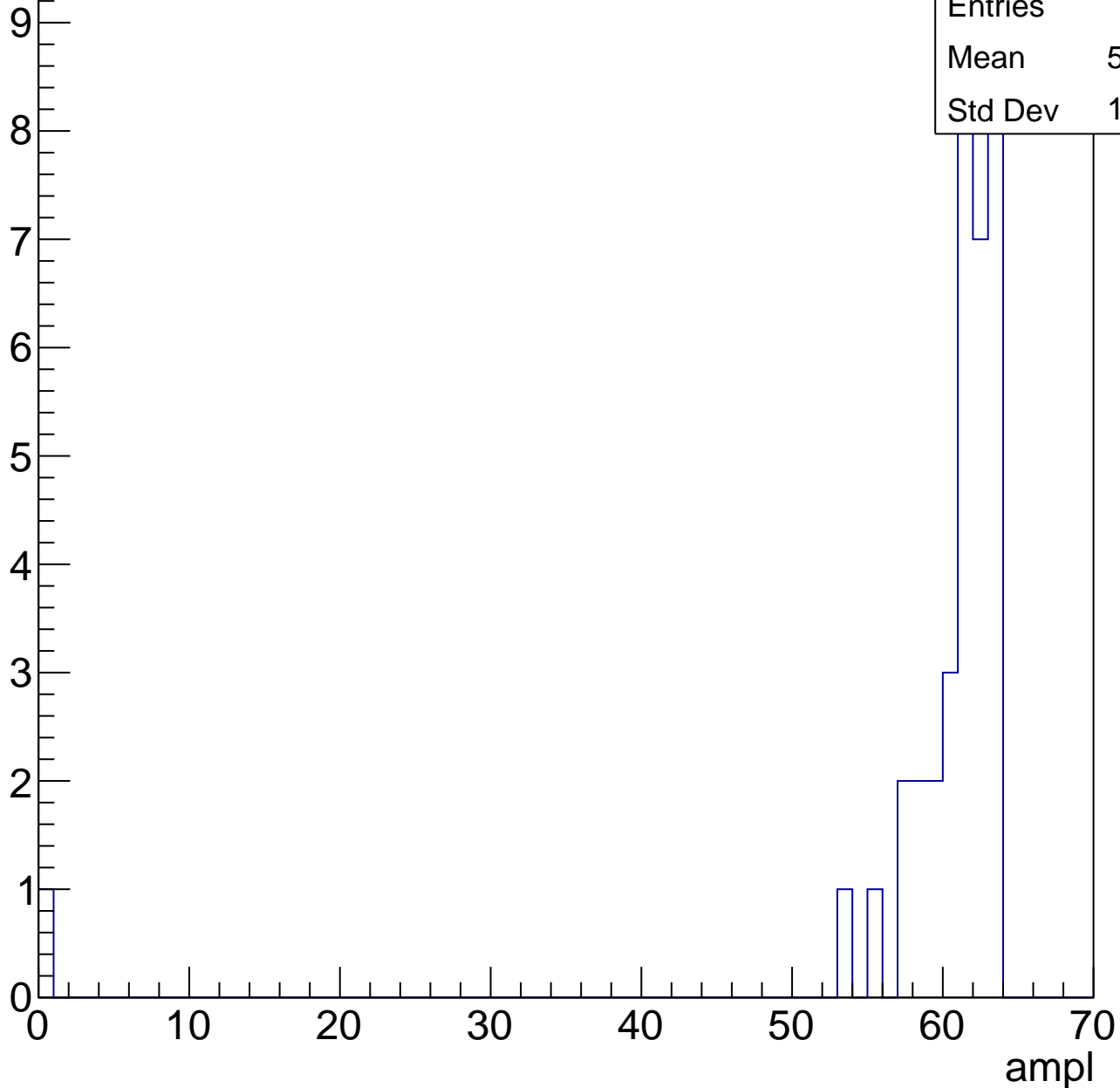
Entries	50
Mean	56.94
Std Dev	2.824



B1L100S, U25-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

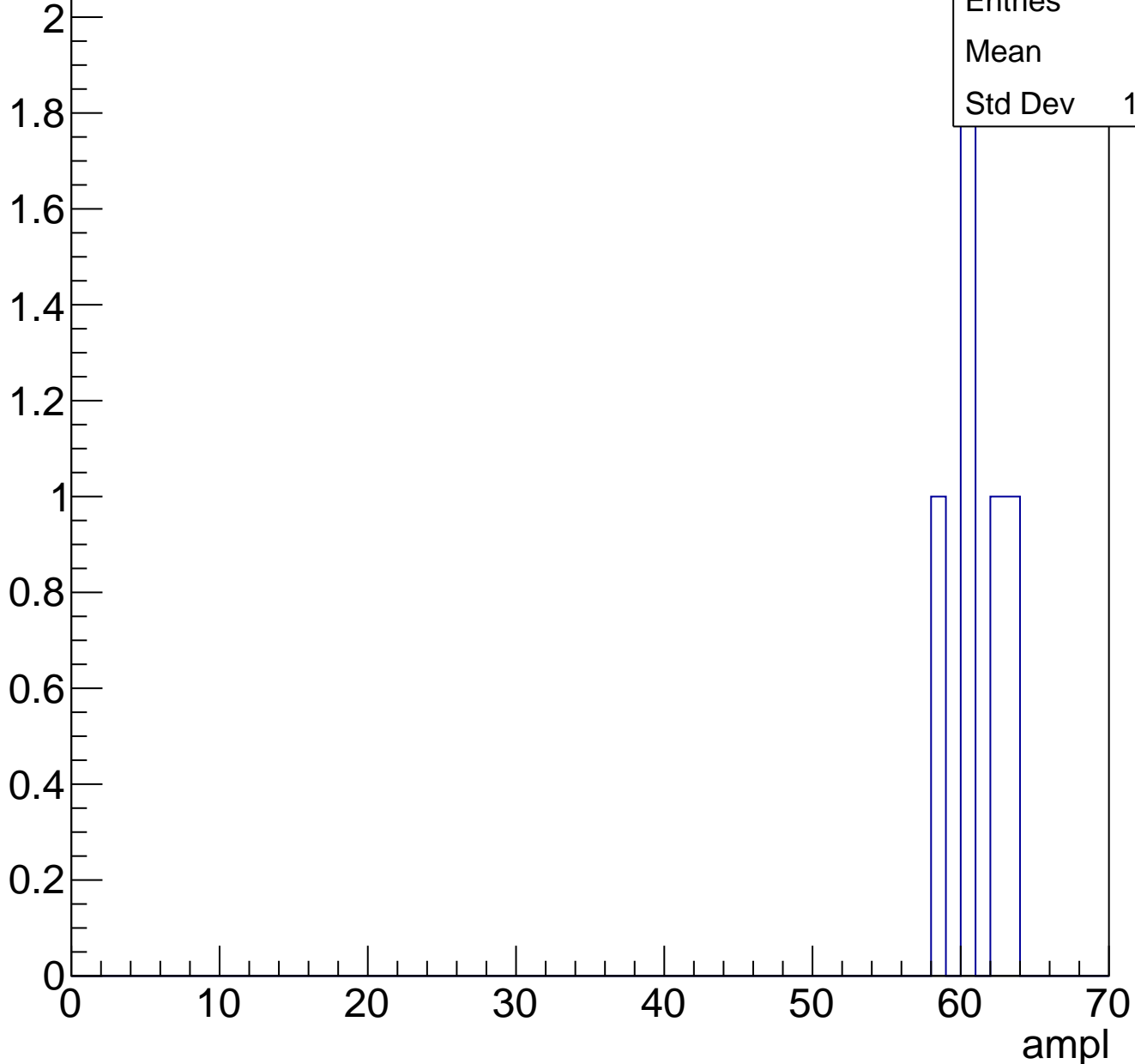
Entry



B1L100S, U25-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch25, adc0

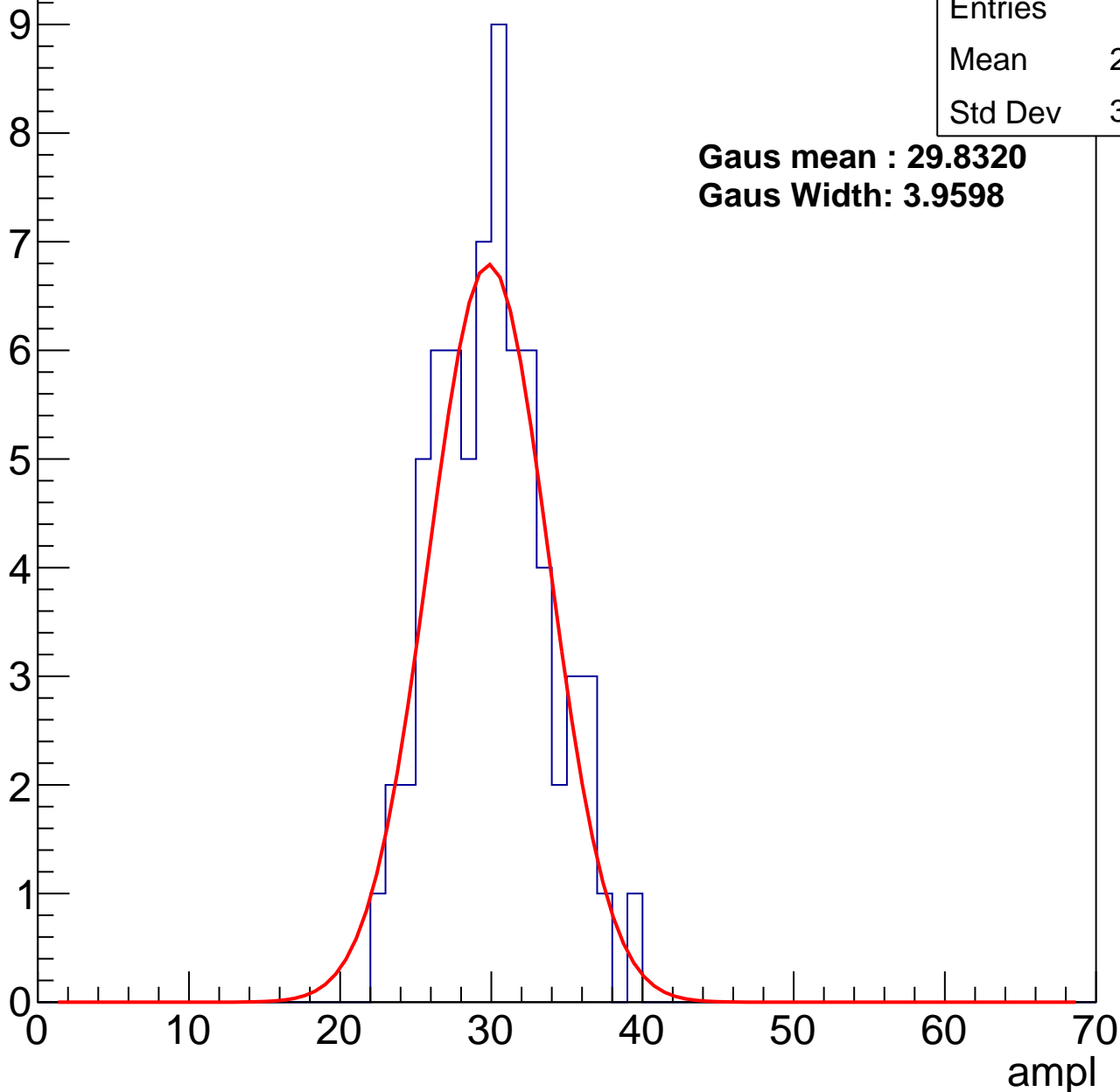
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.55
Std Dev	3.677

Gaus mean : 29.8320

Gaus Width: 3.9598



B1L100S, U25-ch25, adc1

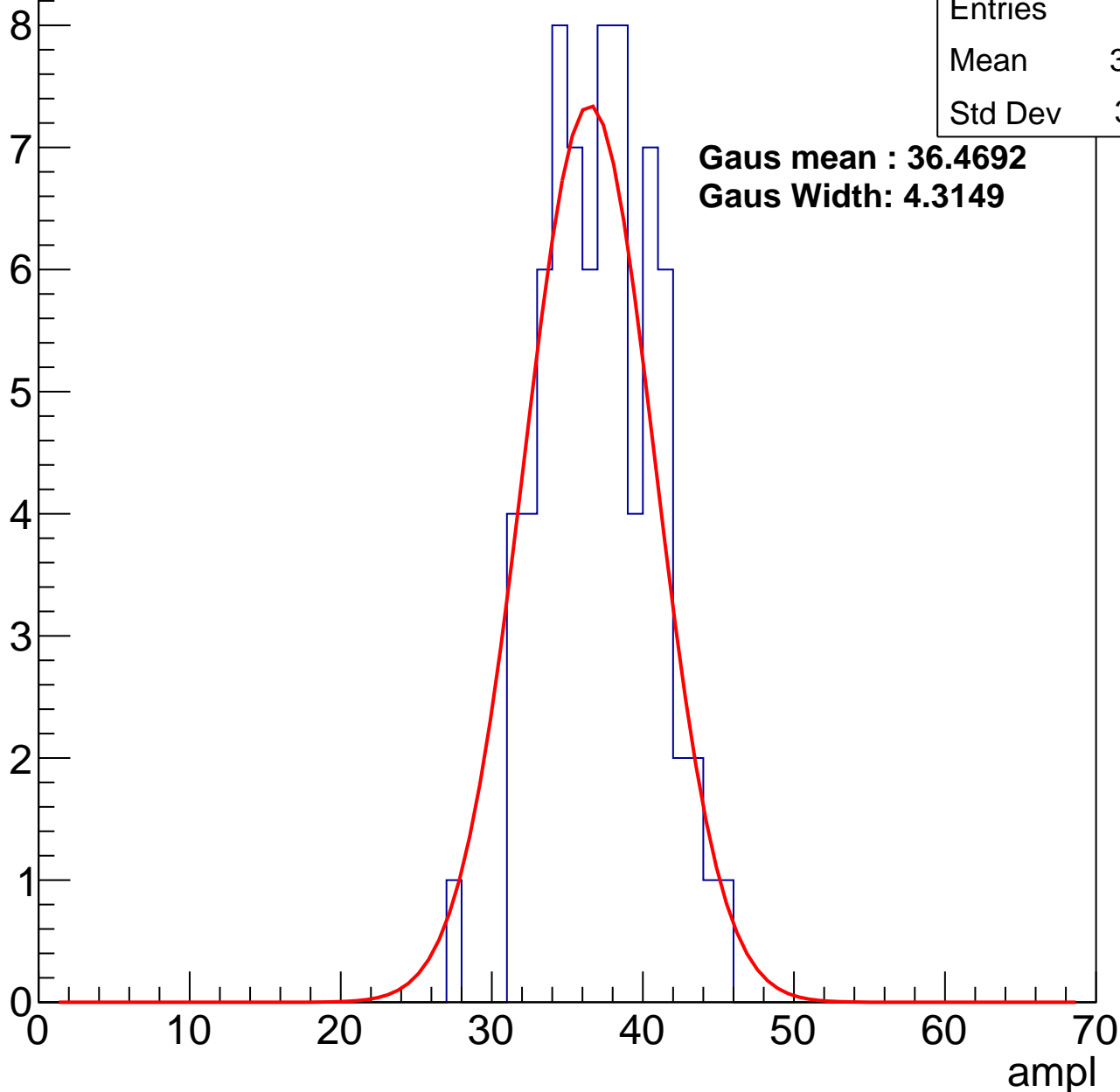
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.68
Std Dev	3.571

Gaus mean : 36.4692

Gaus Width: 4.3149



B1L100S, U25-ch25, adc2

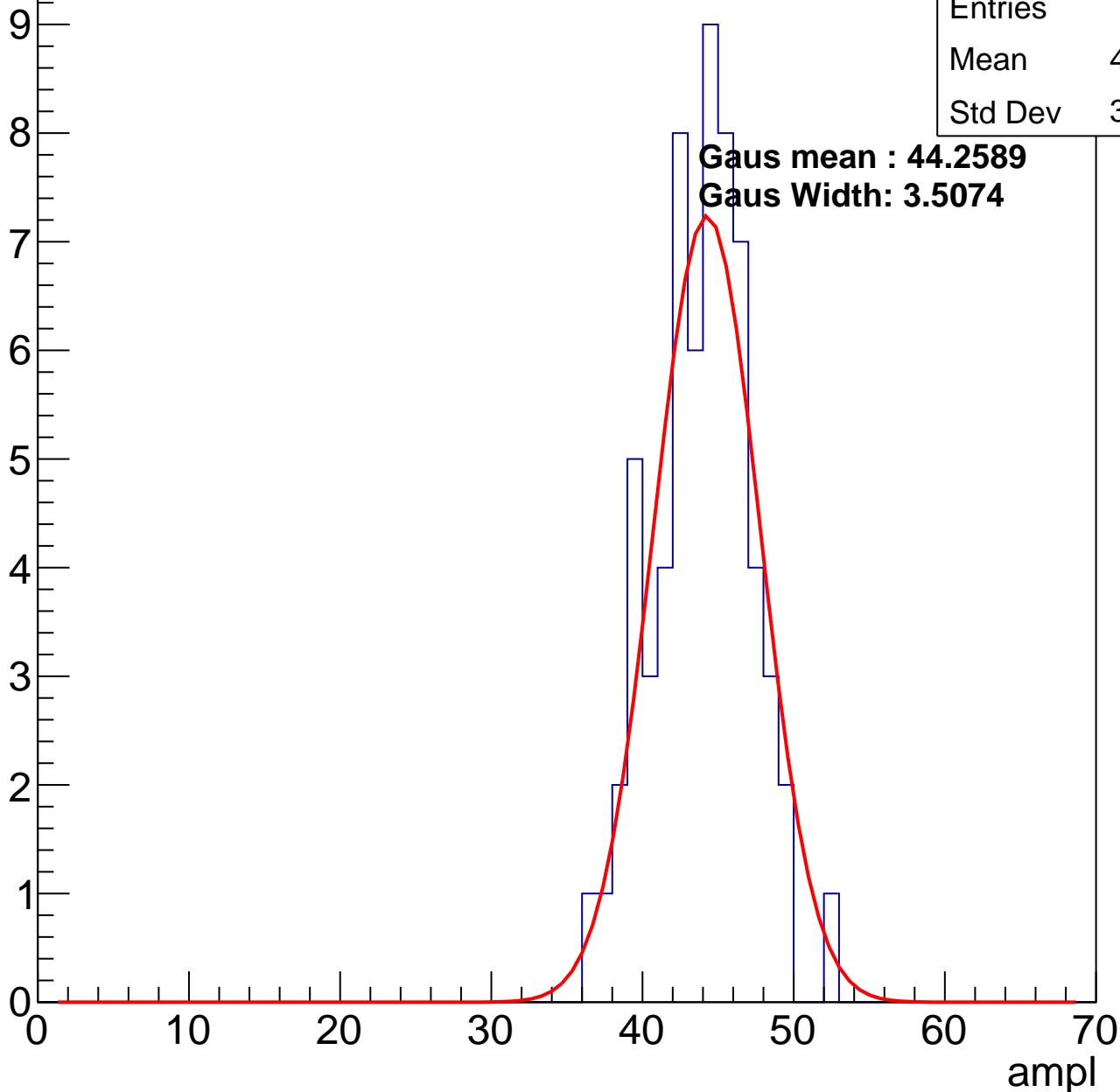
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.47
Std Dev	3.182

Gaus mean : 44.2589

Gaus Width: 3.5074

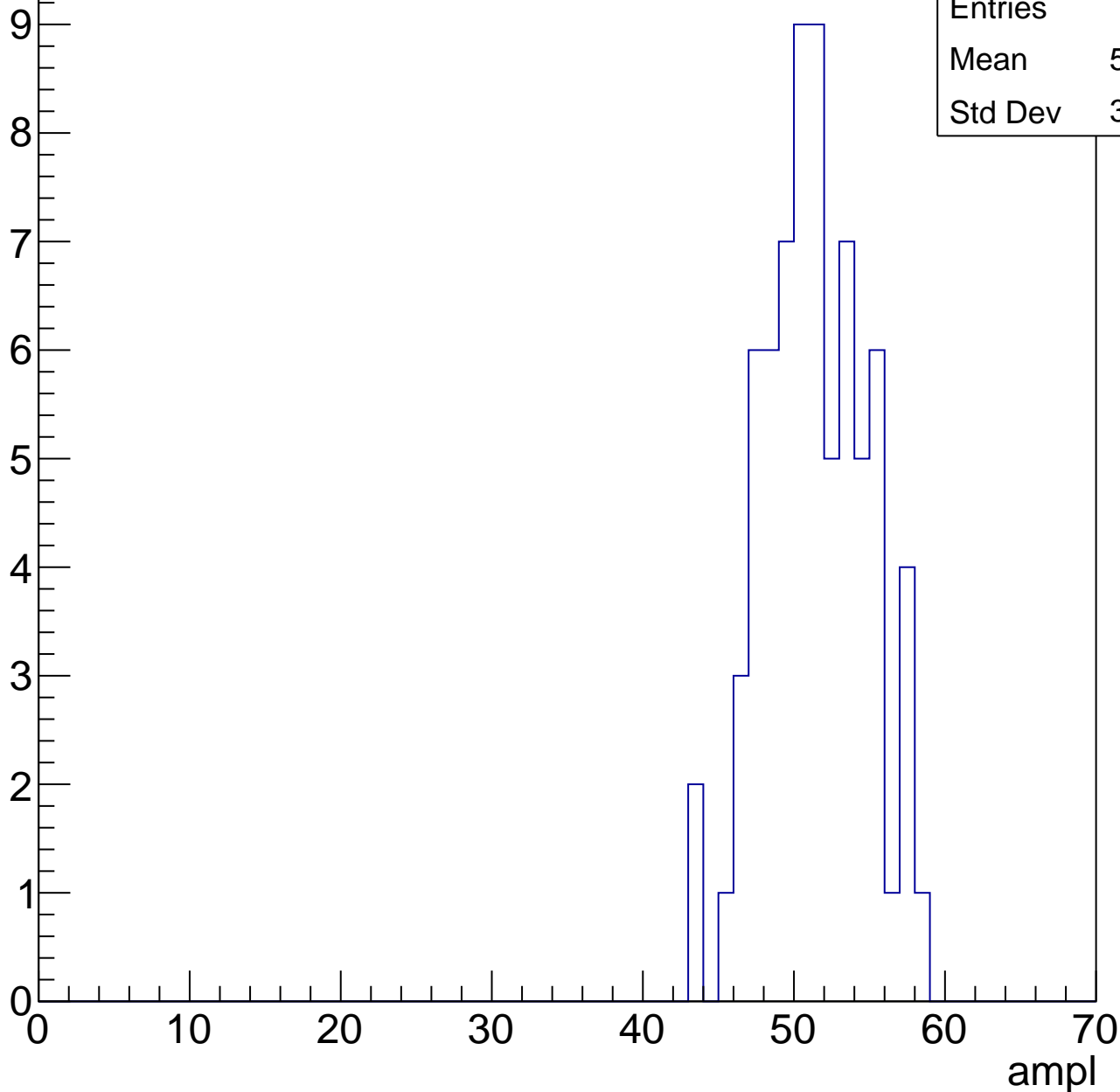


B1L100S, U25-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	50.89
Std Dev	3.369

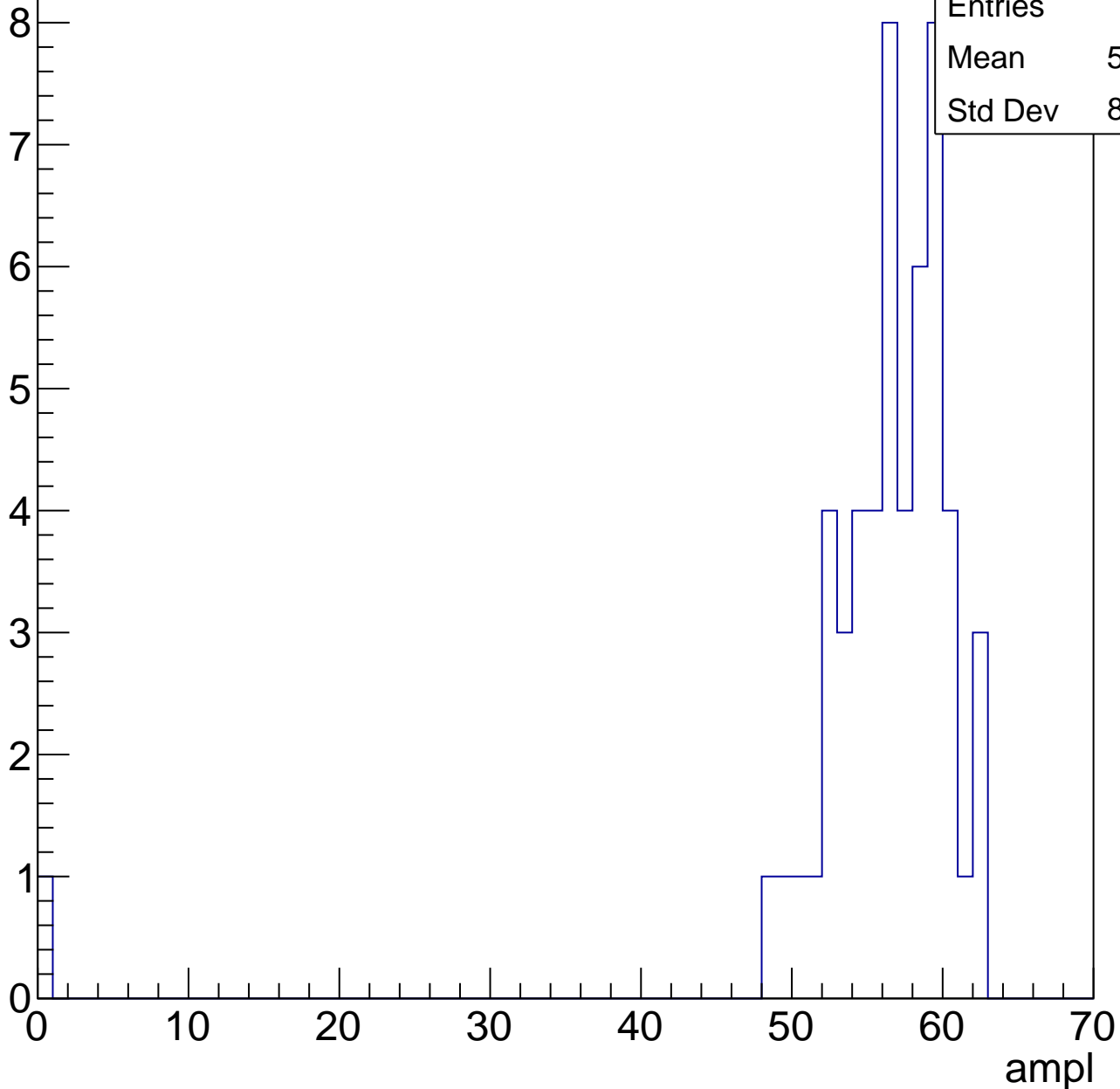


B1L100S, U25-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	55.26
Std Dev	8.265

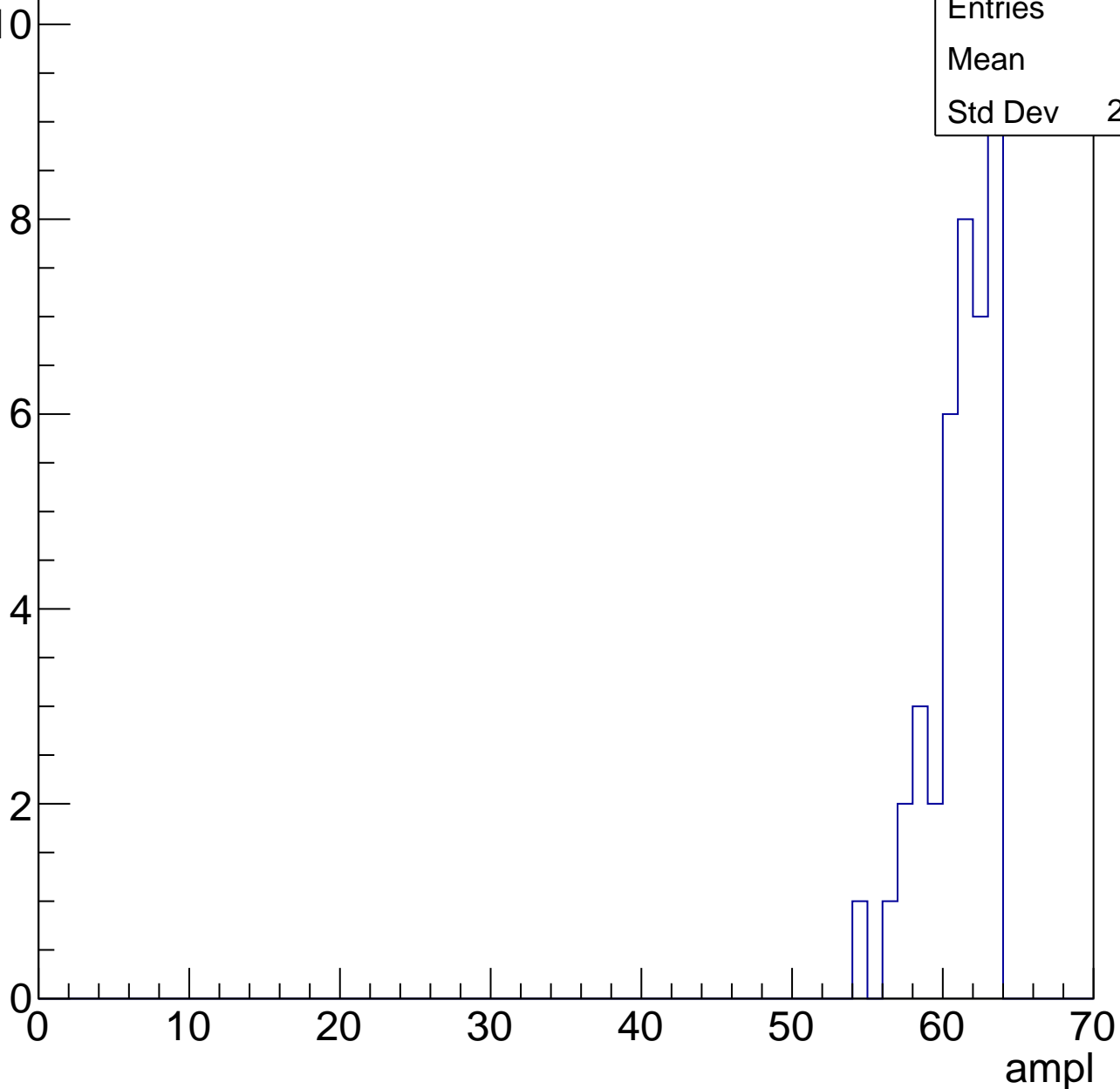


B1L100S, U25-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

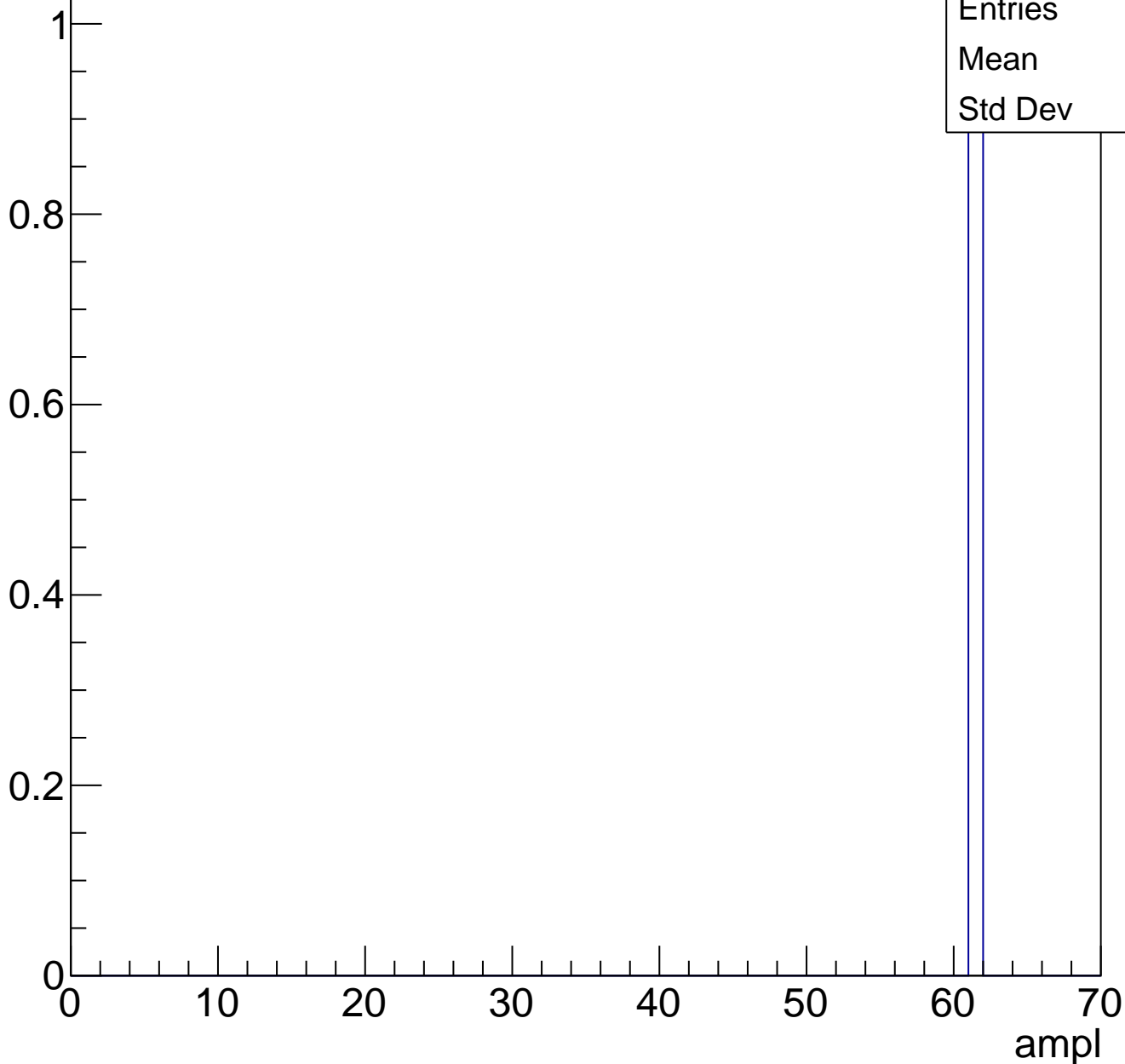
Entries	40
Mean	60.7
Std Dev	2.182



B1L100S, U25-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch26, adc0

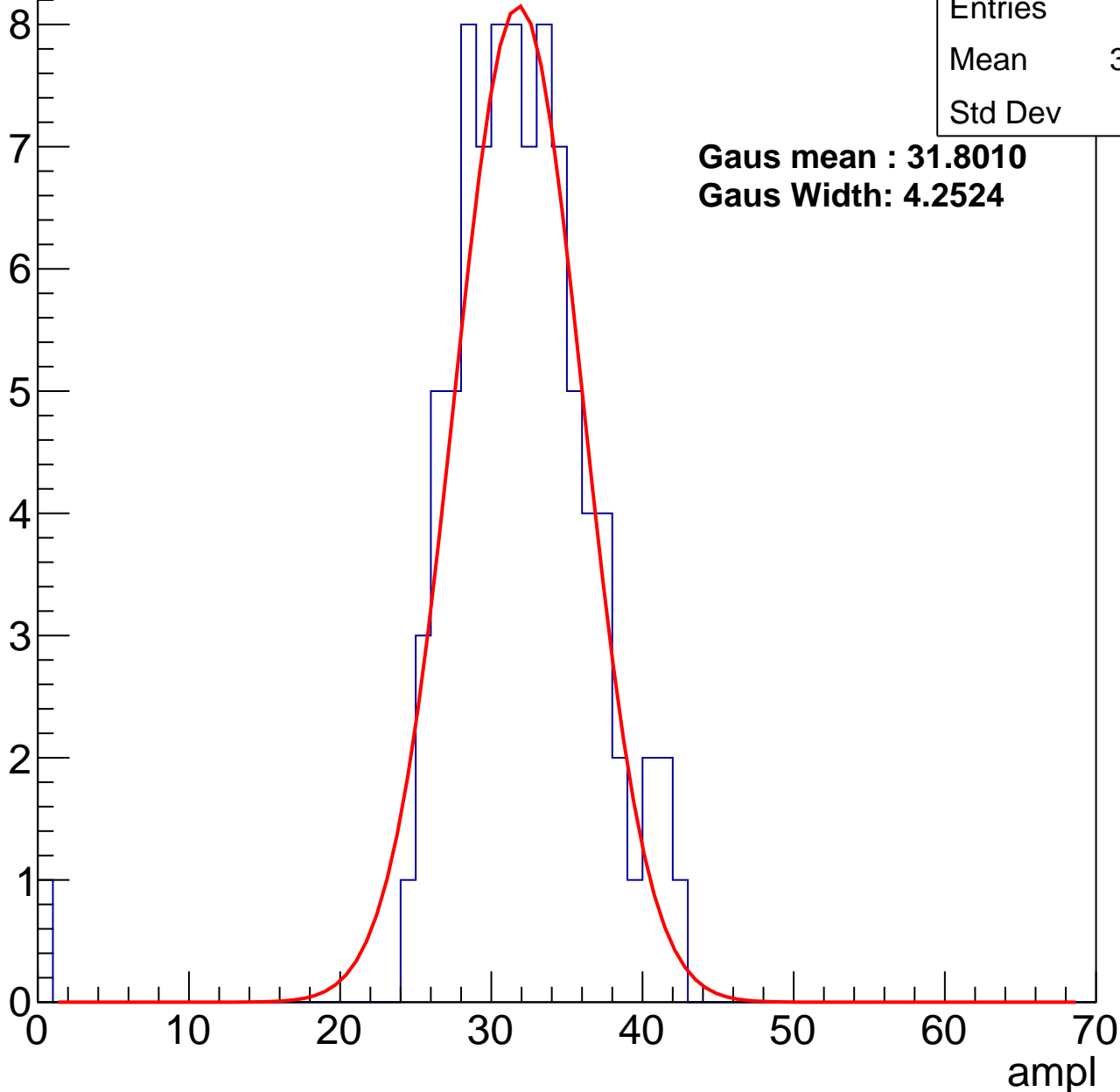
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	31.36
Std Dev	5.29

Gaus mean : 31.8010

Gaus Width: 4.2524



B1L100S, U25-ch26, adc1

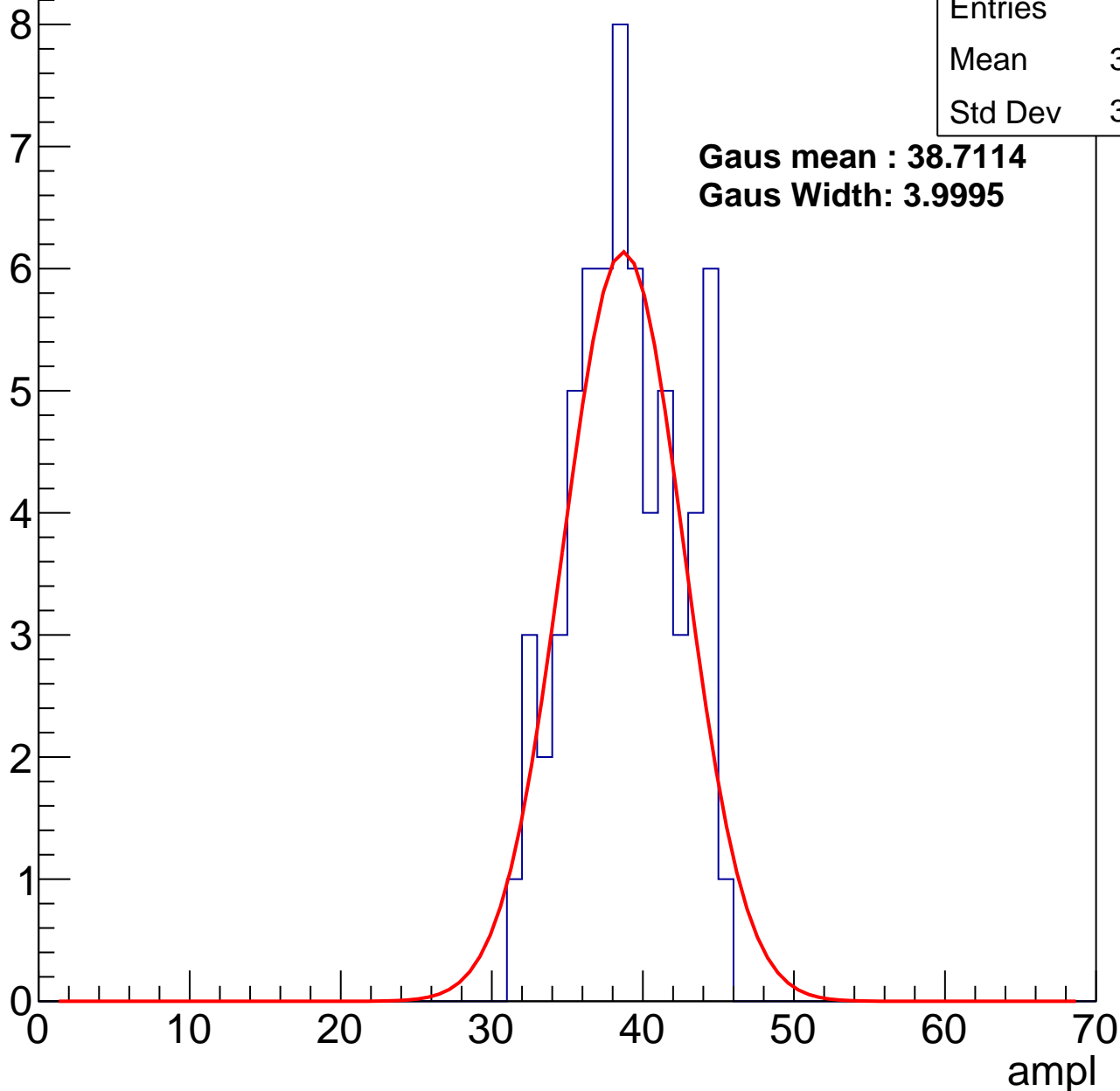
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	38.38
Std Dev	3.565

Gaus mean : 38.7114

Gaus Width: 3.9995



B1L100S, U25-ch26, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.97
Std Dev	3.316

Gaus mean : 44.4529

Gaus Width: 3.8904

10

8

6

4

2

0

ampl

0

10

20

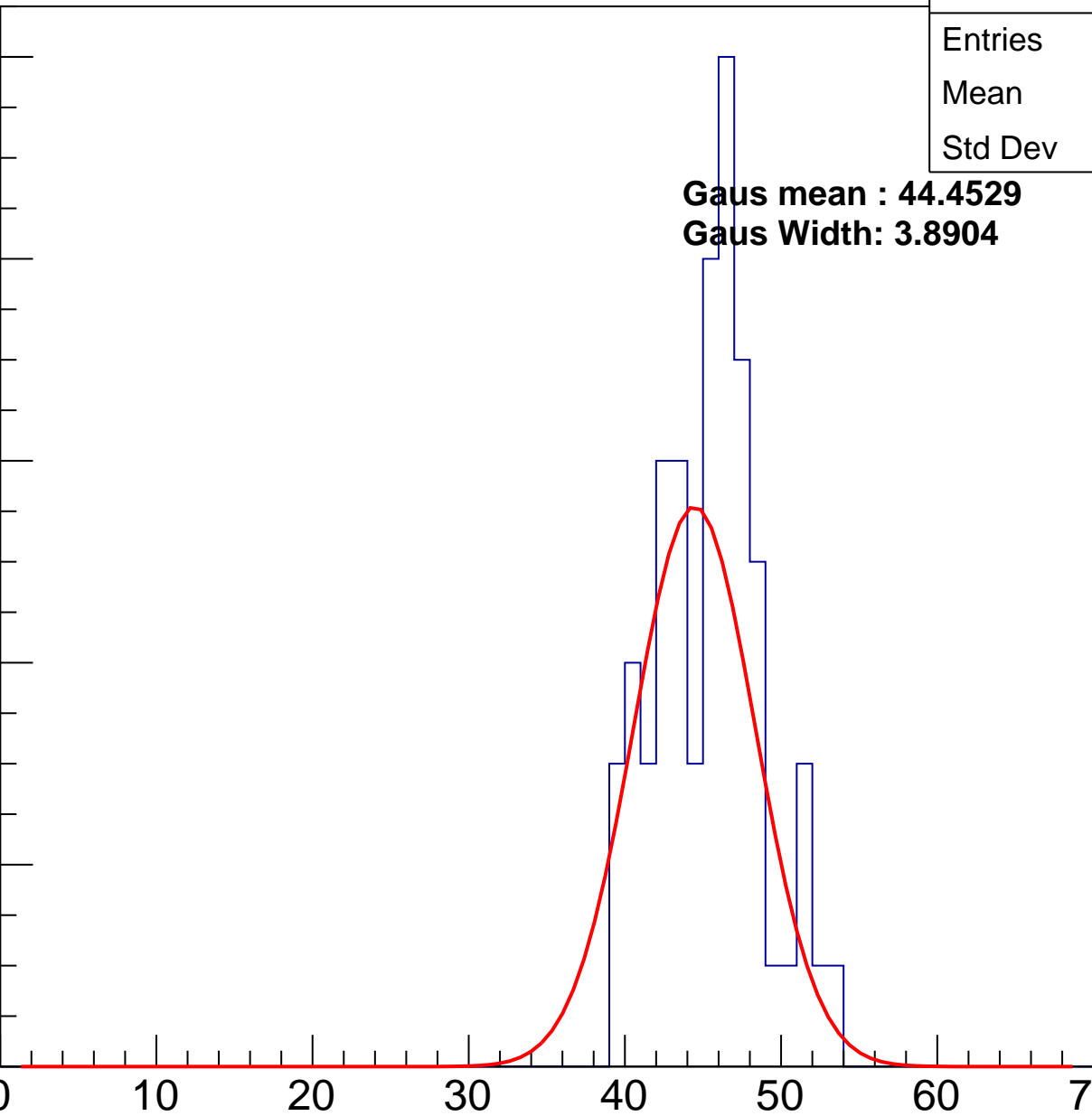
30

40

50

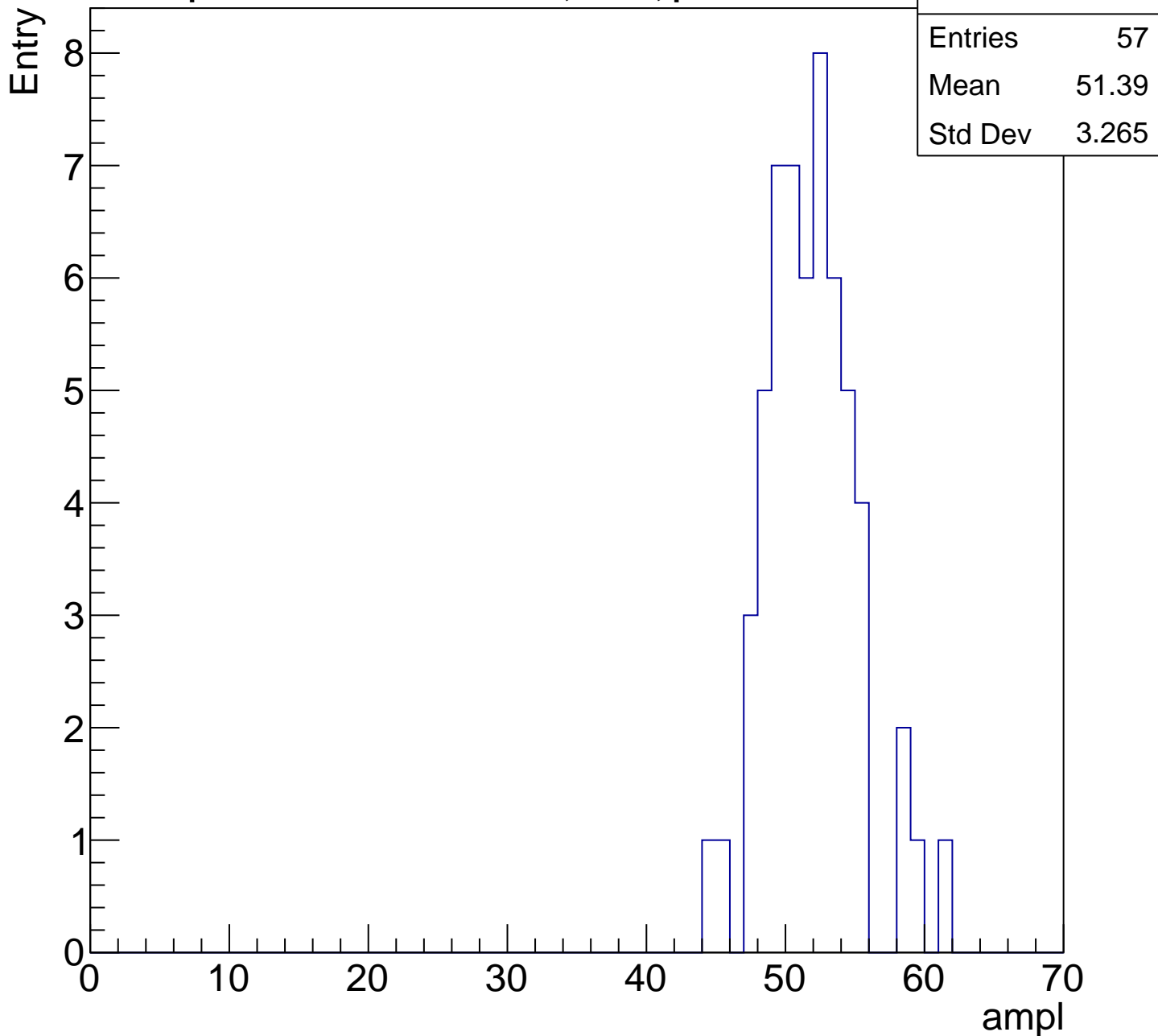
60

70



B1L100S, U25-ch26, adc3

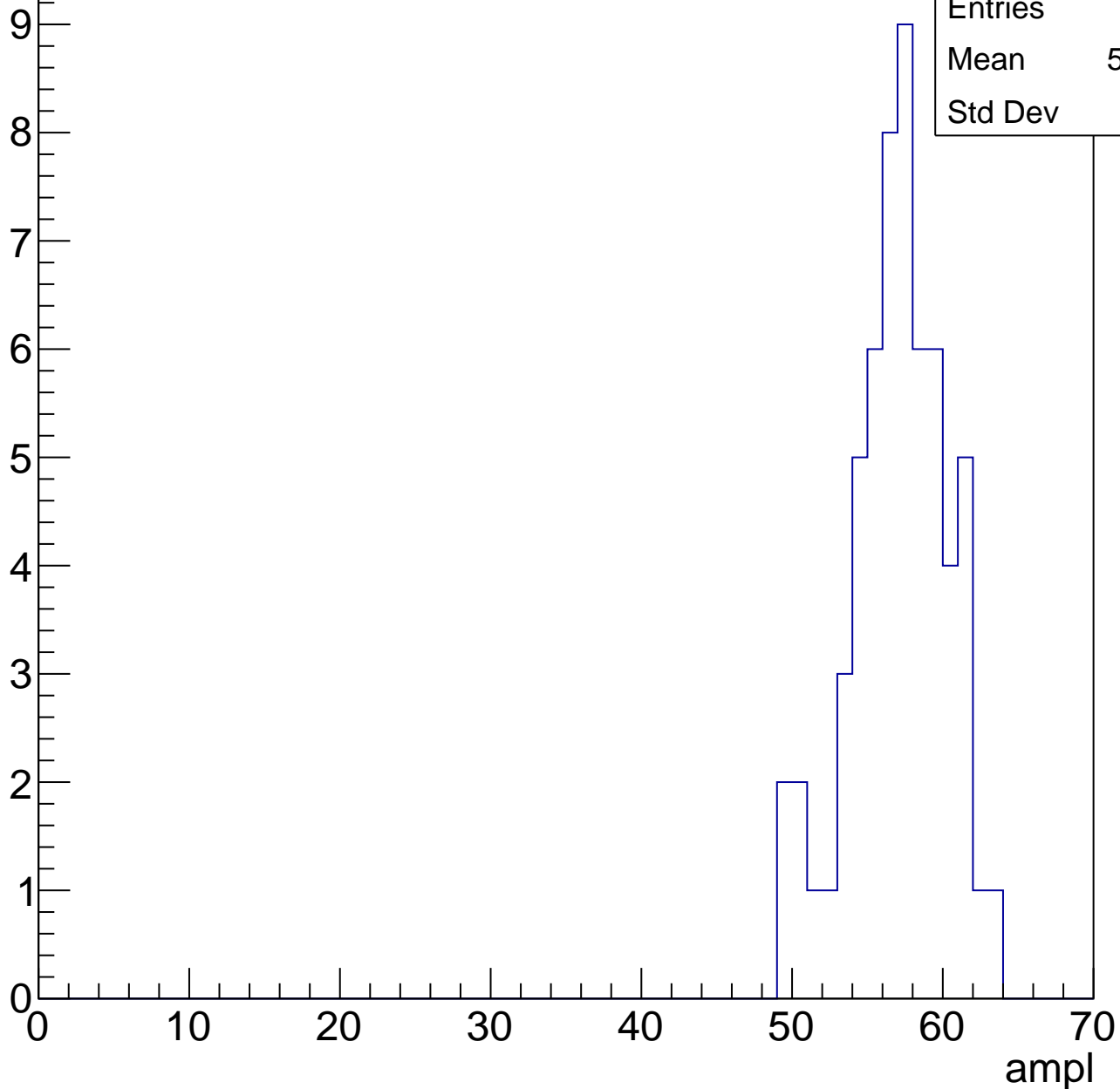
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

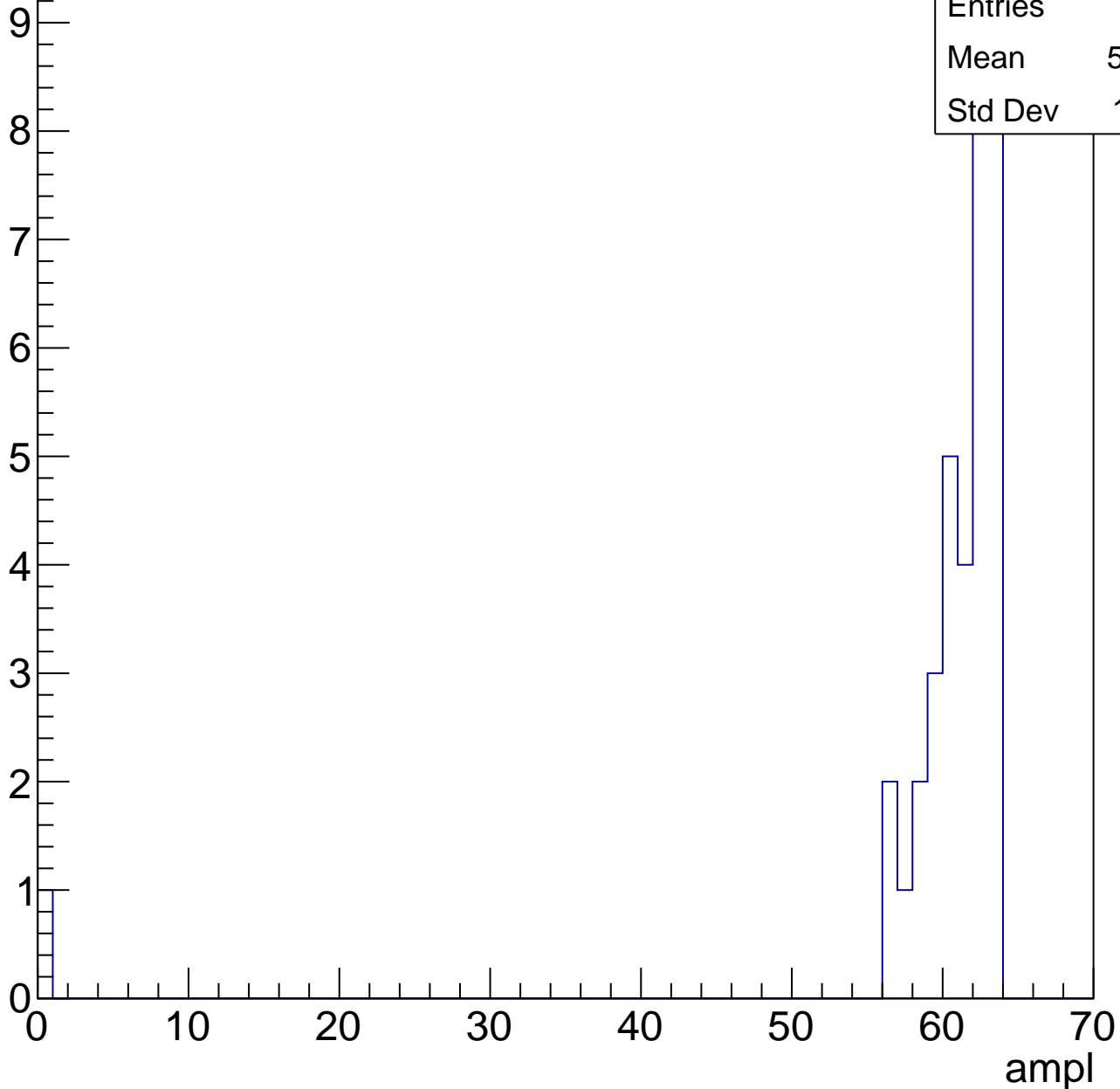


Entries	60
Mean	56.55
Std Dev	3.18

B1L100S, U25-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch27, adc0

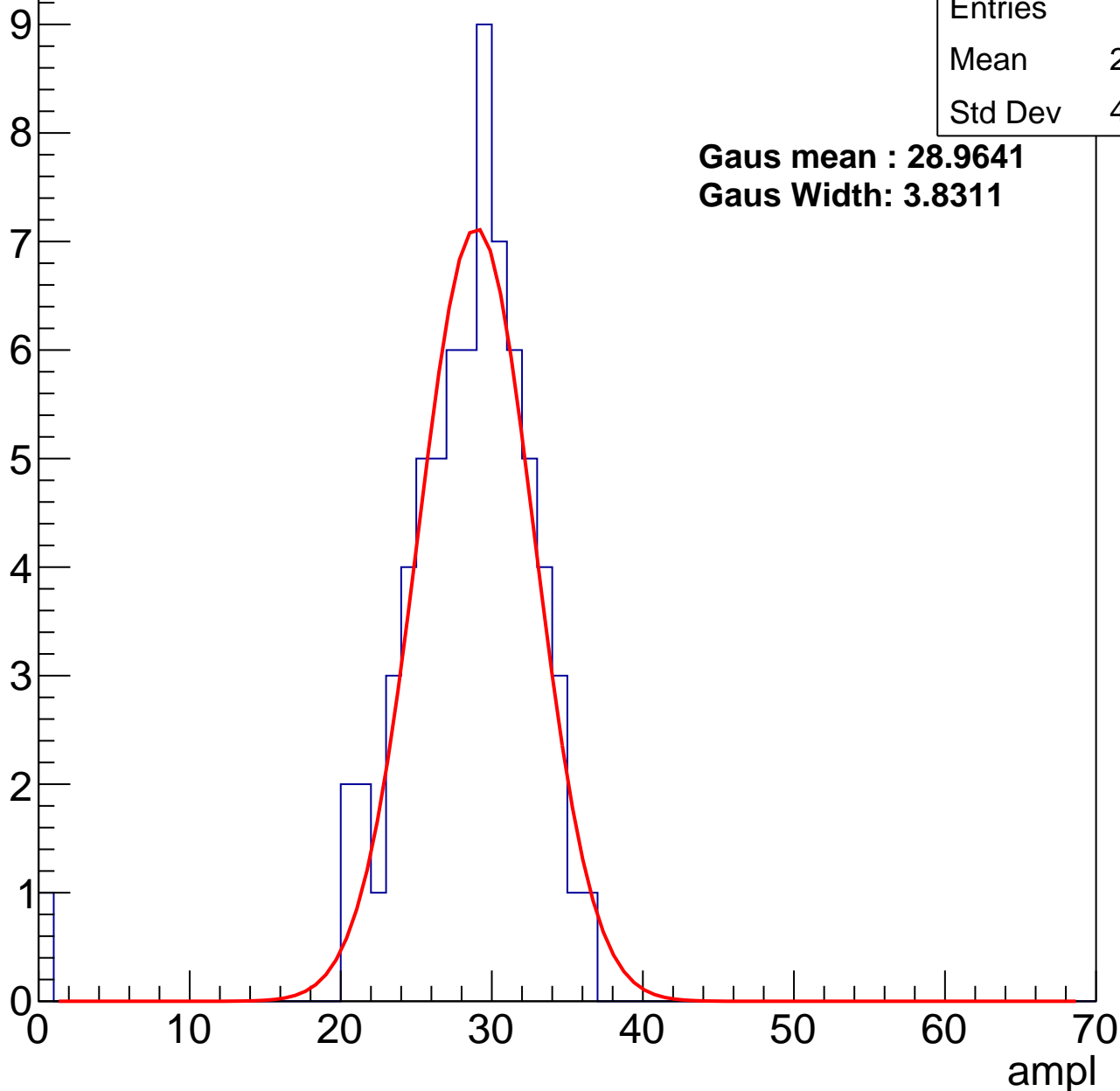
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.83
Std Dev	4.959

Gaus mean : 28.9641

Gaus Width: 3.8311



B1L100S, U25-ch27, adc1

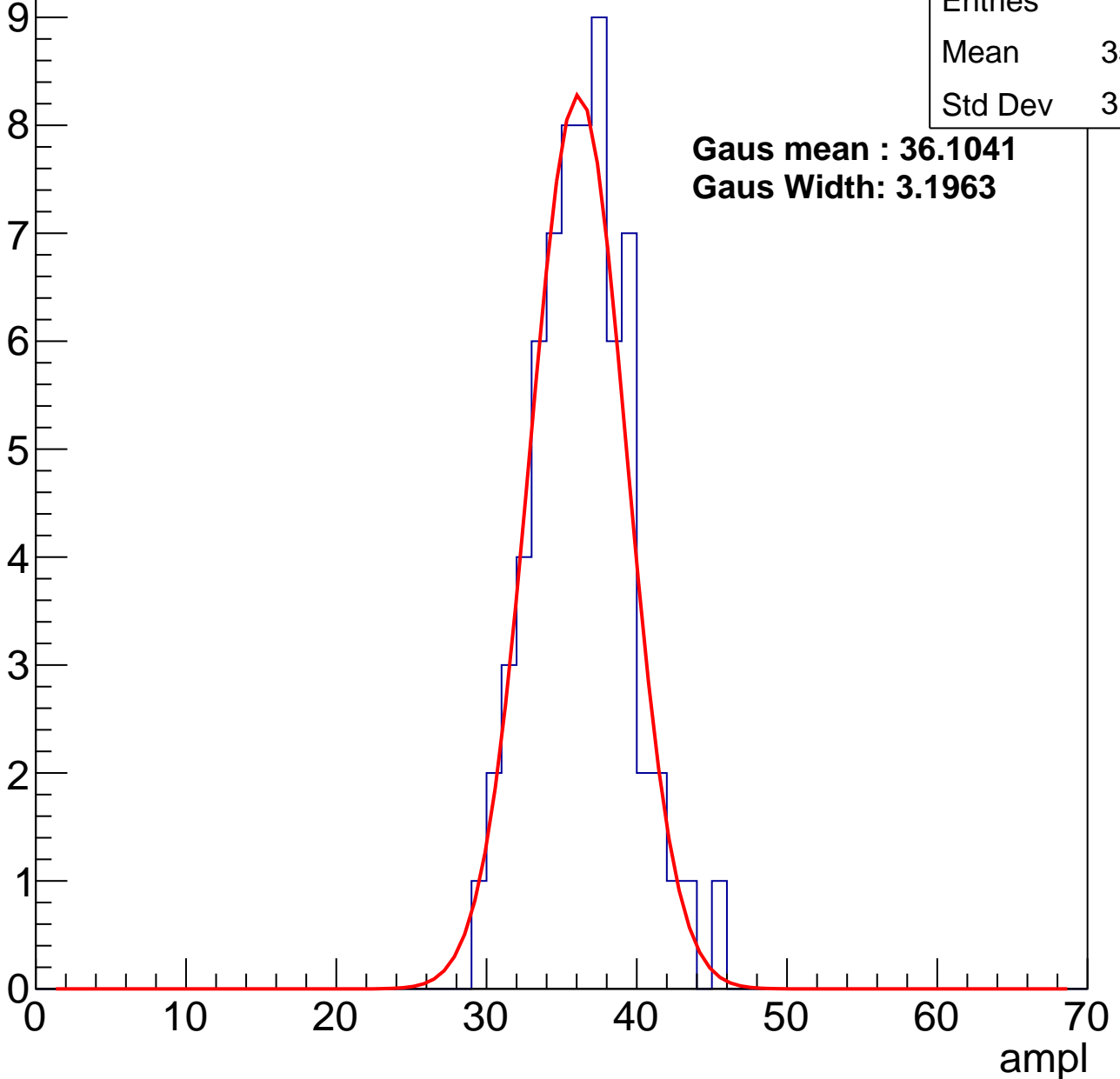
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.88
Std Dev	3.192

Gaus mean : 36.1041

Gaus Width: 3.1963



B1L100S, U25-ch27, adc2

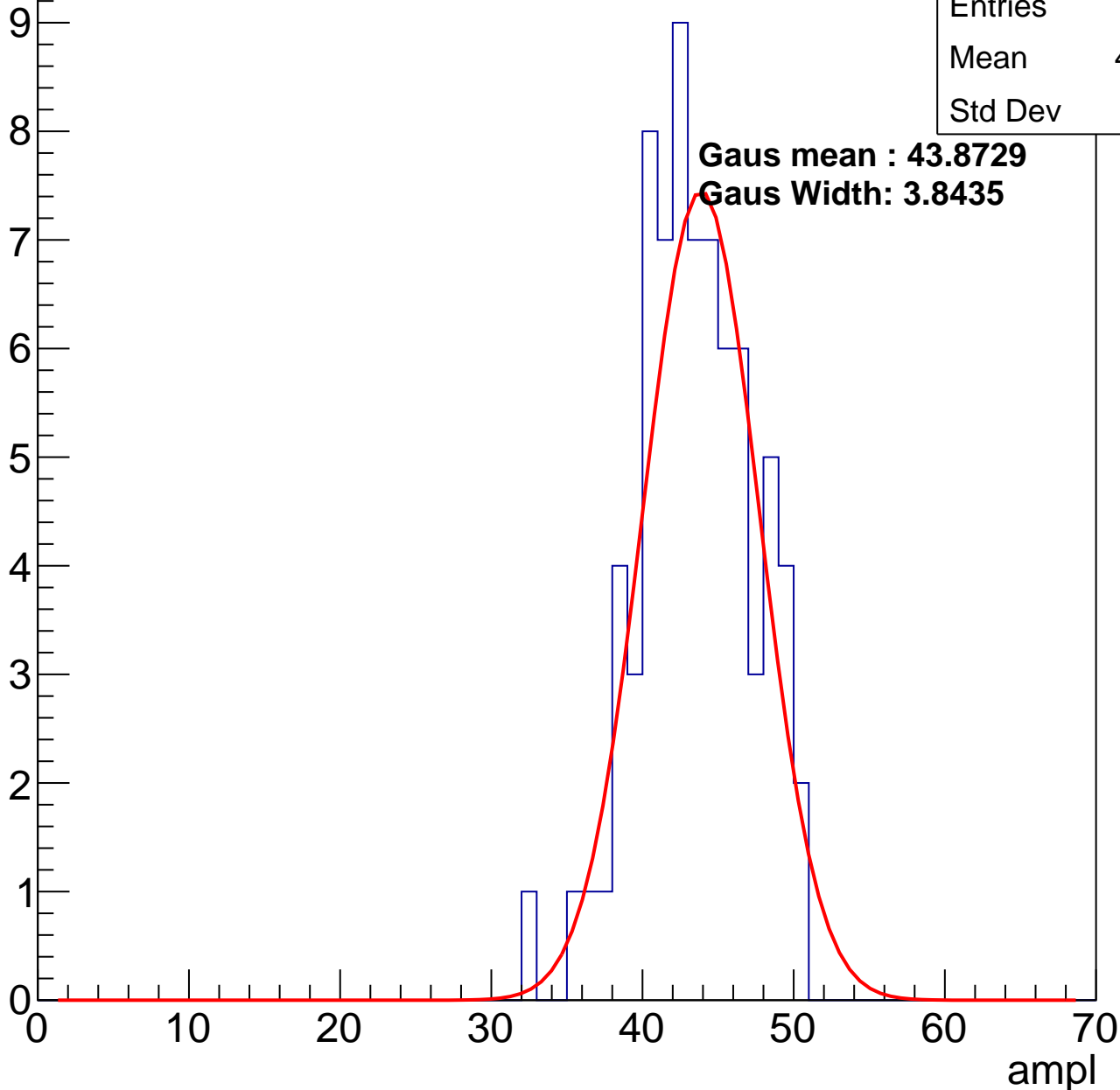
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	43.01
Std Dev	3.7

Gaus mean : 43.8729

Gaus Width: 3.8435

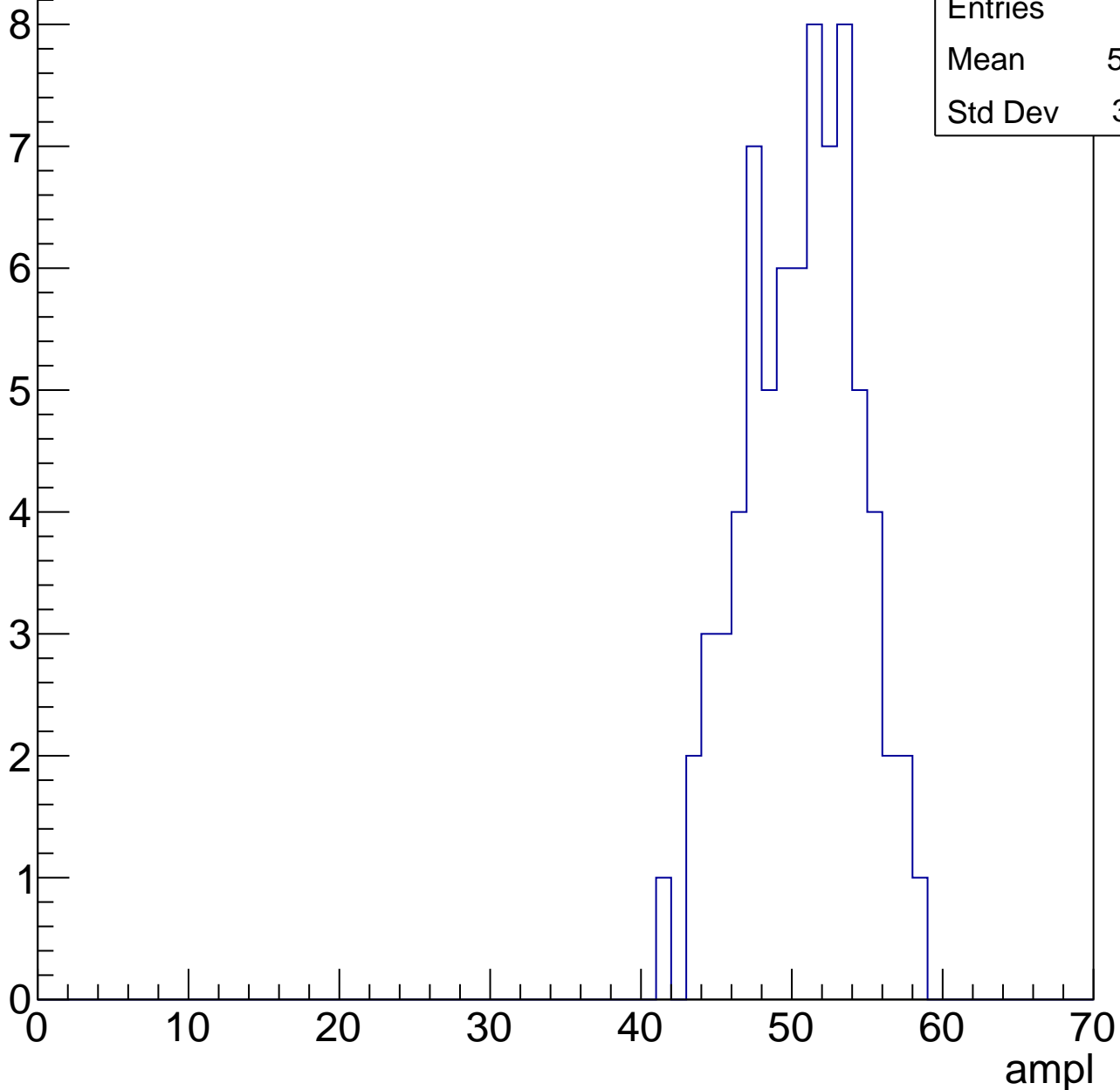


B1L100S, U25-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.15
Std Dev	3.741

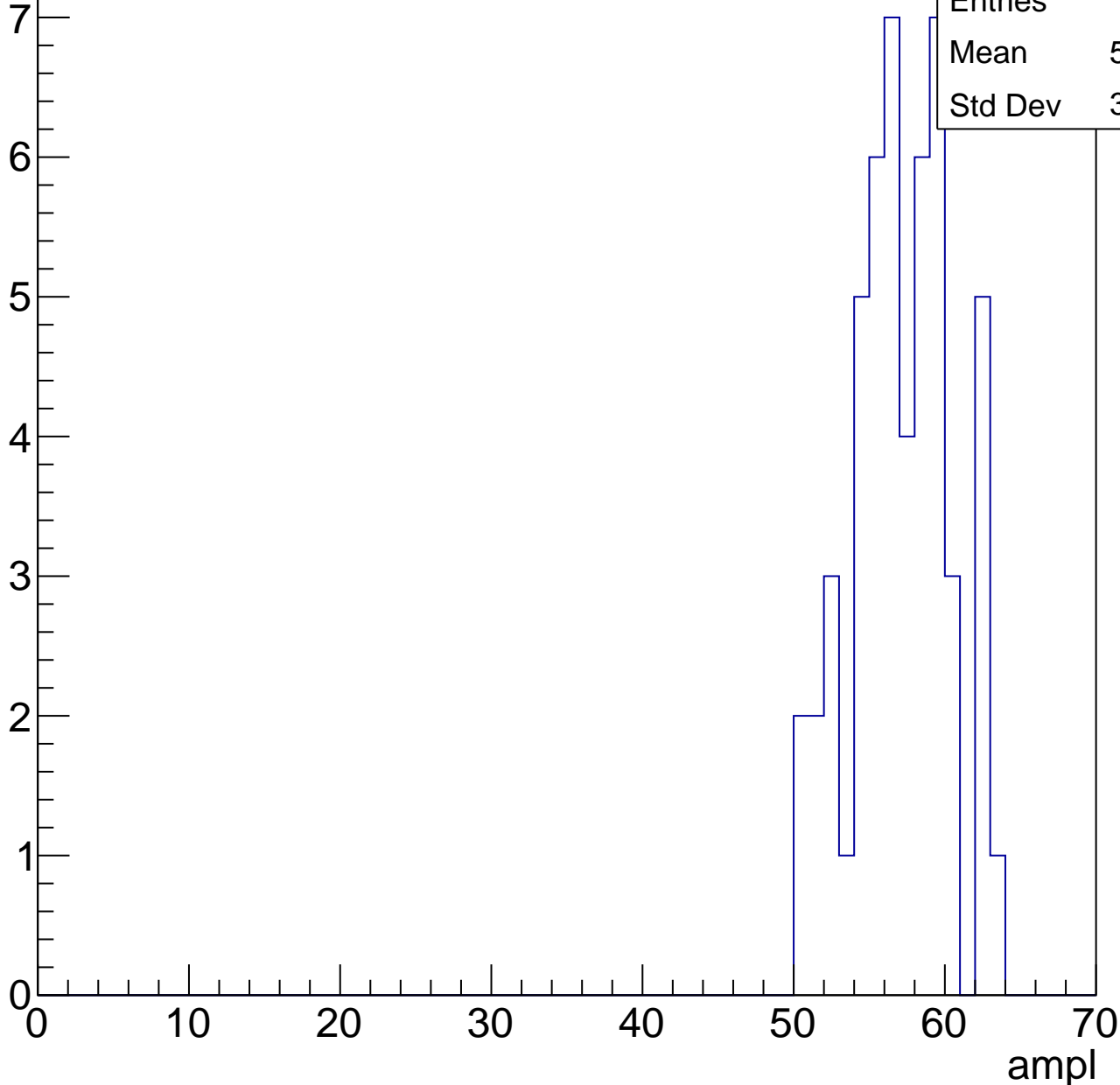


B1L100S, U25-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.63
Std Dev	3.258

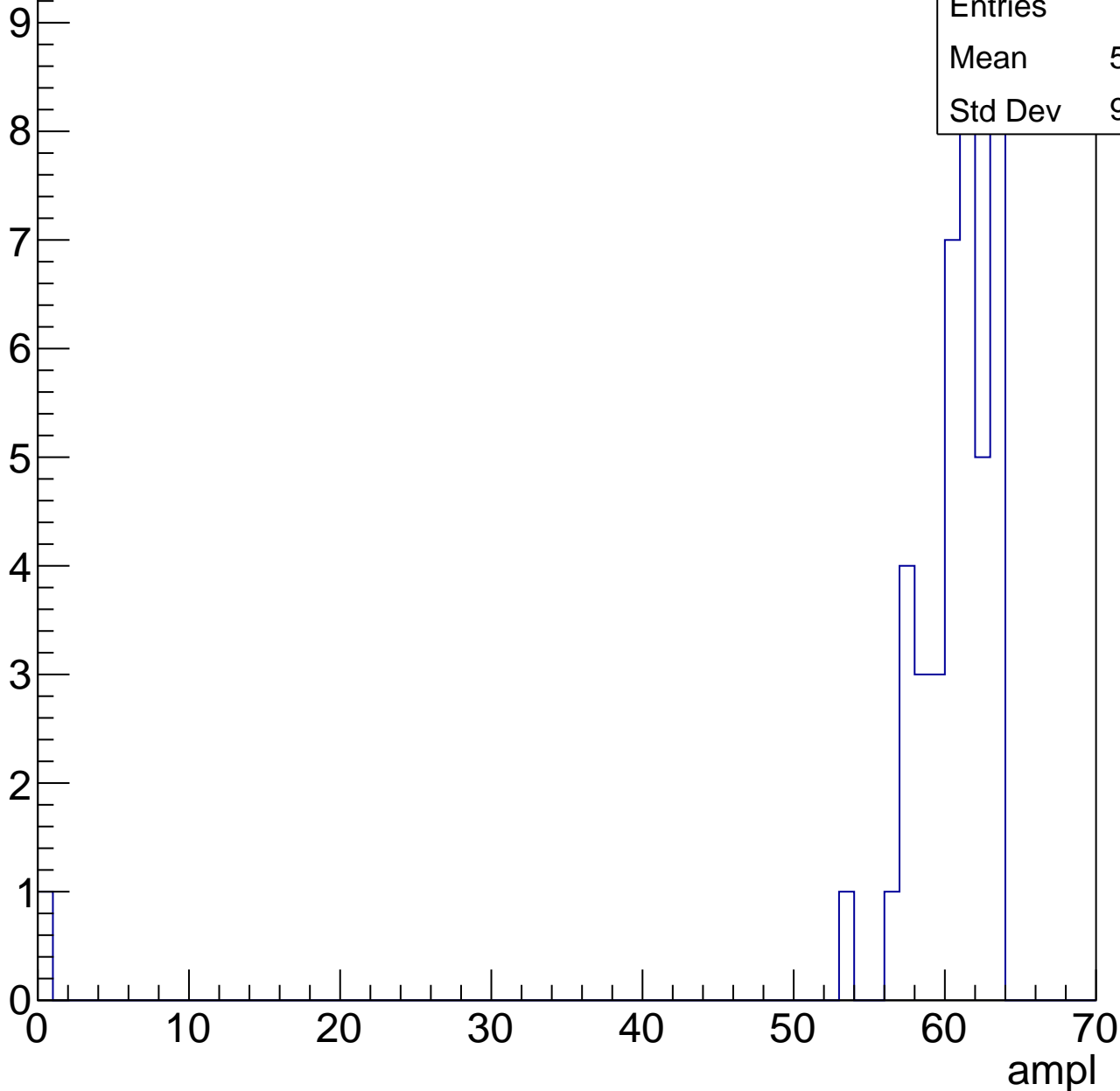


B1L100S, U25-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	58.93
Std Dev	9.372



B1L100S, U25-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch28, adc0

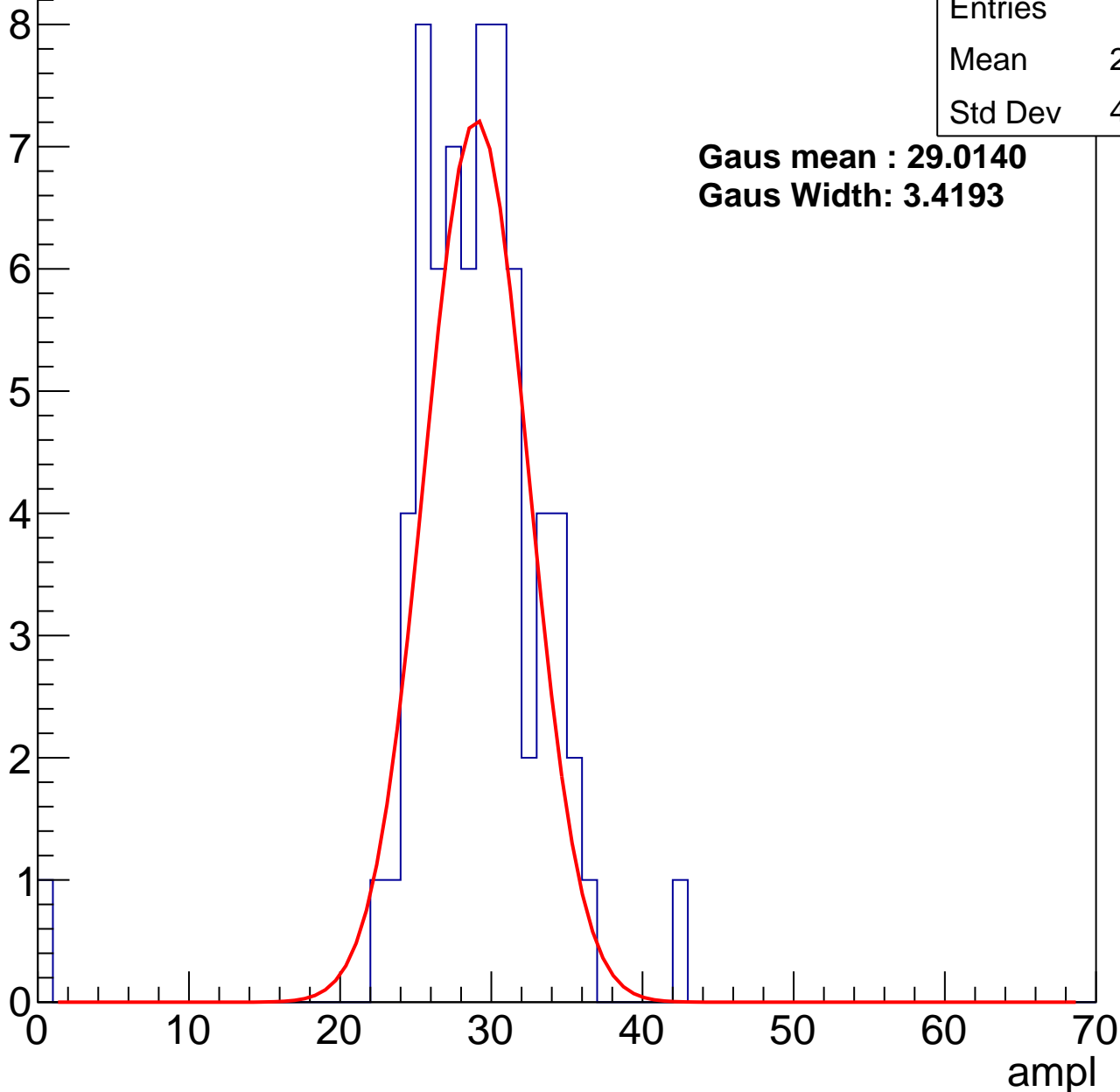
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.46
Std Dev	4.965

Gaus mean : 29.0140

Gaus Width: 3.4193



B1L100S, U25-ch28, adc1

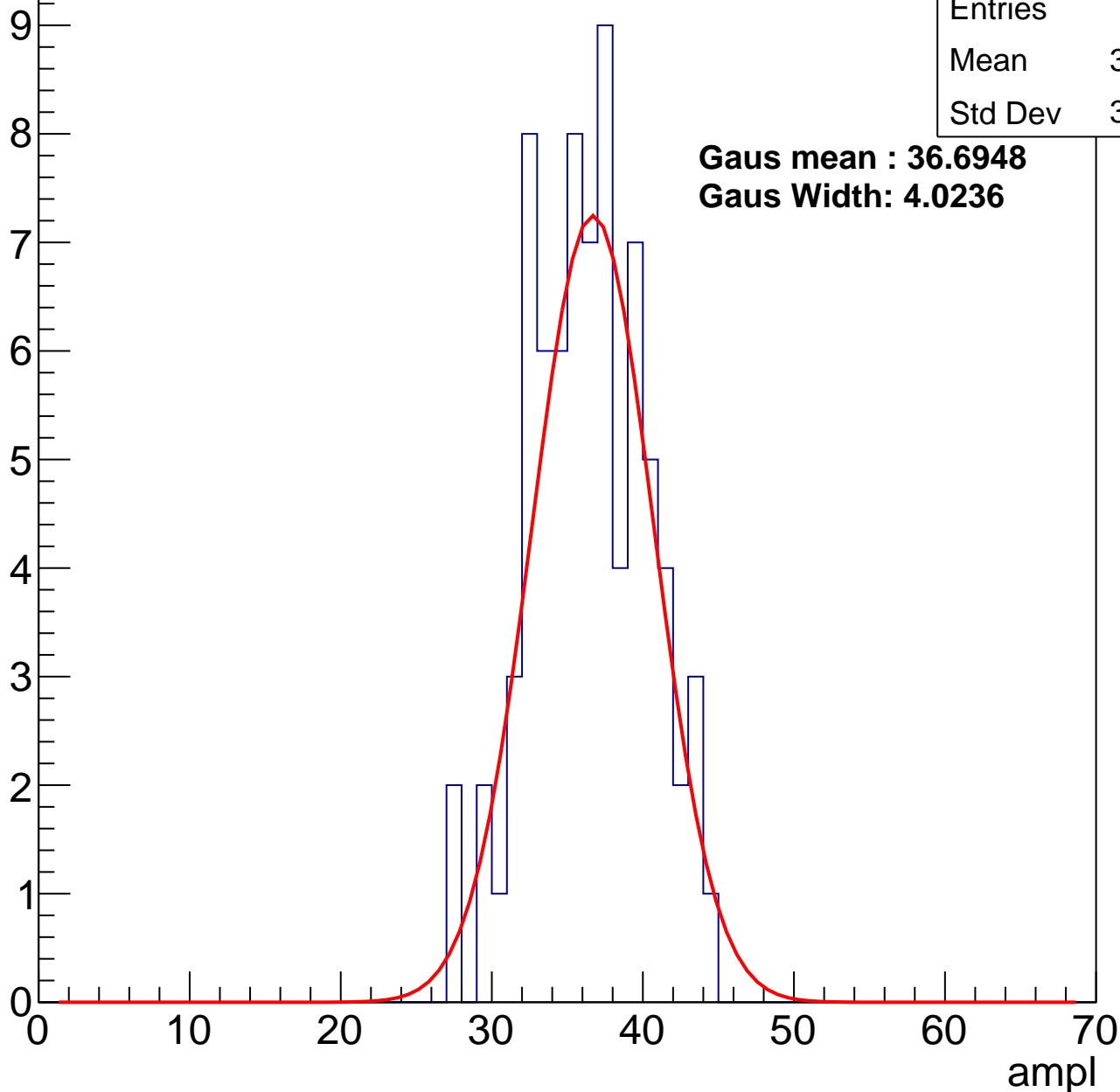
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	35.95
Std Dev	3.816

Gaus mean : 36.6948

Gaus Width: 4.0236



B1L100S, U25-ch28, adc2

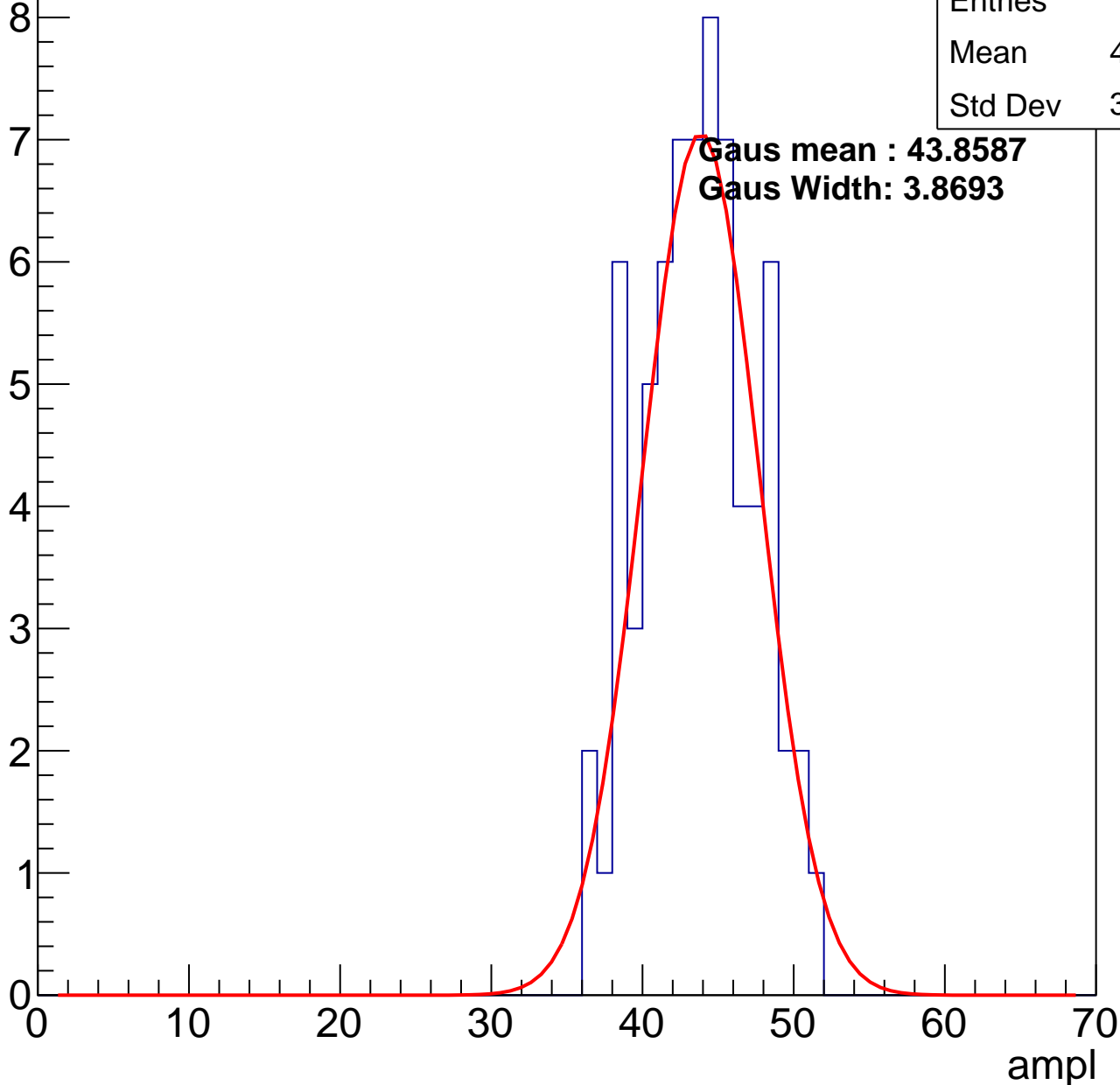
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.25
Std Dev	3.606

Gaus mean : 43.8587

Gaus Width: 3.8693

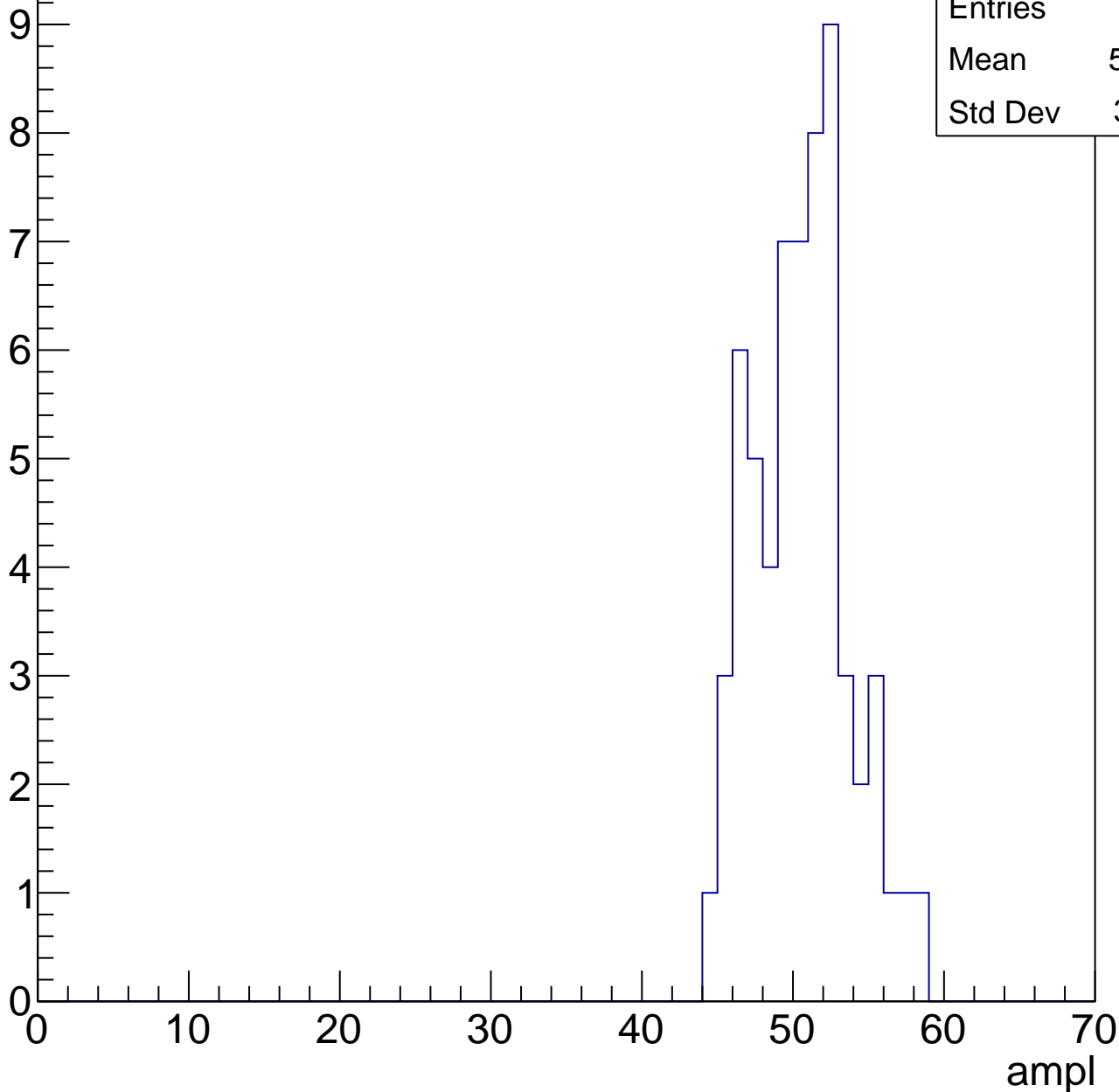


B1L100S, U25-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.07
Std Dev	3.141

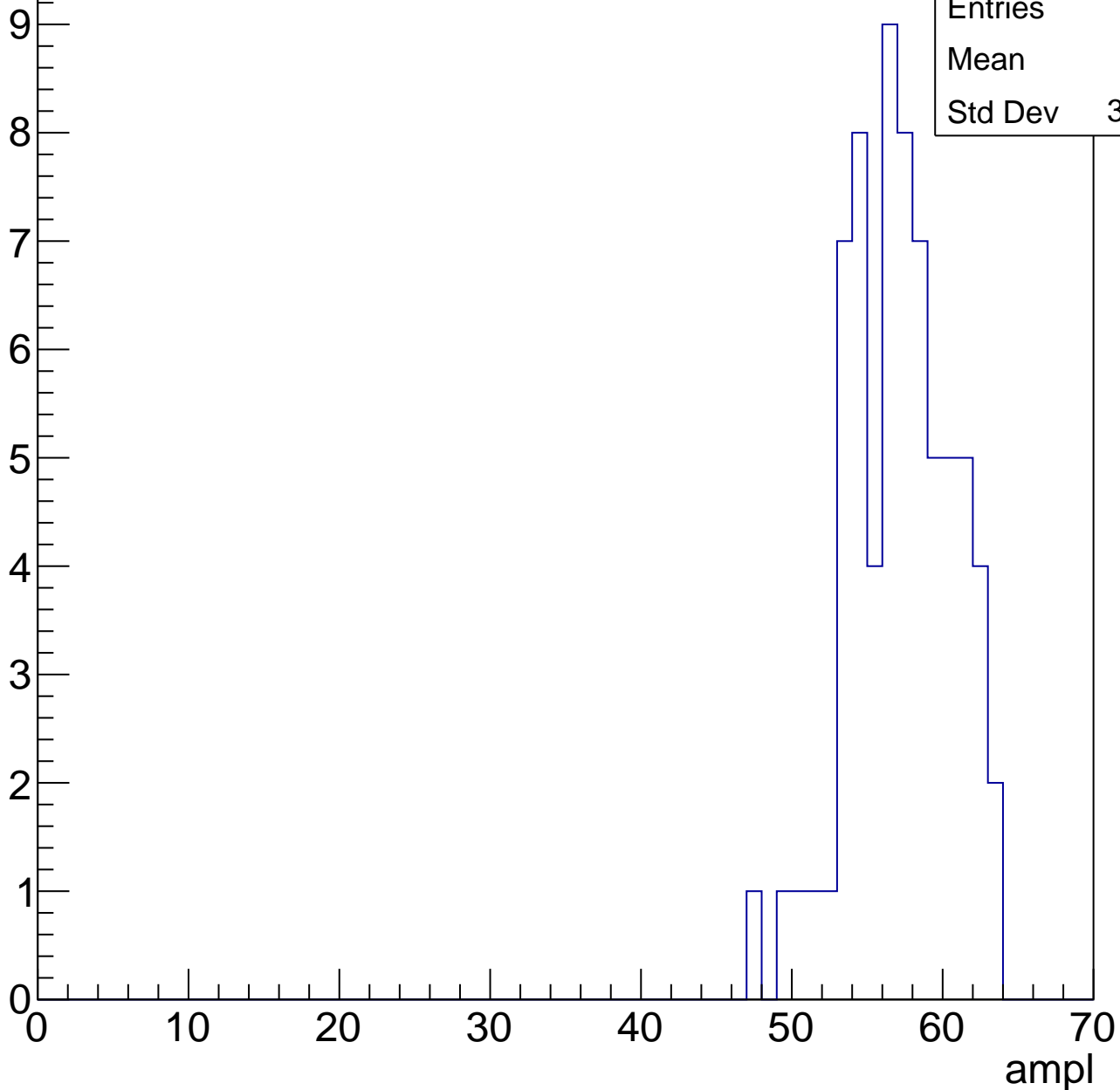


B1L100S, U25-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	56.7
Std Dev	3.406

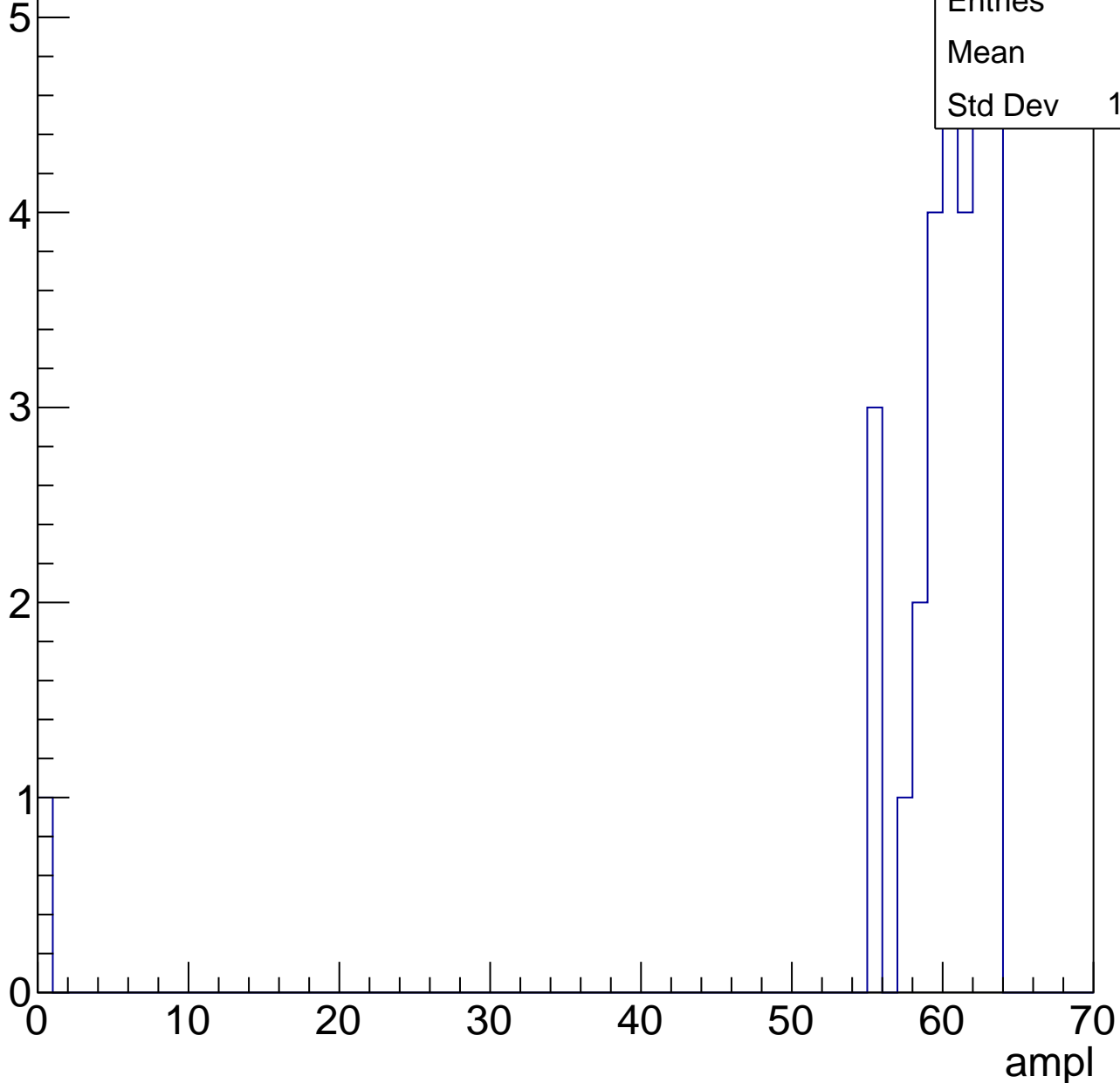


B1L100S, U25-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.1
Std Dev	11.04



B1L100S, U25-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch29, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	30.4
Std Dev	4.749

Gaus mean : 31.1081

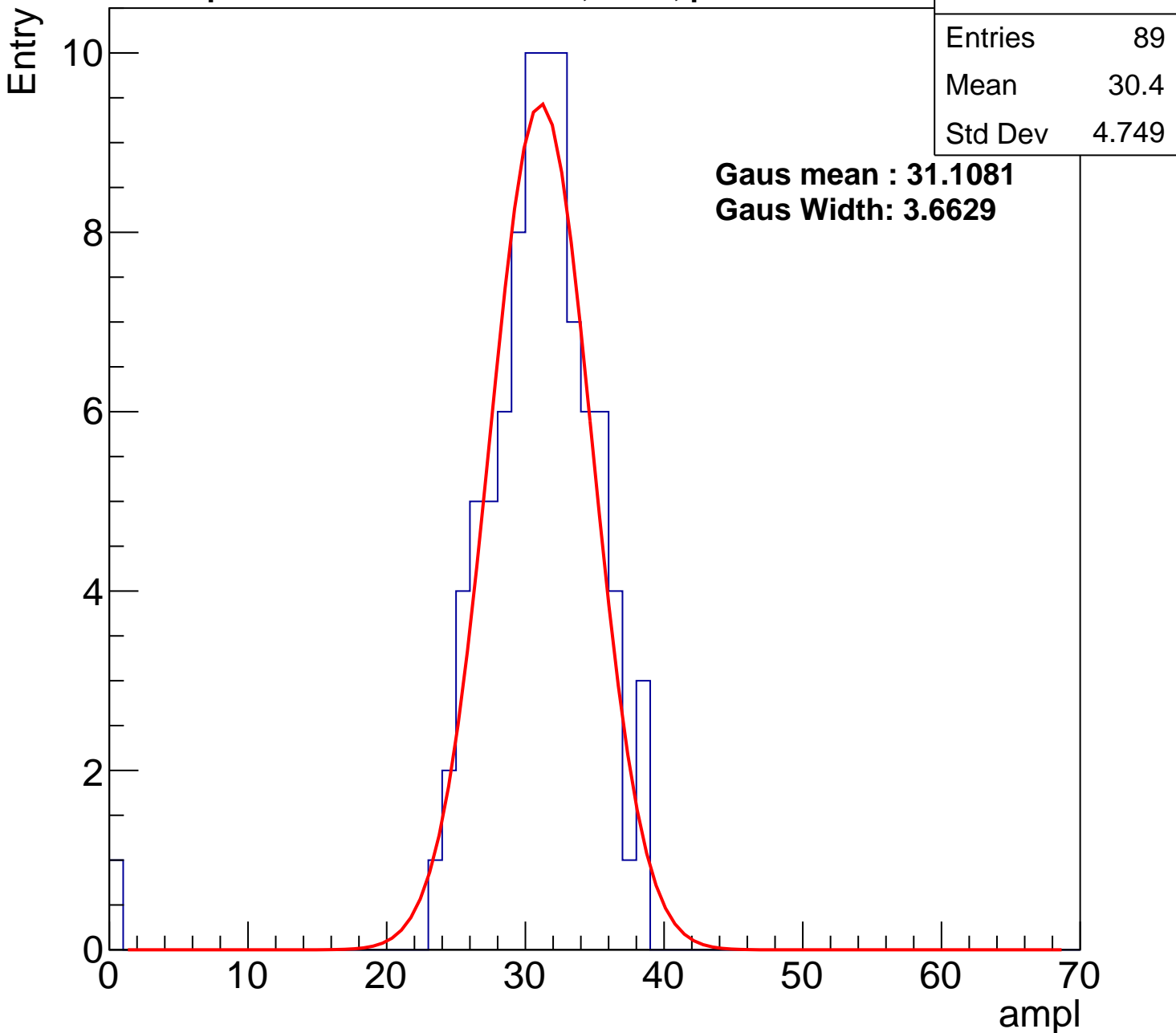
Gaus Width: 3.6629

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch29, adc1

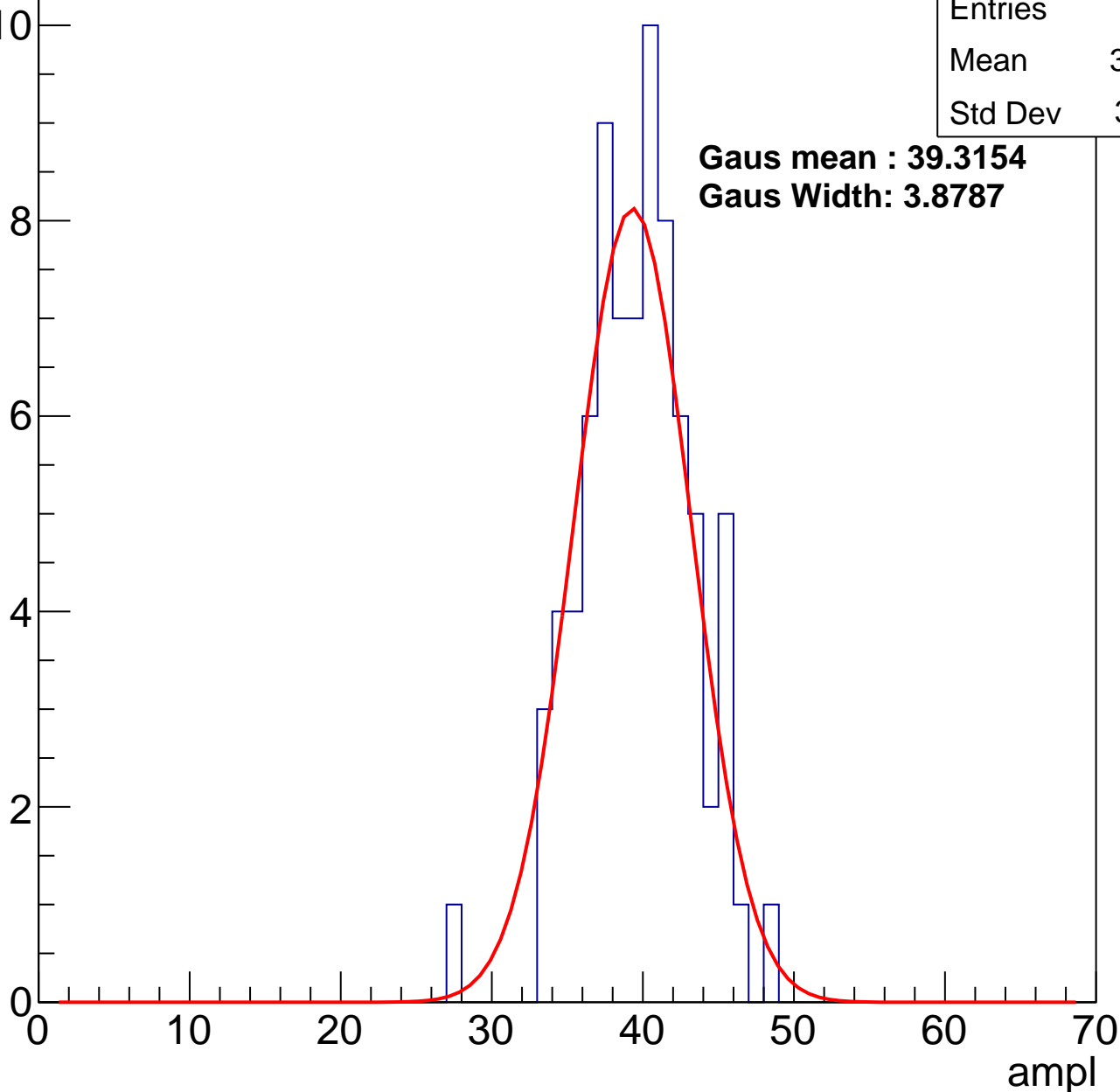
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	39.14
Std Dev	3.641

Gaus mean : 39.3154

Gaus Width: 3.8787



B1L100S, U25-ch29, adc2

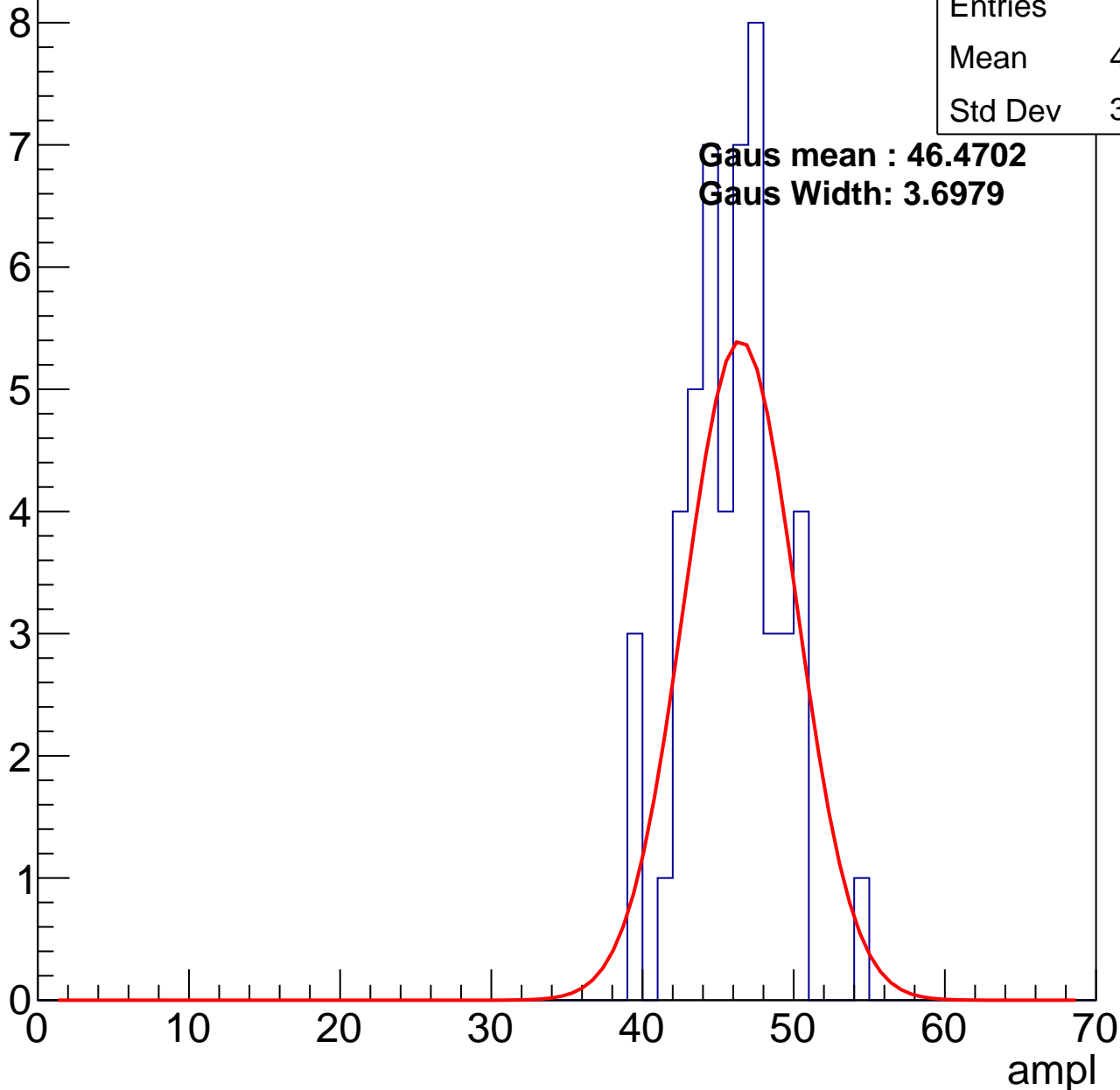
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	45.44
Std Dev	3.073

Gaus mean : 46.4702

Gaus Width: 3.6979

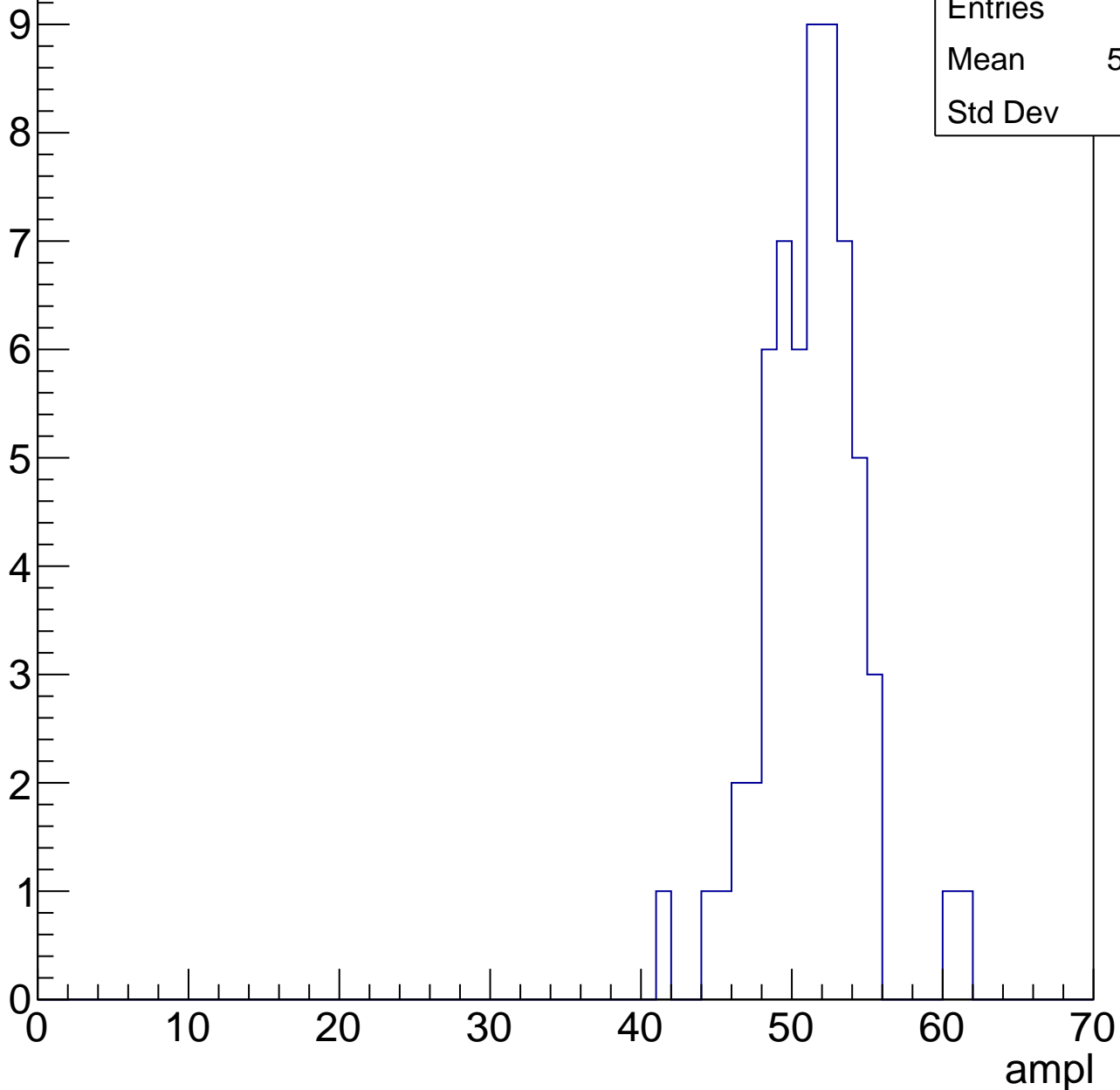


B1L100S, U25-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.84
Std Dev	3.3

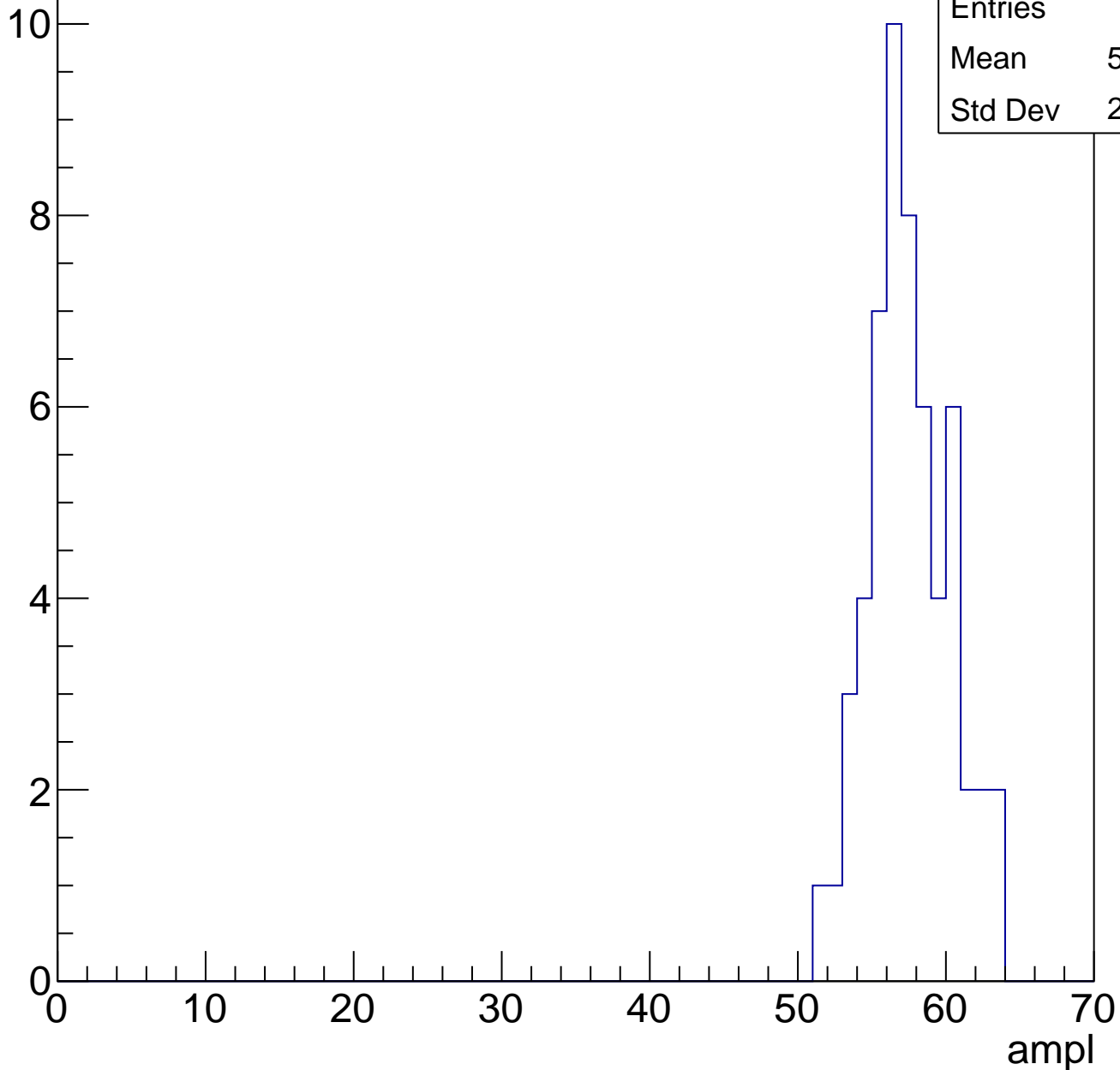


B1L100S, U25-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	57.05
Std Dev	2.715

Entry

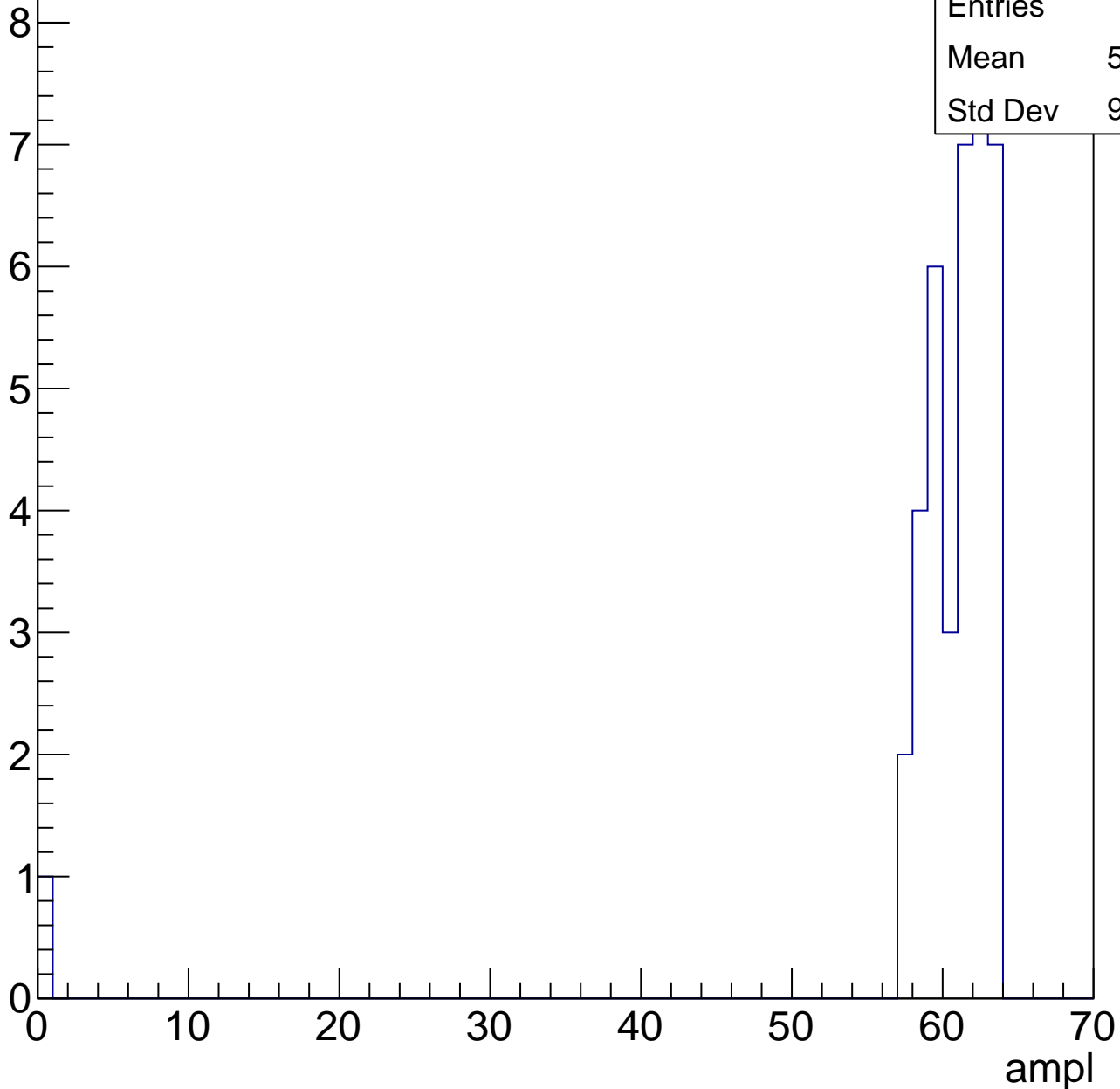


B1L100S, U25-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	59.05
Std Dev	9.878



B1L100S, U25-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch30, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	27.14
Std Dev	4.589

Gaus mean : 28.0453

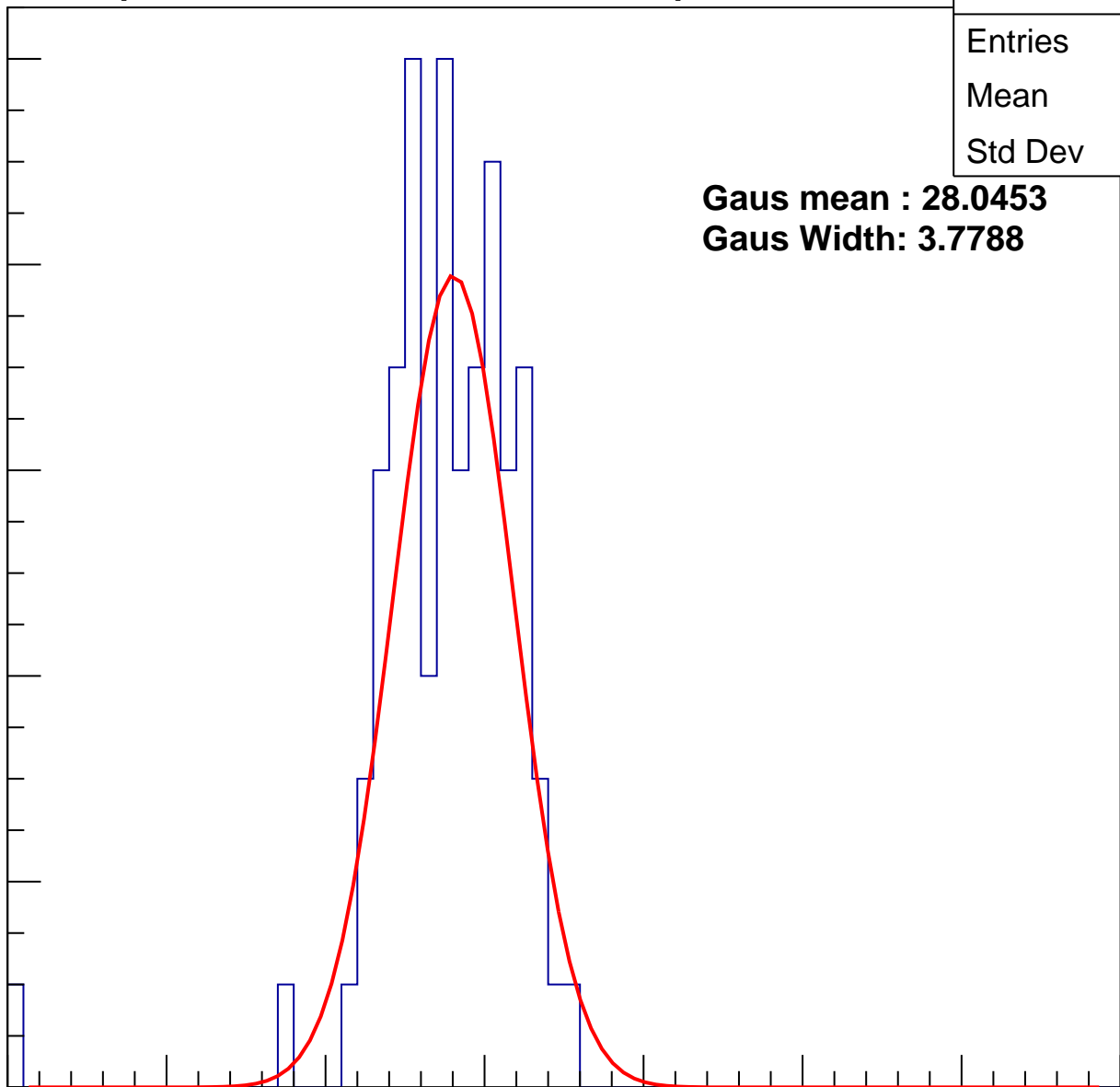
Gaus Width: 3.7788

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch30, adc1

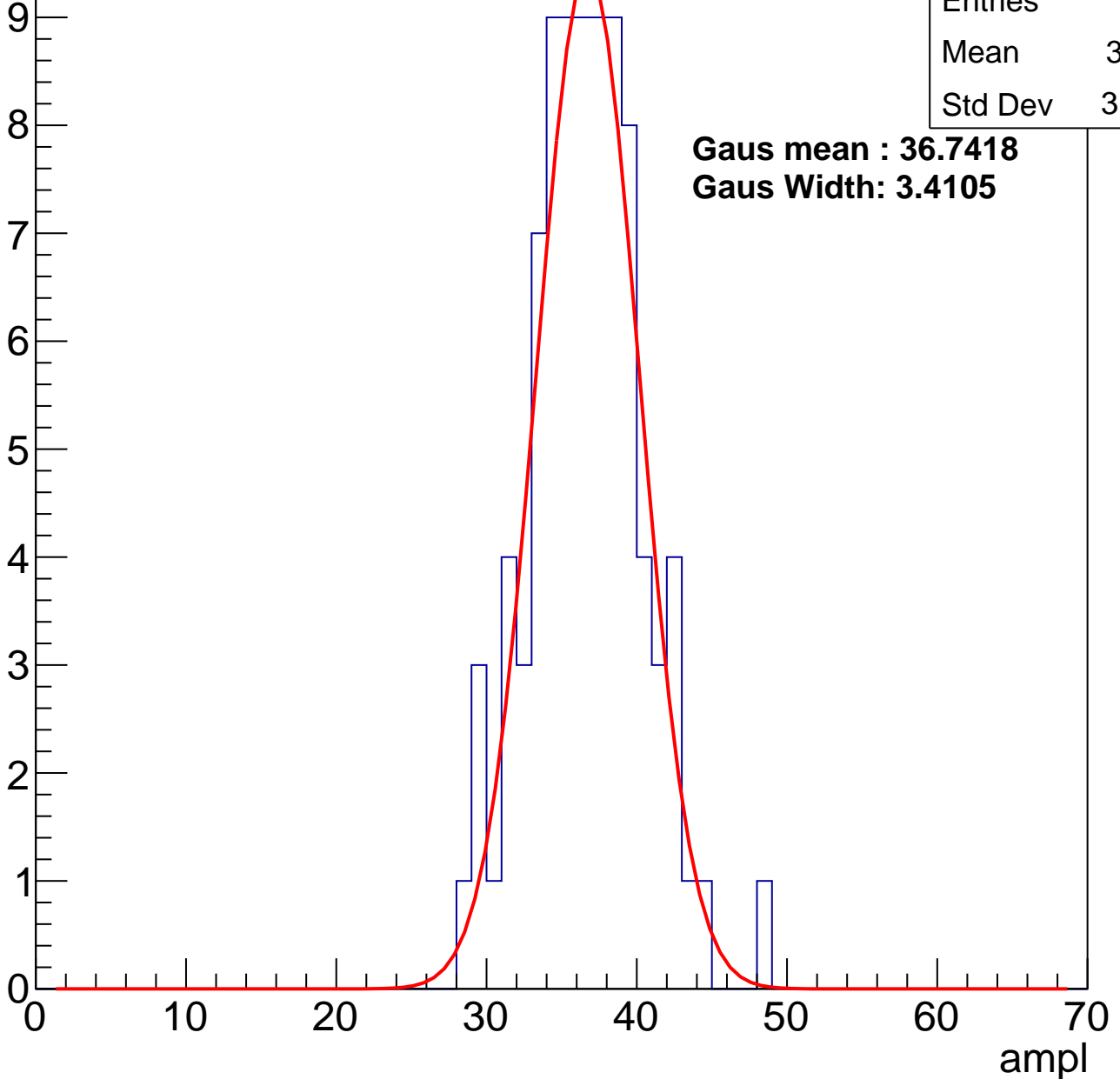
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	36.21
Std Dev	3.667

Gaus mean : 36.7418

Gaus Width: 3.4105



B1L100S, U25-ch30, adc2

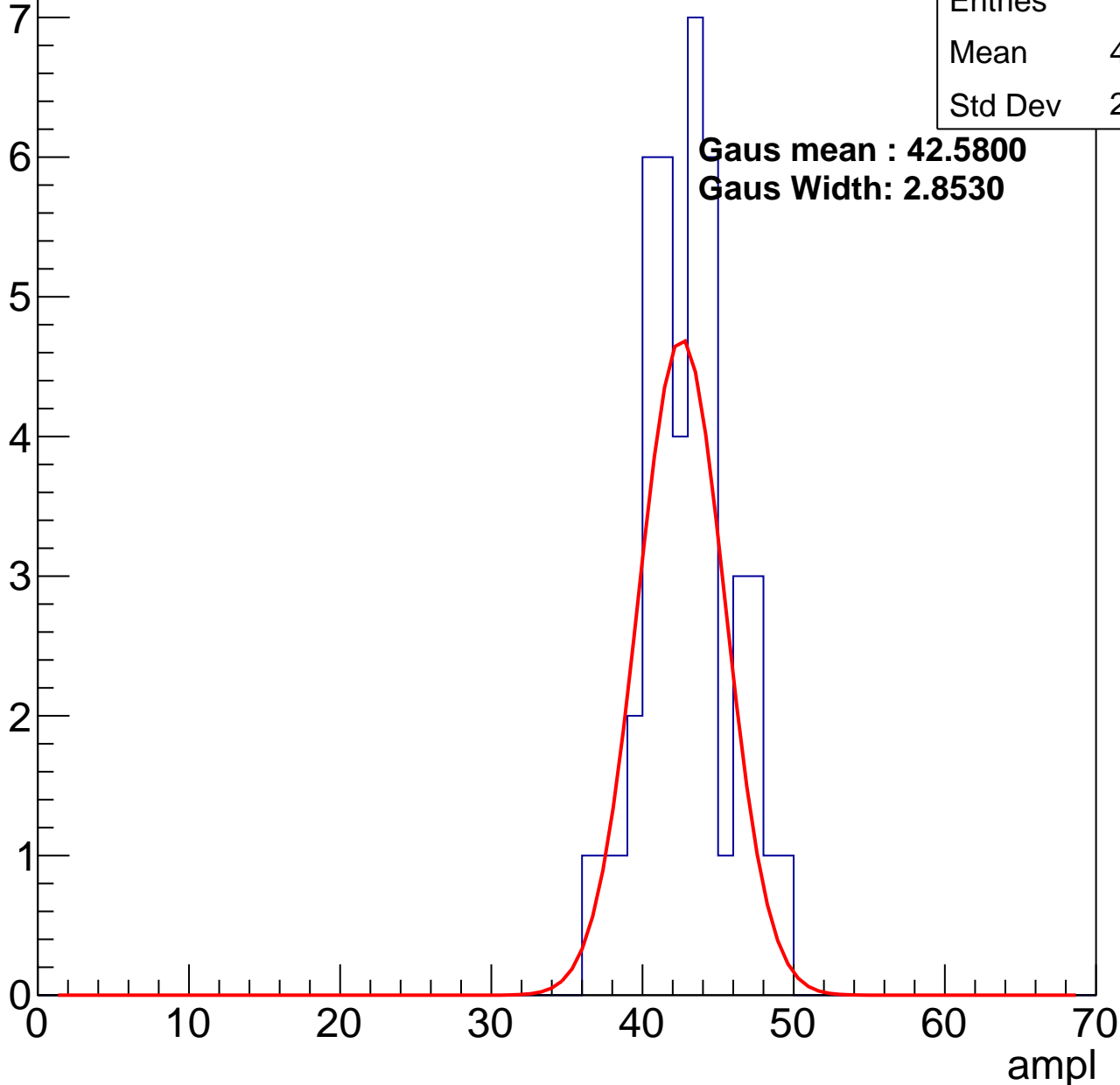
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	42.53
Std Dev	2.896

Gaus mean : 42.5800

Gaus Width: 2.8530

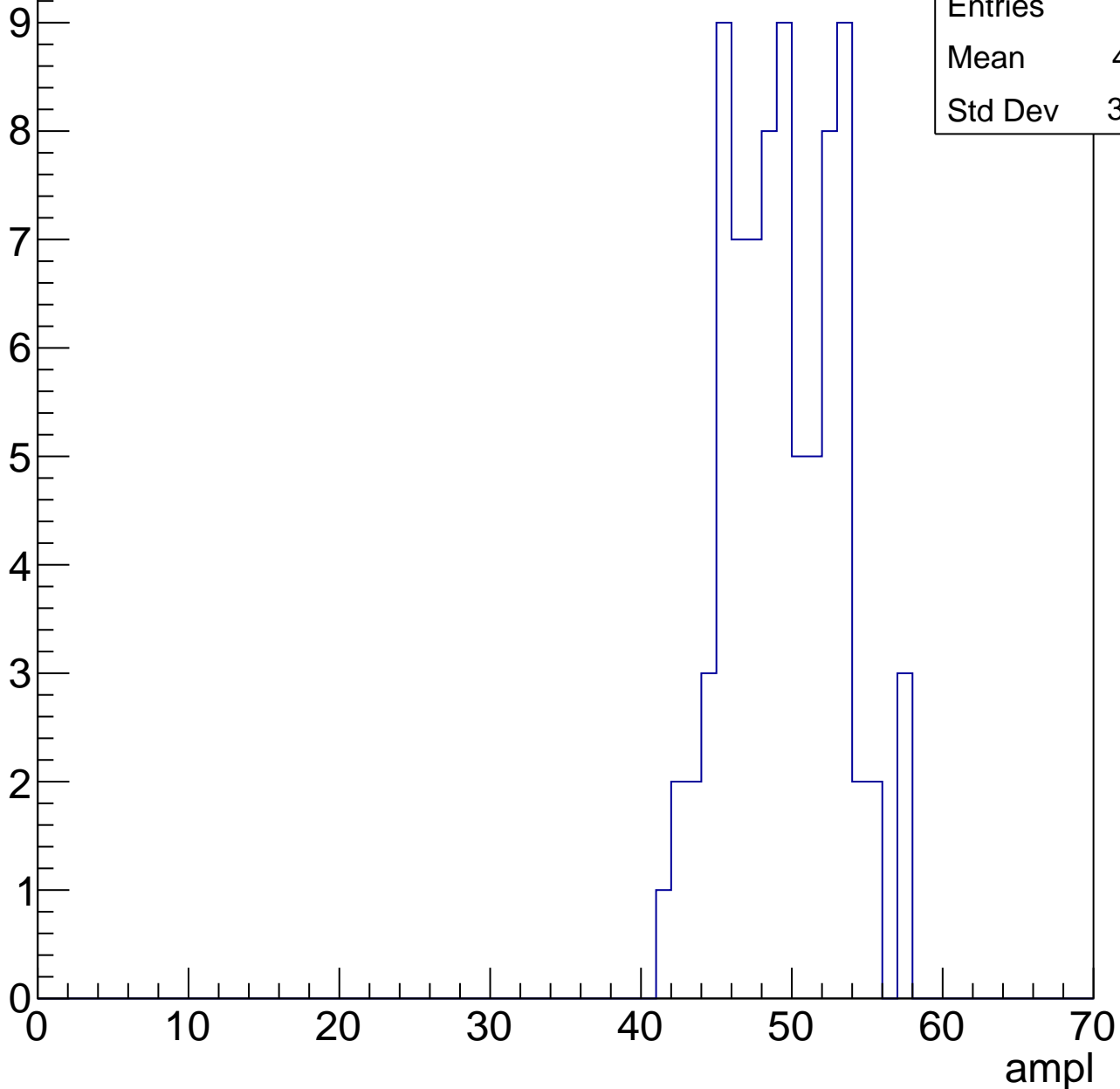


B1L100S, U25-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	48.91
Std Dev	3.673

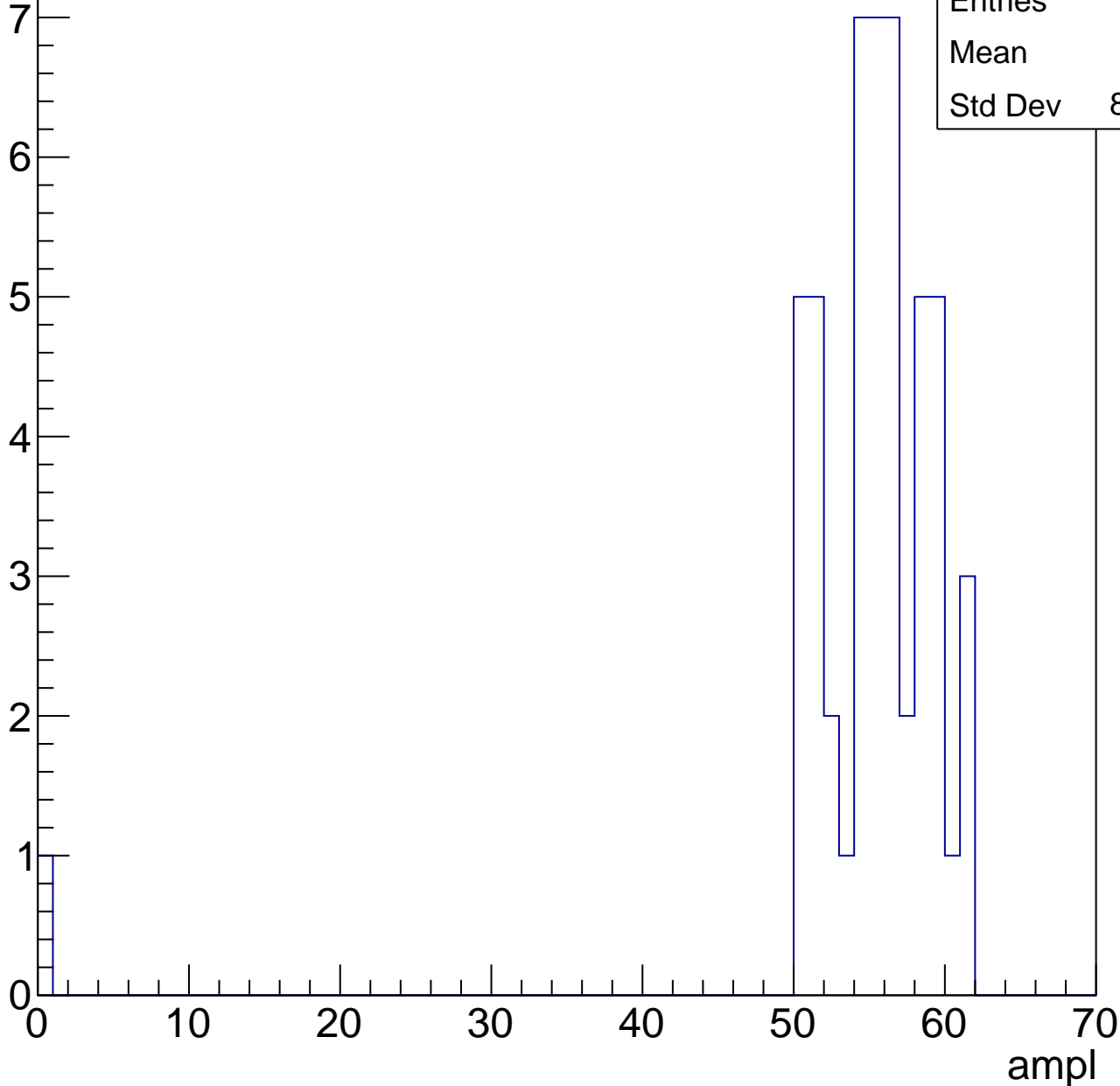


B1L100S, U25-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	54.1
Std Dev	8.273



B1L100S, U25-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.76
Std Dev	2.361

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L100S, U25-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch31, adc0

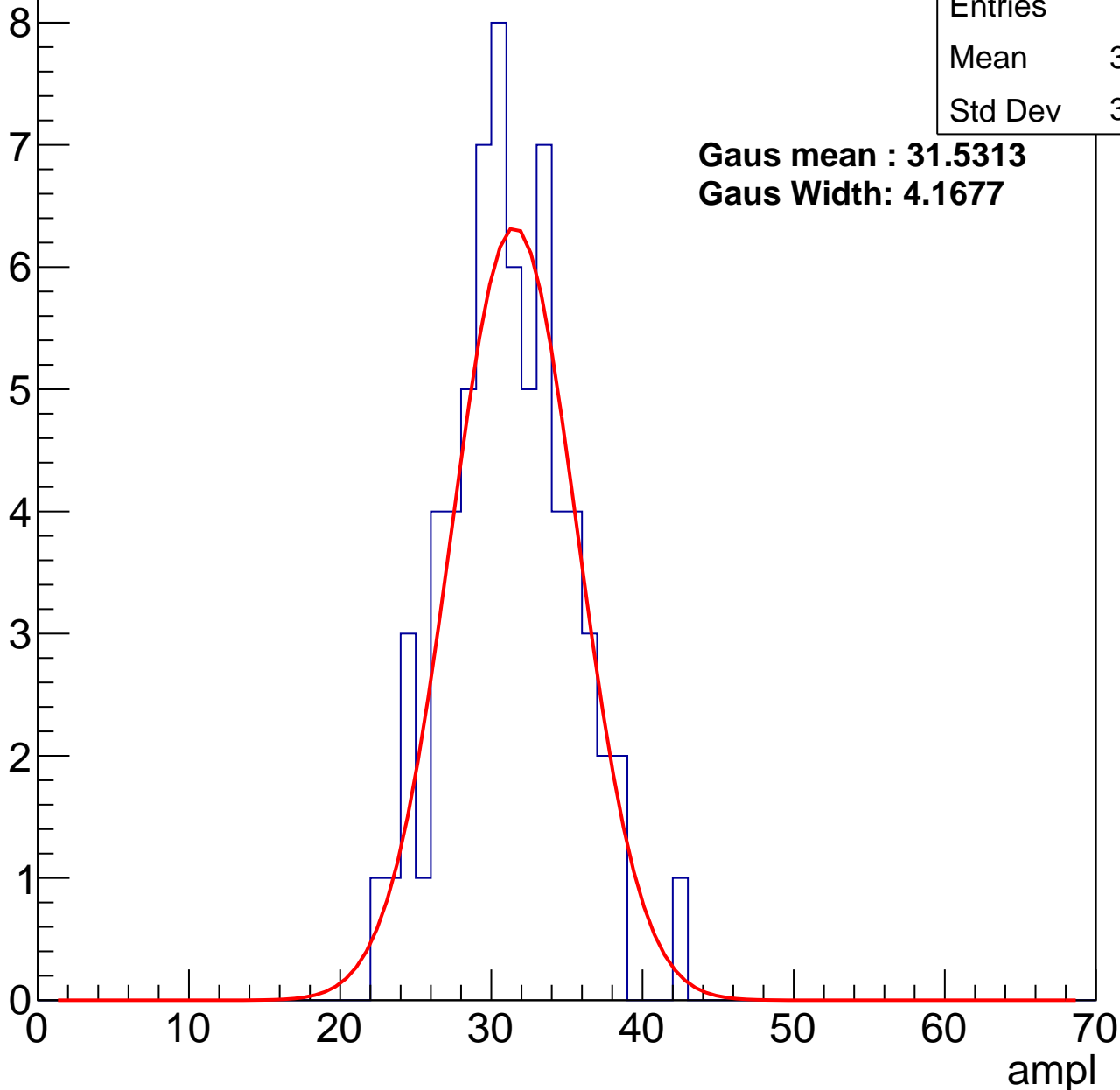
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.74
Std Dev	3.962

Gaus mean : 31.5313

Gaus Width: 4.1677



B1L100S, U25-ch31, adc1

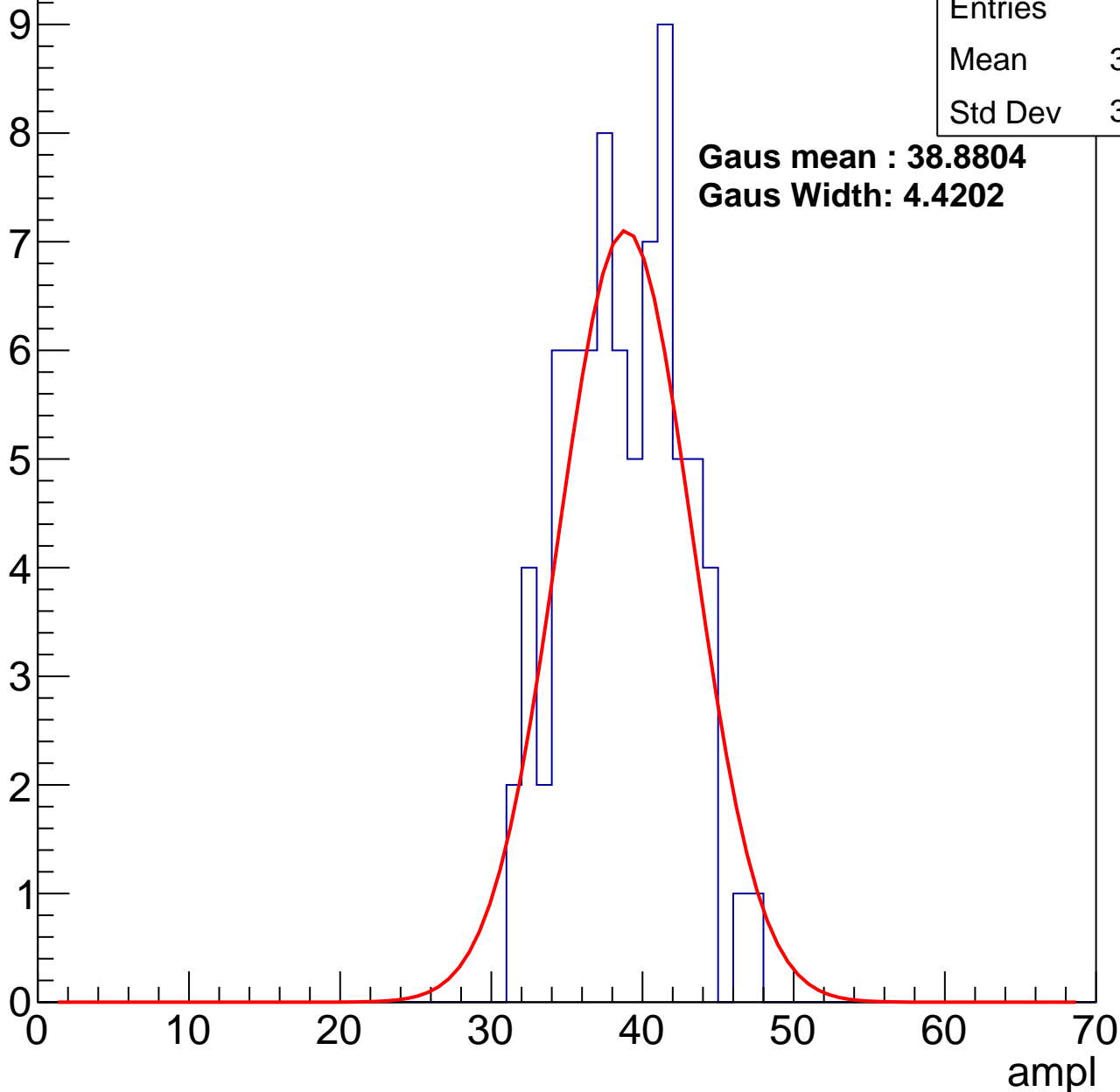
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.29
Std Dev	3.738

Gaus mean : 38.8804

Gaus Width: 4.4202



B1L100S, U25-ch31, adc2

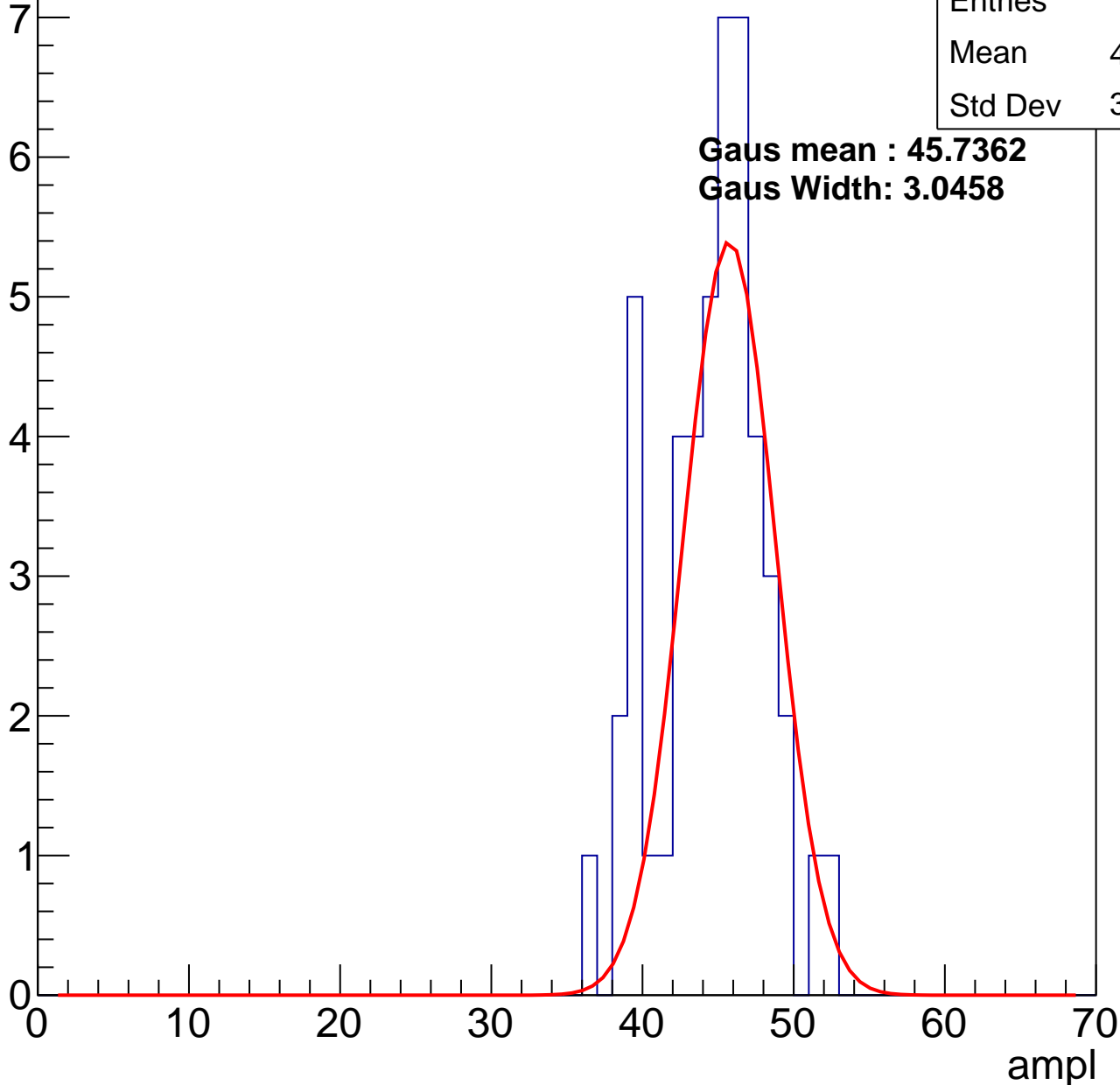
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	44.12
Std Dev	3.498

Gaus mean : 45.7362

Gaus Width: 3.0458



B1L100S, U25-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	51.06
Std Dev	4.121

Entry

10

8

6

4

2

0

0

10

20

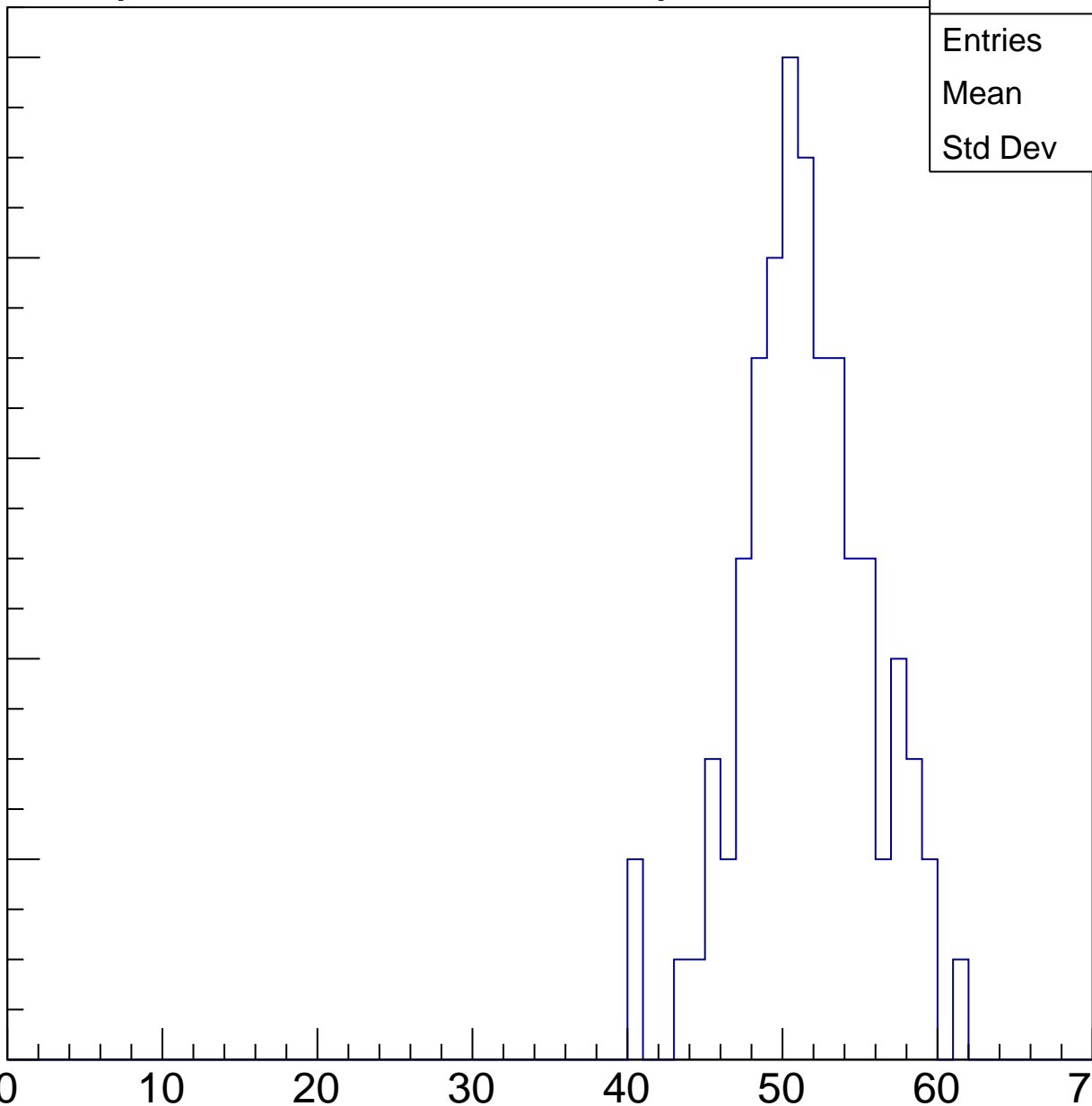
30

40

50

60

ampl

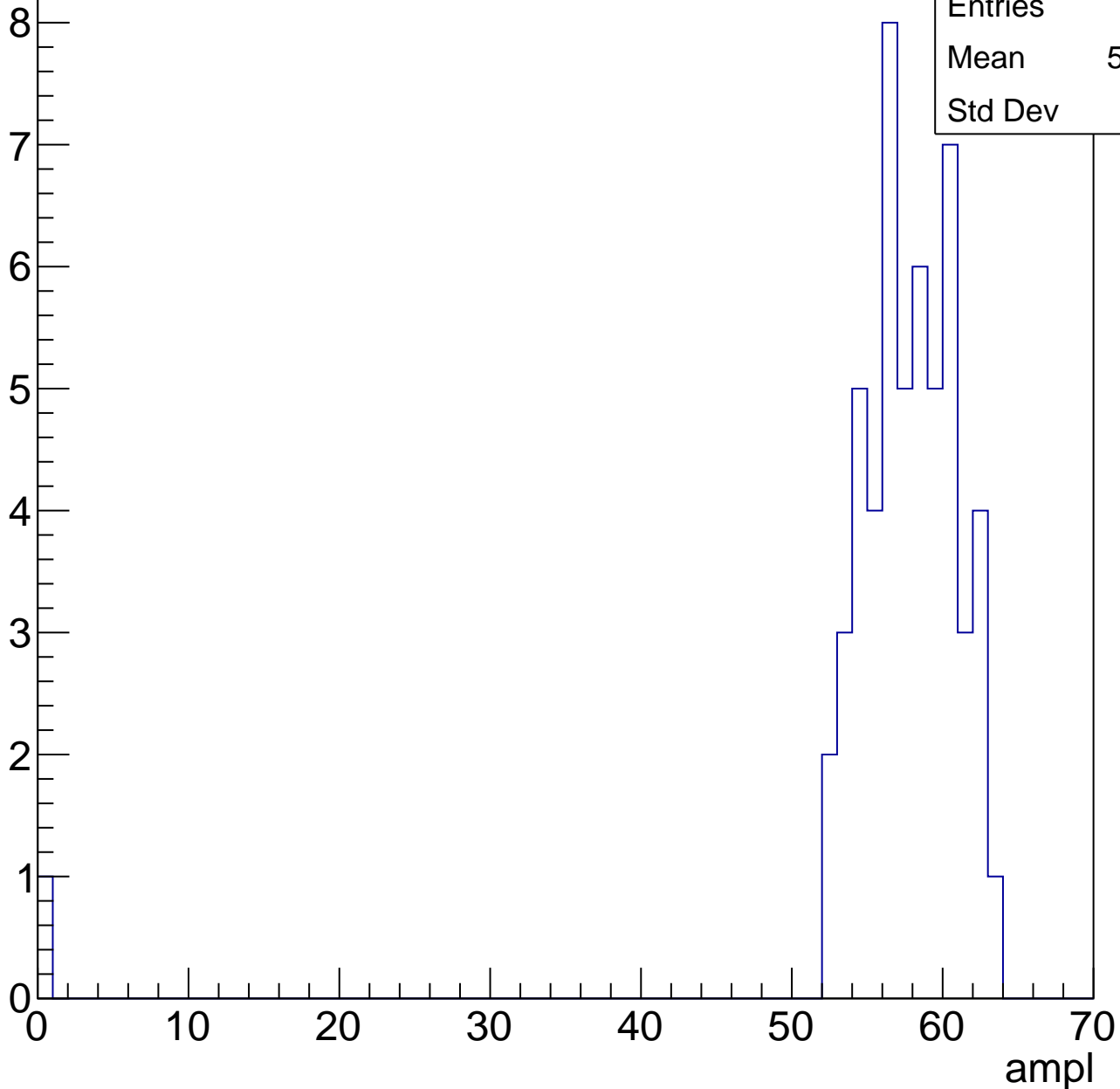


B1L100S, U25-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.35
Std Dev	8.24

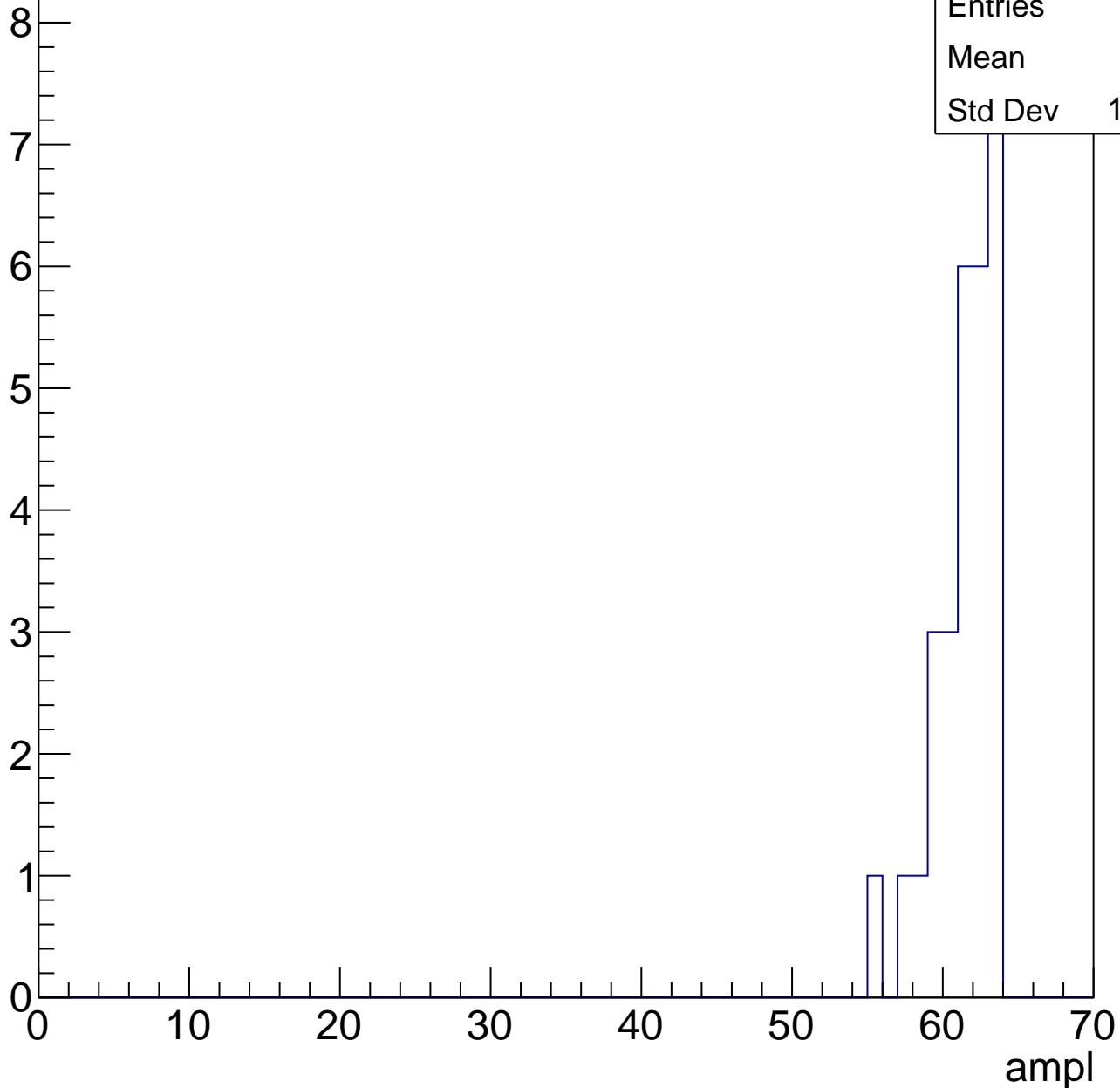


B1L100S, U25-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	61
Std Dev	1.983



B1L100S, U25-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch32, adc0

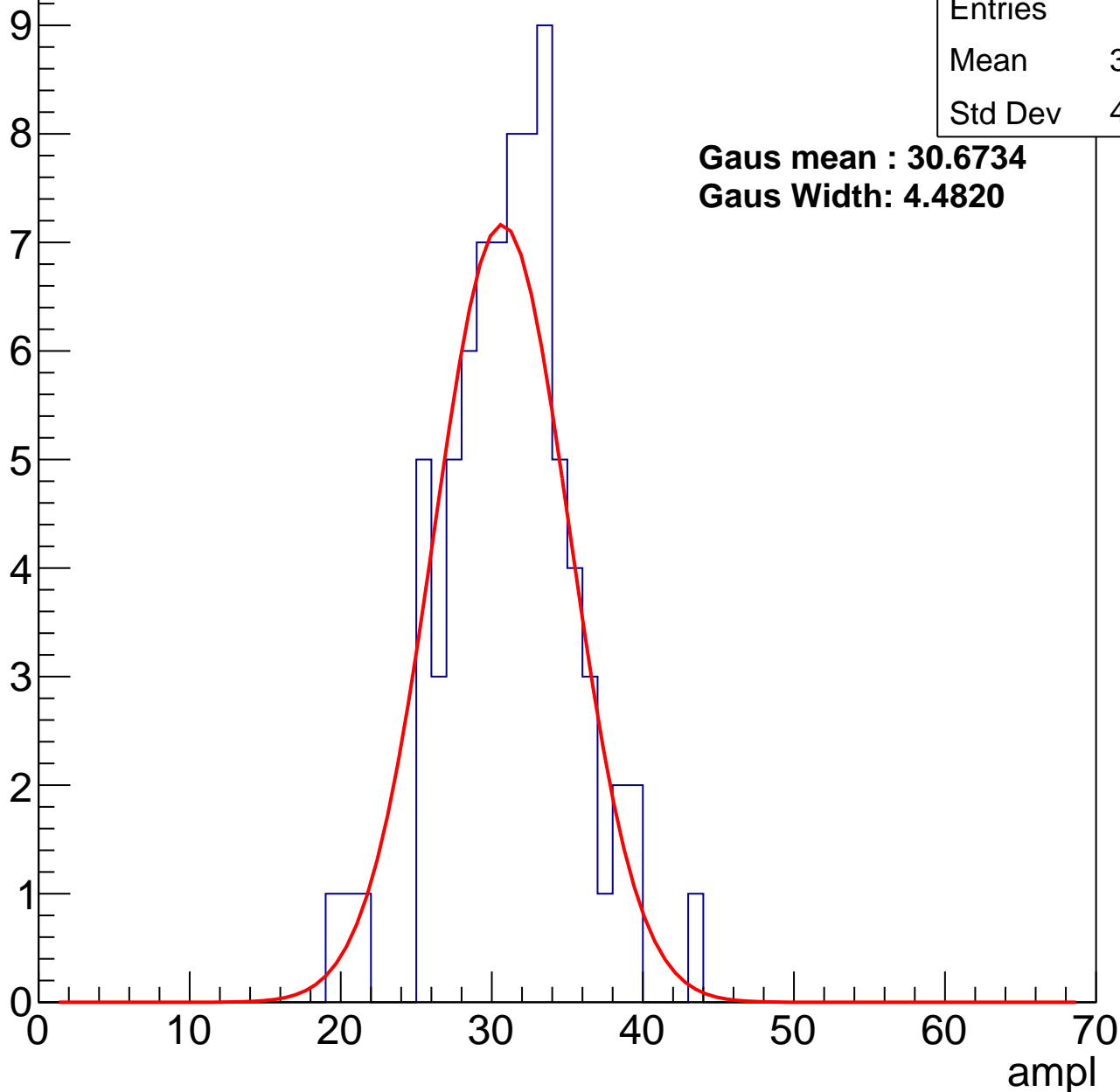
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.78
Std Dev	4.239

Gaus mean : 30.6734

Gaus Width: 4.4820



B1L100S, U25-ch32, adc1

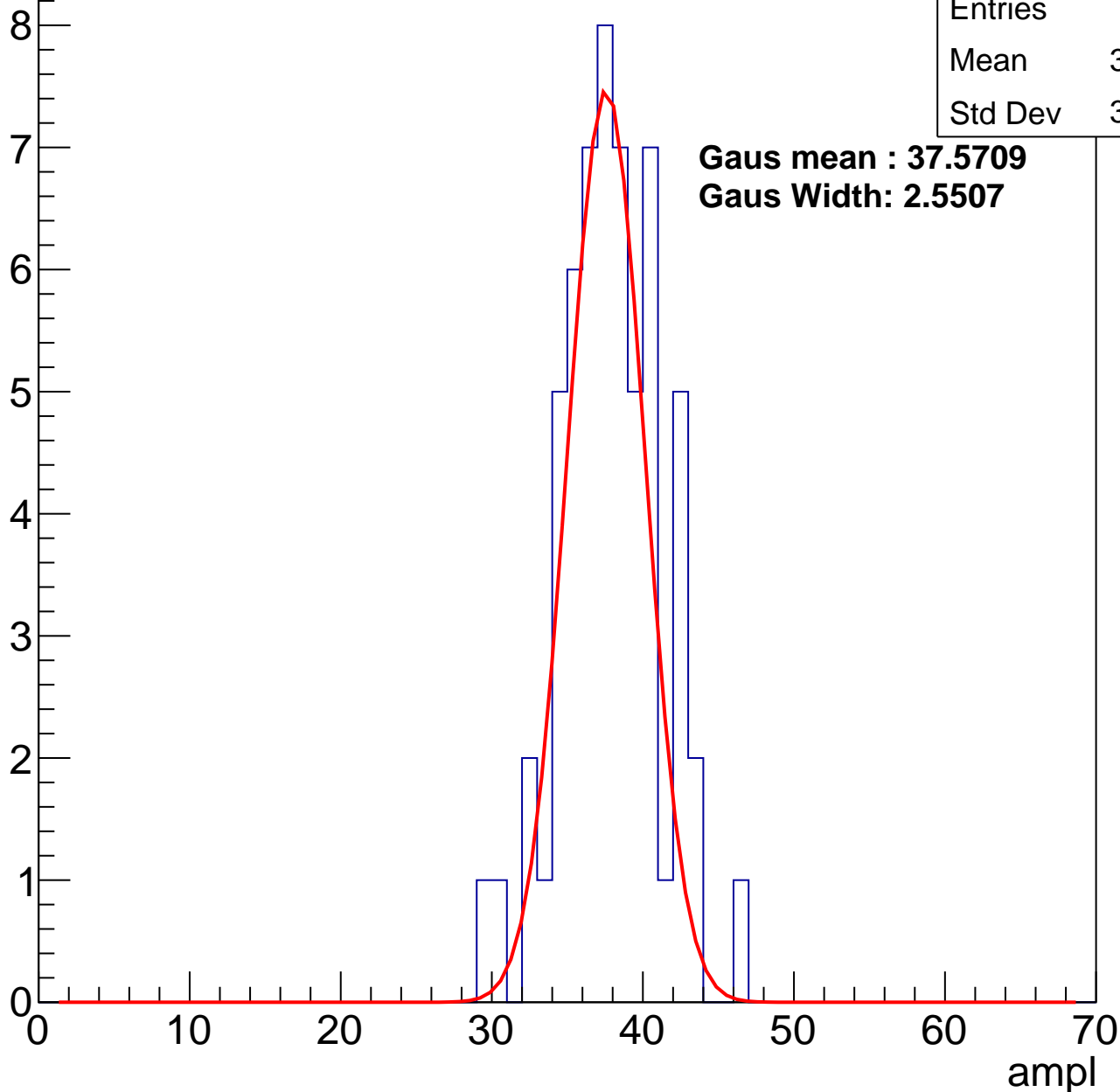
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.42
Std Dev	3.264

Gaus mean : 37.5709

Gaus Width: 2.5507



B1L100S, U25-ch32, adc2

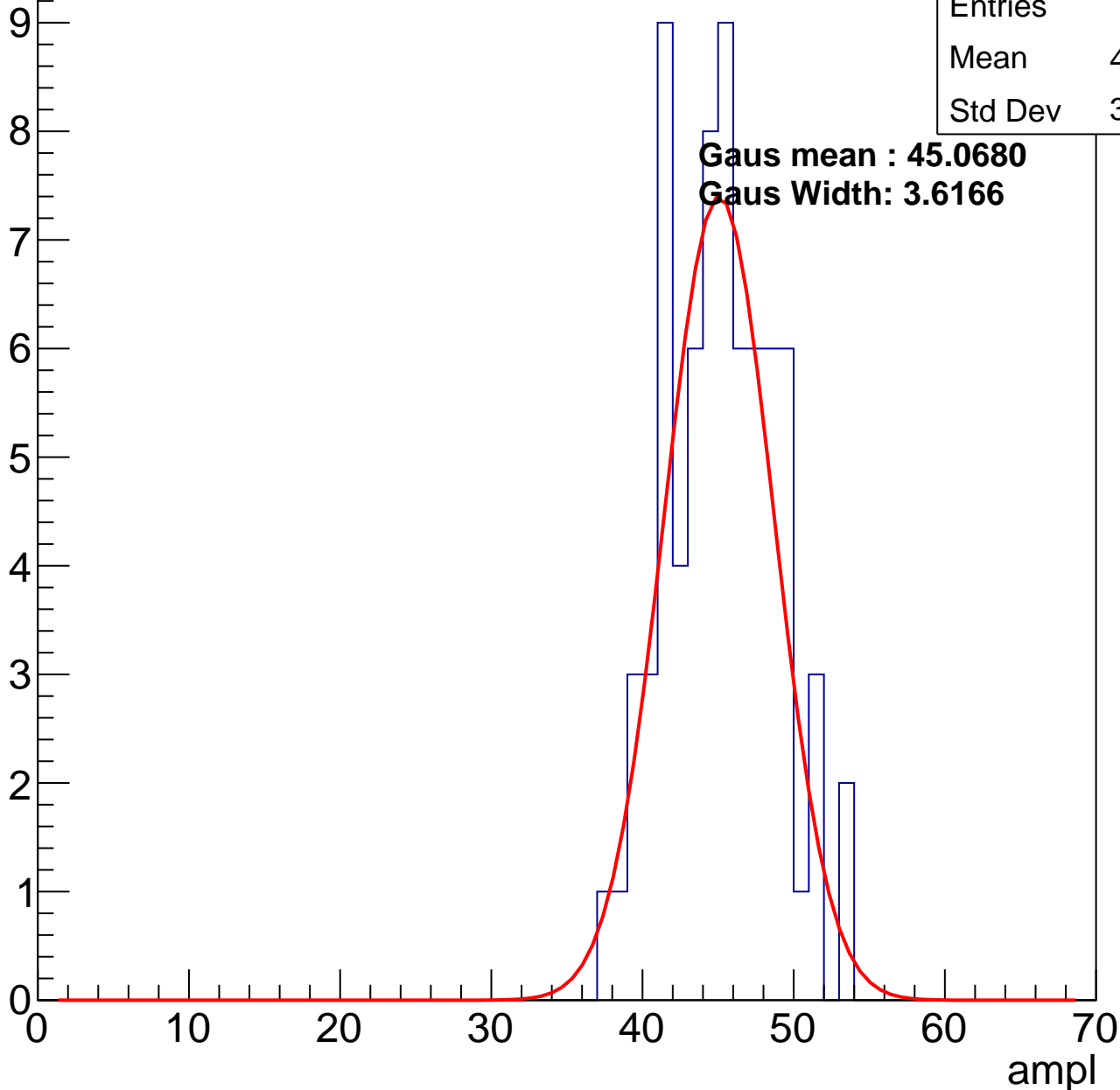
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	44.77
Std Dev	3.574

Gaus mean : 45.0680

Gaus Width: 3.6166

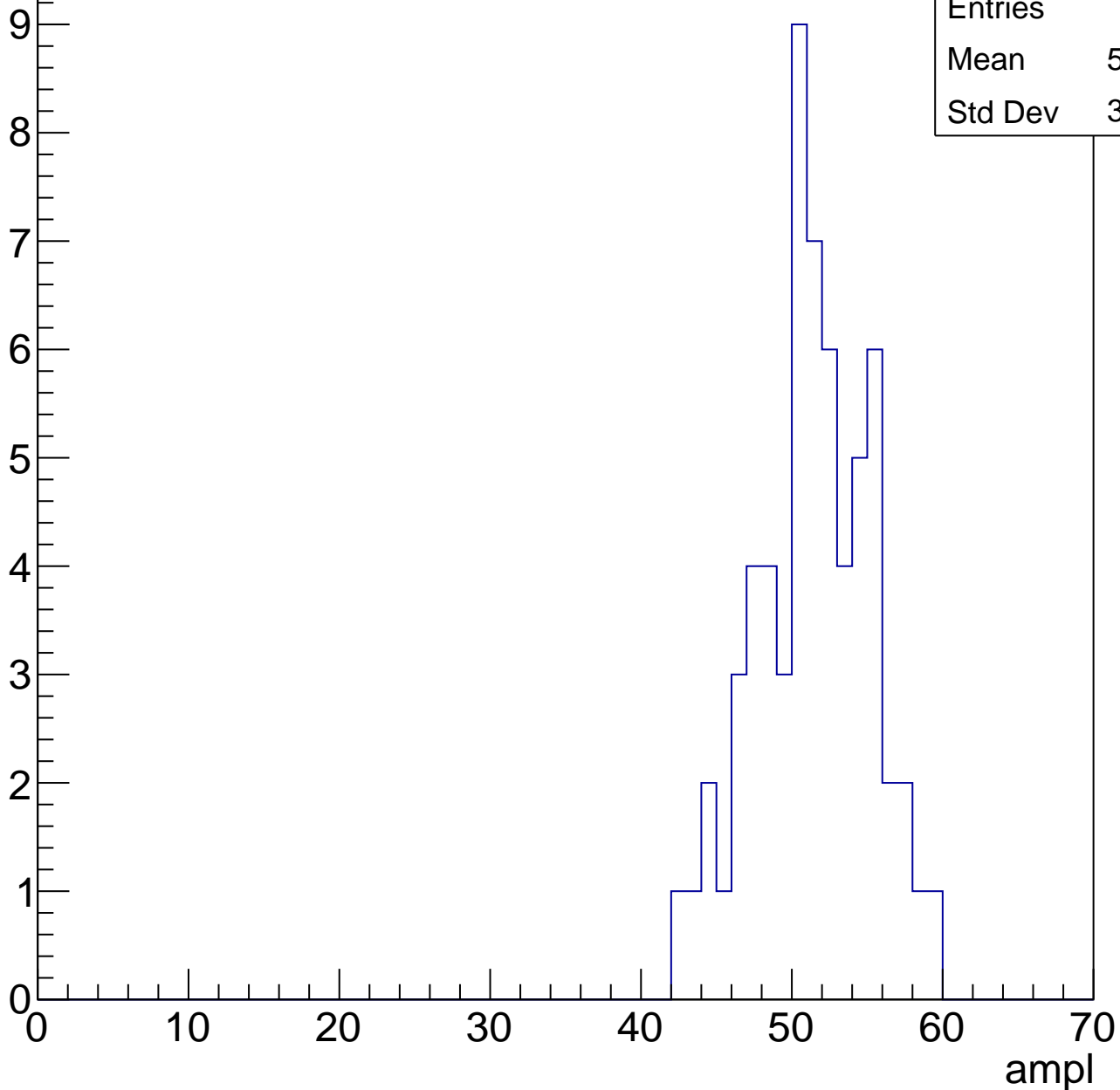


B1L100S, U25-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.92
Std Dev	3.752

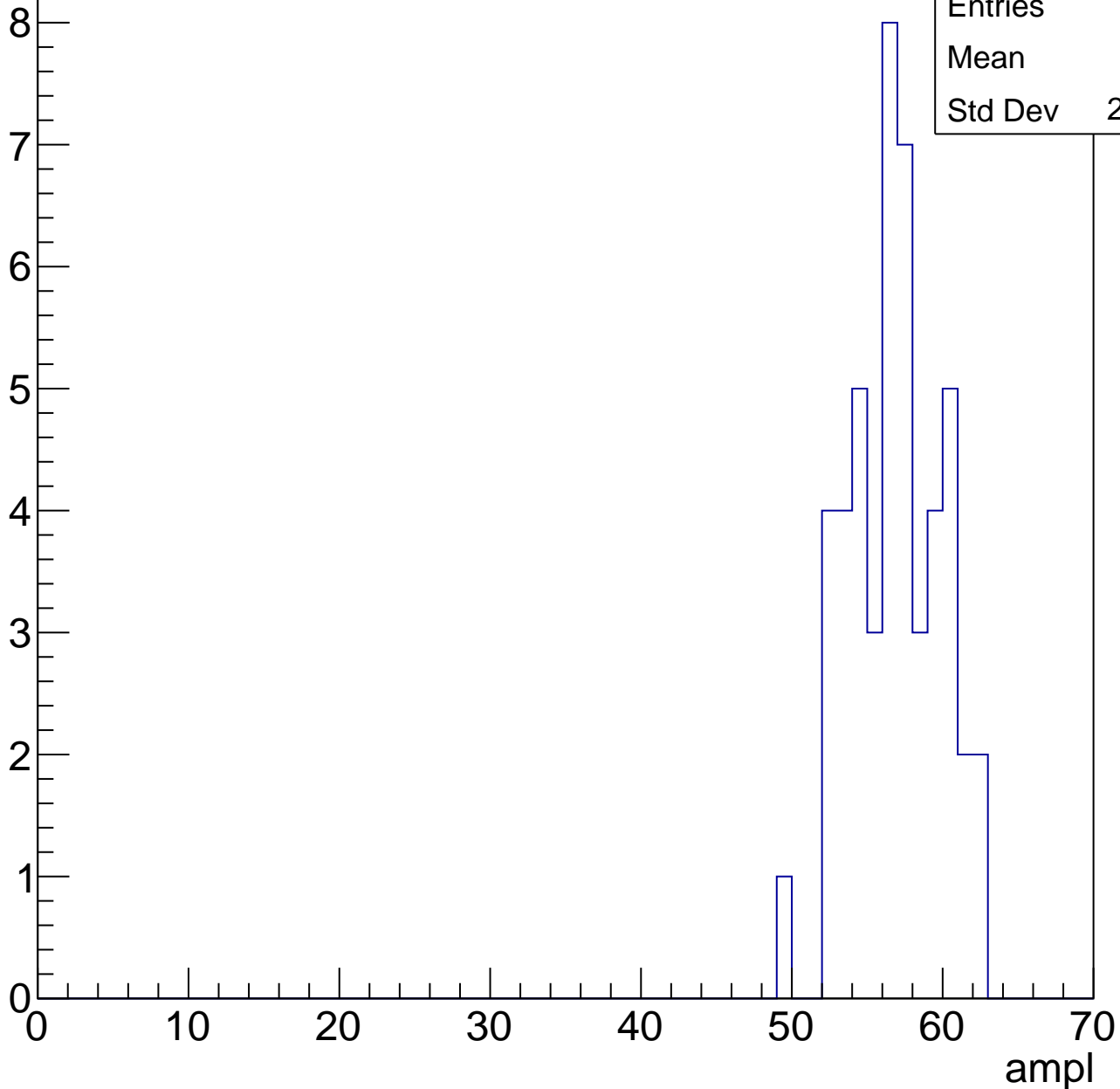


B1L100S, U25-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	56.4
Std Dev	2.963

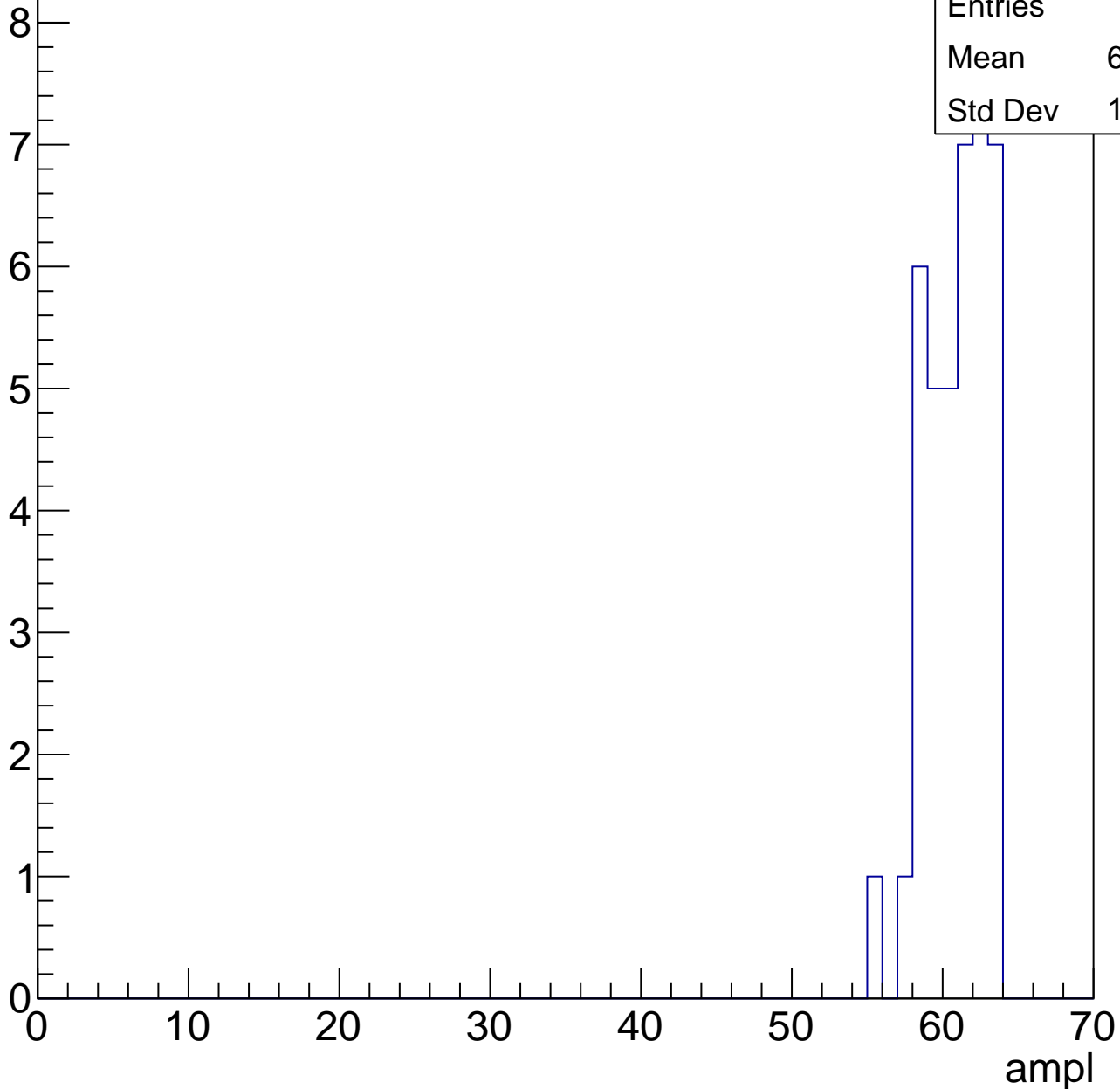


B1L100S, U25-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

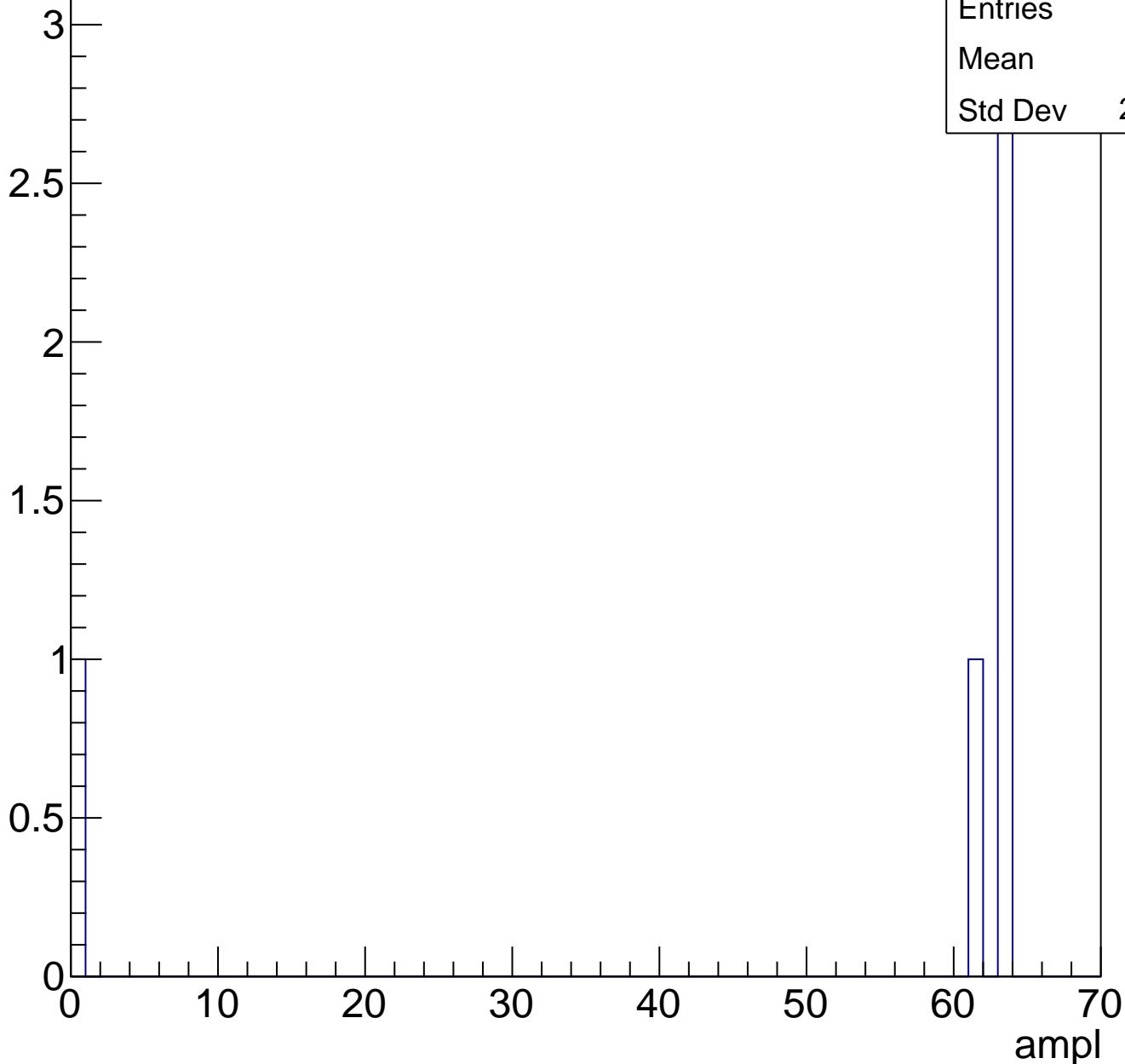
Entries	40
Mean	60.48
Std Dev	1.975



B1L100S, U25-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch33, adc0

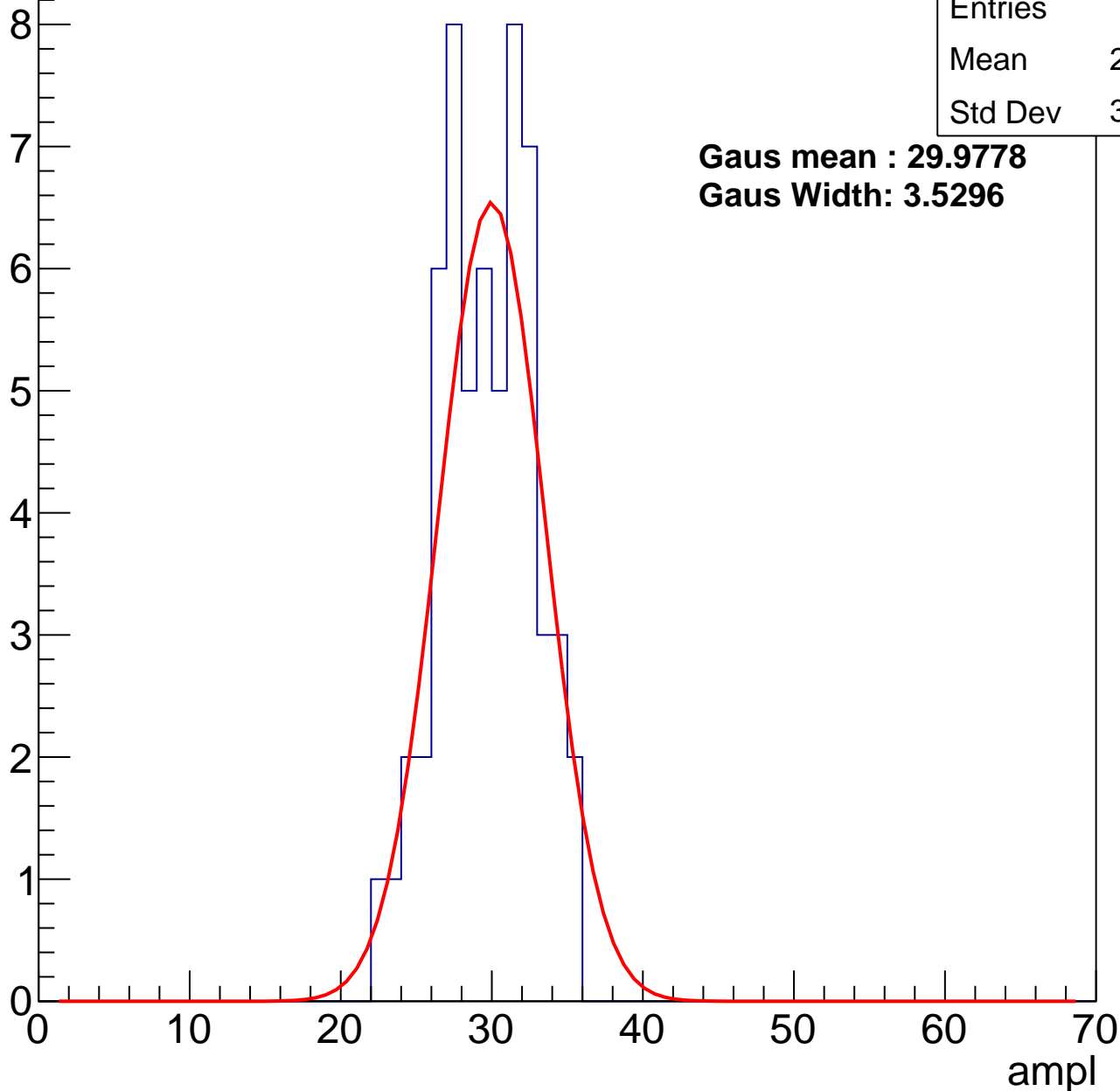
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.19
Std Dev	3.056

Gaus mean : 29.9778

Gaus Width: 3.5296



B1L100S, U25-ch33, adc1

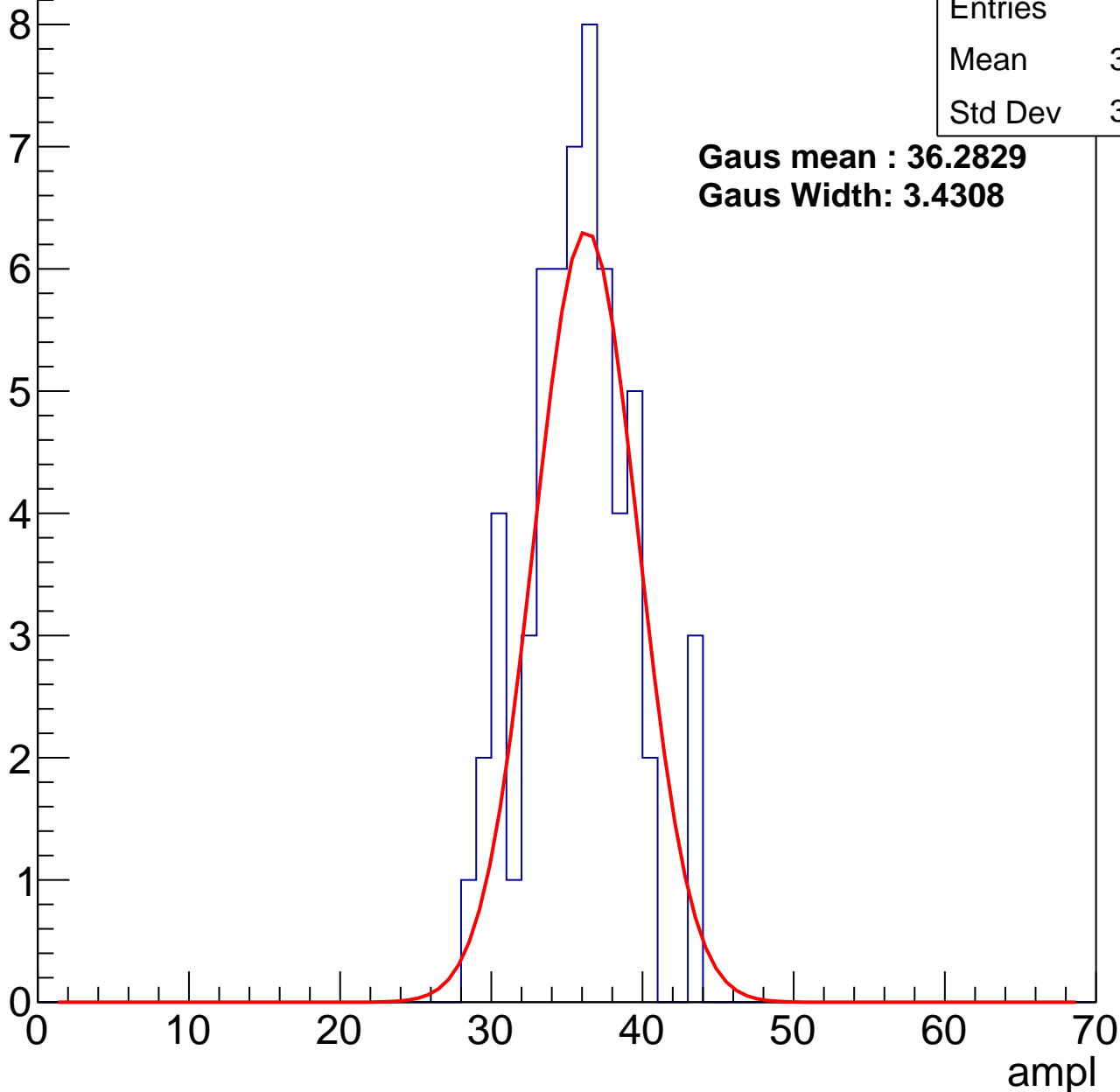
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.28
Std Dev	3.423

Gaus mean : 36.2829

Gaus Width: 3.4308



B1L100S, U25-ch33, adc2

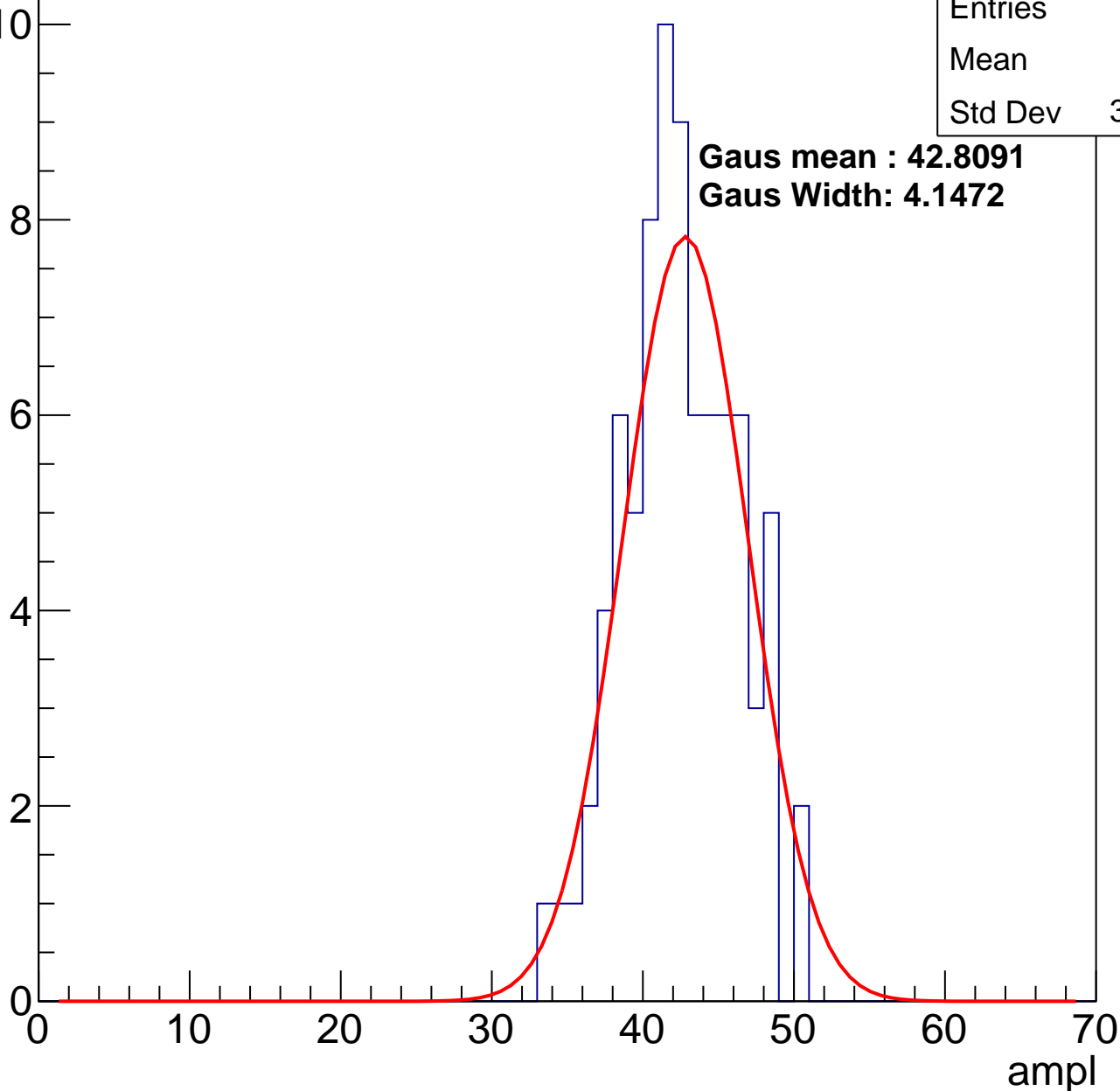
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	42
Std Dev	3.705

Gaus mean : 42.8091

Gaus Width: 4.1472

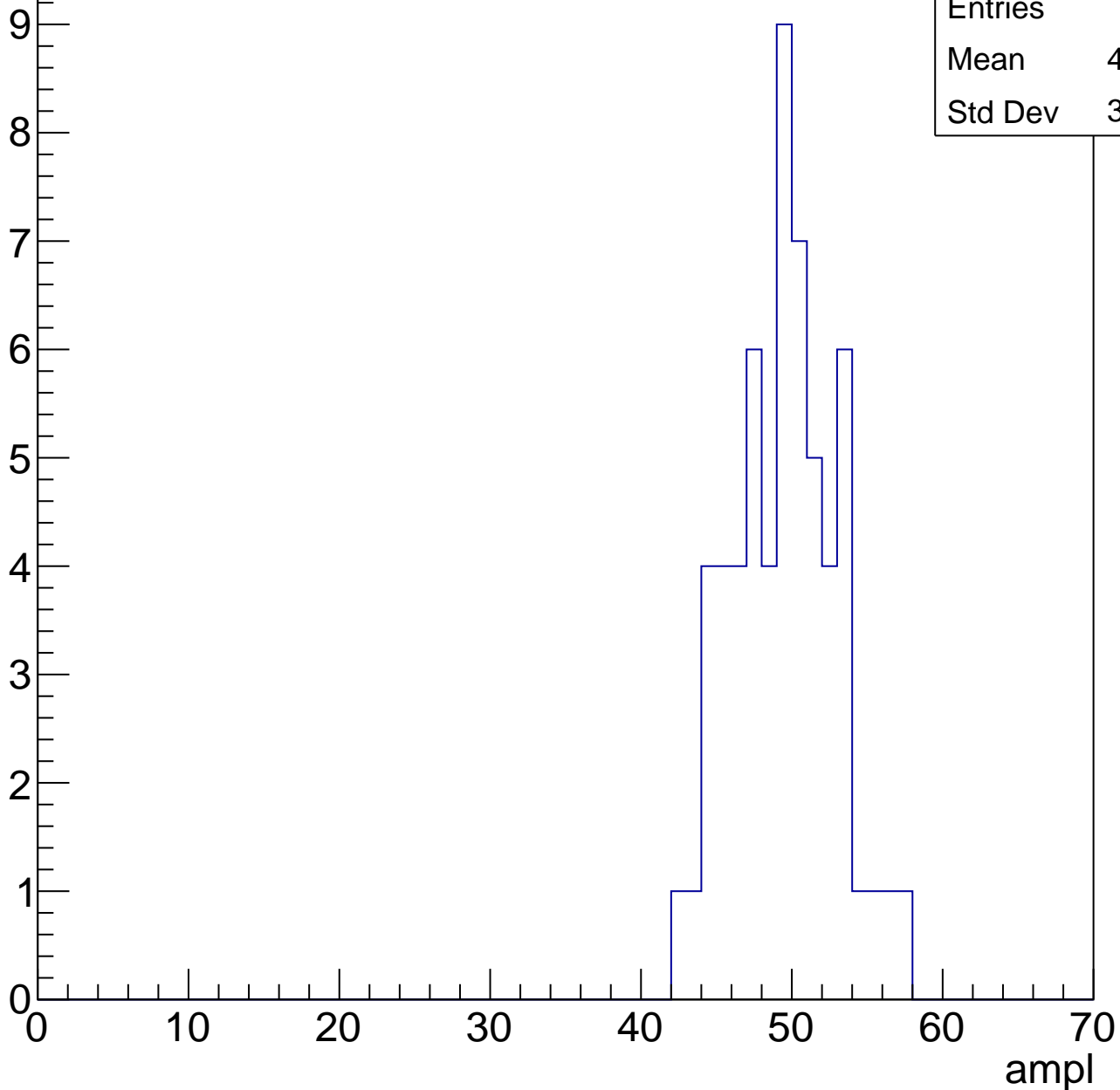


B1L100S, U25-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

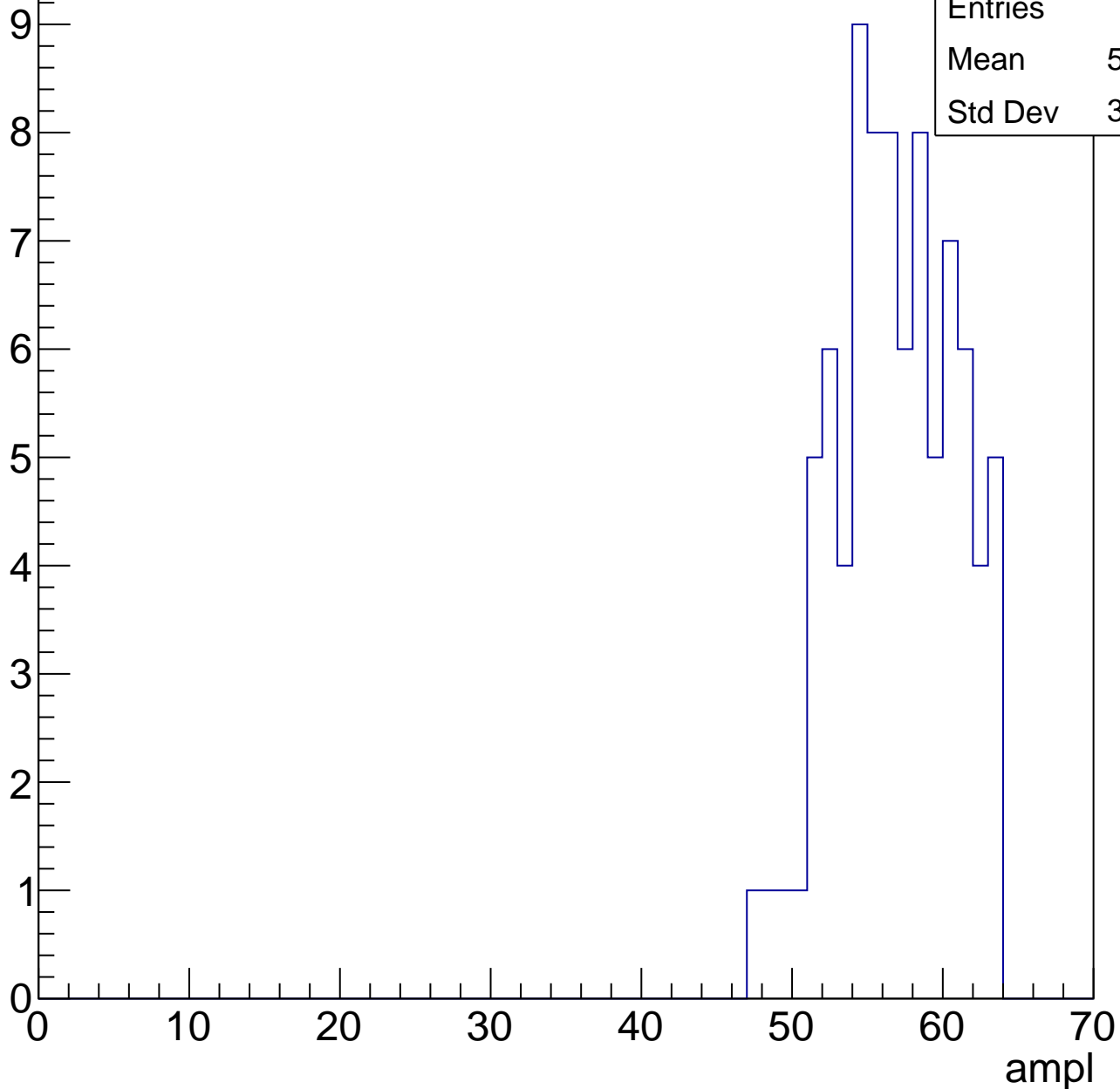
Entries	59
Mean	49.03
Std Dev	3.309



B1L100S, U25-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



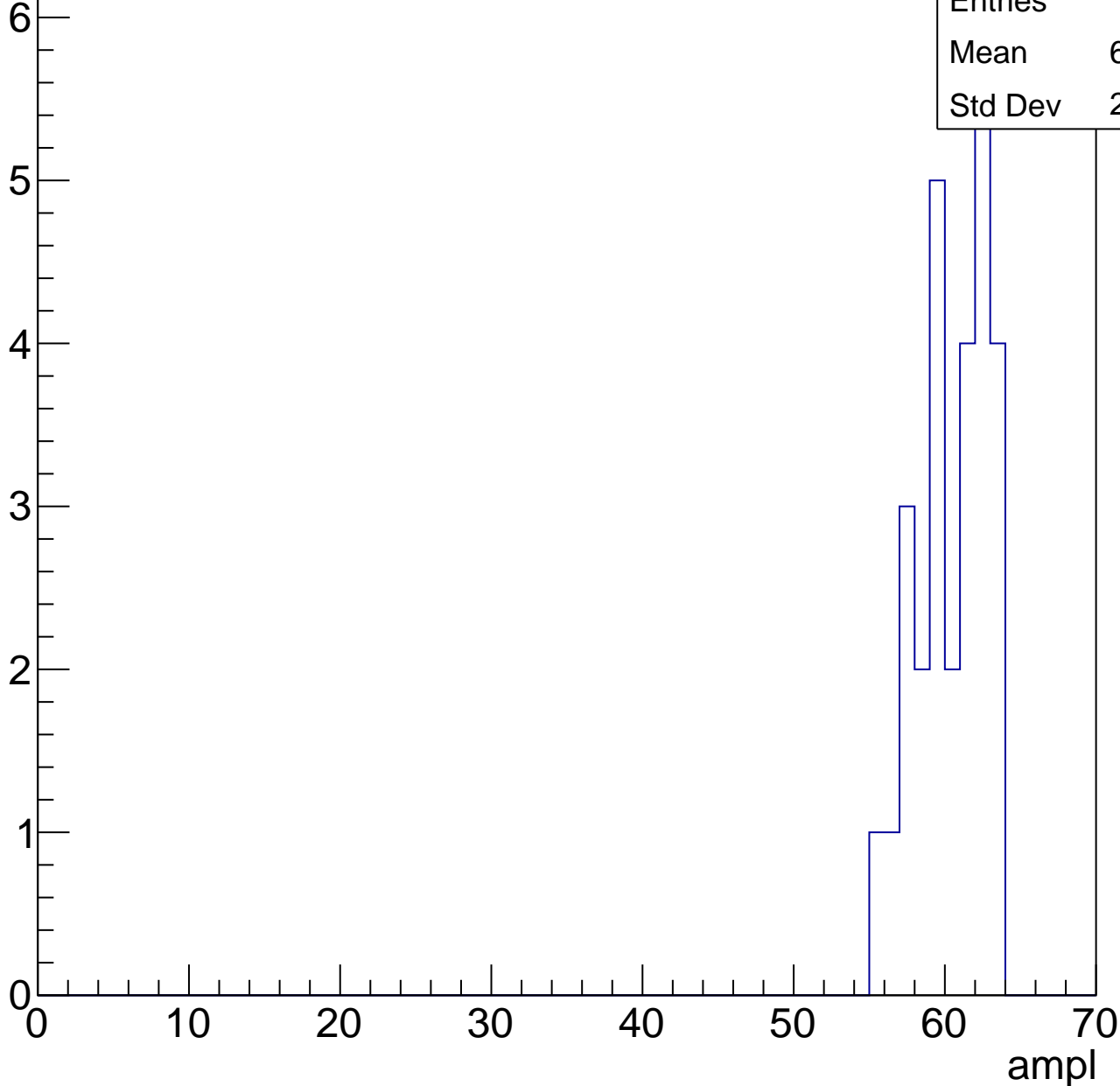
Entries	85
Mean	56.44
Std Dev	3.833

B1L100S, U25-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

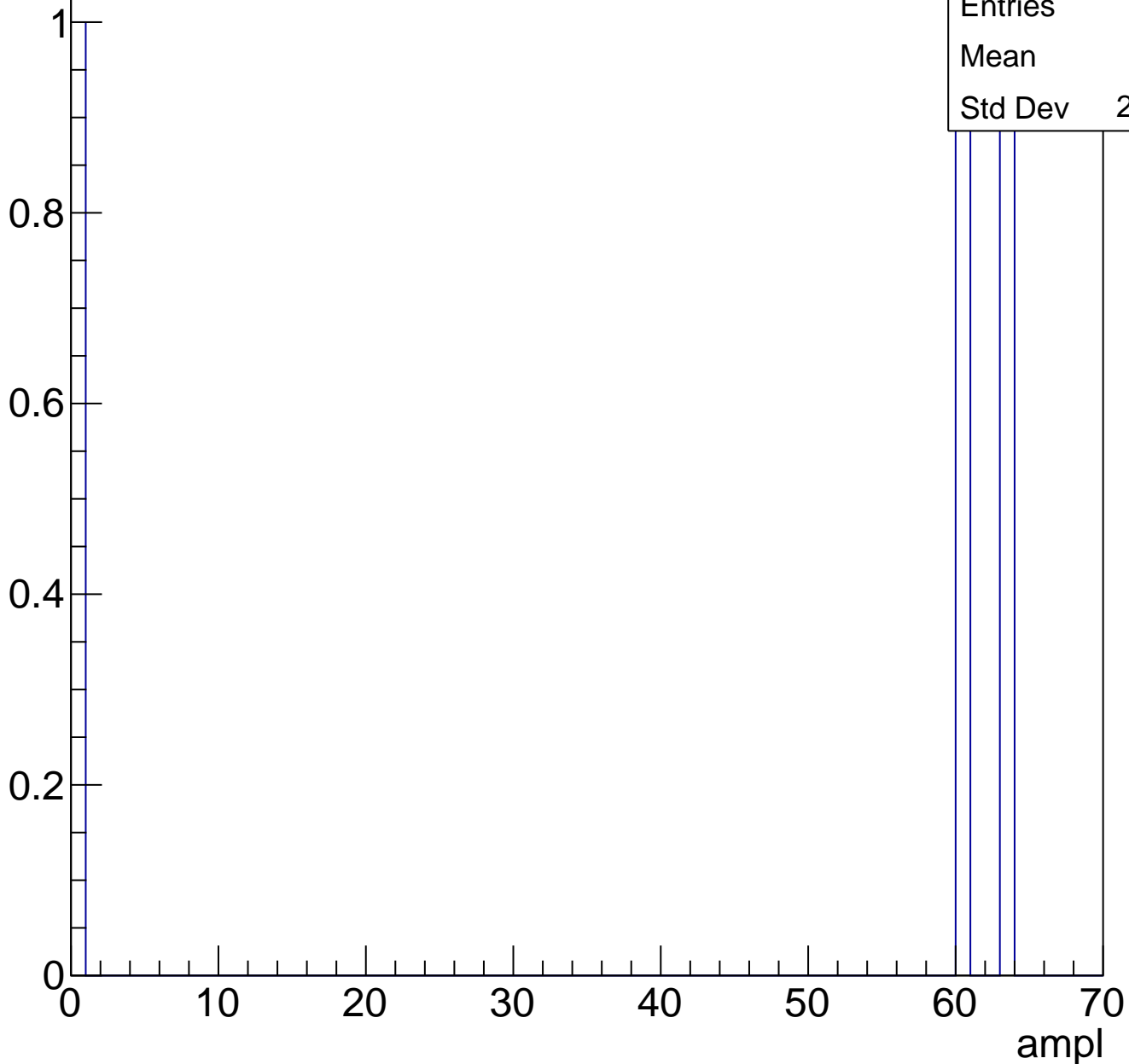
Entries	28
Mean	60.04
Std Dev	2.275



B1L100S, U25-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch34, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	28.55
Std Dev	5.495

Gaus mean : 29.7497

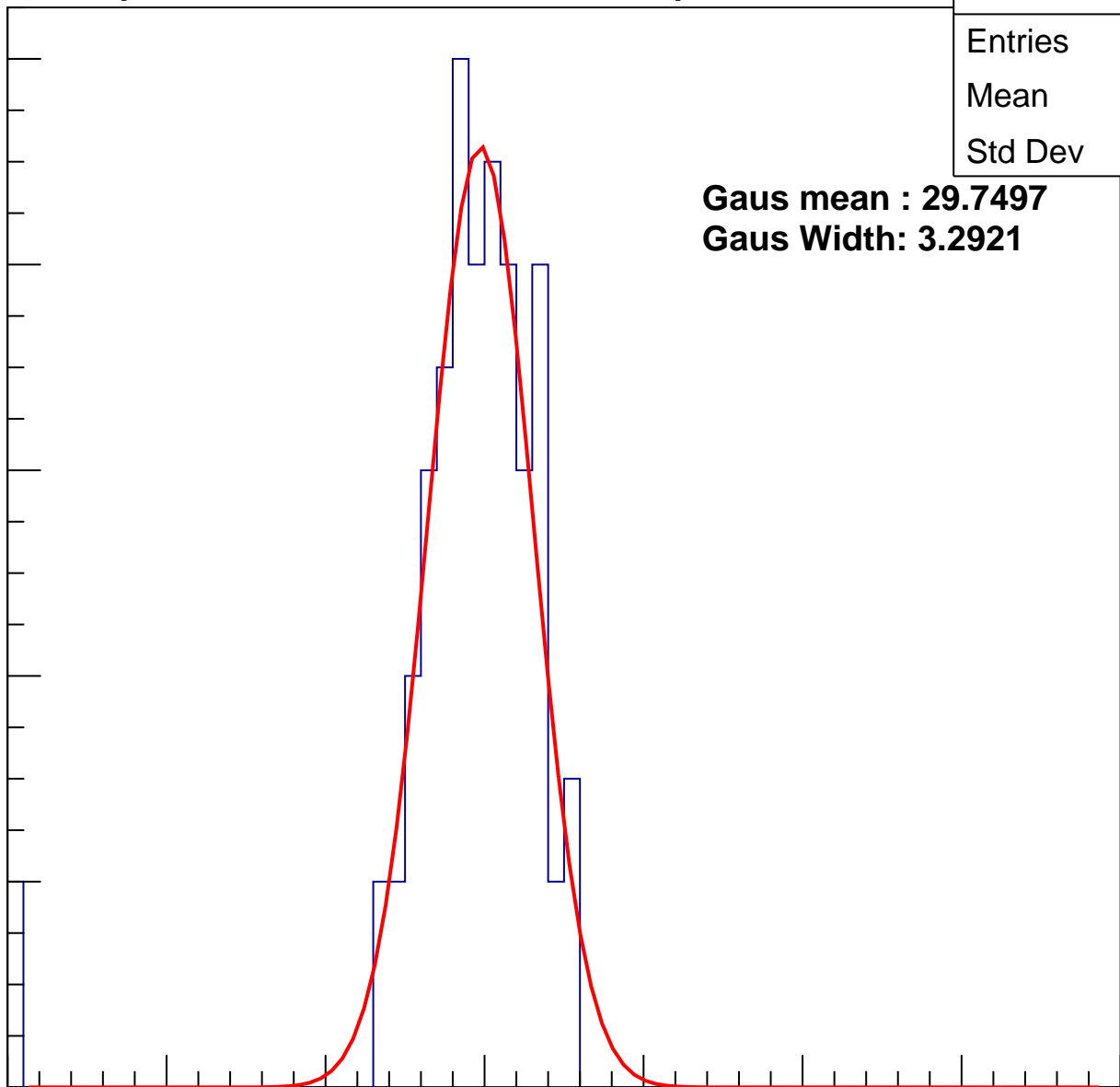
Gaus Width: 3.2921

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch34, adc1

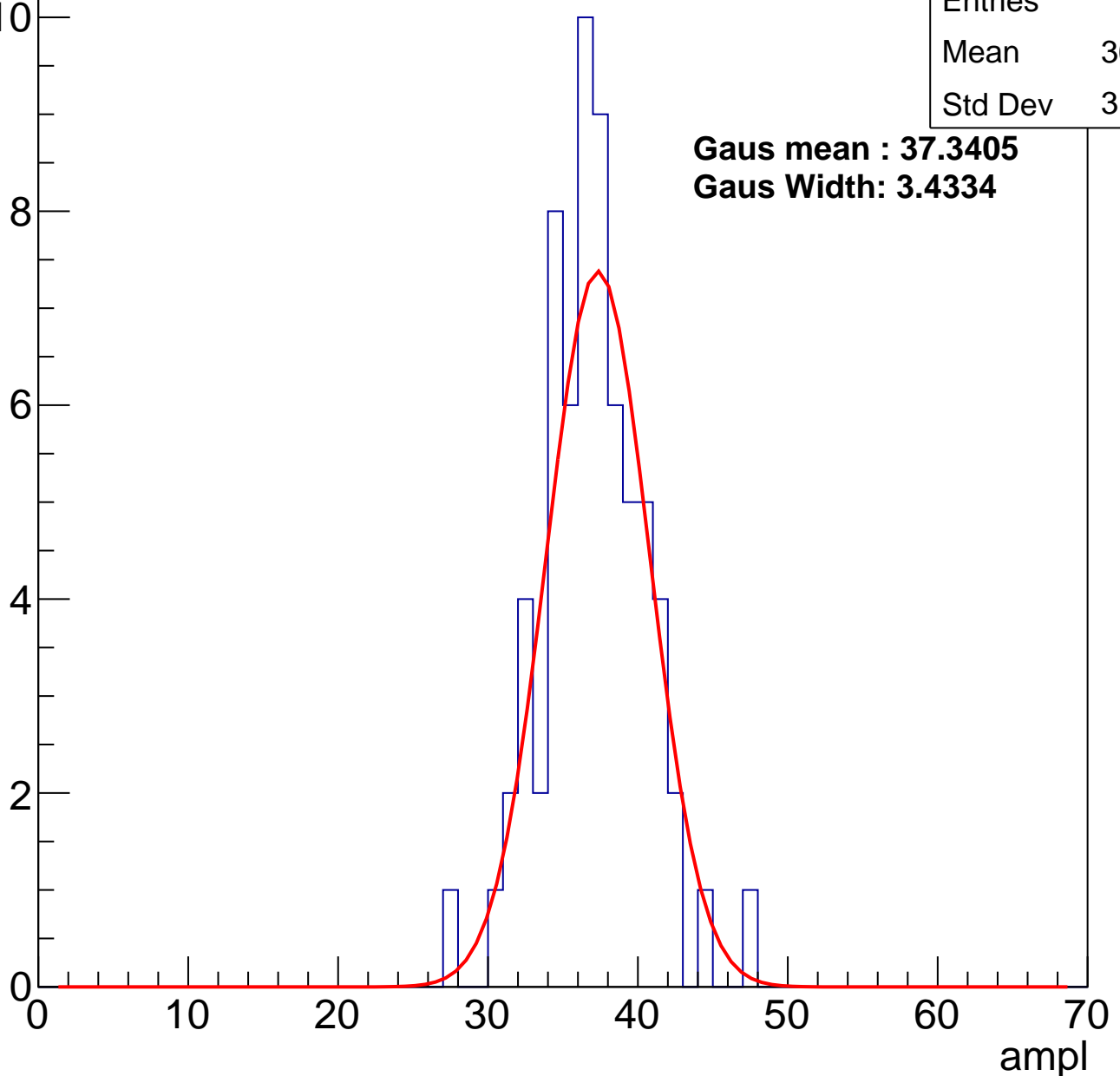
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.57
Std Dev	3.413

Gaus mean : 37.3405

Gaus Width: 3.4334



B1L100S, U25-ch34, adc2

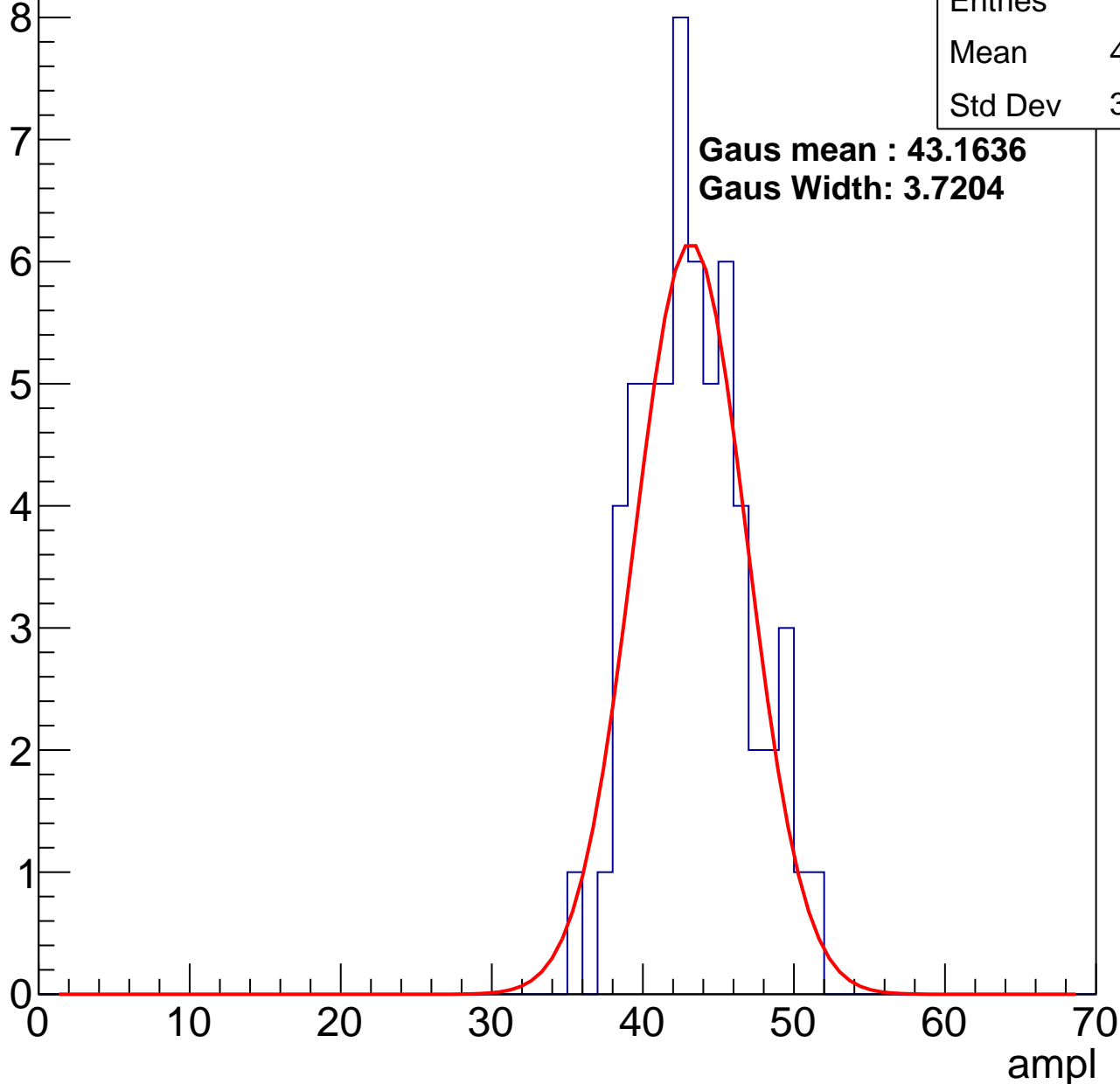
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.88
Std Dev	3.494

Gaus mean : 43.1636

Gaus Width: 3.7204

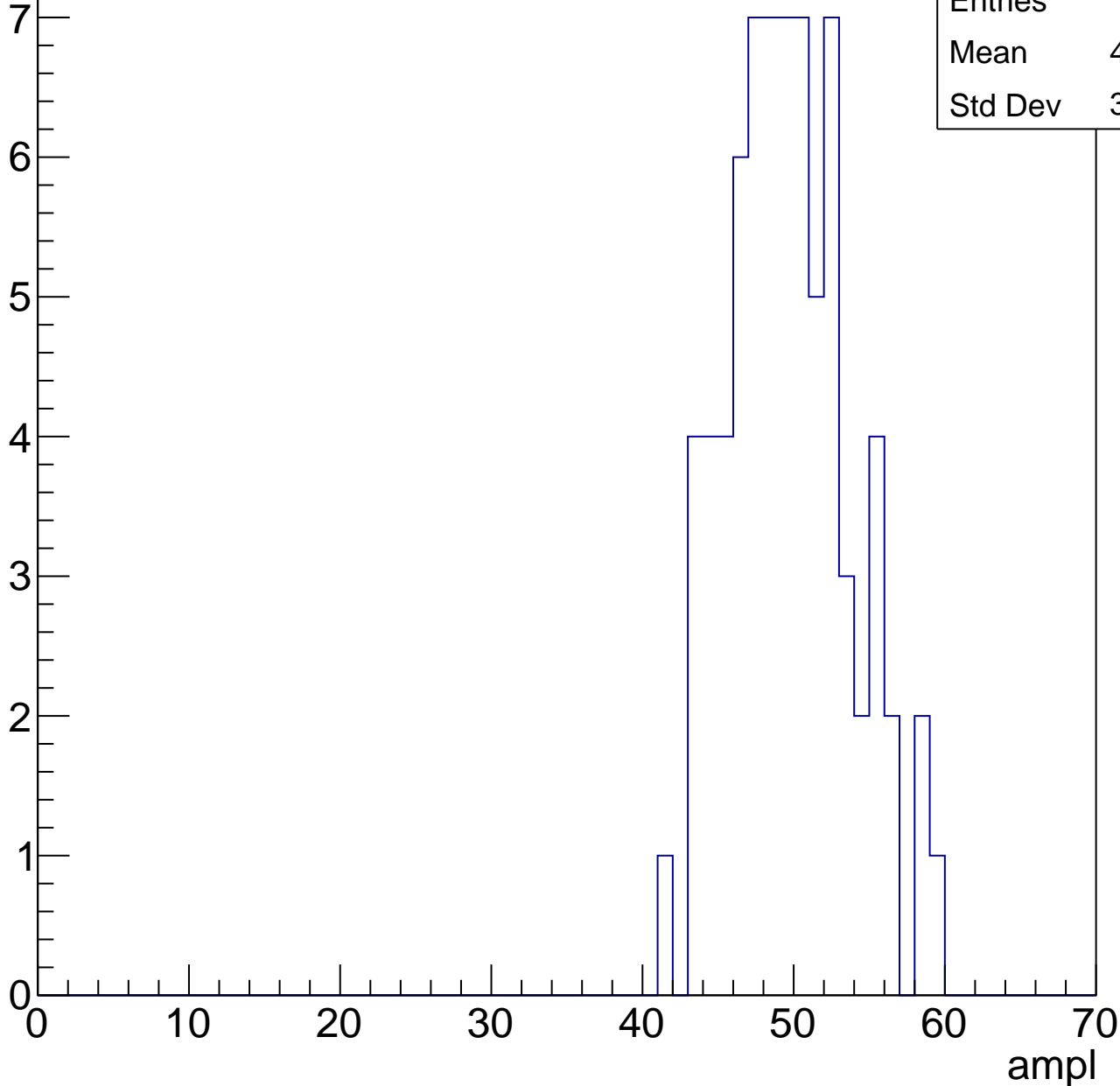


B1L100S, U25-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.26
Std Dev	3.966

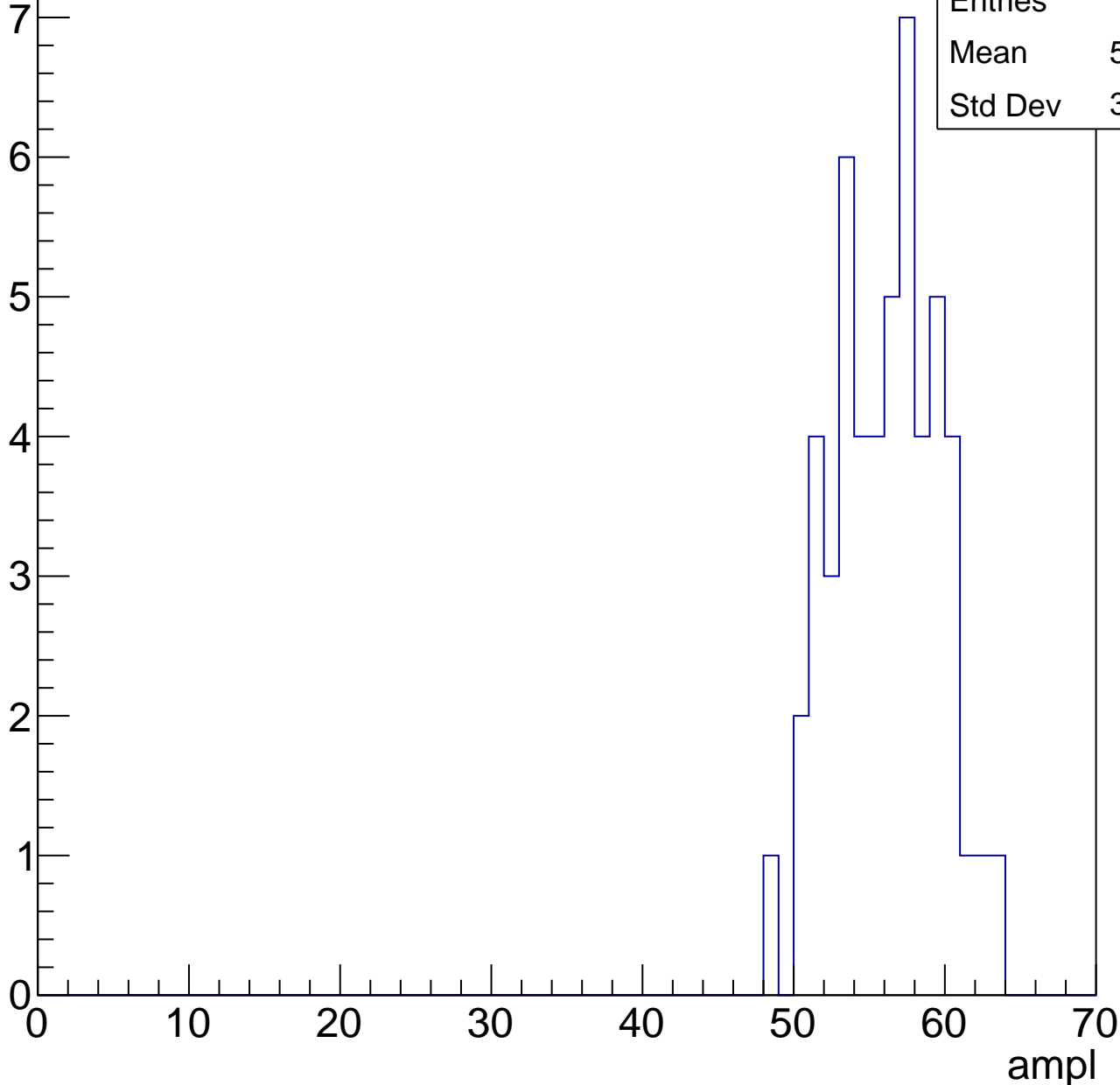


B1L100S, U25-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.65
Std Dev	3.379



B1L100S, U25-ch34, adc5

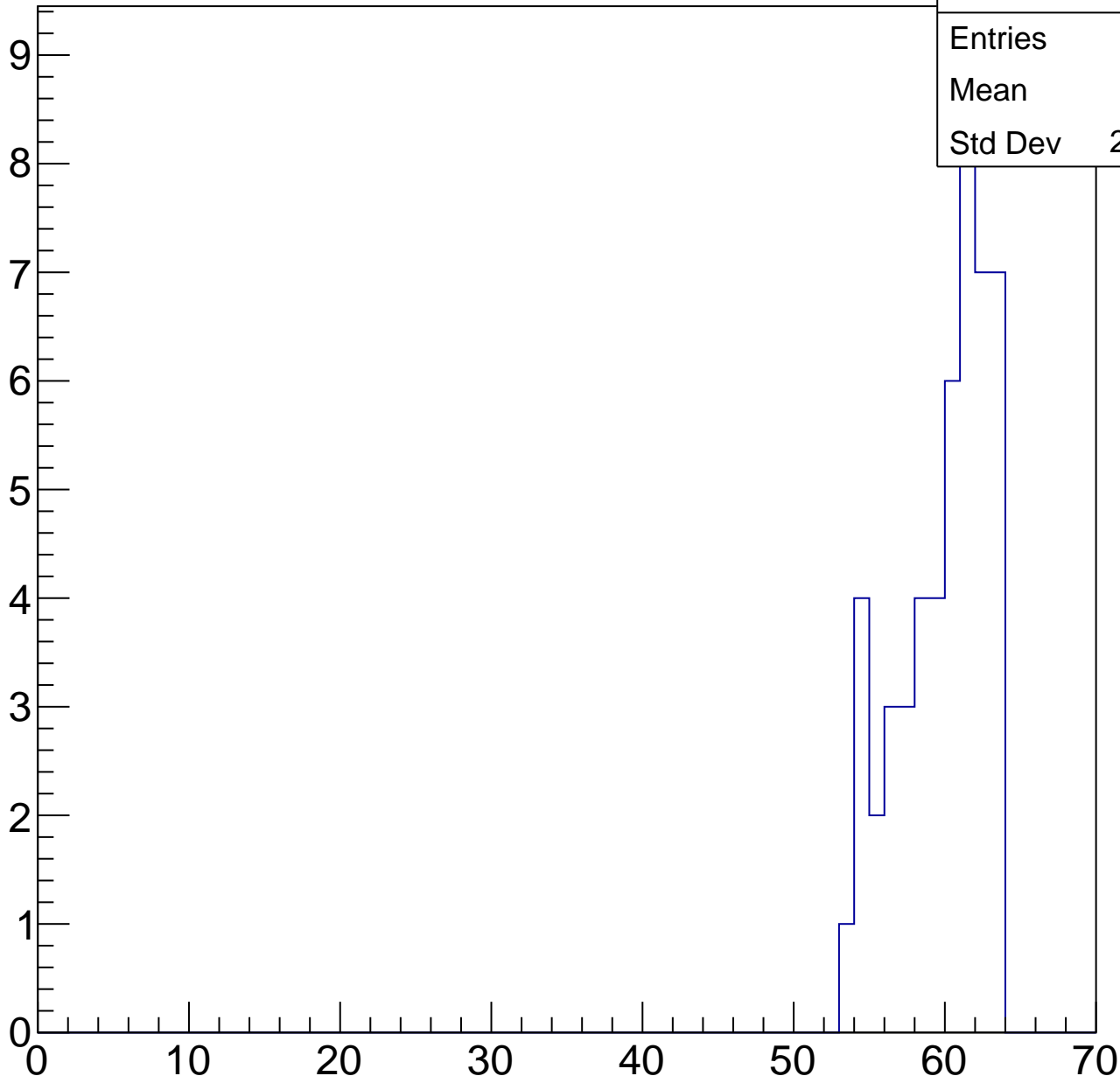
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.4
Std Dev	2.898

ampl



B1L100S, U25-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch35, adc0

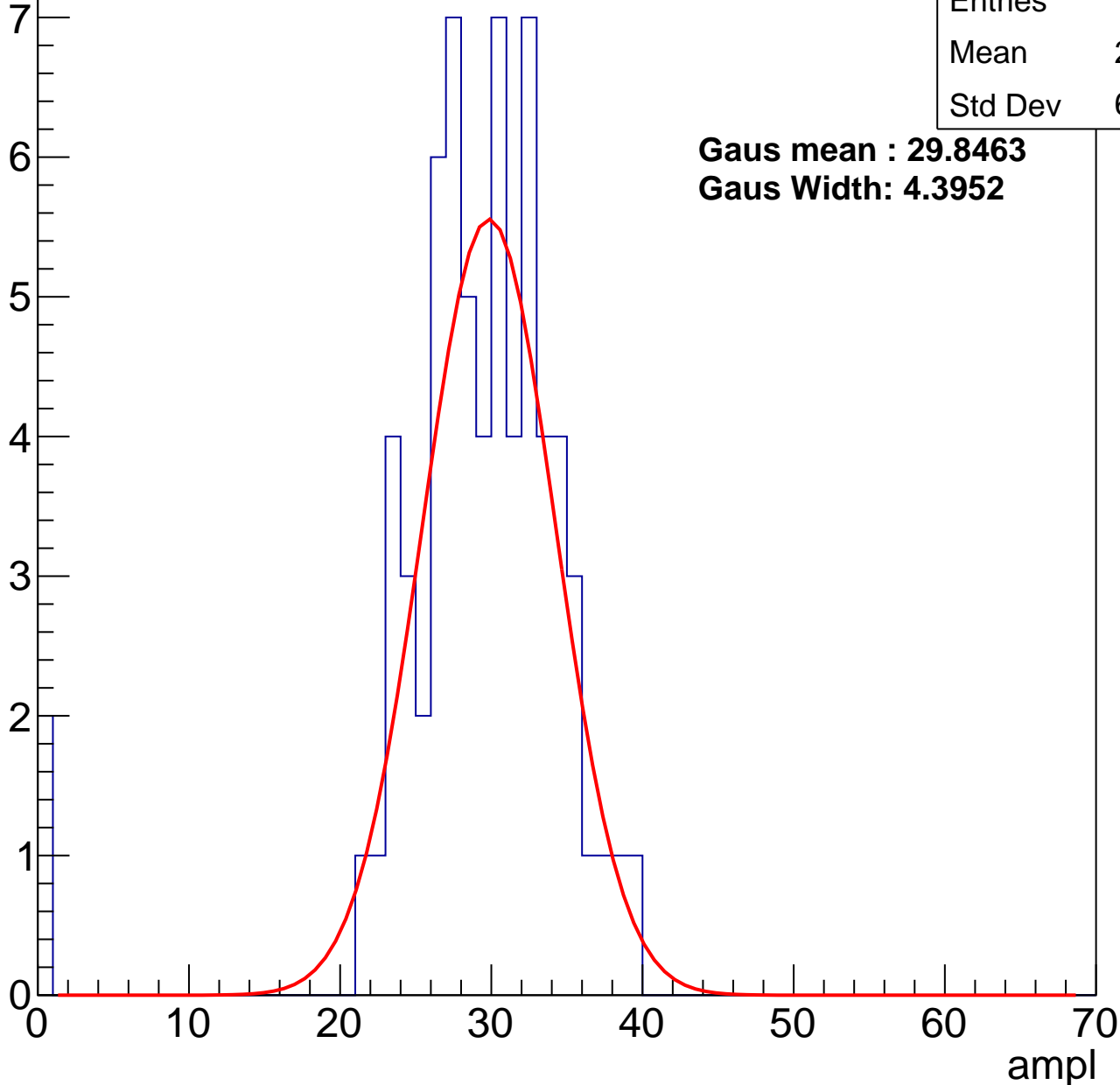
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.51
Std Dev	6.381

Gaus mean : 29.8463

Gaus Width: 4.3952



B1L100S, U25-ch35, adc1

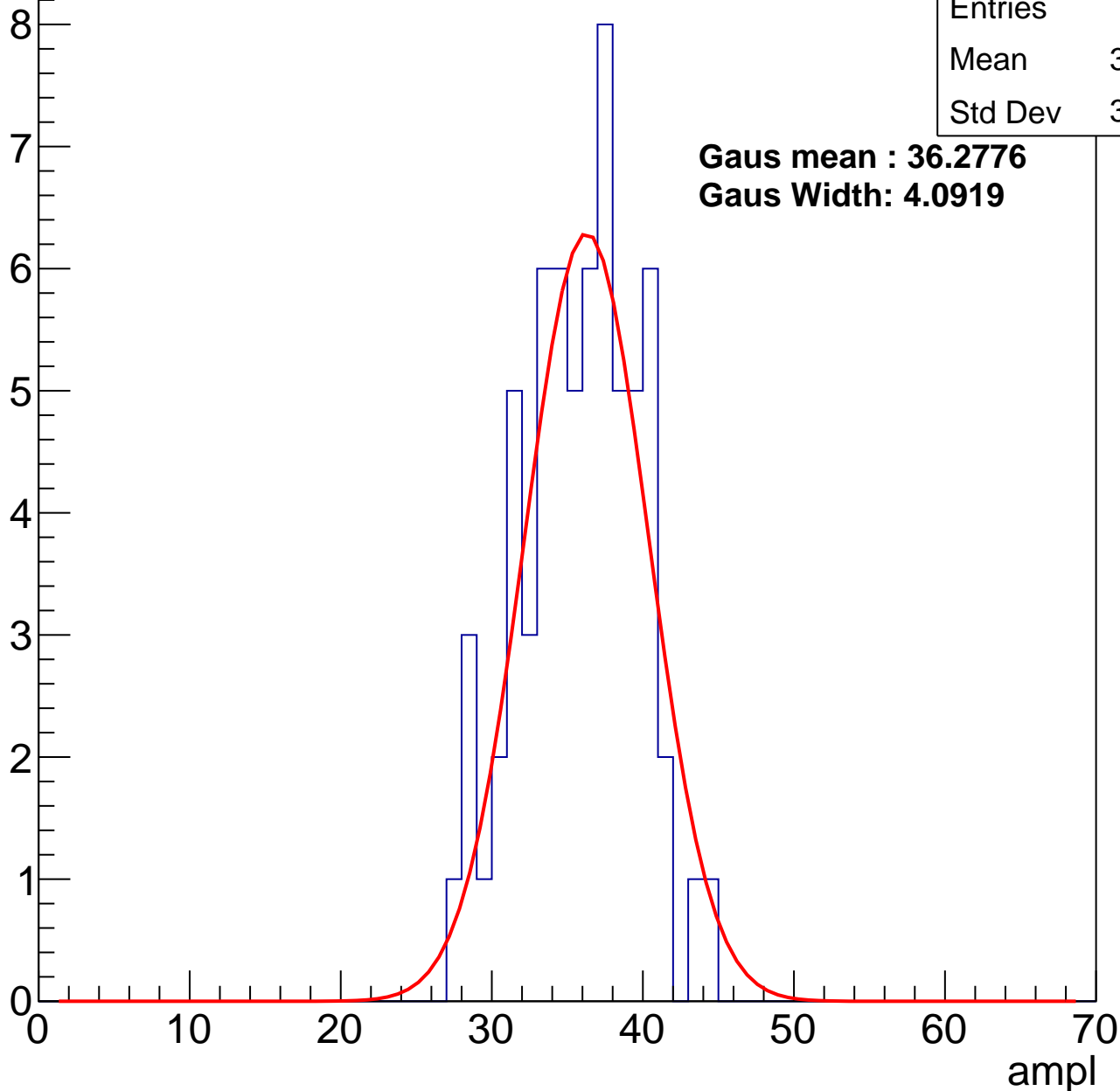
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	35.36
Std Dev	3.804

Gaus mean : 36.2776

Gaus Width: 4.0919



B1L100S, U25-ch35, adc2

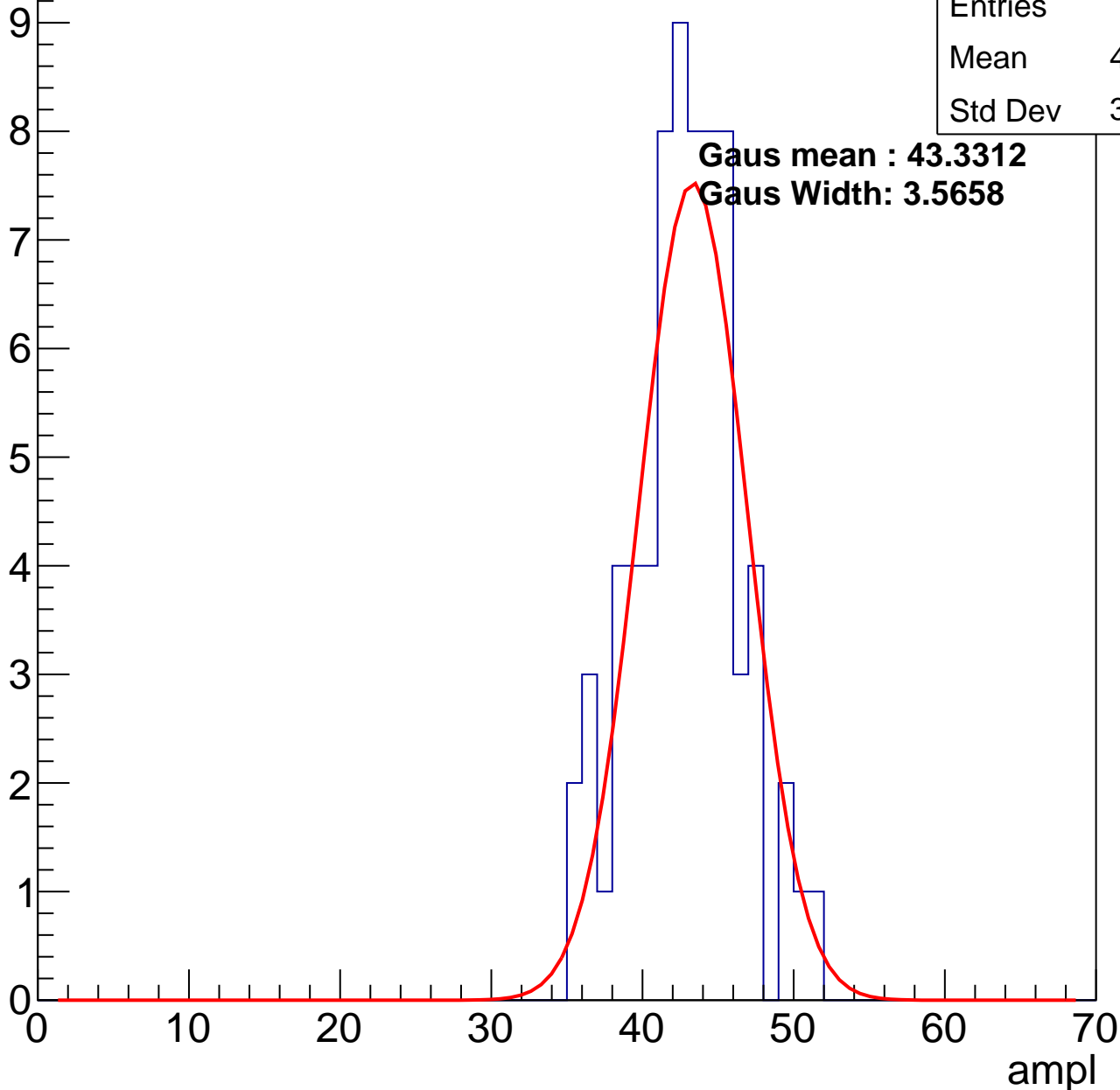
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.43
Std Dev	3.475

Gaus mean : 43.3312

Gaus Width: 3.5658



B1L100S, U25-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	49.45
Std Dev	3.187

Entry

10

8

6

4

2

0

0

10

20

30

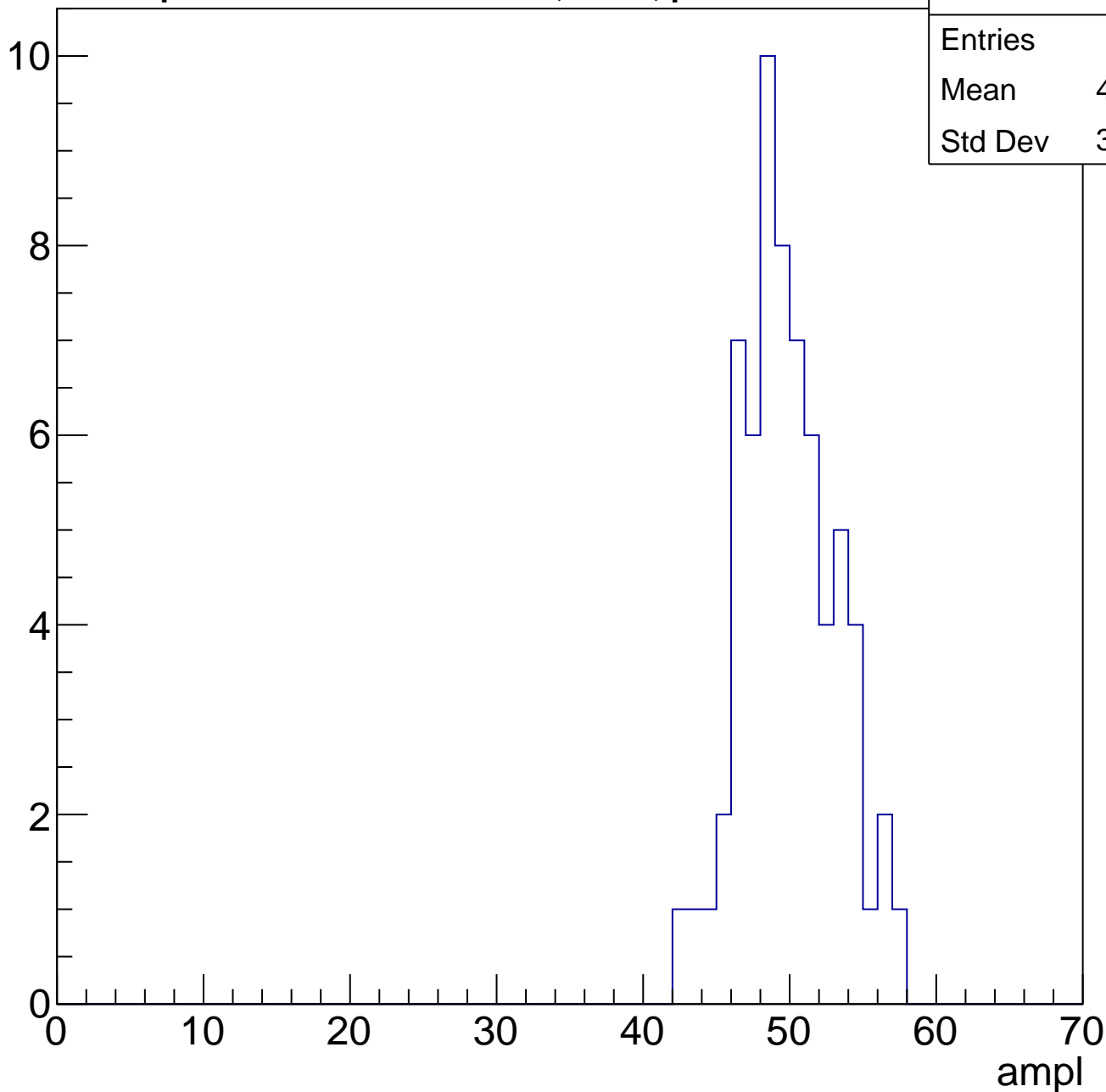
40

50

60

70

ampl

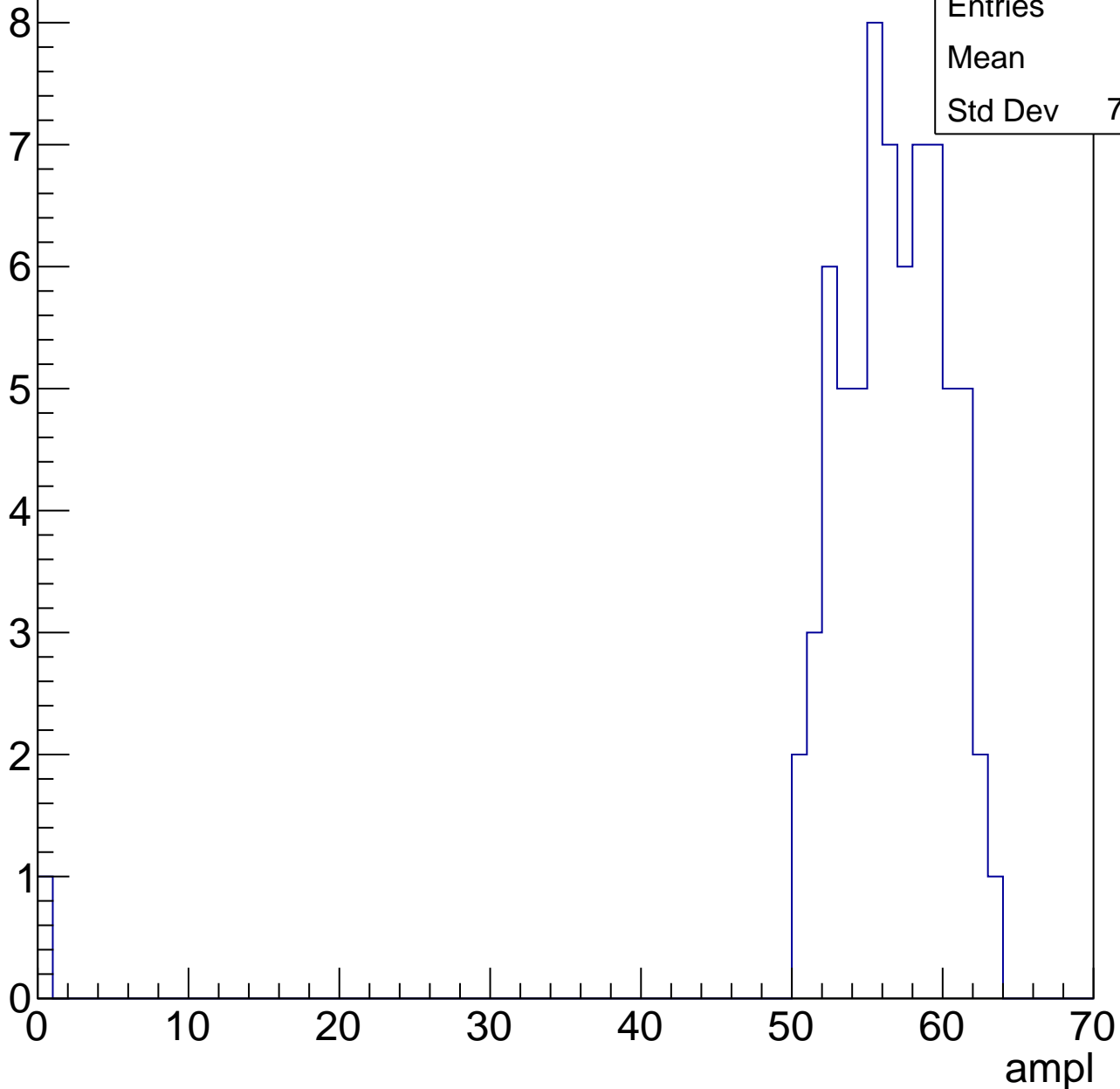


B1L100S, U25-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	55.5
Std Dev	7.423

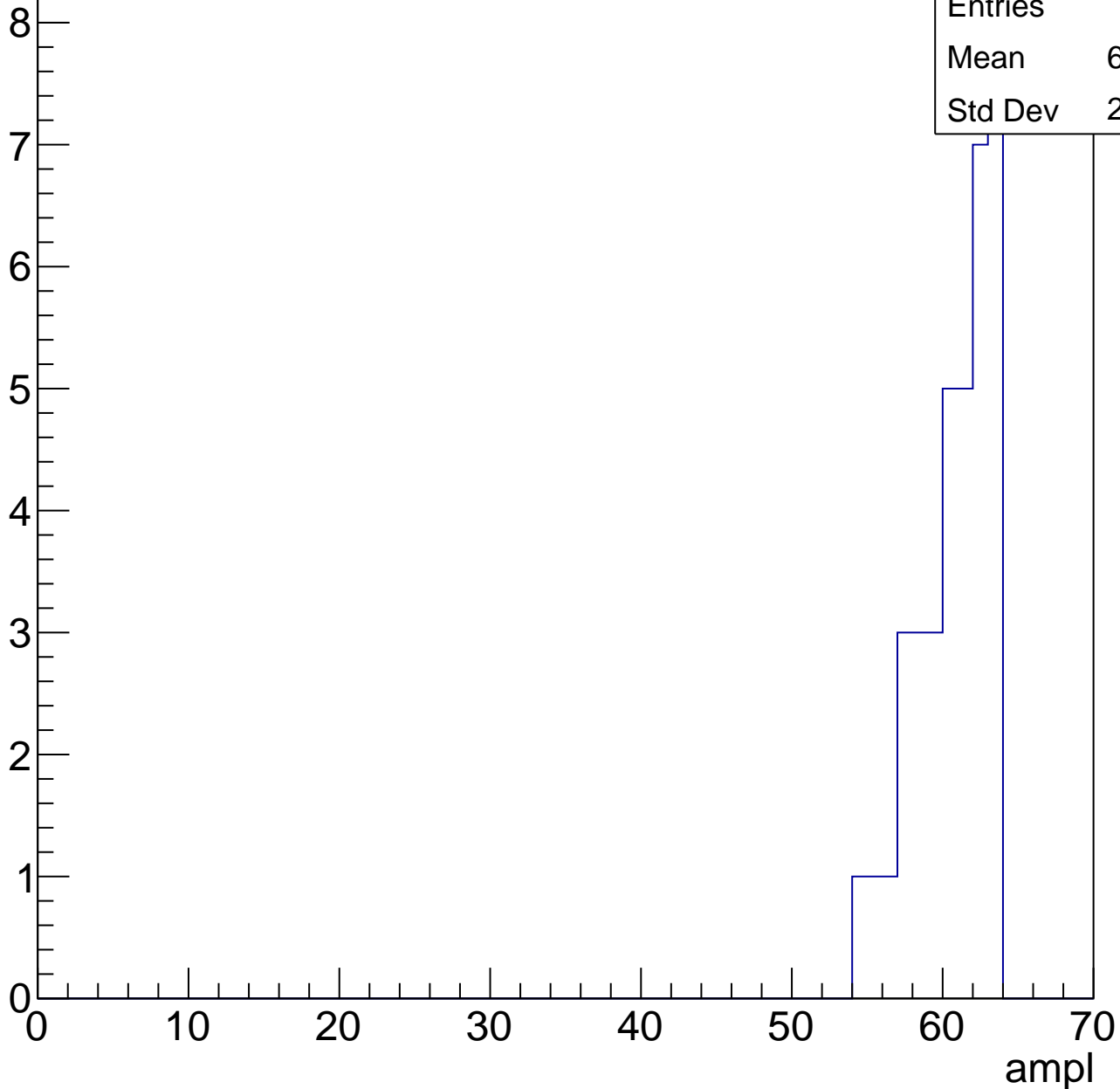


B1L100S, U25-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	60.27
Std Dev	2.446



B1L100S, U25-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch36, adc0

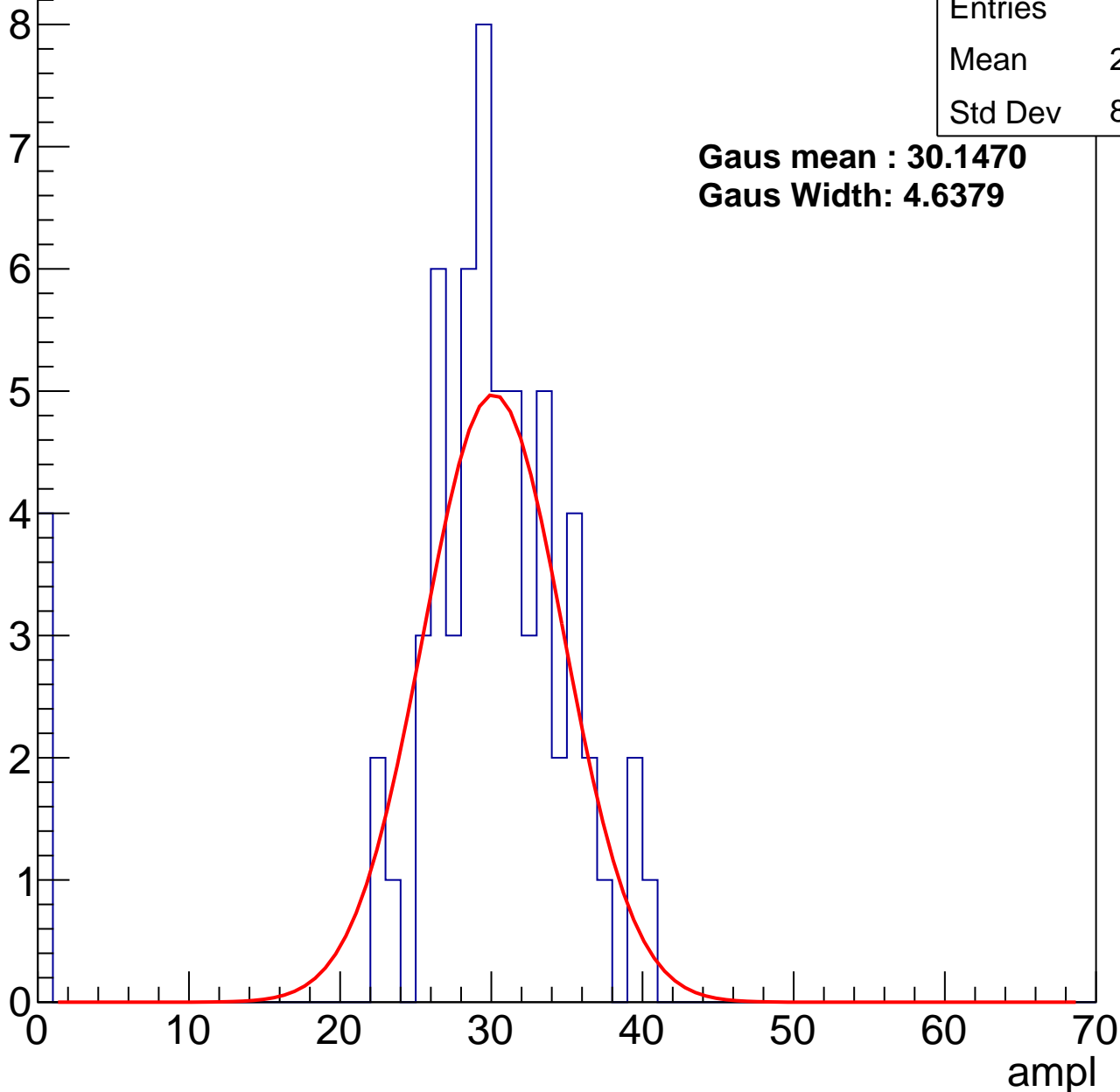
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.25
Std Dev	8.349

Gaus mean : 30.1470

Gaus Width: 4.6379

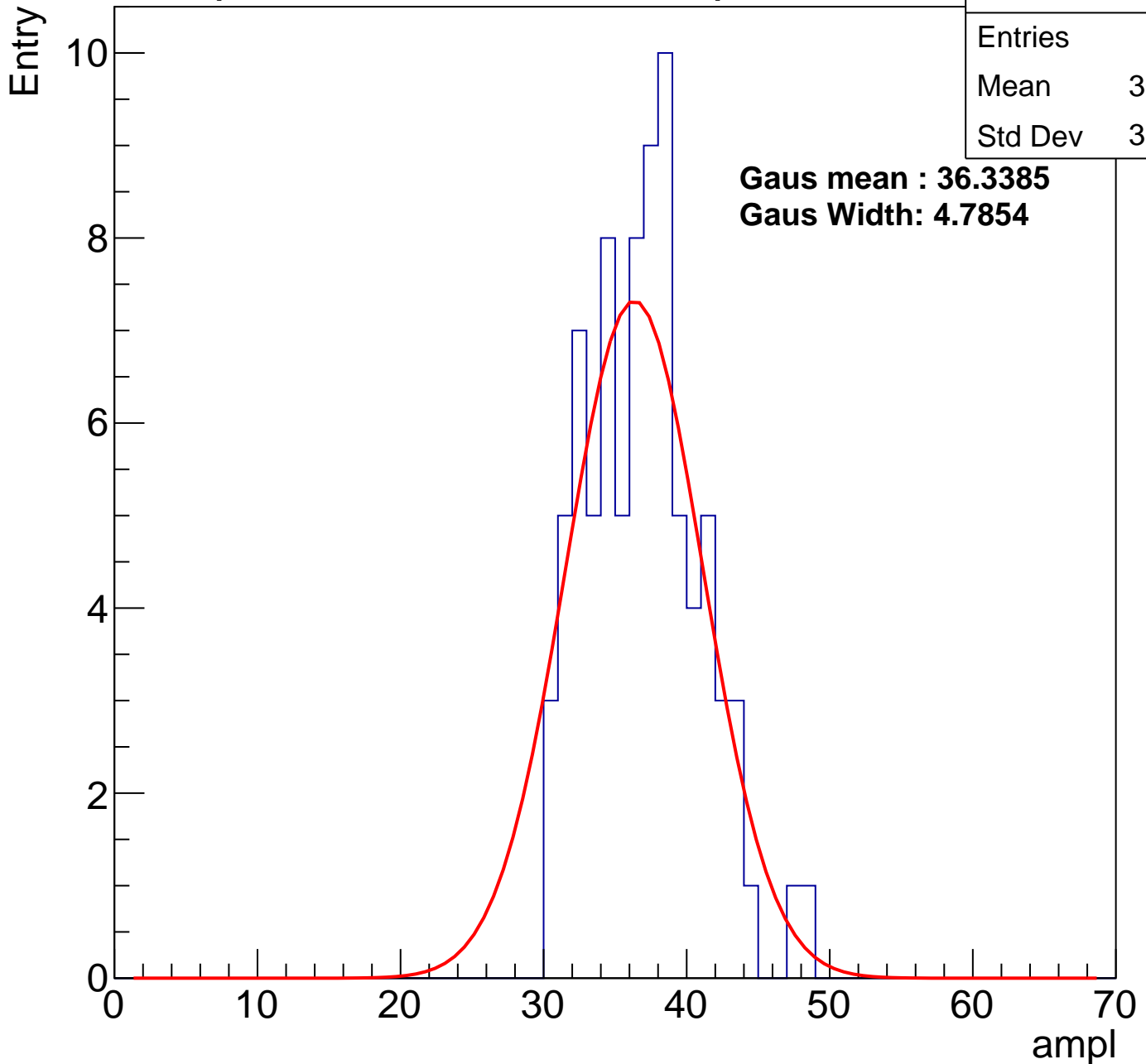


B1L100S, U25-ch36, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	36.58
Std Dev	3.906

Gaus mean : 36.3385
Gaus Width: 4.7854



B1L100S, U25-ch36, adc2

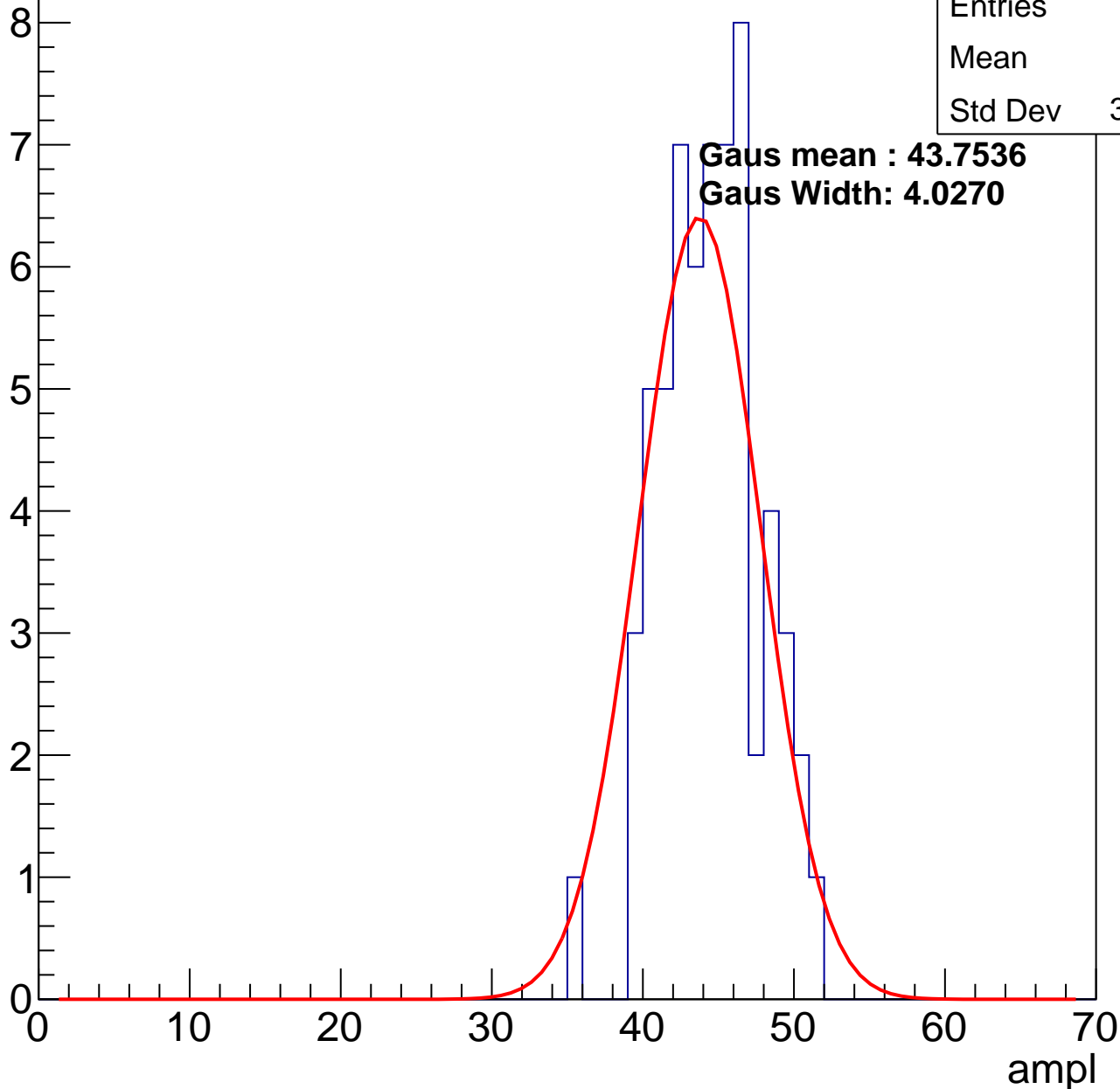
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44
Std Dev	3.219

Gaus mean : 43.7536

Gaus Width: 4.0270



B1L100S, U25-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	50.88
Std Dev	3.367

Entry

10

8

6

4

2

0

0

10

20

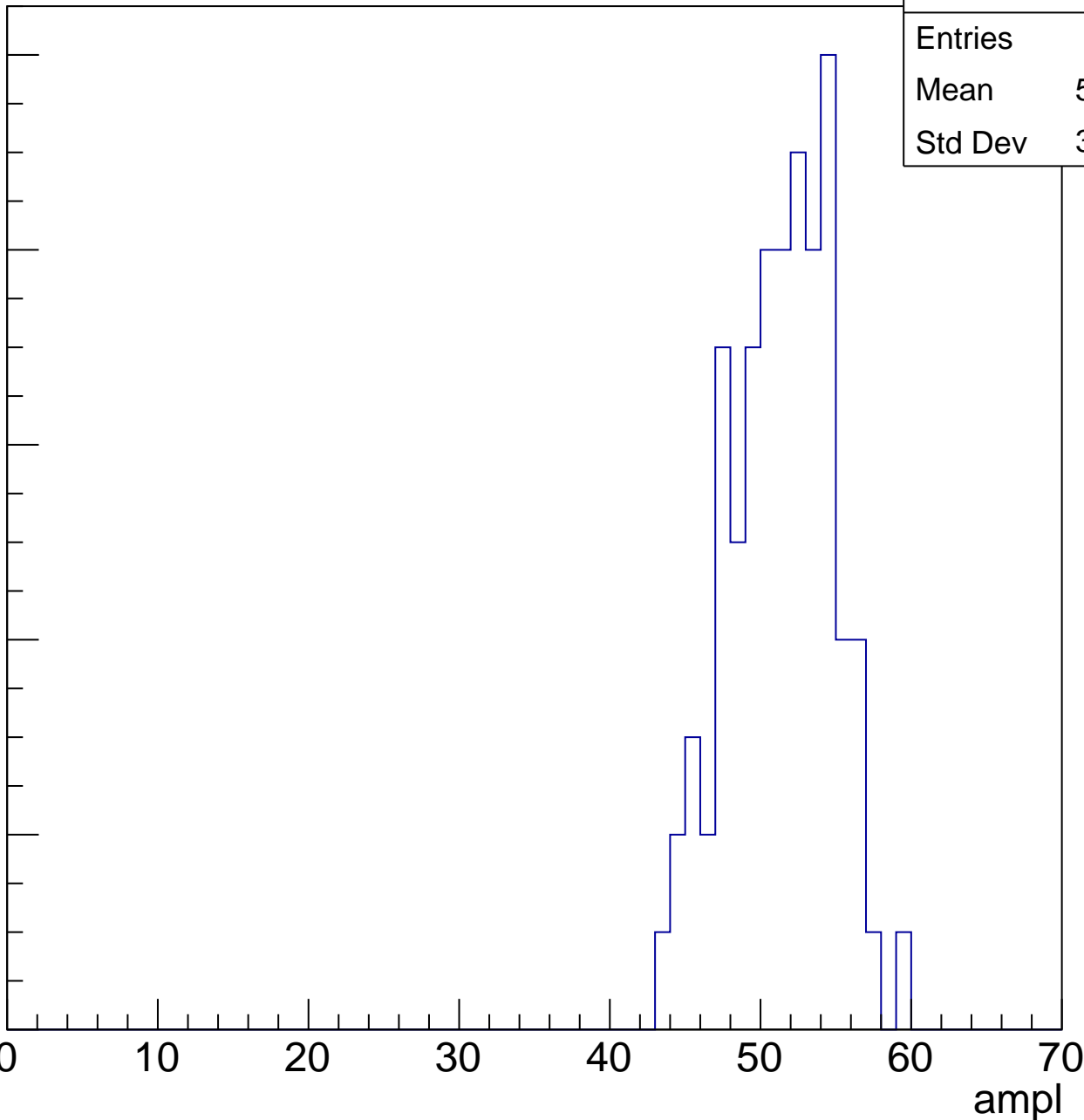
30

40

50

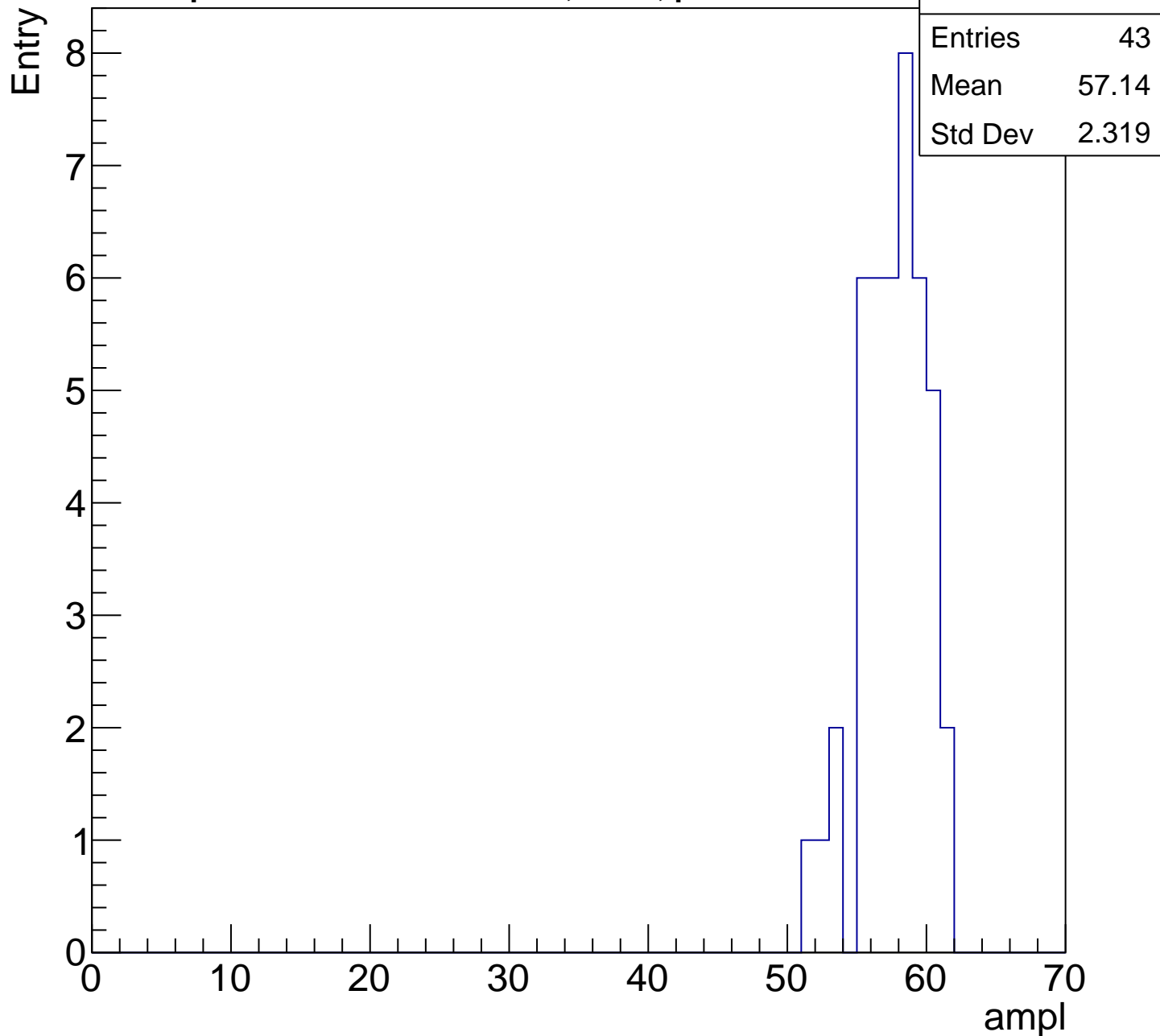
60

ampl



B1L100S, U25-ch36, adc4

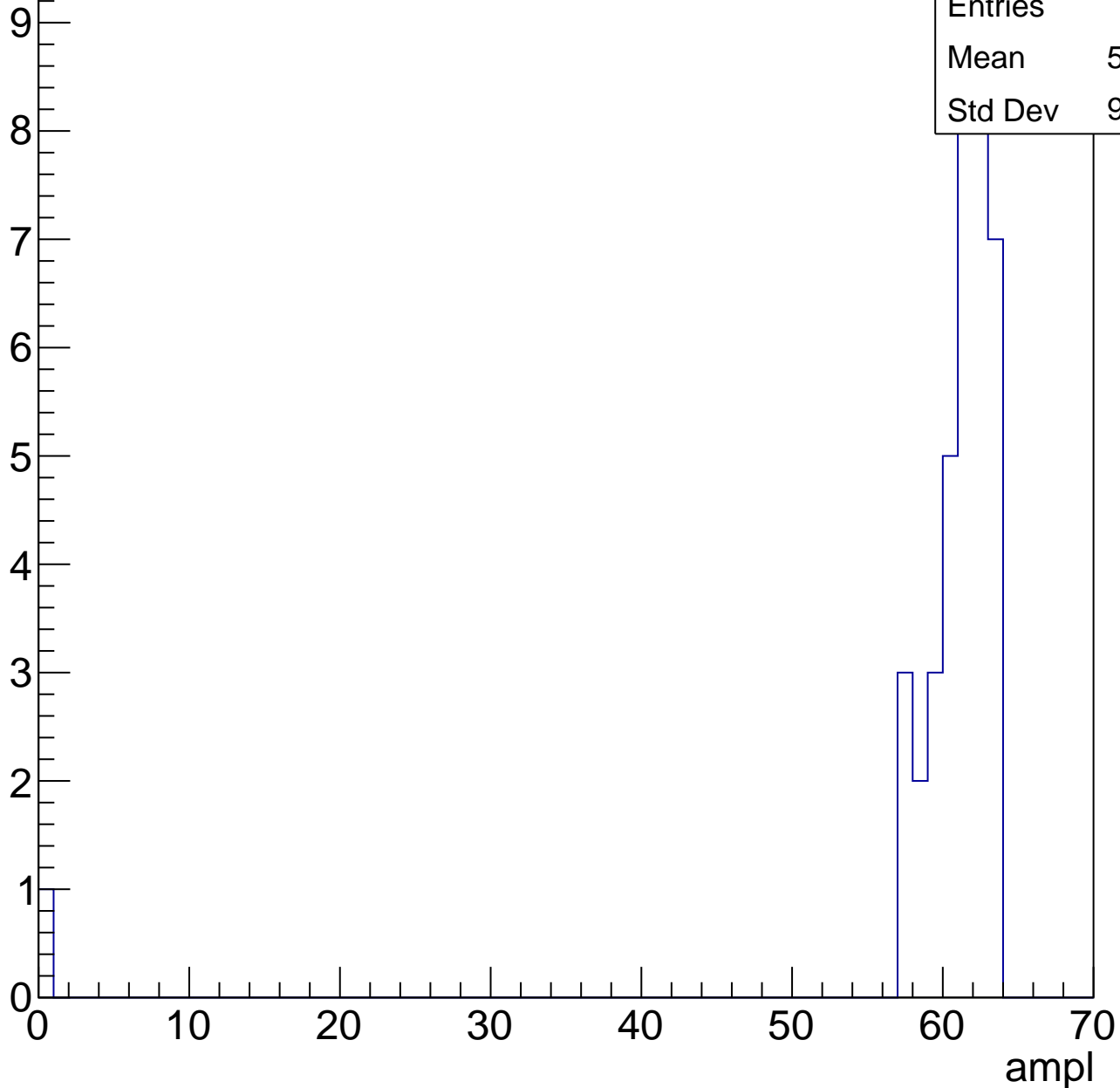
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch37, adc0

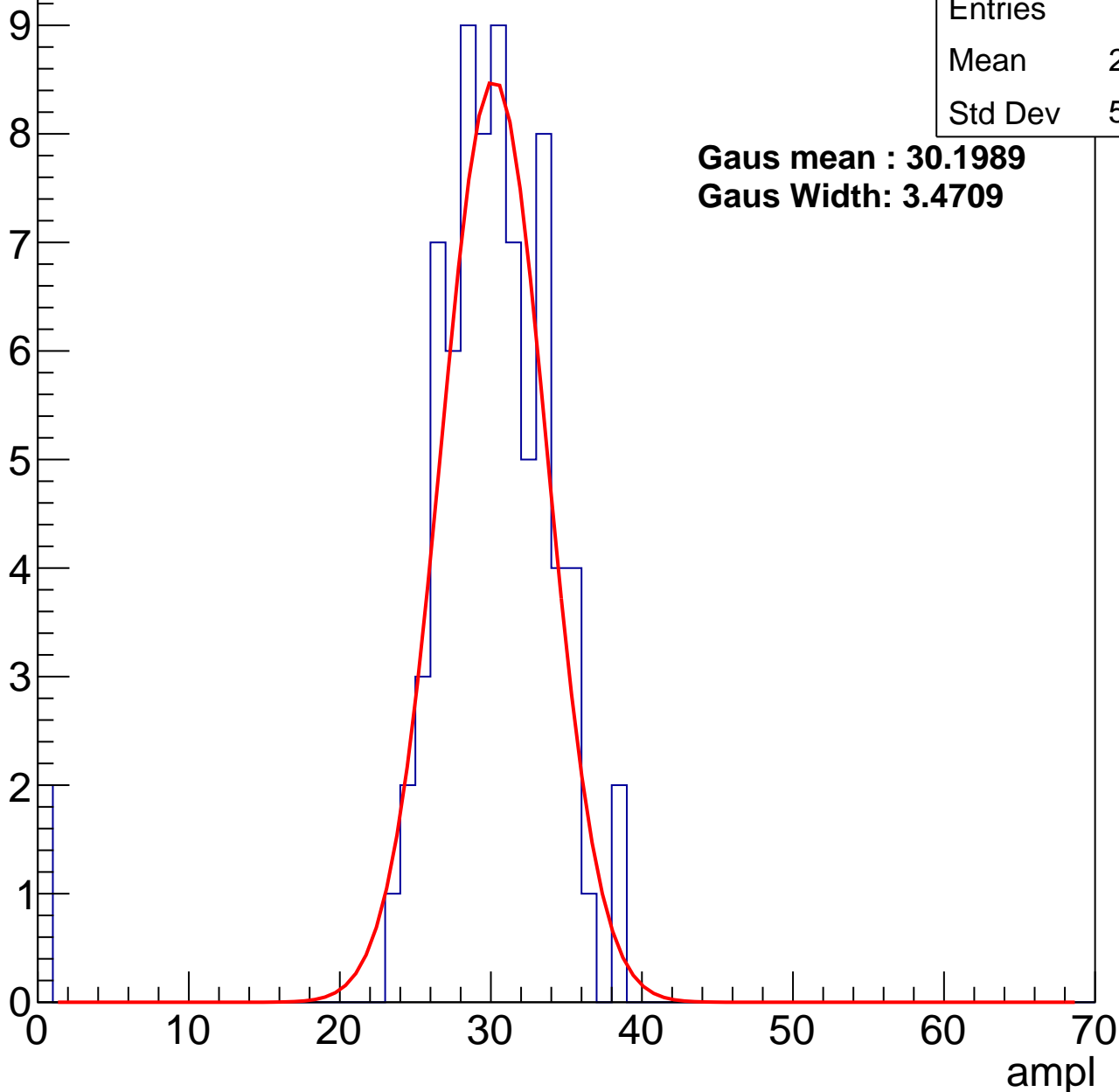
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.14
Std Dev	5.746

Gaus mean : 30.1989

Gaus Width: 3.4709



B1L100S, U25-ch37, adc1

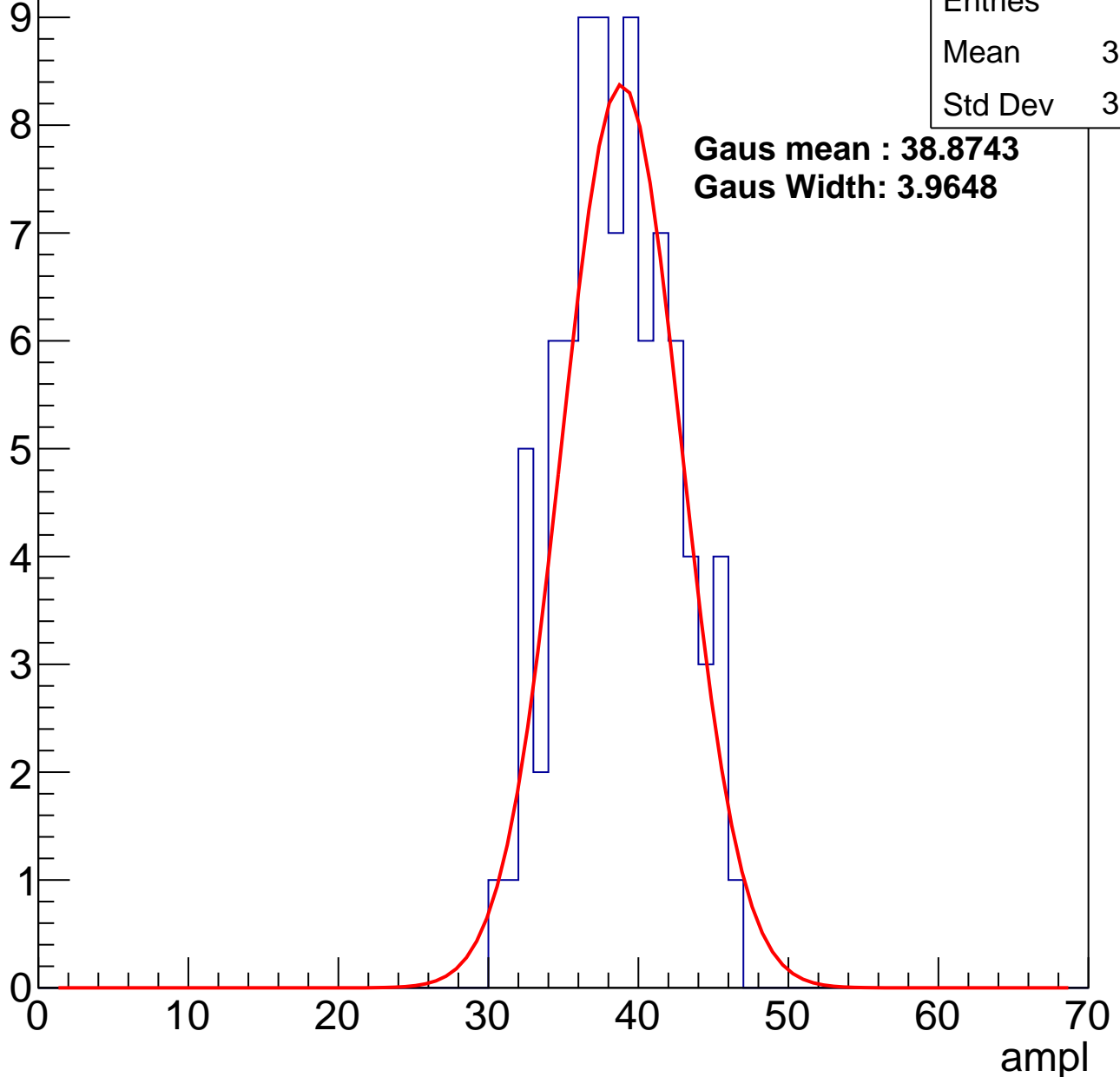
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	38.19
Std Dev	3.728

Gaus mean : 38.8743

Gaus Width: 3.9648



B1L100S, U25-ch37, adc2

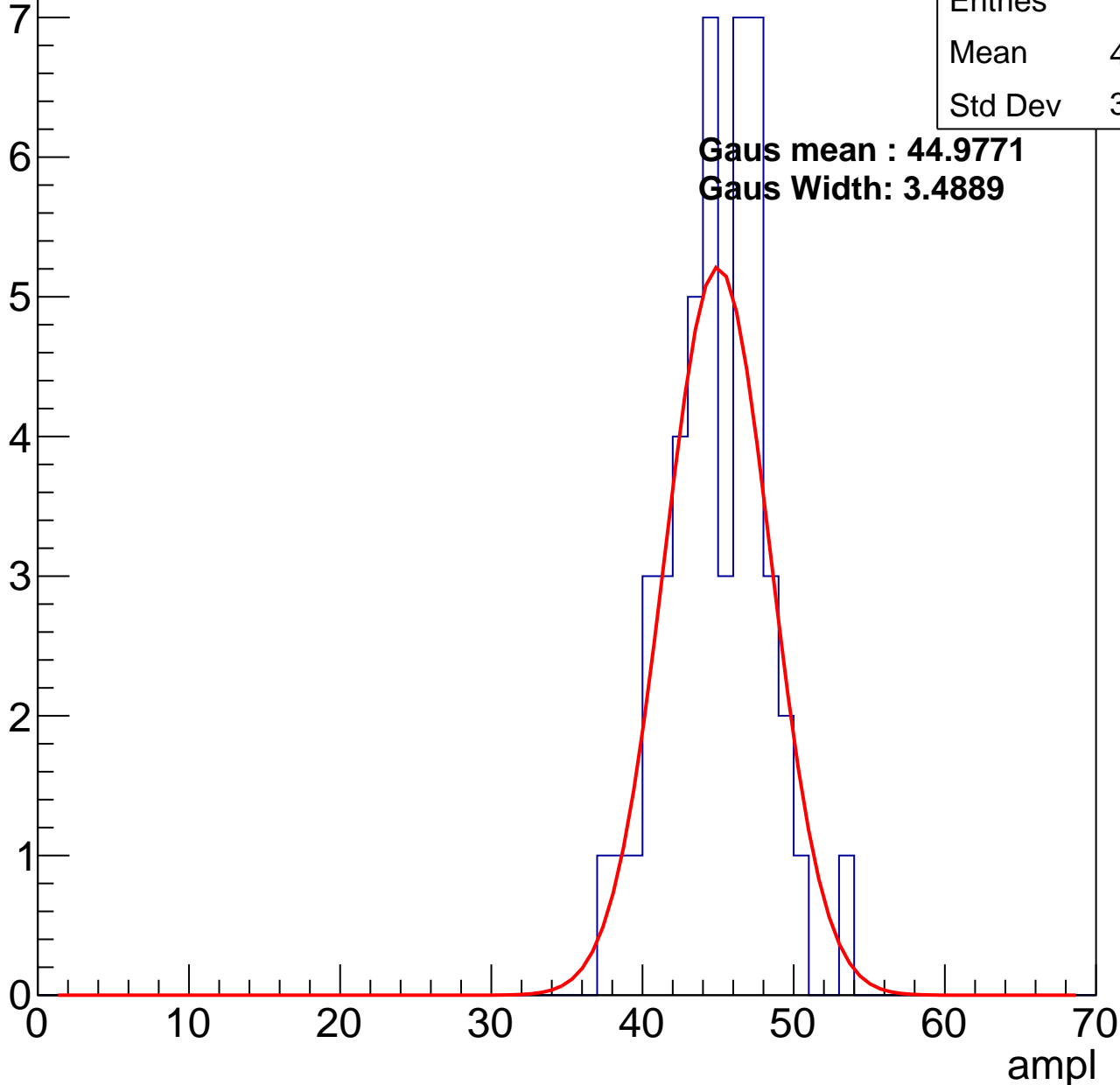
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	44.47
Std Dev	3.208

Gaus mean : 44.9771

Gaus Width: 3.4889



B1L100S, U25-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	51.28
Std Dev	3.563

Entry

10

8

6

4

2

0

0

10

20

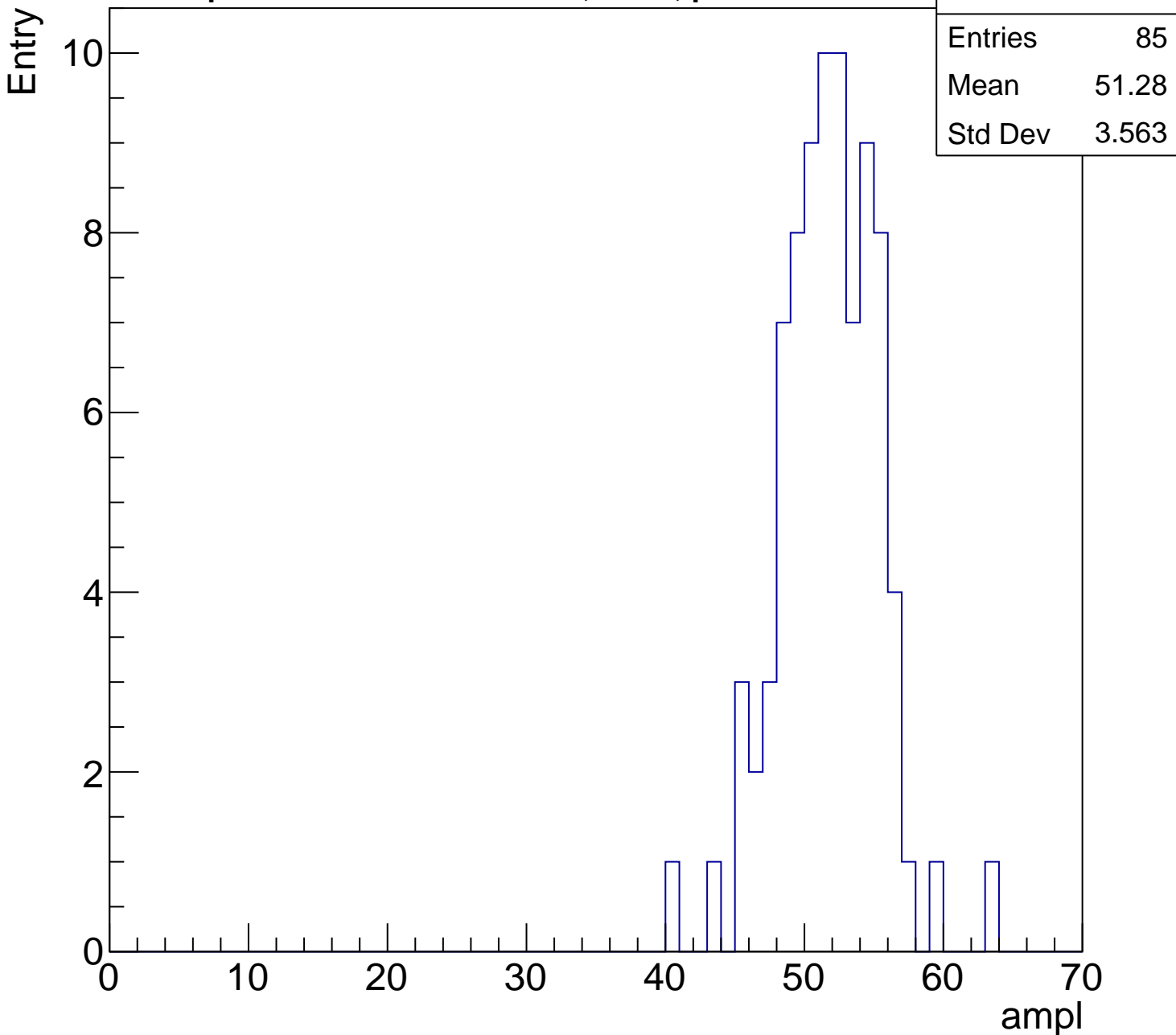
30

40

50

60

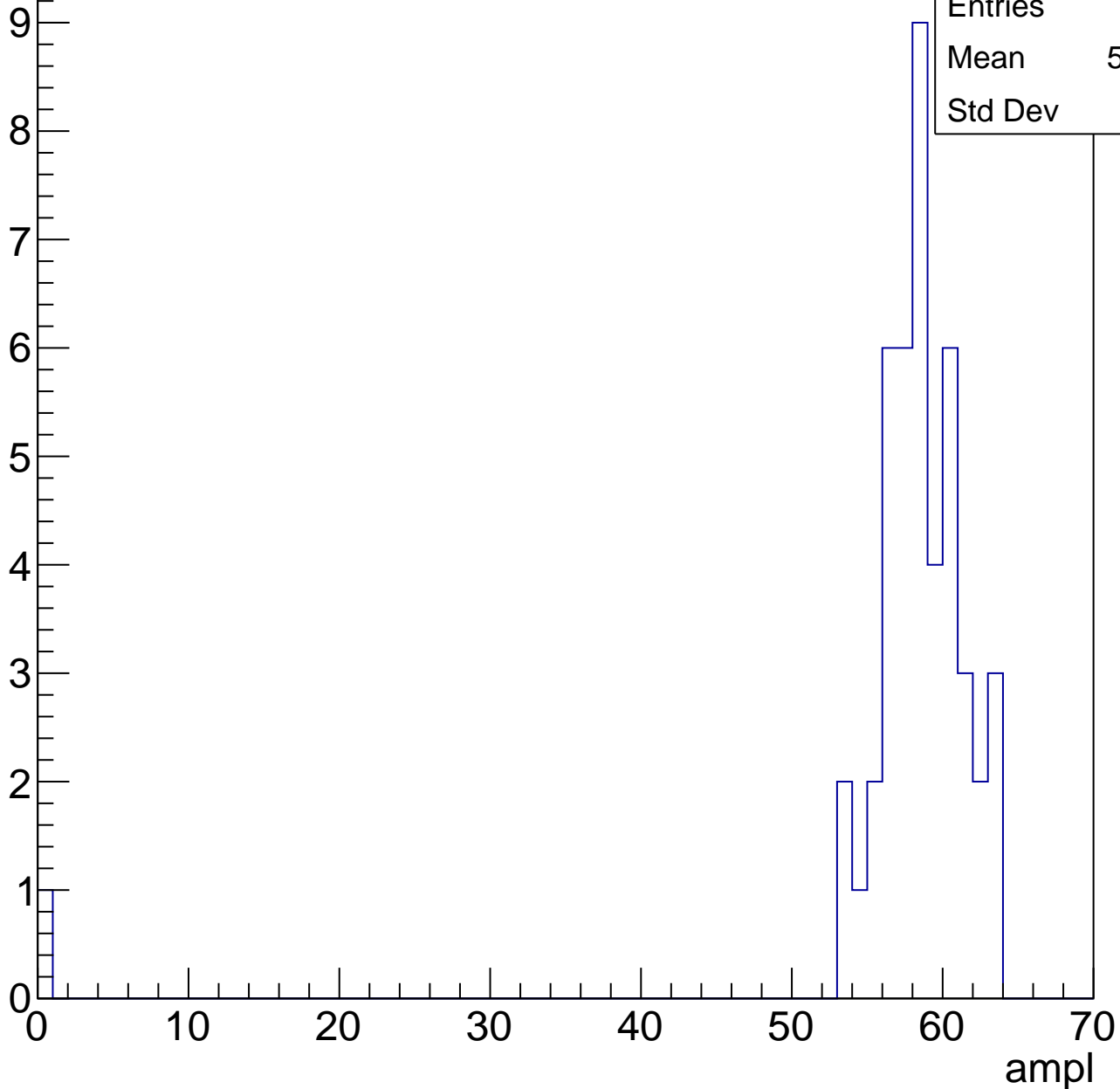
ampl



B1L100S, U25-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

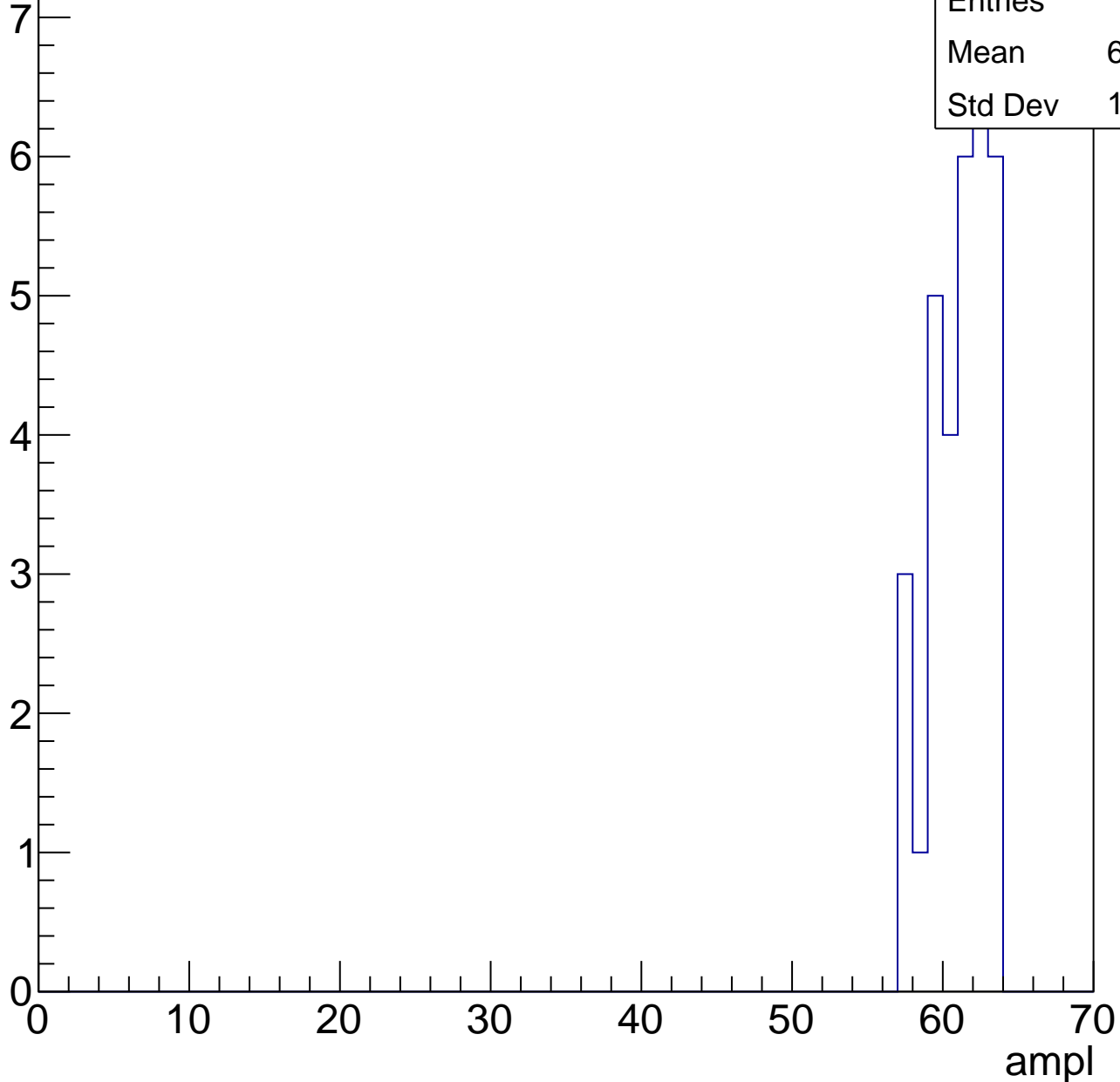
Entry



B1L100S, U25-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L100S, U25-ch38, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	29.52
Std Dev	5.176

Gaus mean : 30.6359

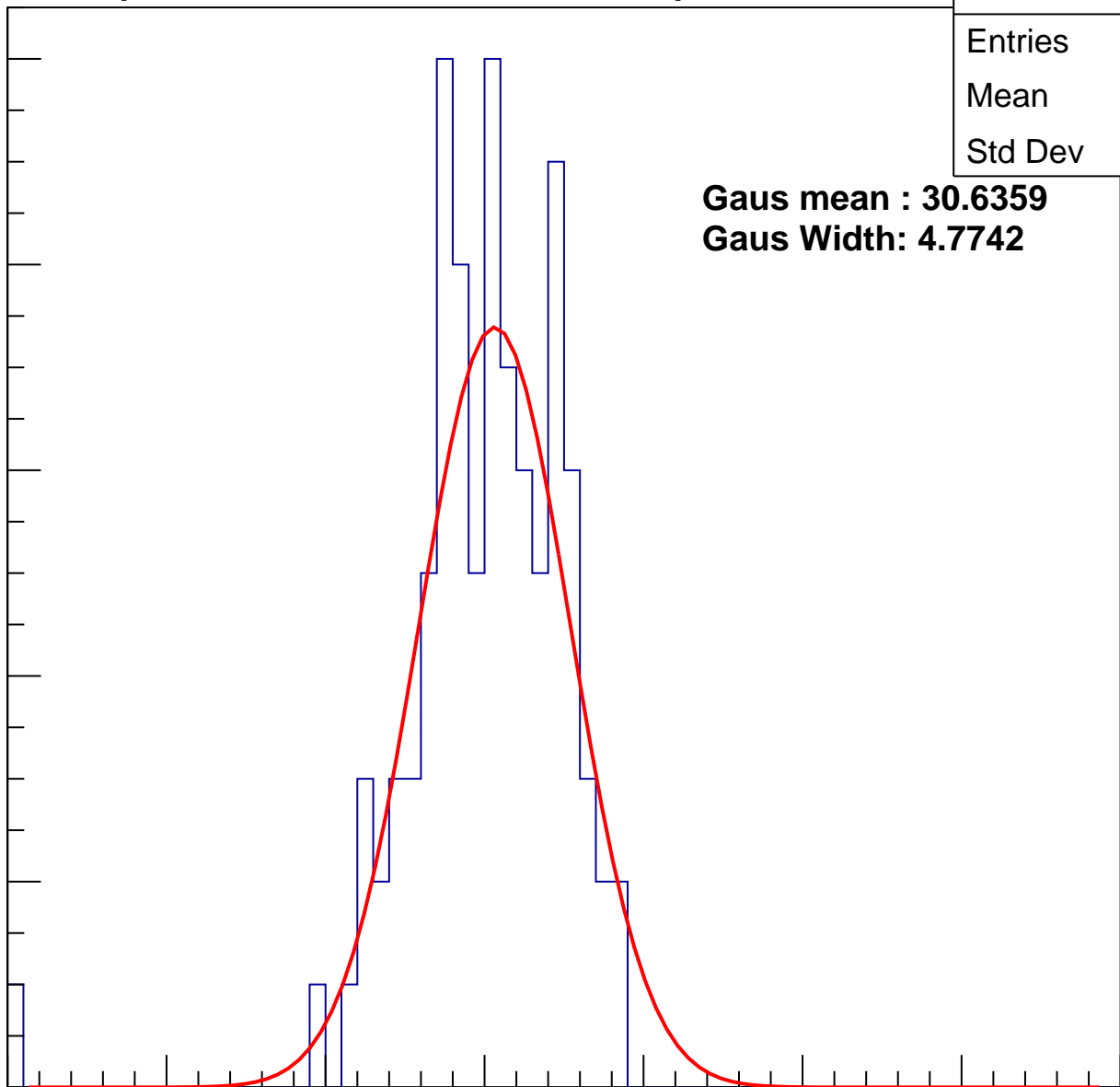
Gaus Width: 4.7742

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch38, adc1

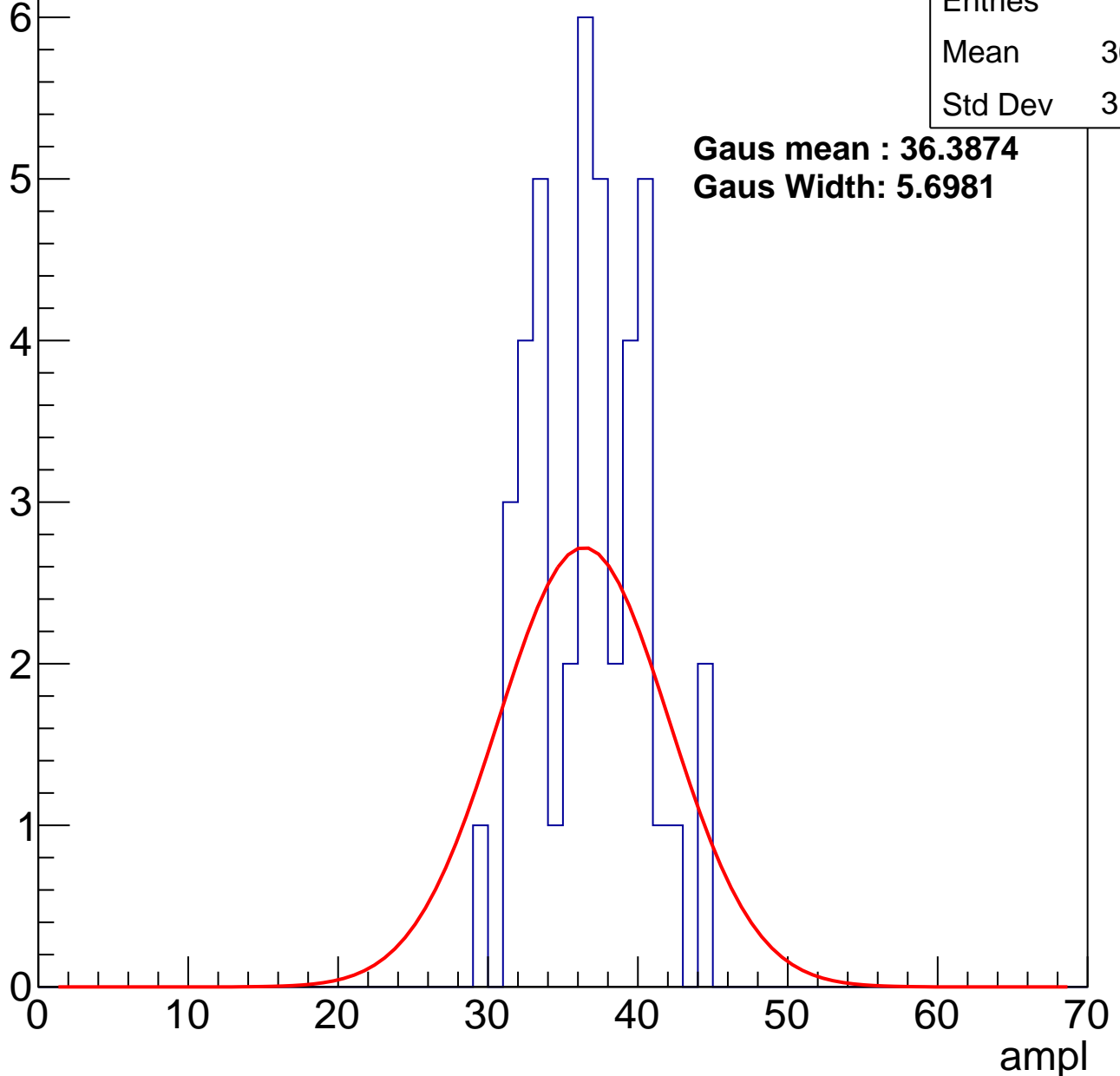
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	36.26
Std Dev	3.632

Gaus mean : 36.3874

Gaus Width: 5.6981



B1L100S, U25-ch38, adc2

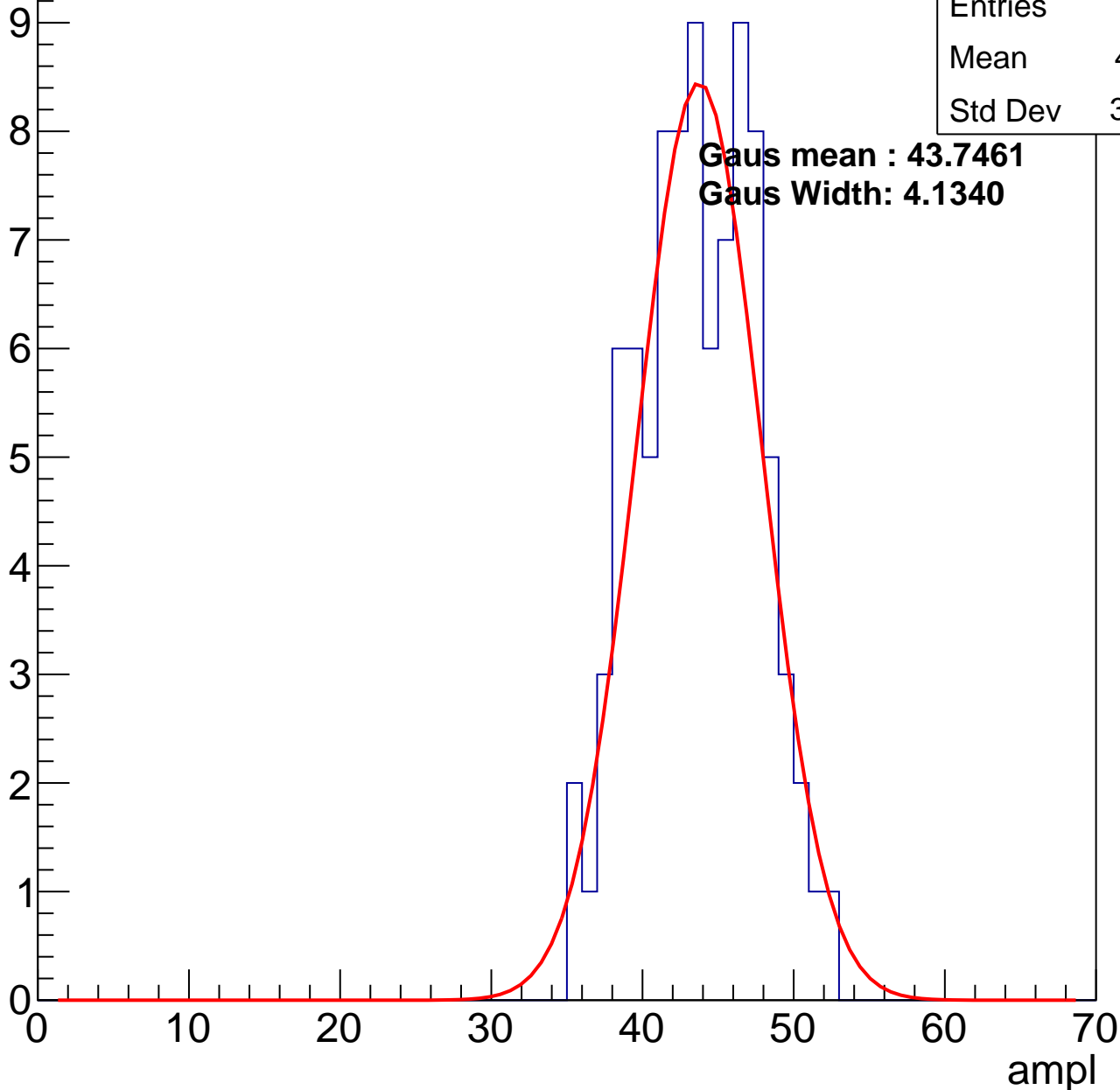
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	43.21
Std Dev	3.846

Gaus mean : 43.7461

Gaus Width: 4.1340

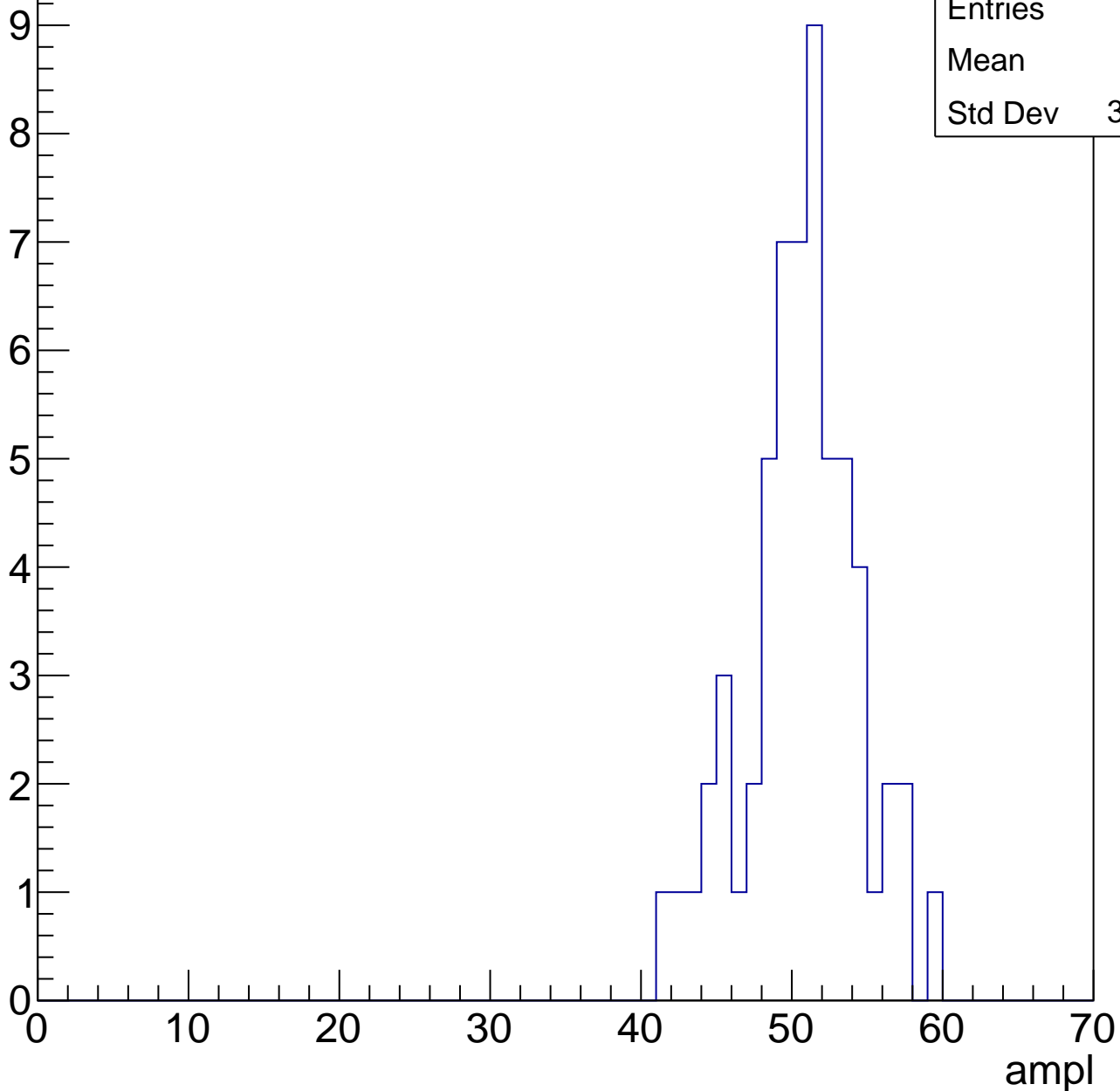


B1L100S, U25-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

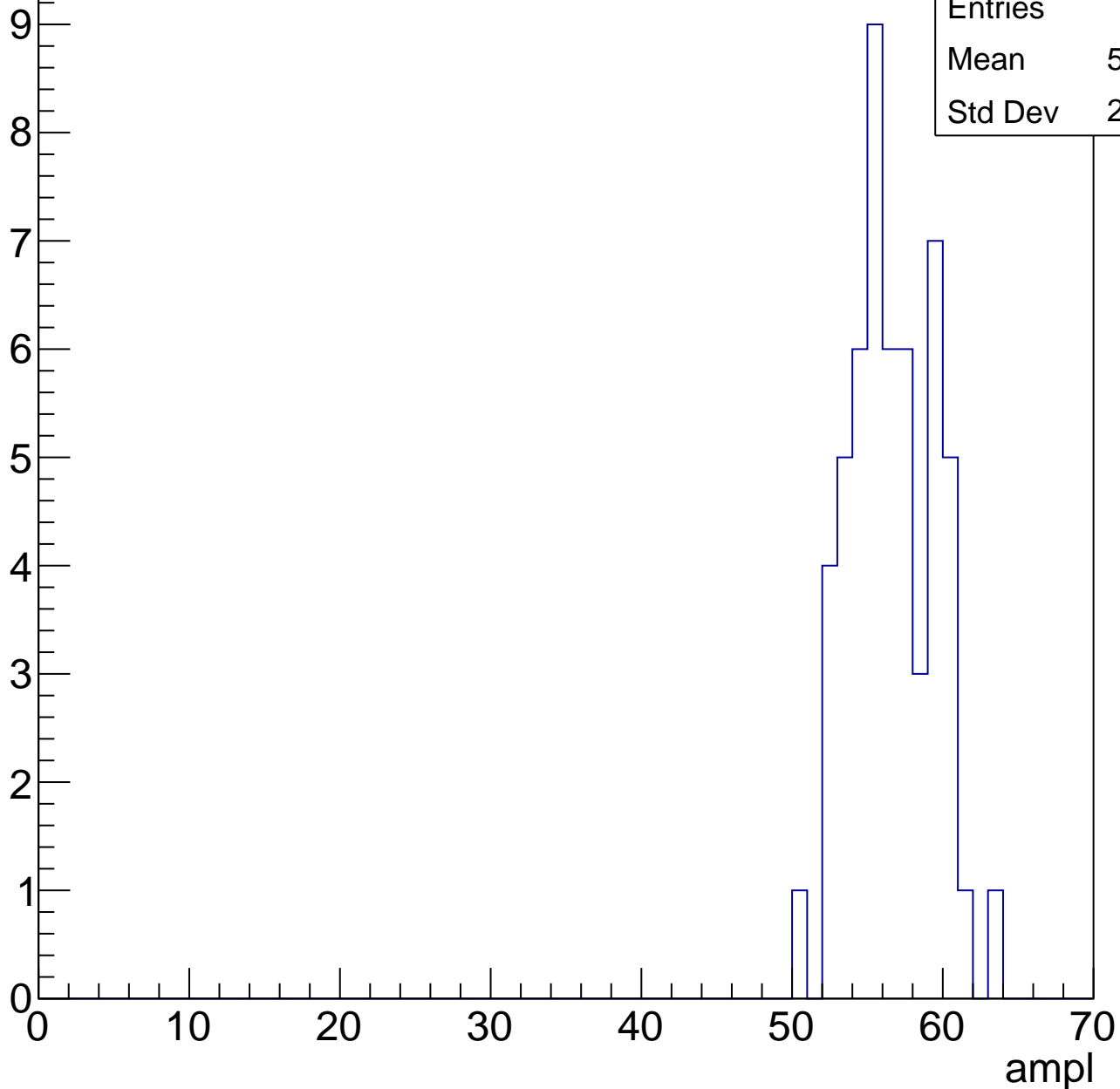
Entries	59
Mean	50.2
Std Dev	3.713



B1L100S, U25-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

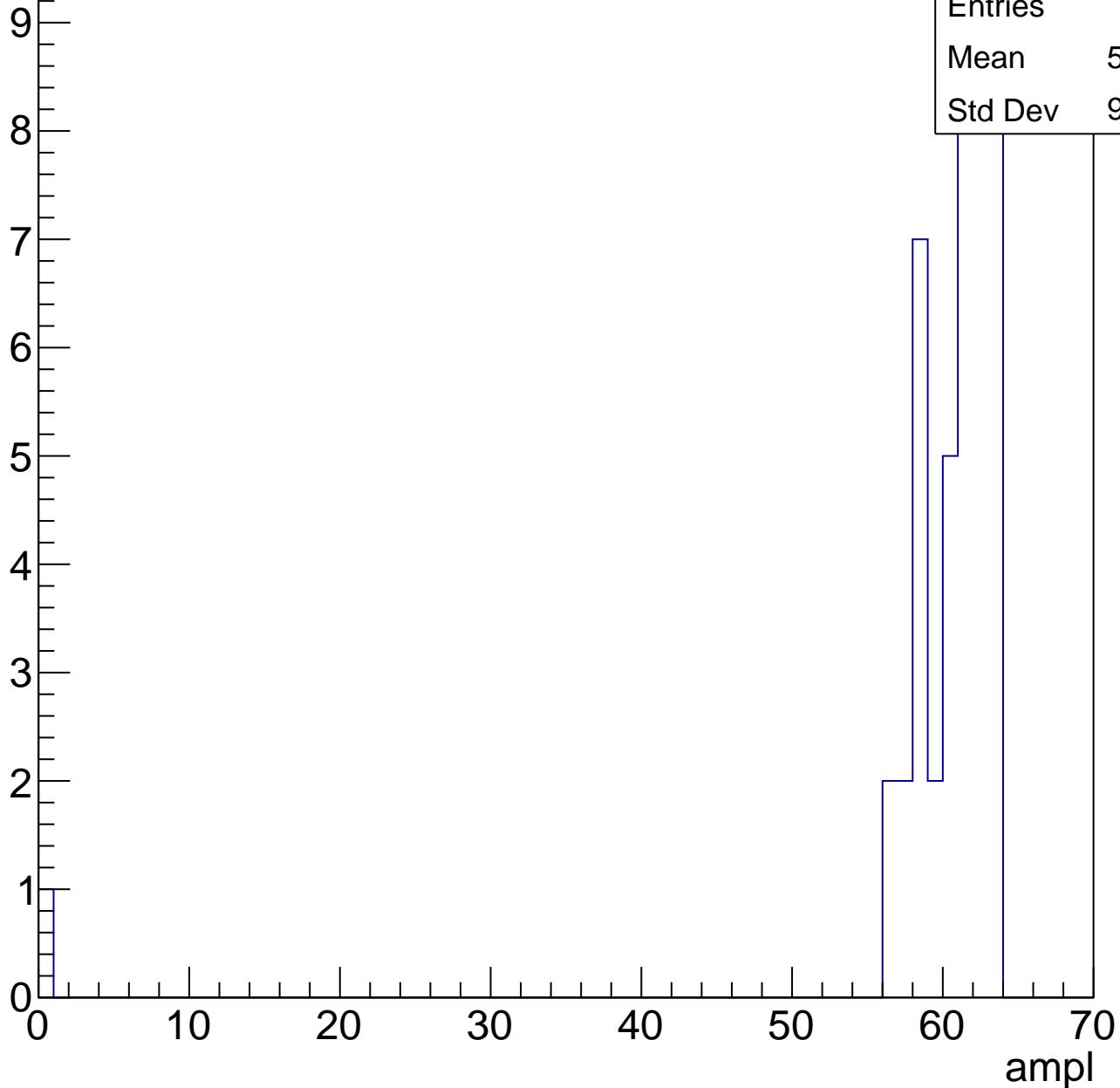
Entry



B1L100S, U25-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch39, adc0

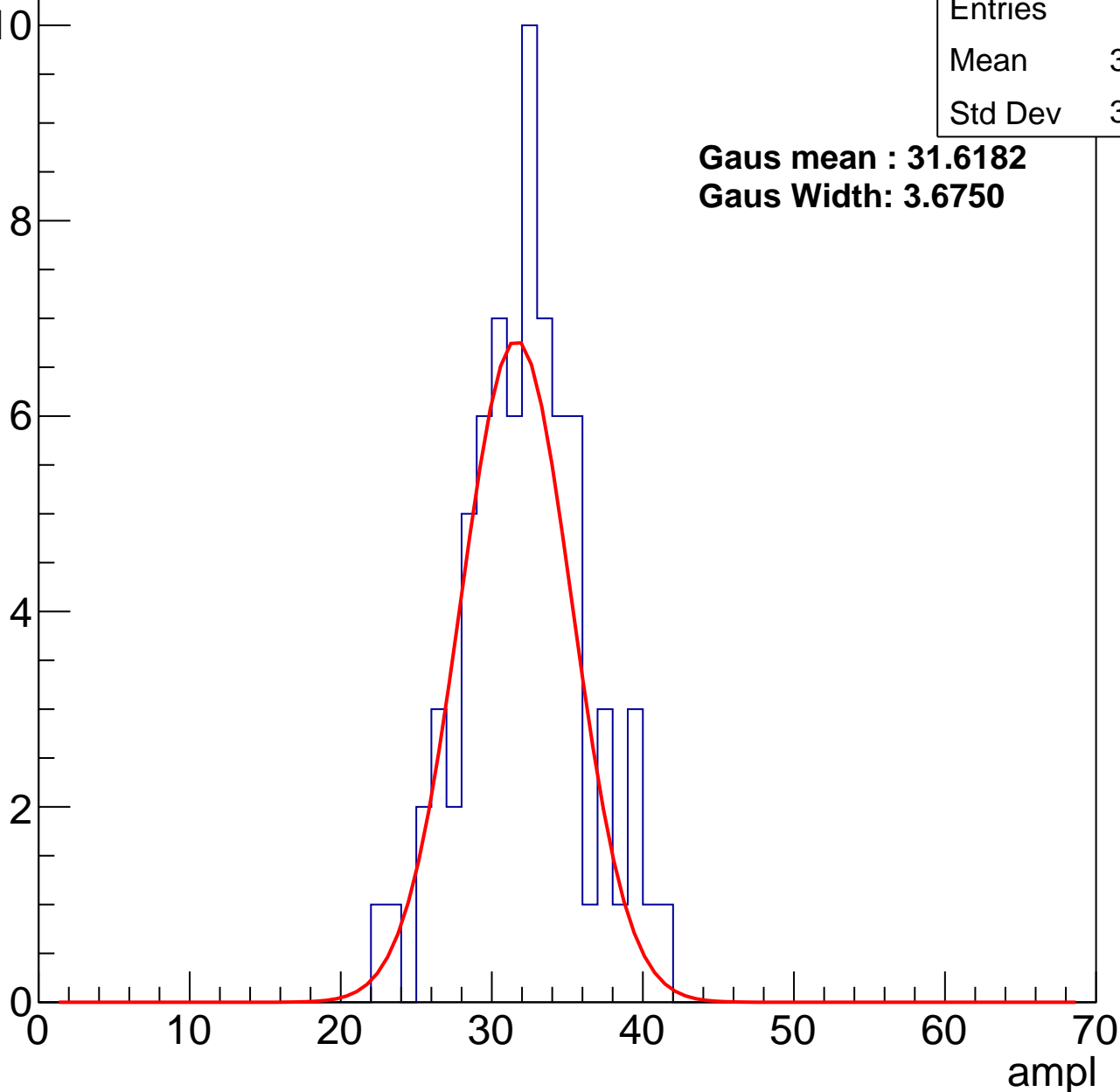
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	31.74
Std Dev	3.916

Gaus mean : 31.6182

Gaus Width: 3.6750



B1L100S, U25-ch39, adc1

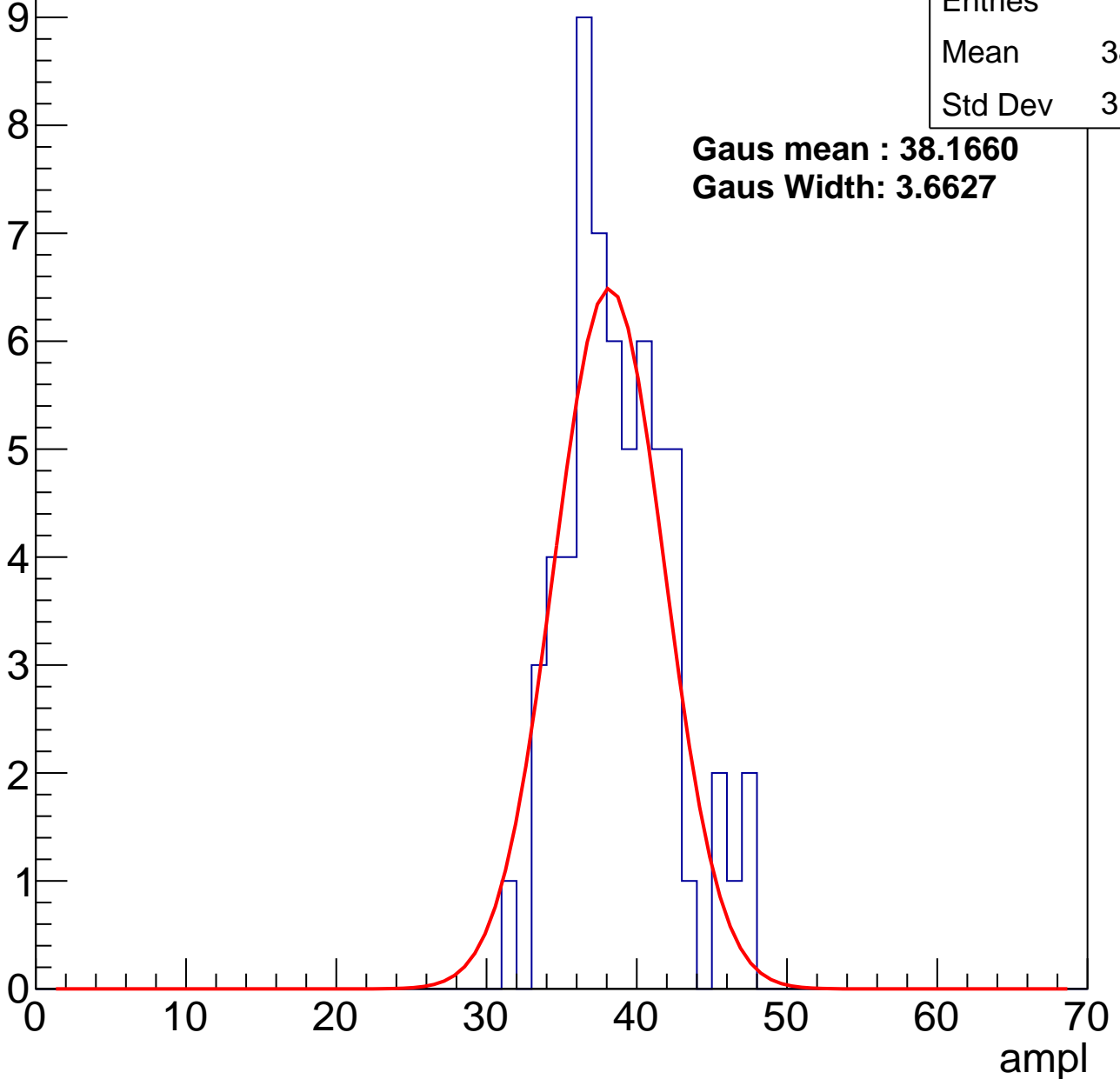
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	38.36
Std Dev	3.535

Gaus mean : 38.1660

Gaus Width: 3.6627



B1L100S, U25-ch39, adc2

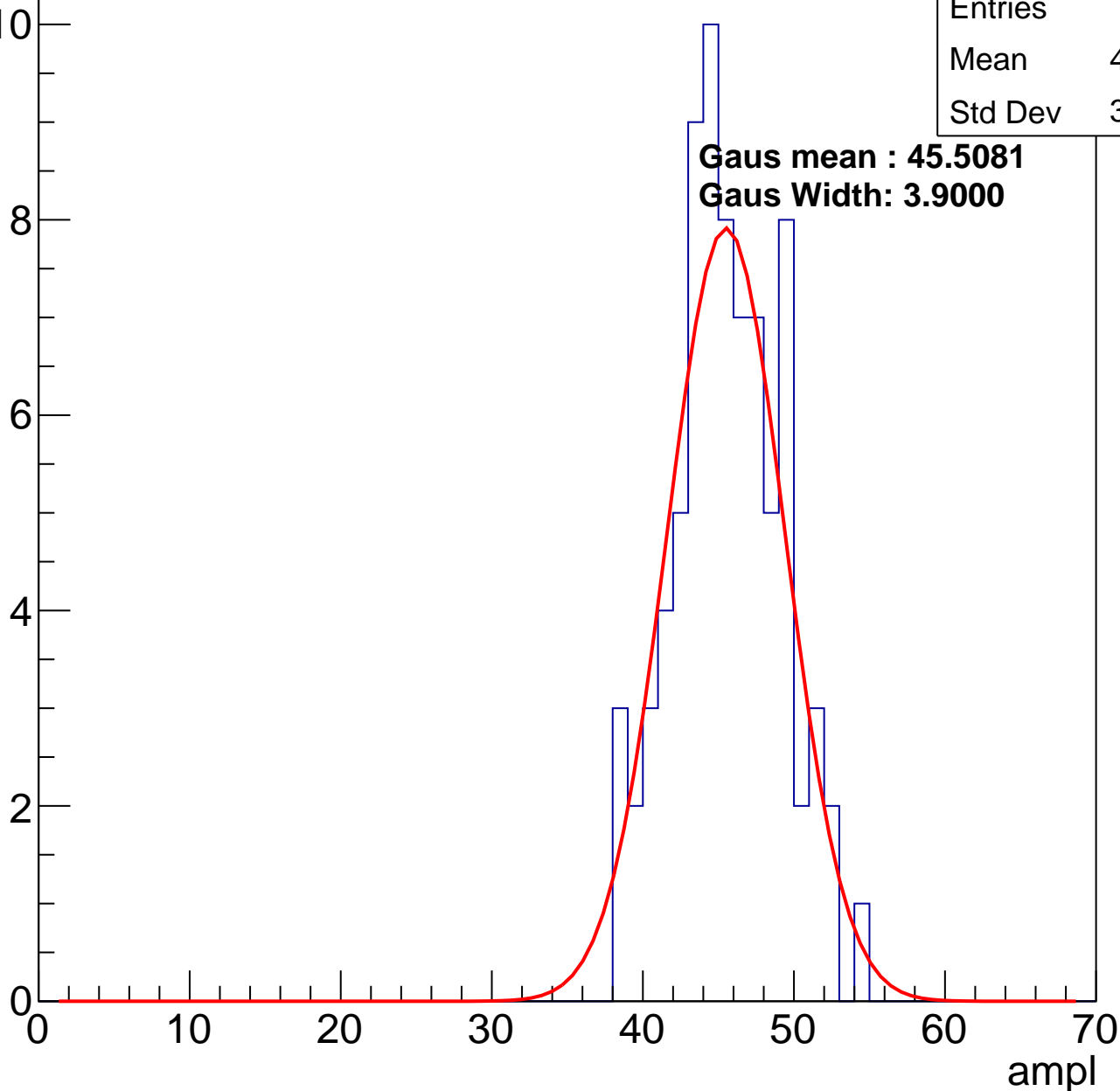
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	45.15
Std Dev	3.544

Gaus mean : 45.5081

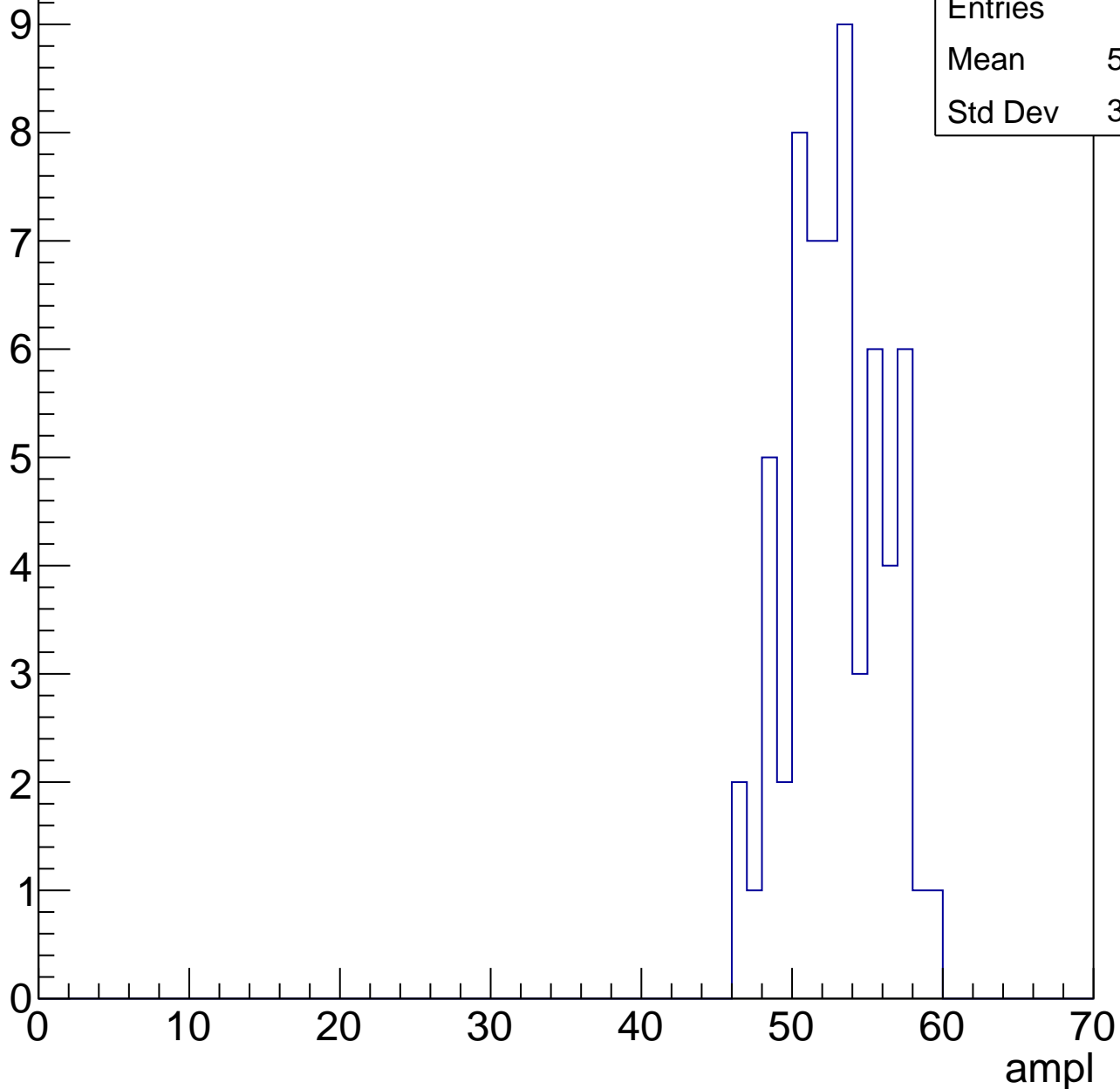
Gaus Width: 3.9000



B1L100S, U25-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

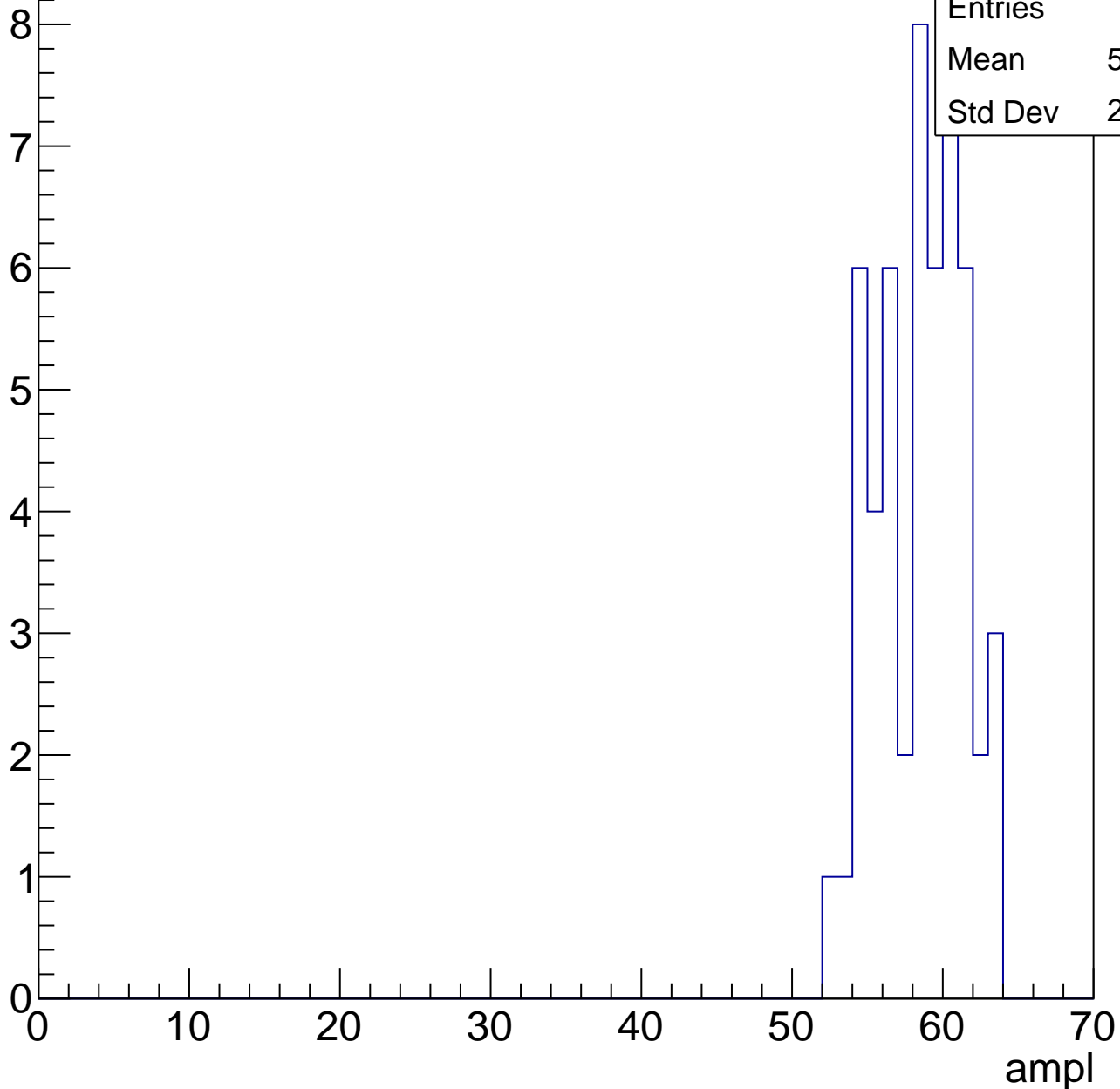


Entries	62
Mean	52.42
Std Dev	3.103

B1L100S, U25-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



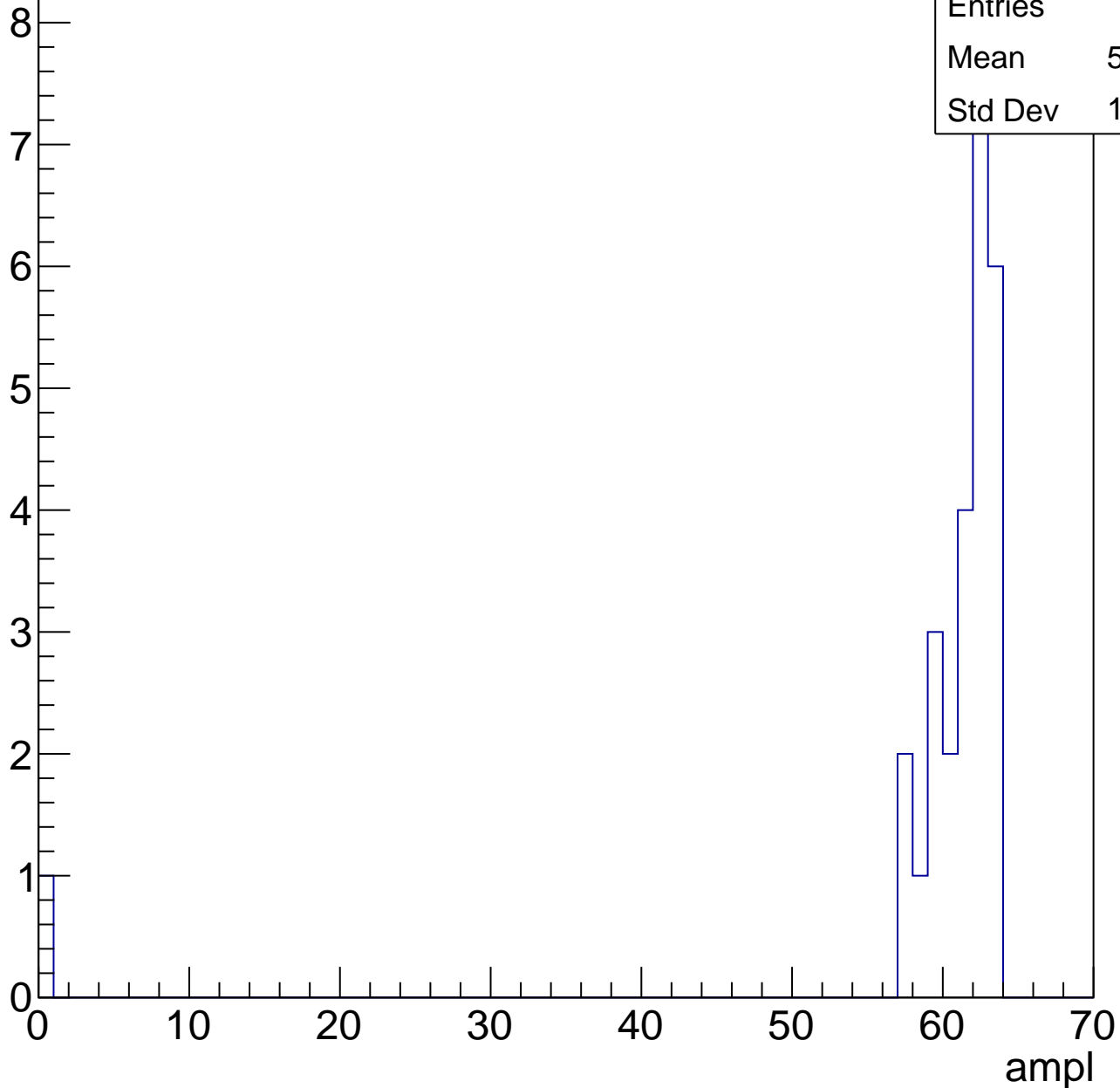
Entries	53
Mean	58.04
Std Dev	2.808

B1L100S, U25-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.78
Std Dev	11.67



B1L100S, U25-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch40, adc0

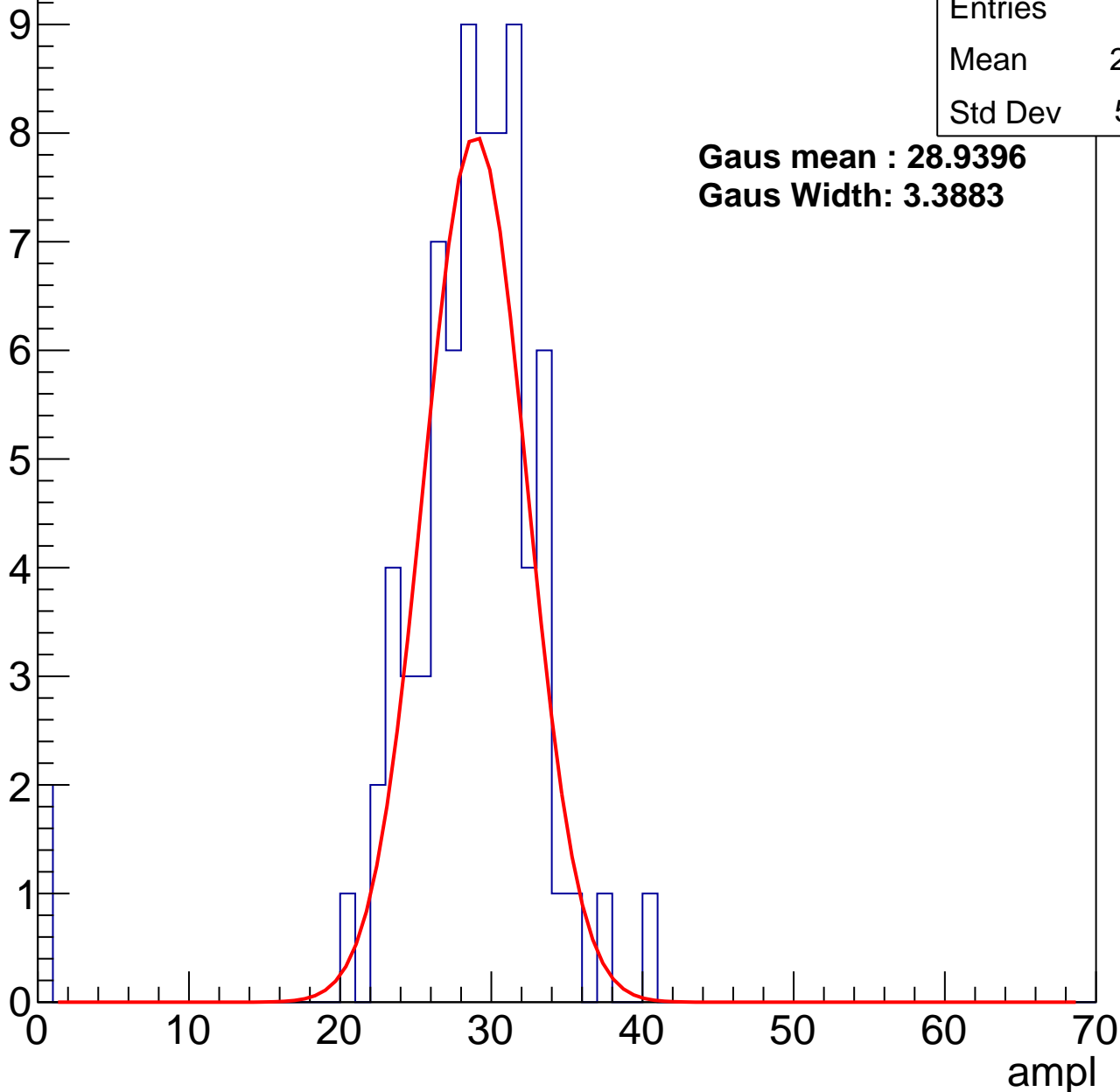
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	27.92
Std Dev	5.801

Gaus mean : 28.9396

Gaus Width: 3.3883



B1L100S, U25-ch40, adc1

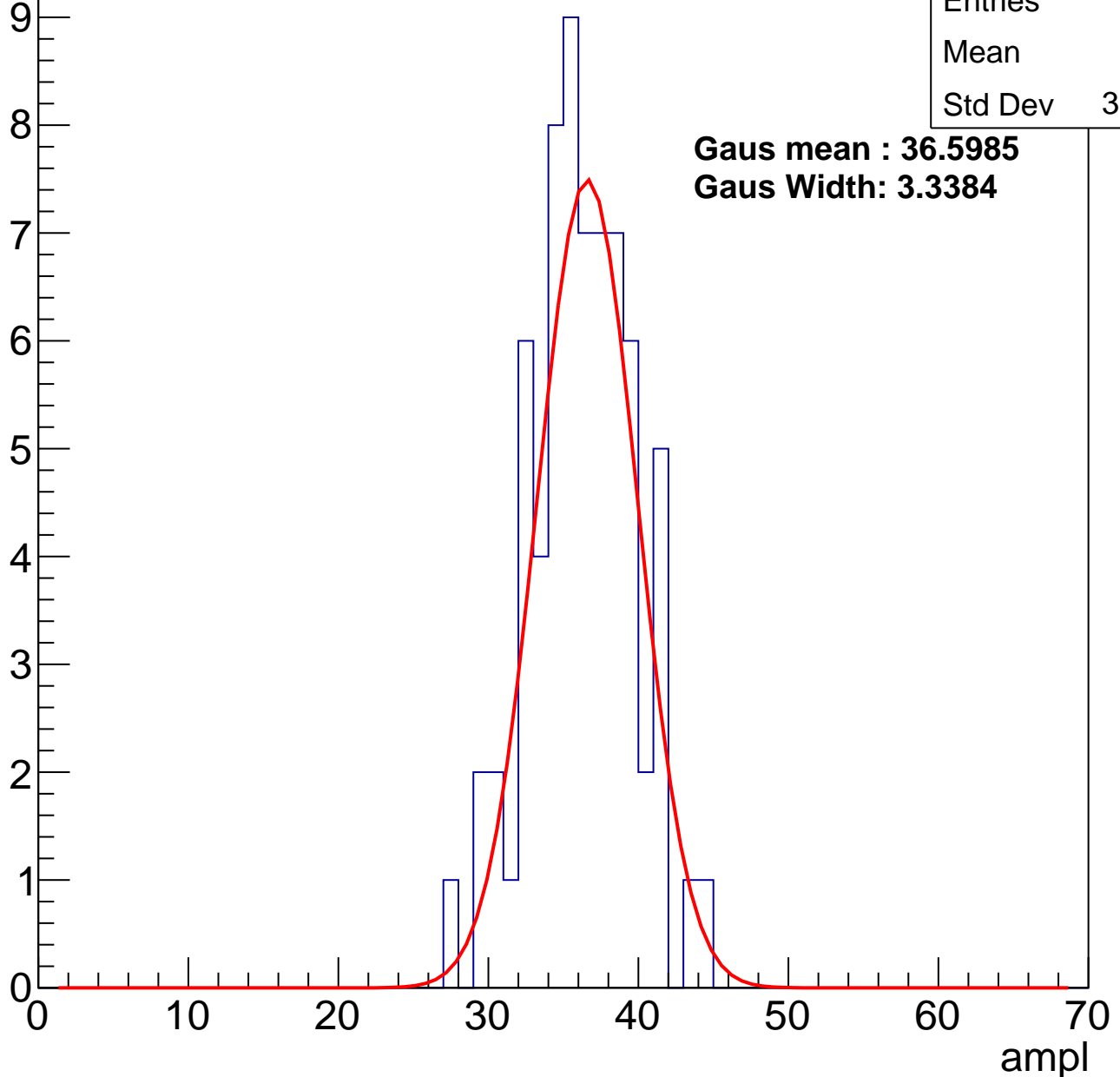
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.8
Std Dev	3.416

Gaus mean : 36.5985

Gaus Width: 3.3384



B1L100S, U25-ch40, adc2

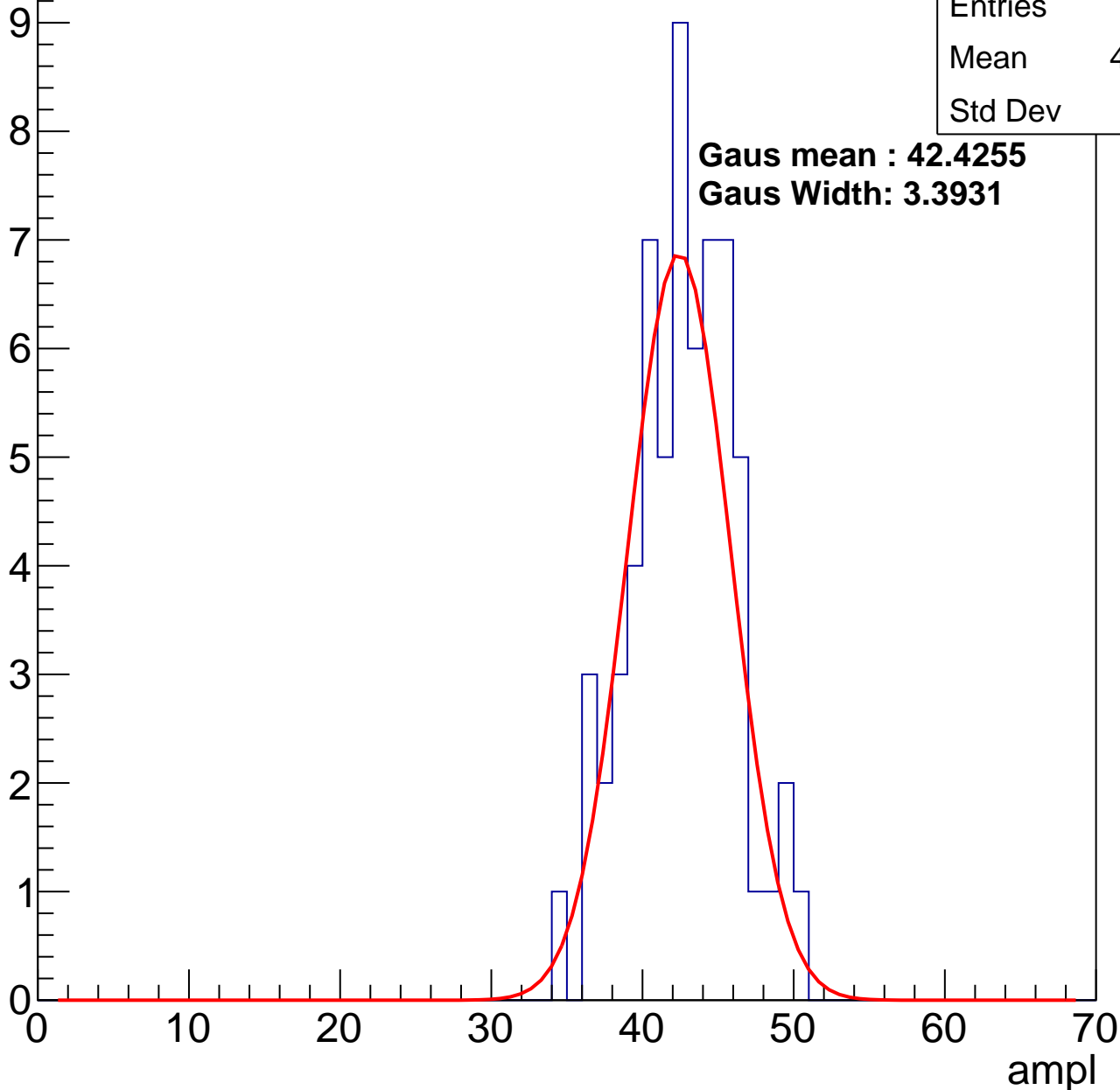
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.23
Std Dev	3.39

Gaus mean : 42.4255

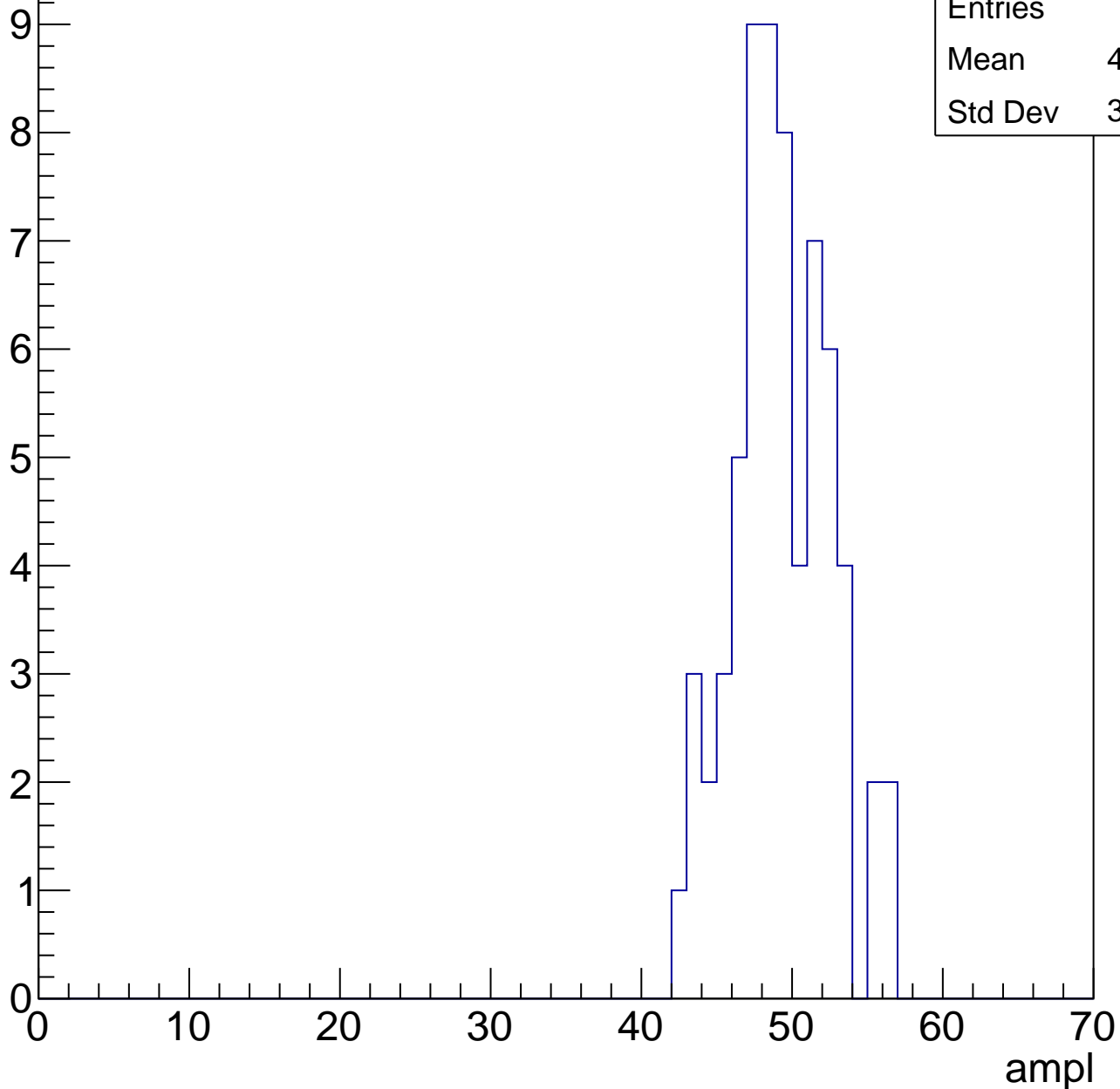
Gaus Width: 3.3931



B1L100S, U25-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



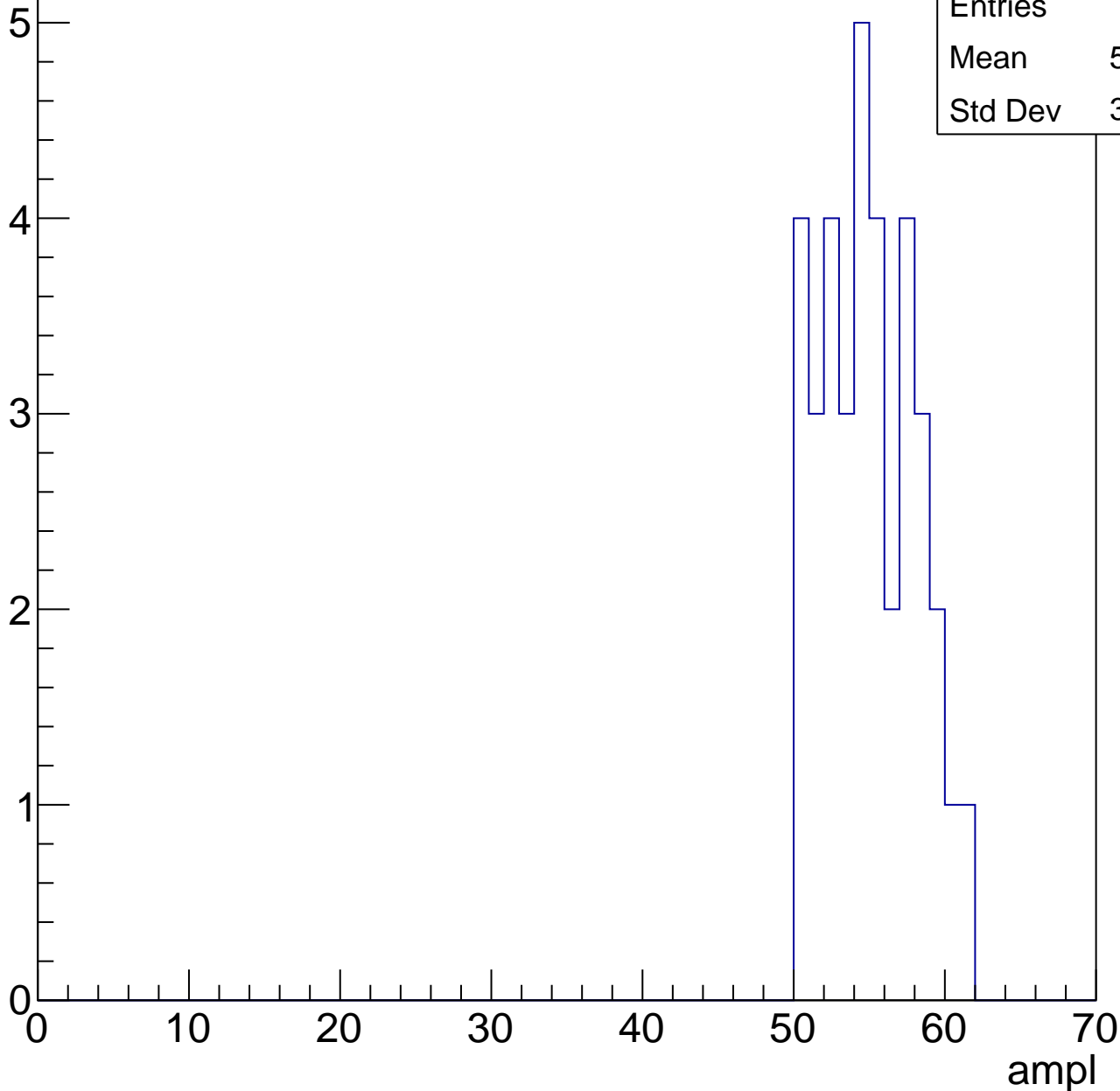
Entries	65
Mean	48.83
Std Dev	3.194

B1L100S, U25-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	54.53
Std Dev	3.032



B1L100S, U25-ch40, adc5

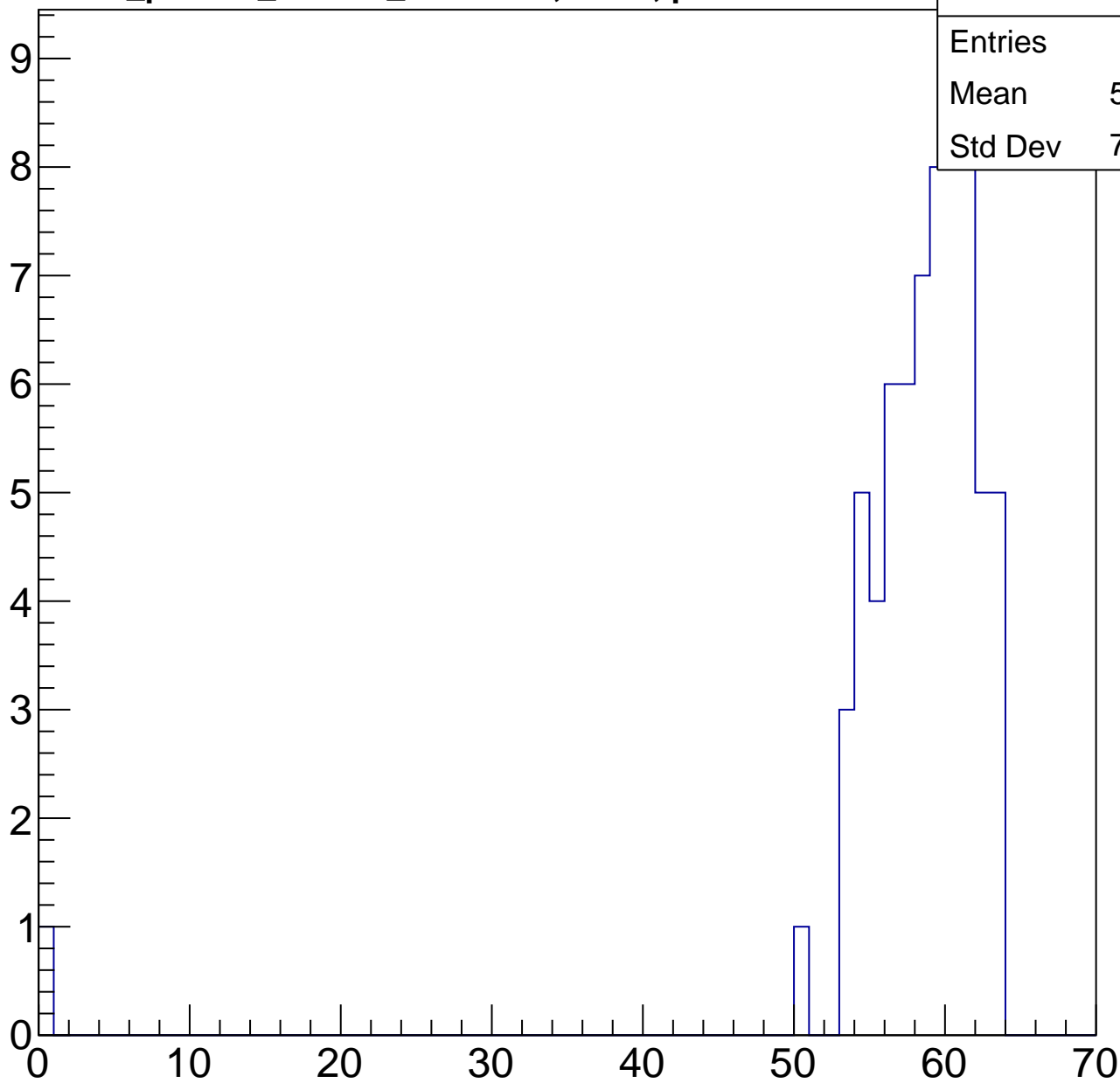
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	68
Mean	57.47
Std Dev	7.622

ampl

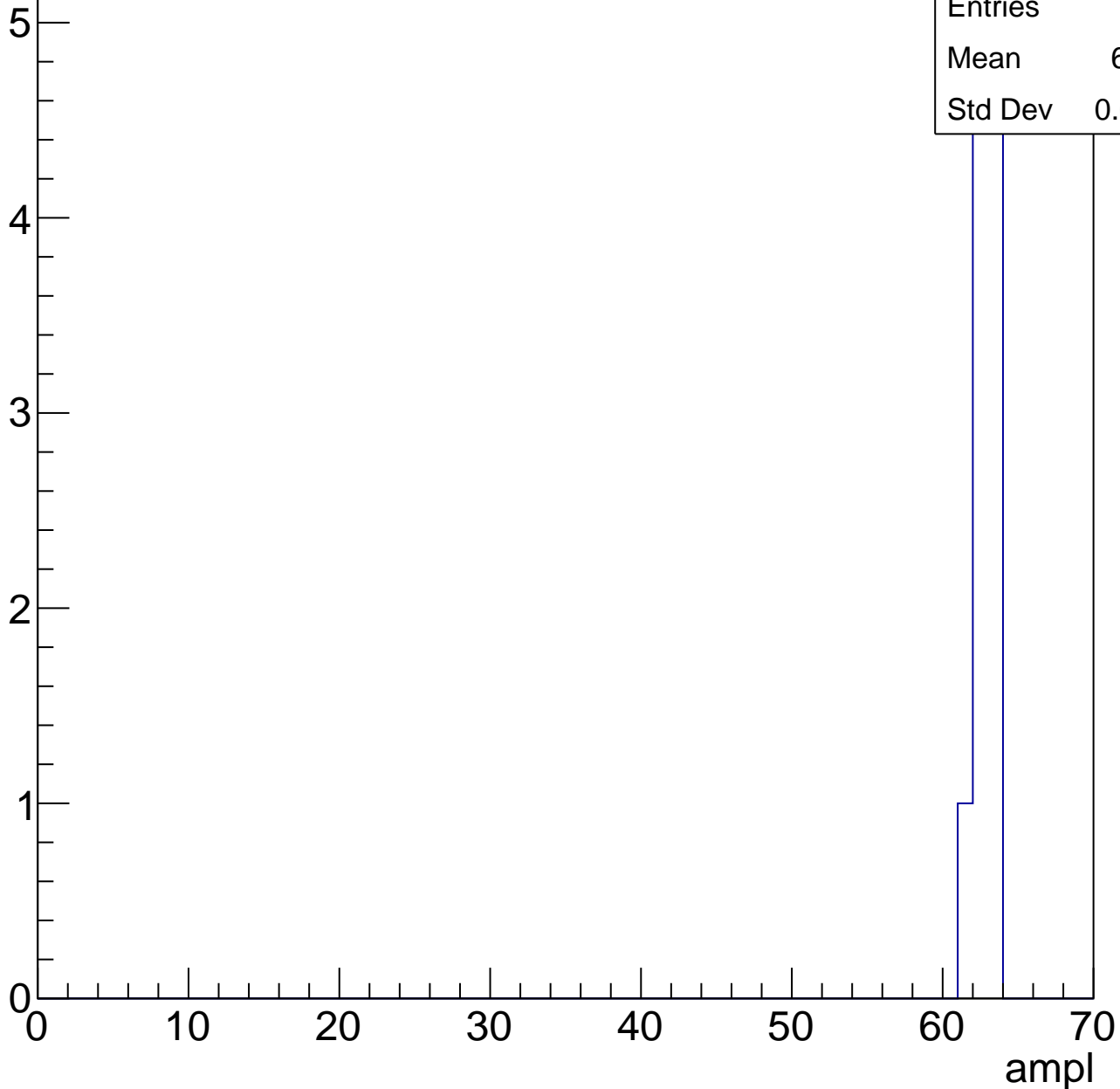


B1L100S, U25-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	62.36
Std Dev	0.6428



B1L100S, U25-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch41, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	29.71
Std Dev	4.712

Gaus mean : 30.6791

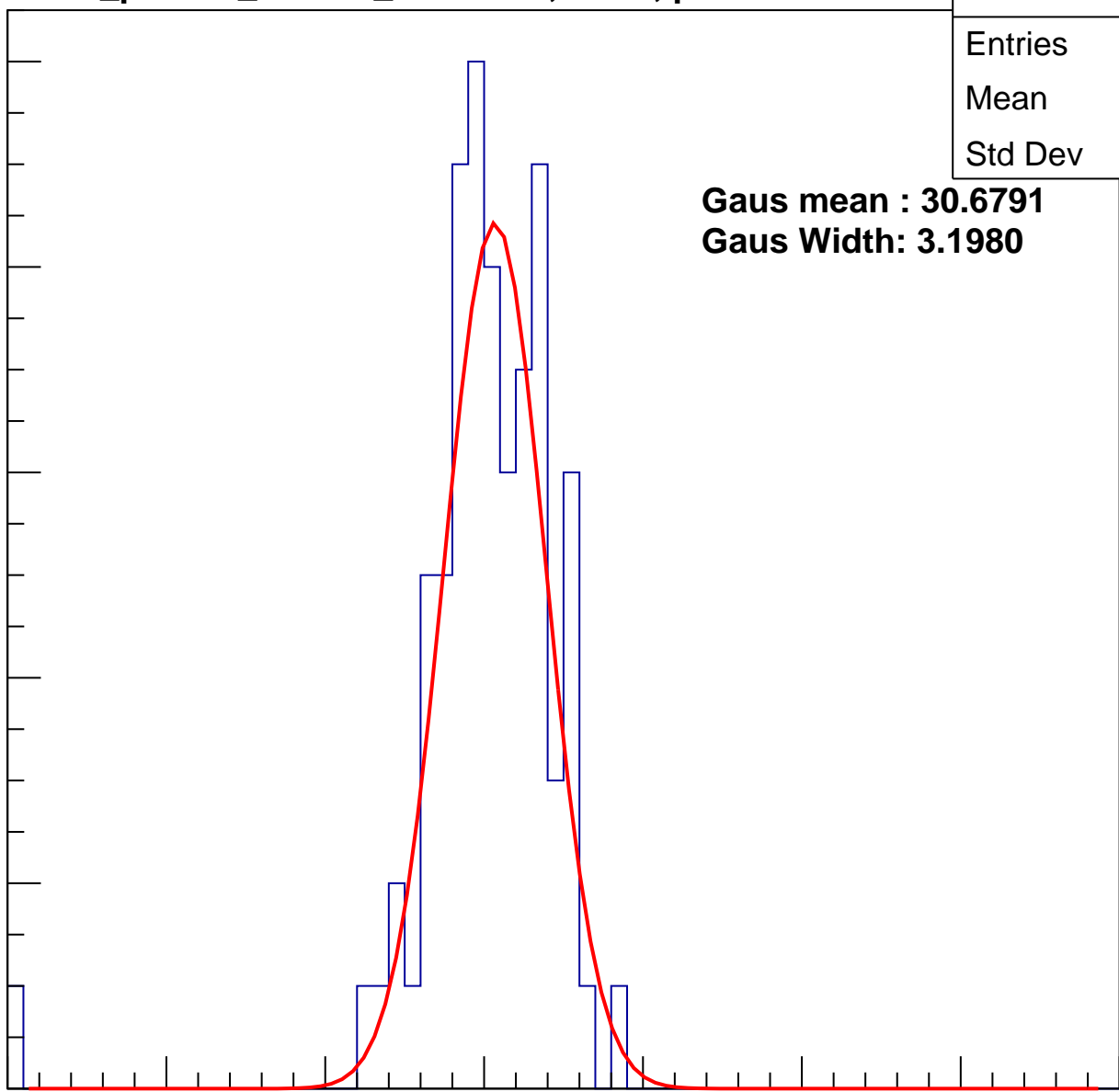
Gaus Width: 3.1980

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



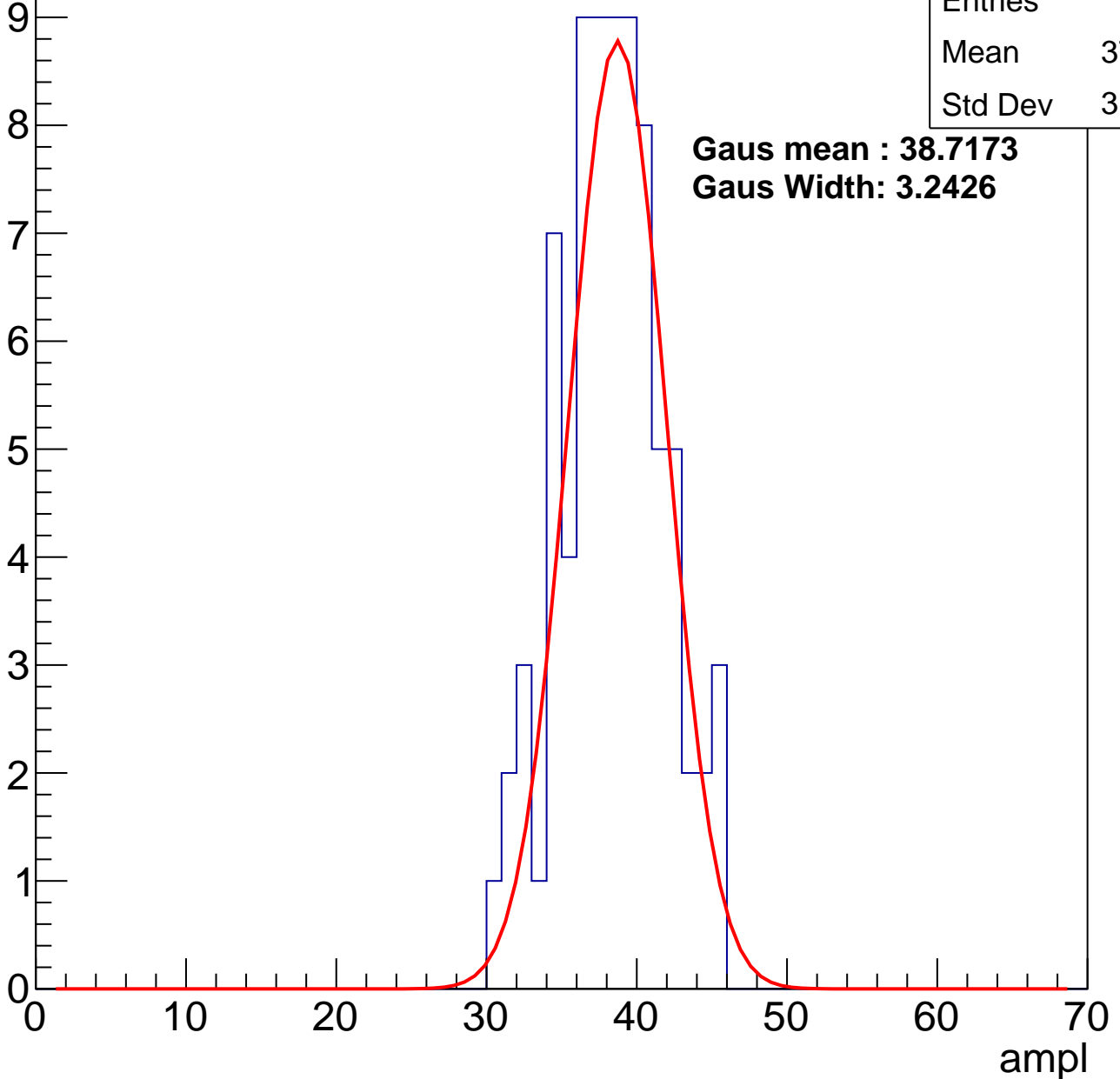
B1L100S, U25-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

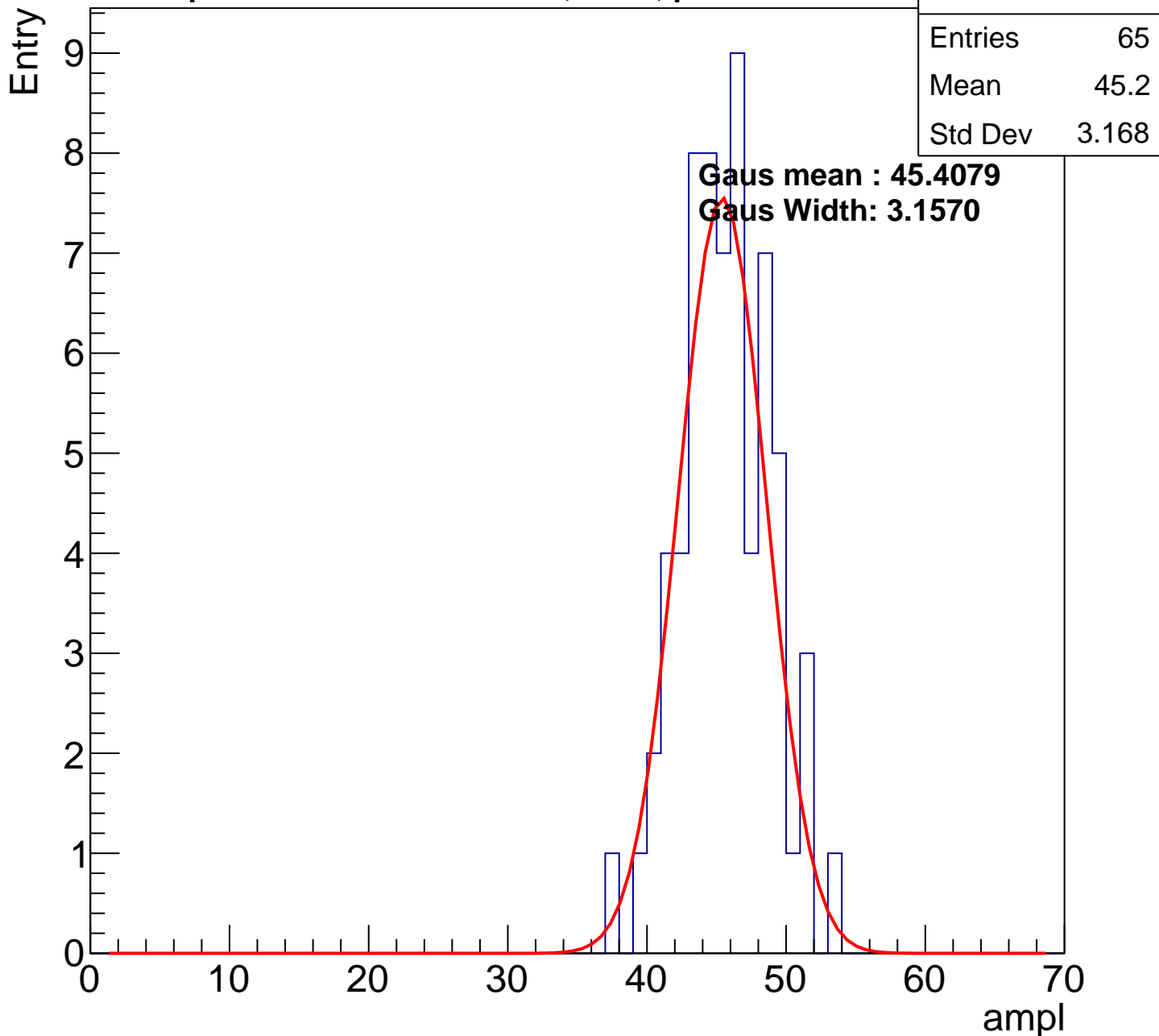
Entries	79
Mean	37.89
Std Dev	3.416

Gaus mean : 38.7173
Gaus Width: 3.2426



B1L100S, U25-ch41, adc2

calib_packv5_042523_0143.root, FC#4, port A2

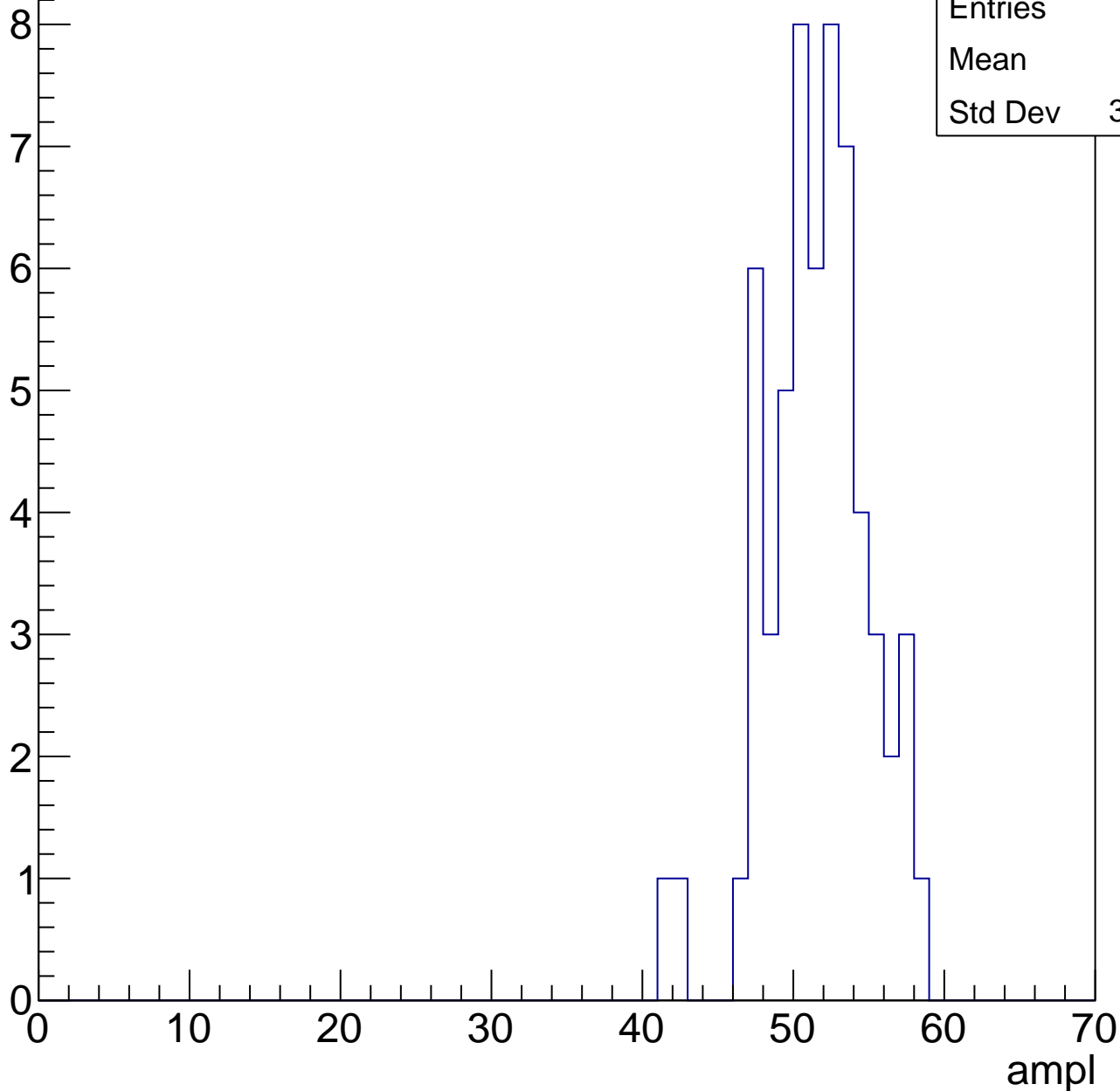


B1L100S, U25-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	51.1
Std Dev	3.403



B1L100S, U25-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	57.61
Std Dev	3.03

Entry

10

8

6

4

2

0

0

10

20

30

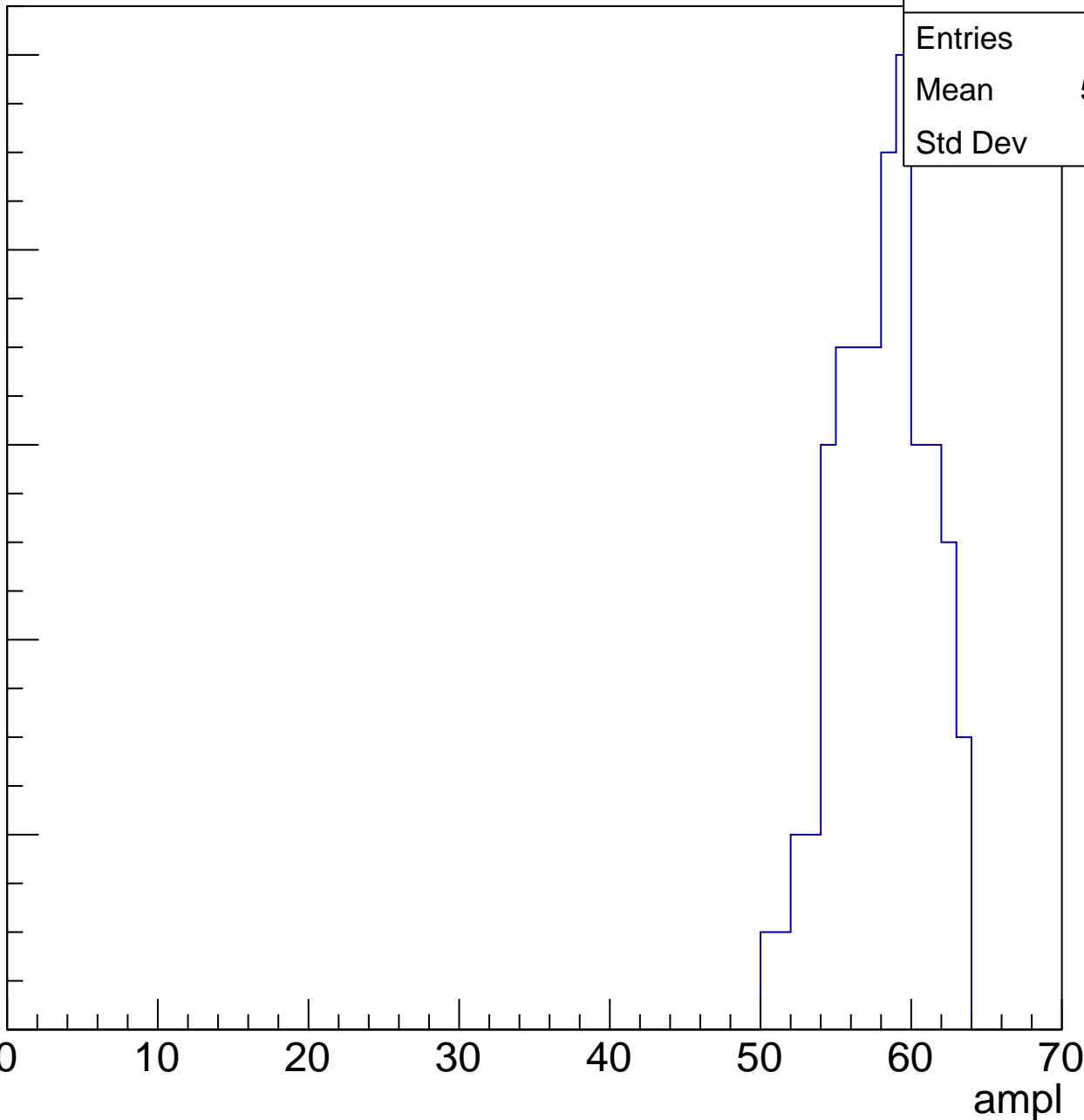
40

50

60

70

ampl

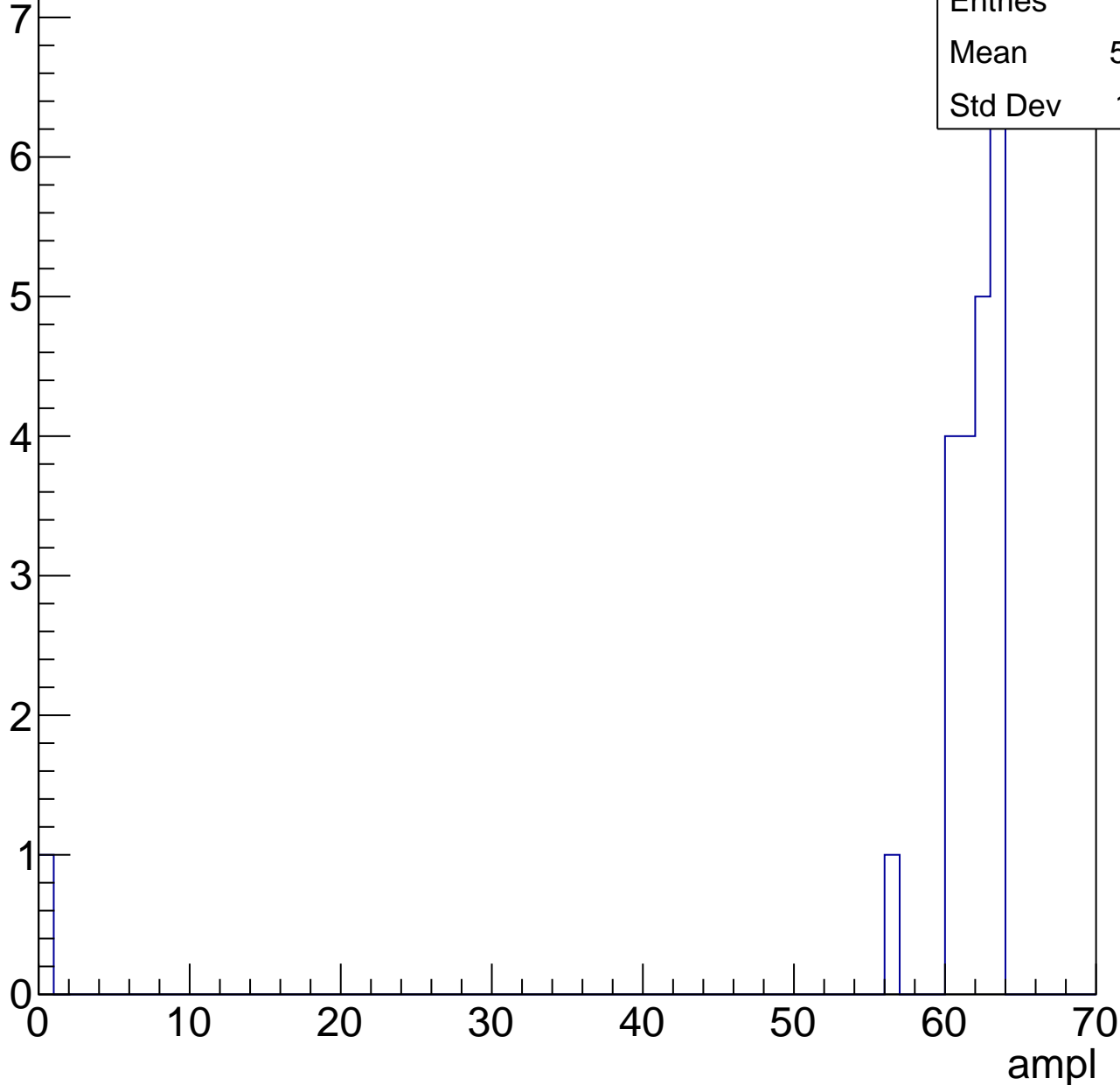


B1L100S, U25-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.68
Std Dev	12.91



B1L100S, U25-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch42, adc0

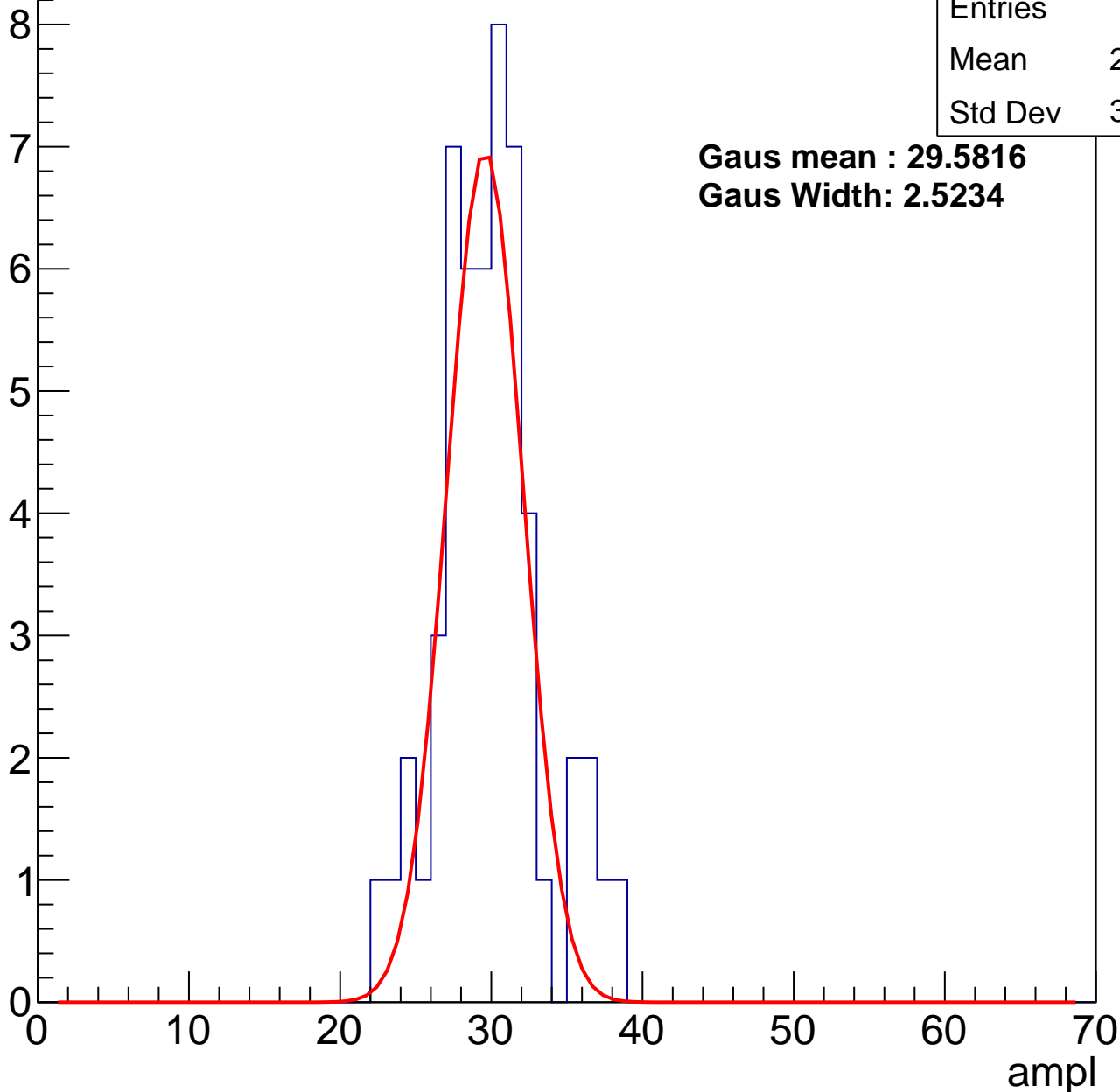
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	29.47
Std Dev	3.374

Gaus mean : 29.5816

Gaus Width: 2.5234



B1L100S, U25-ch42, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	35.04
Std Dev	5.334

Gaus mean : 36.0569

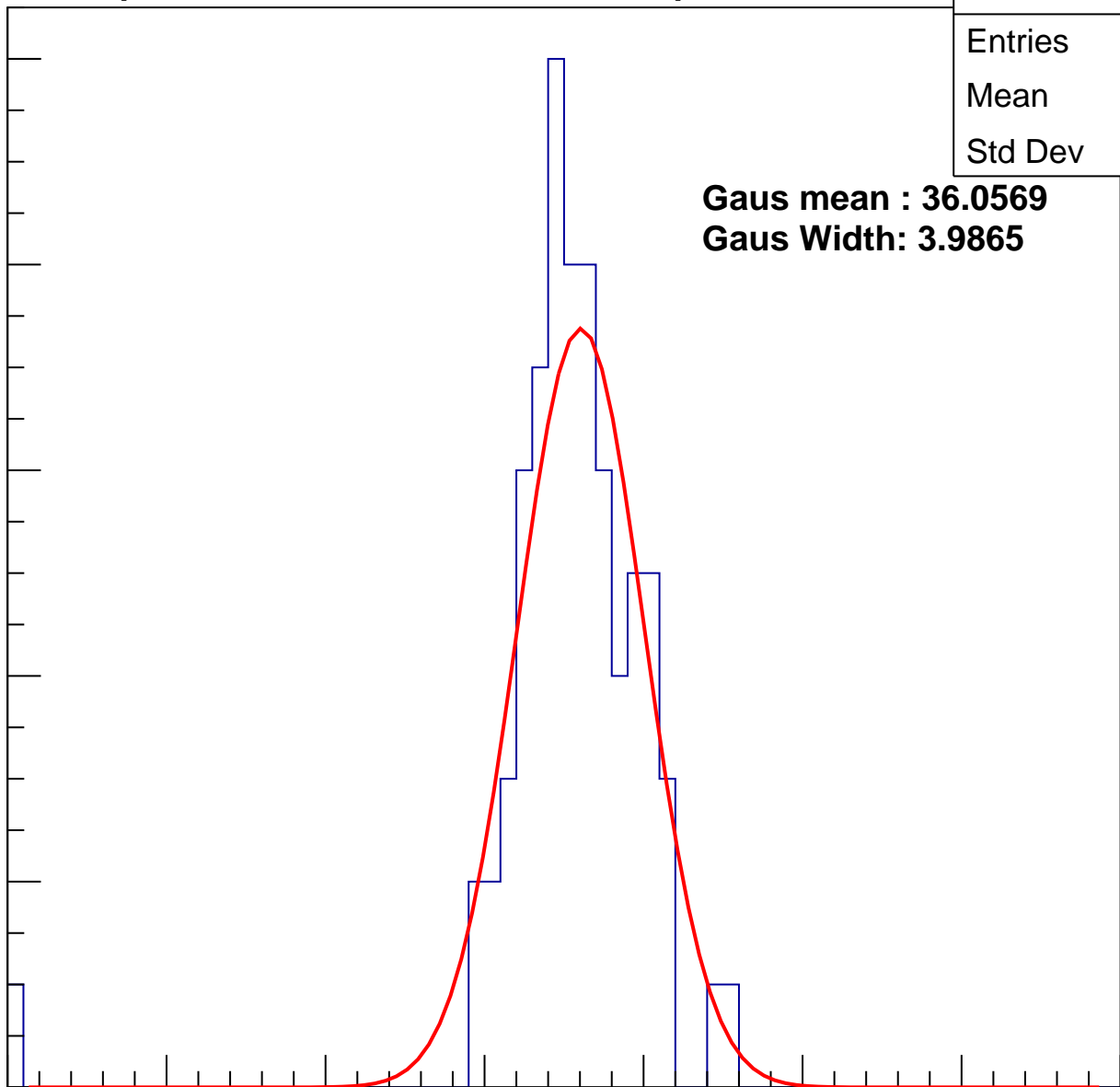
Gaus Width: 3.9865

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch42, adc2

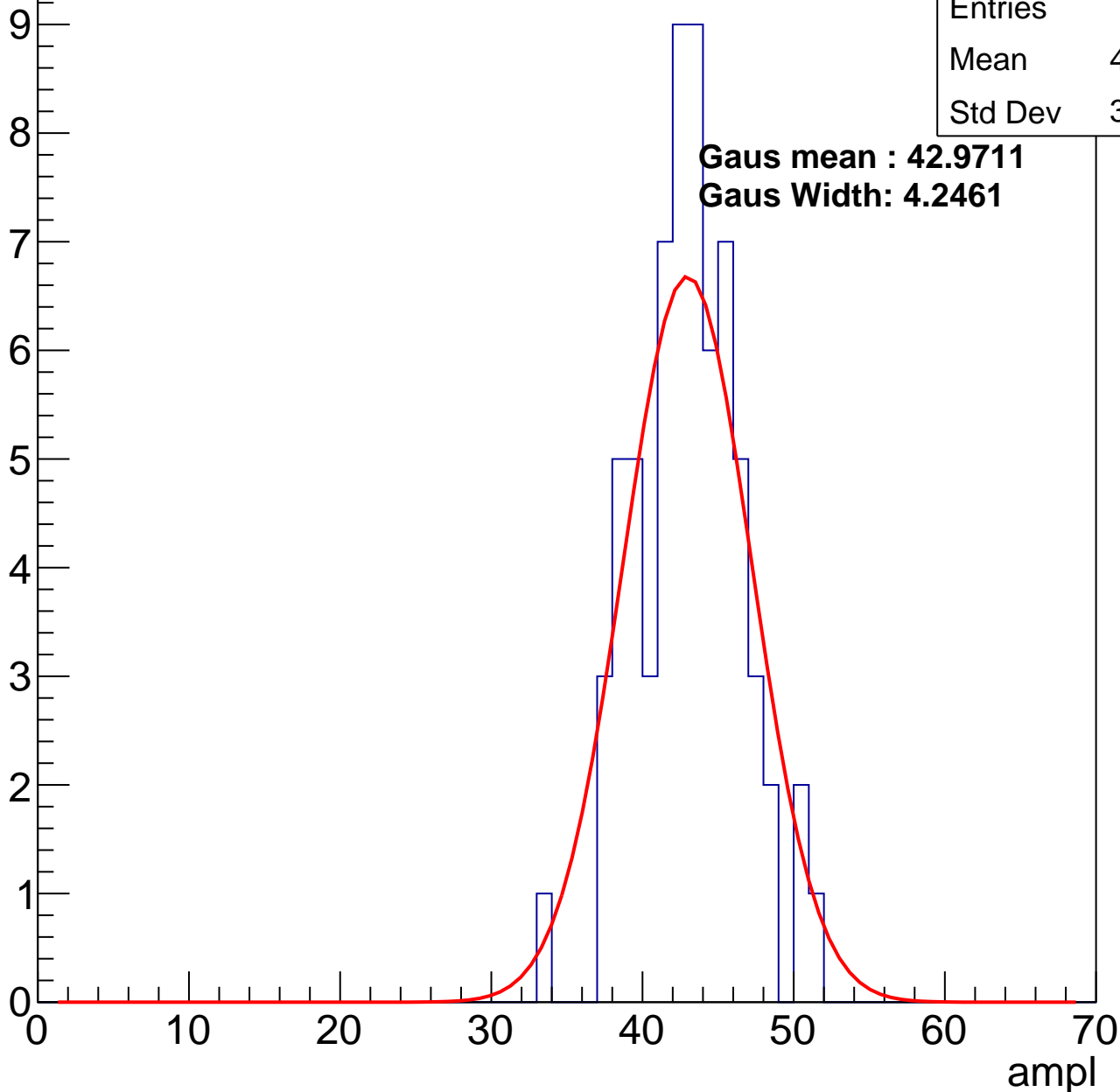
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.62
Std Dev	3.443

Gaus mean : 42.9711

Gaus Width: 4.2461



B1L100S, U25-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	49.35
Std Dev	3.508

Entry

10

8

6

4

2

0

0

10

20

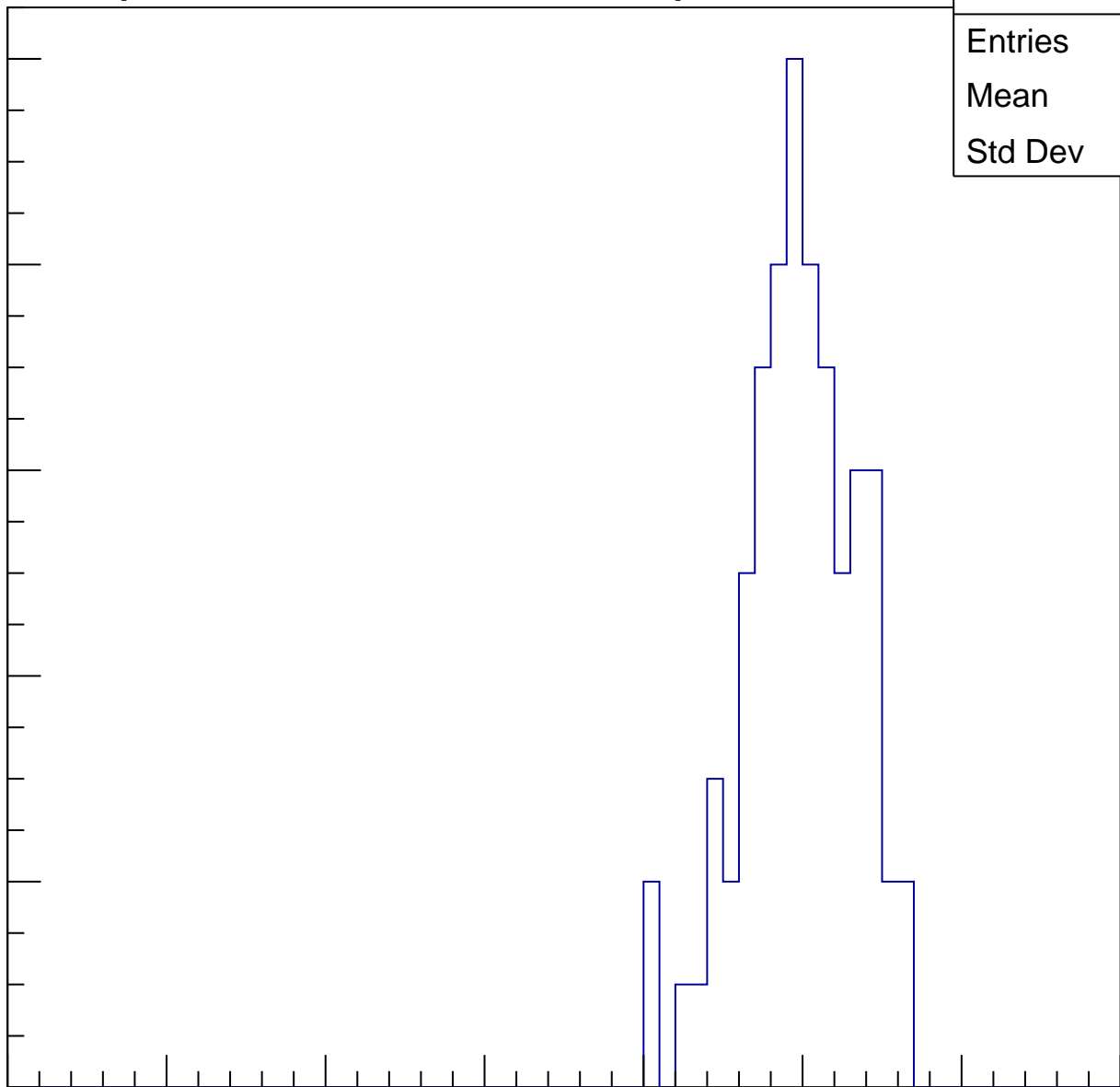
30

40

50

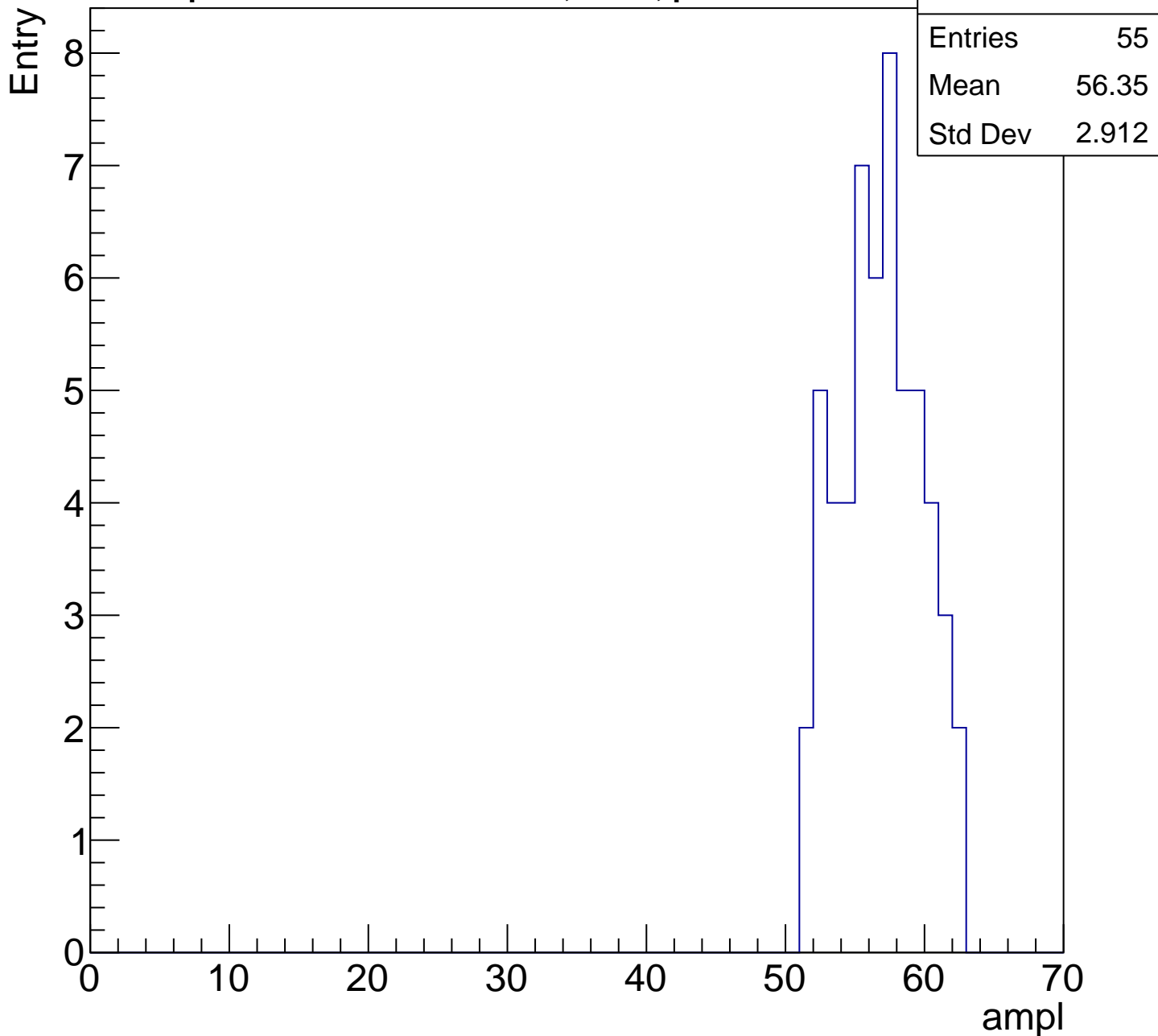
60

ampl



B1L100S, U25-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

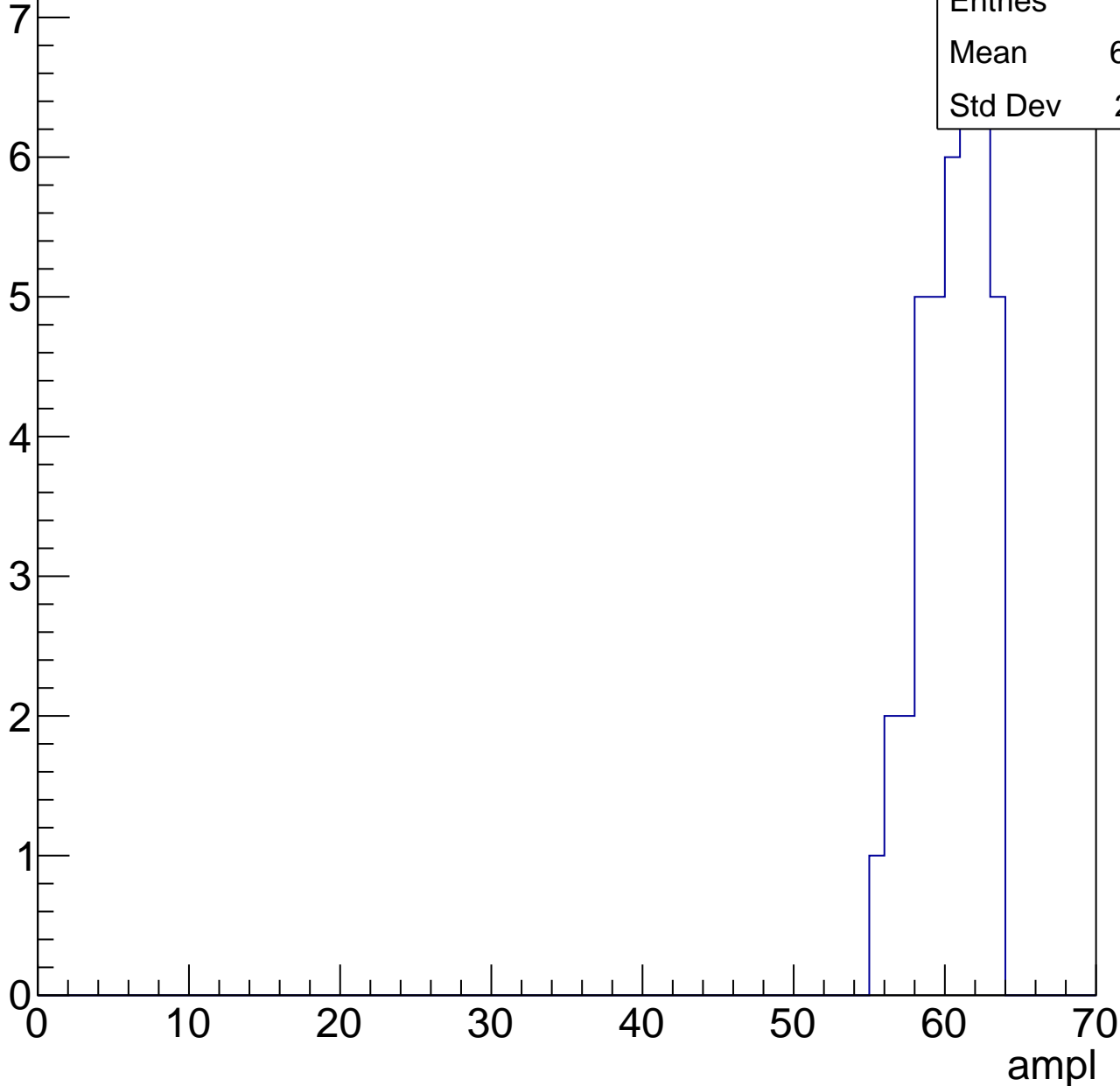


B1L100S, U25-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	60.05
Std Dev	2.121

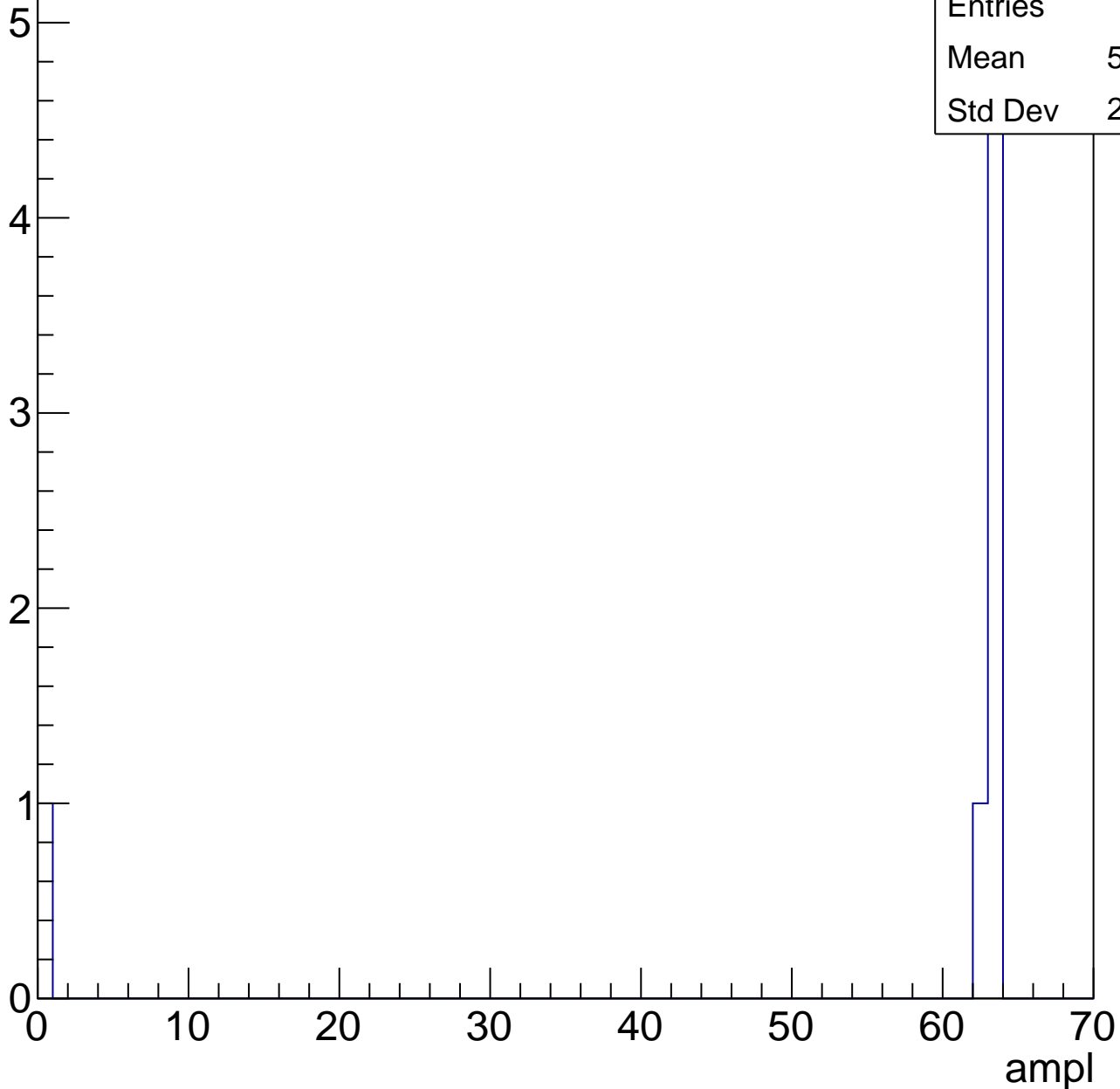


B1L100S, U25-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B1L100S, U25-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch43, adc0

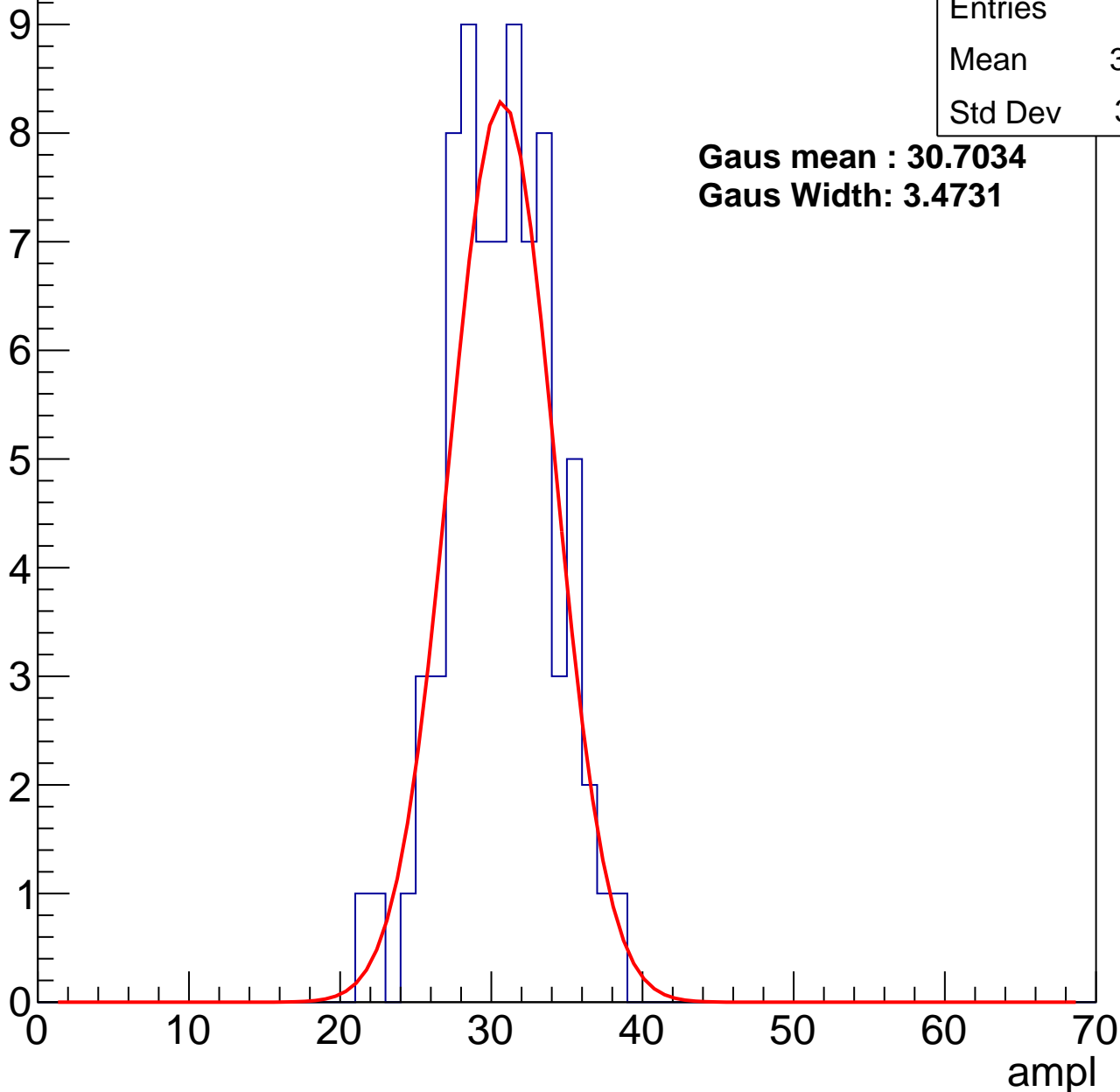
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.16
Std Dev	3.411

Gaus mean : 30.7034

Gaus Width: 3.4731



B1L100S, U25-ch43, adc1

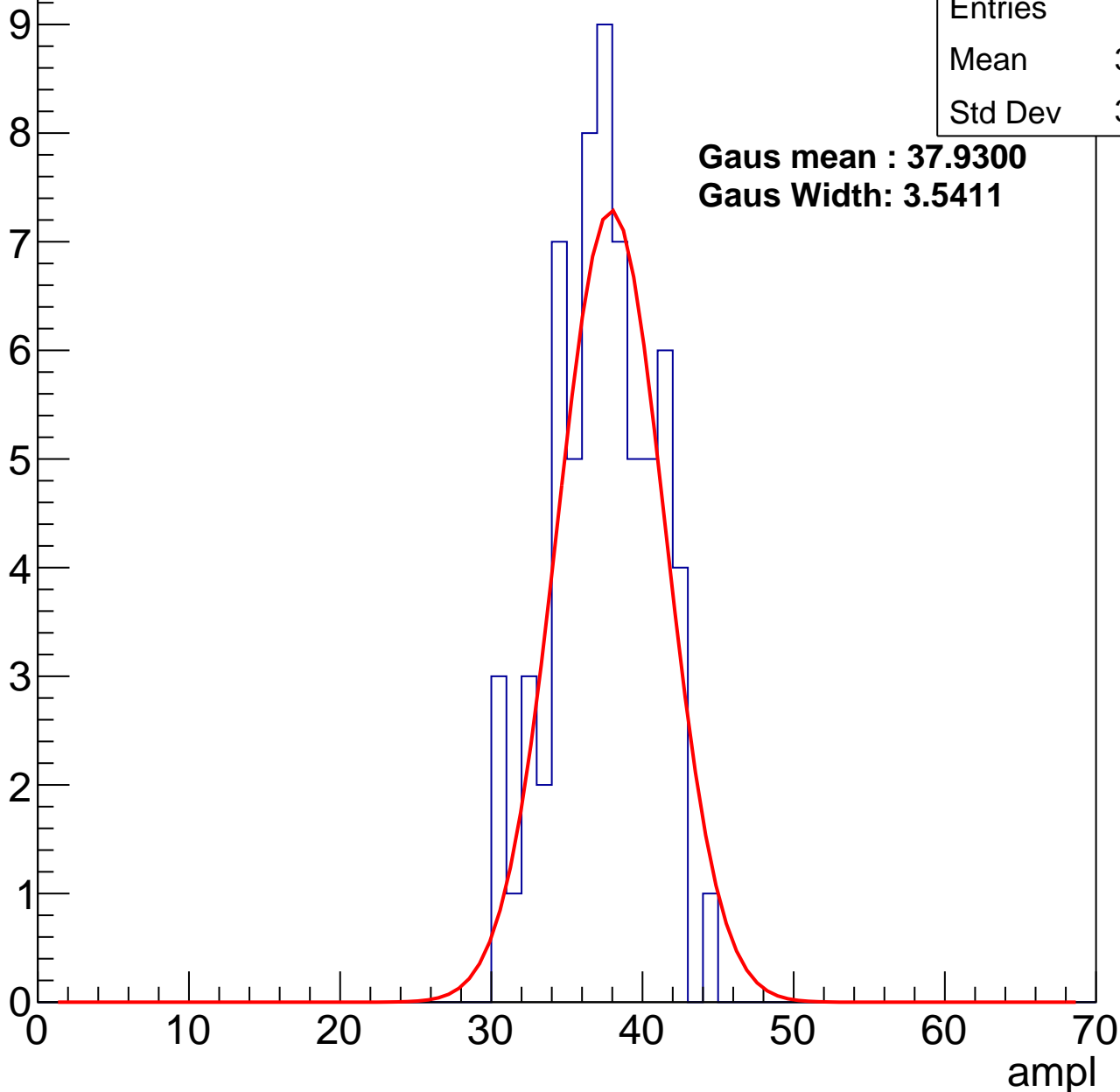
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.91
Std Dev	3.251

Gaus mean : 37.9300

Gaus Width: 3.5411



B1L100S, U25-ch43, adc2

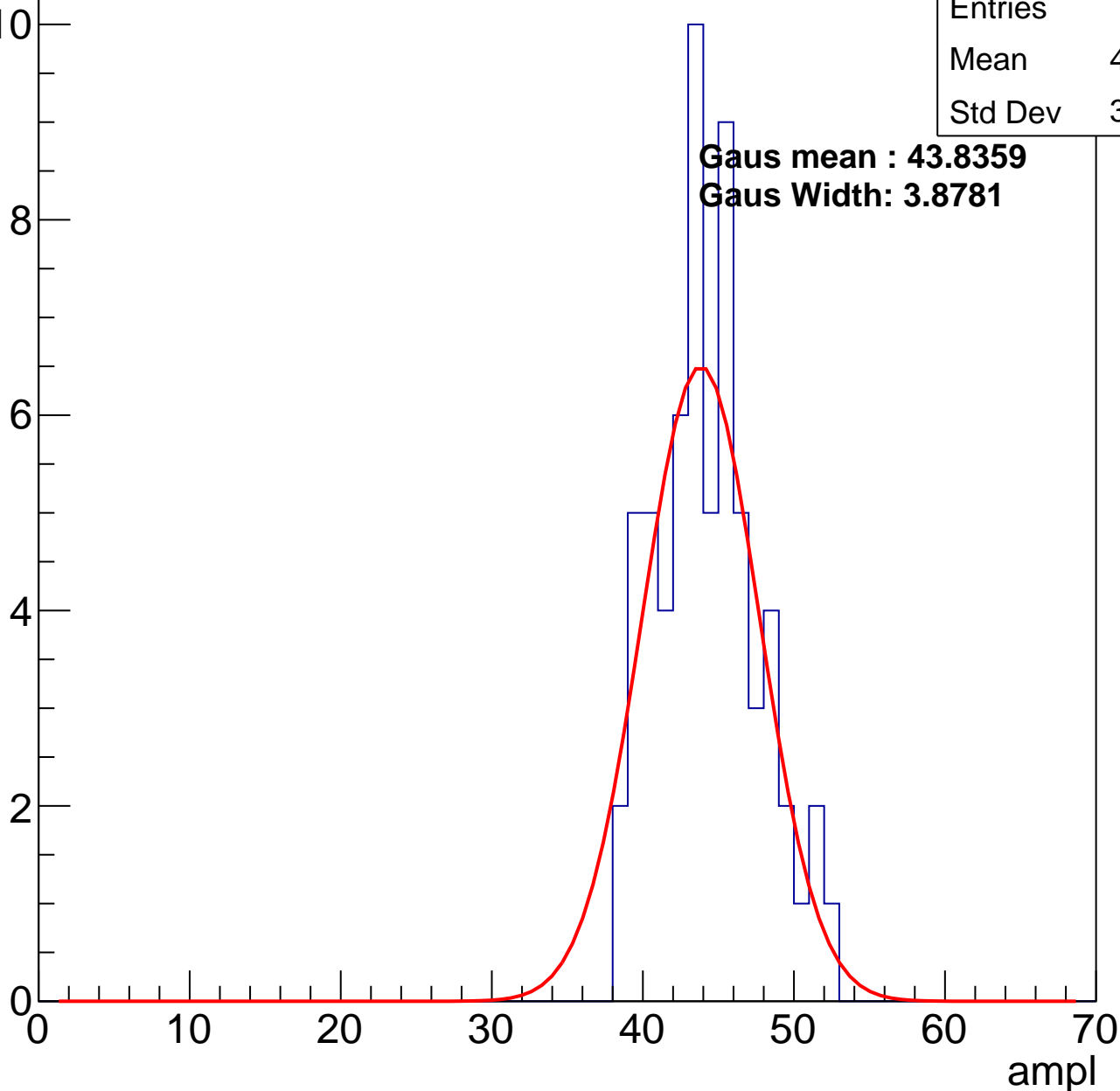
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.86
Std Dev	3.339

Gaus mean : 43.8359

Gaus Width: 3.8781

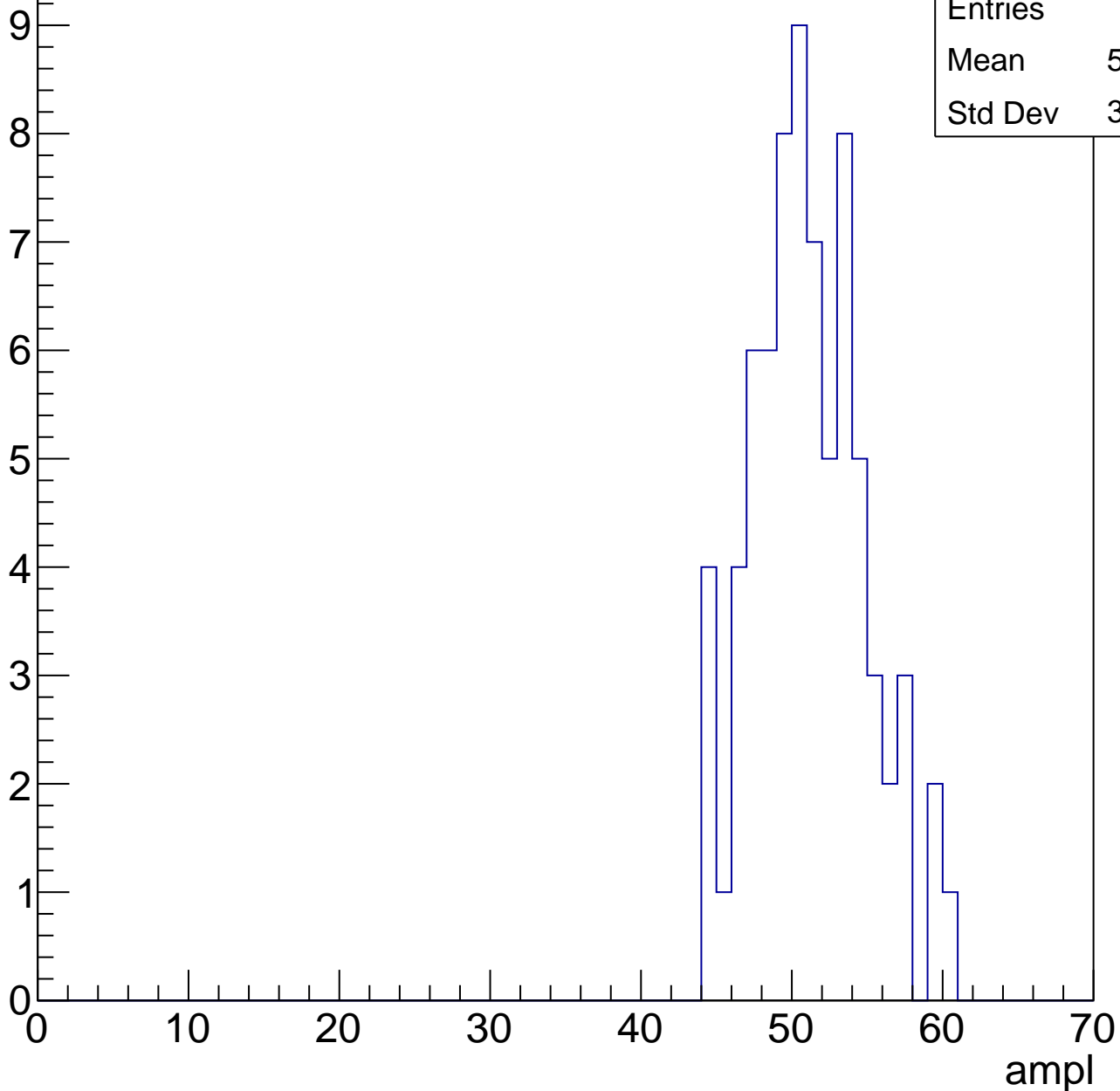


B1L100S, U25-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.73
Std Dev	3.703

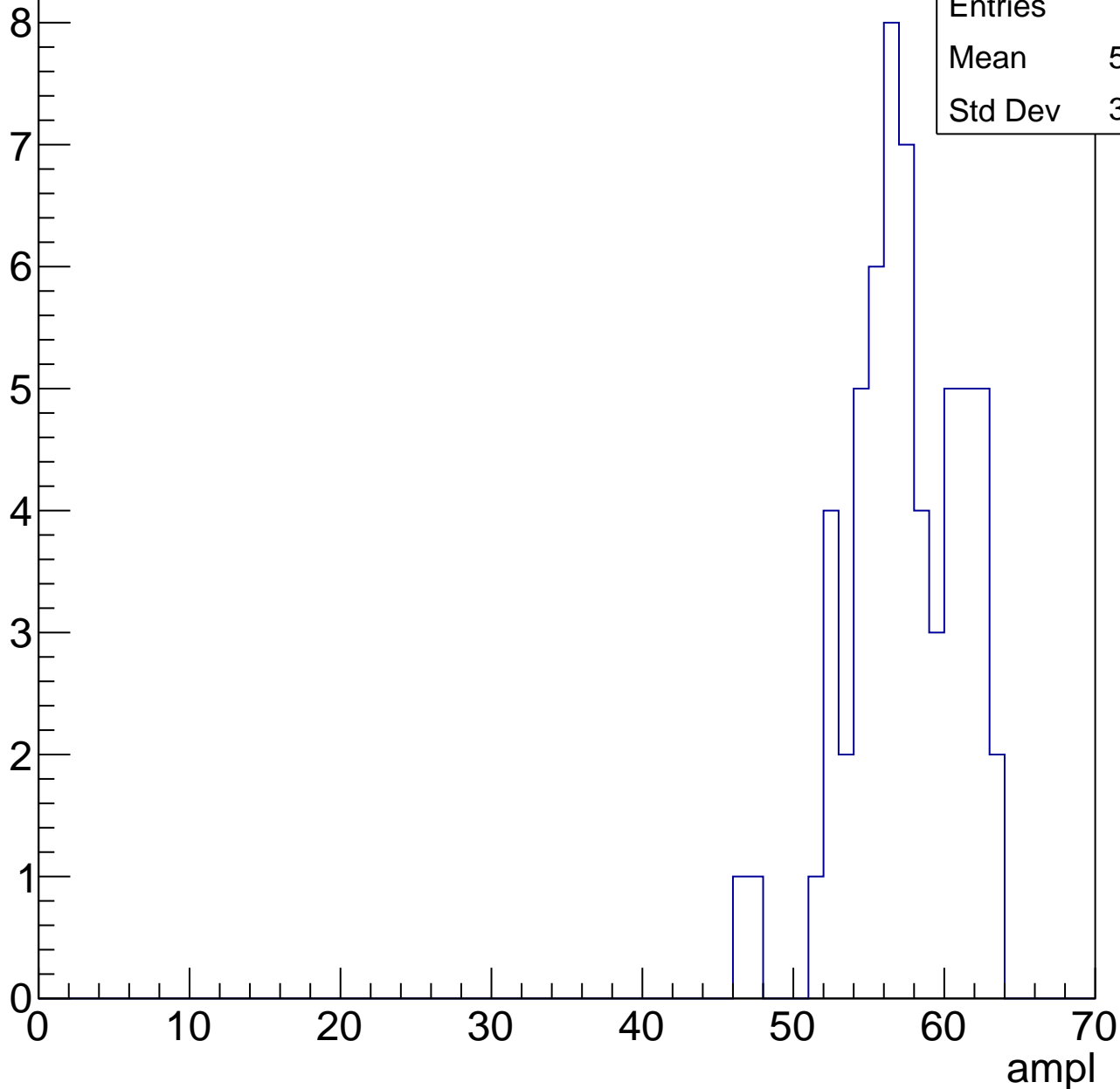


B1L100S, U25-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.86
Std Dev	3.689

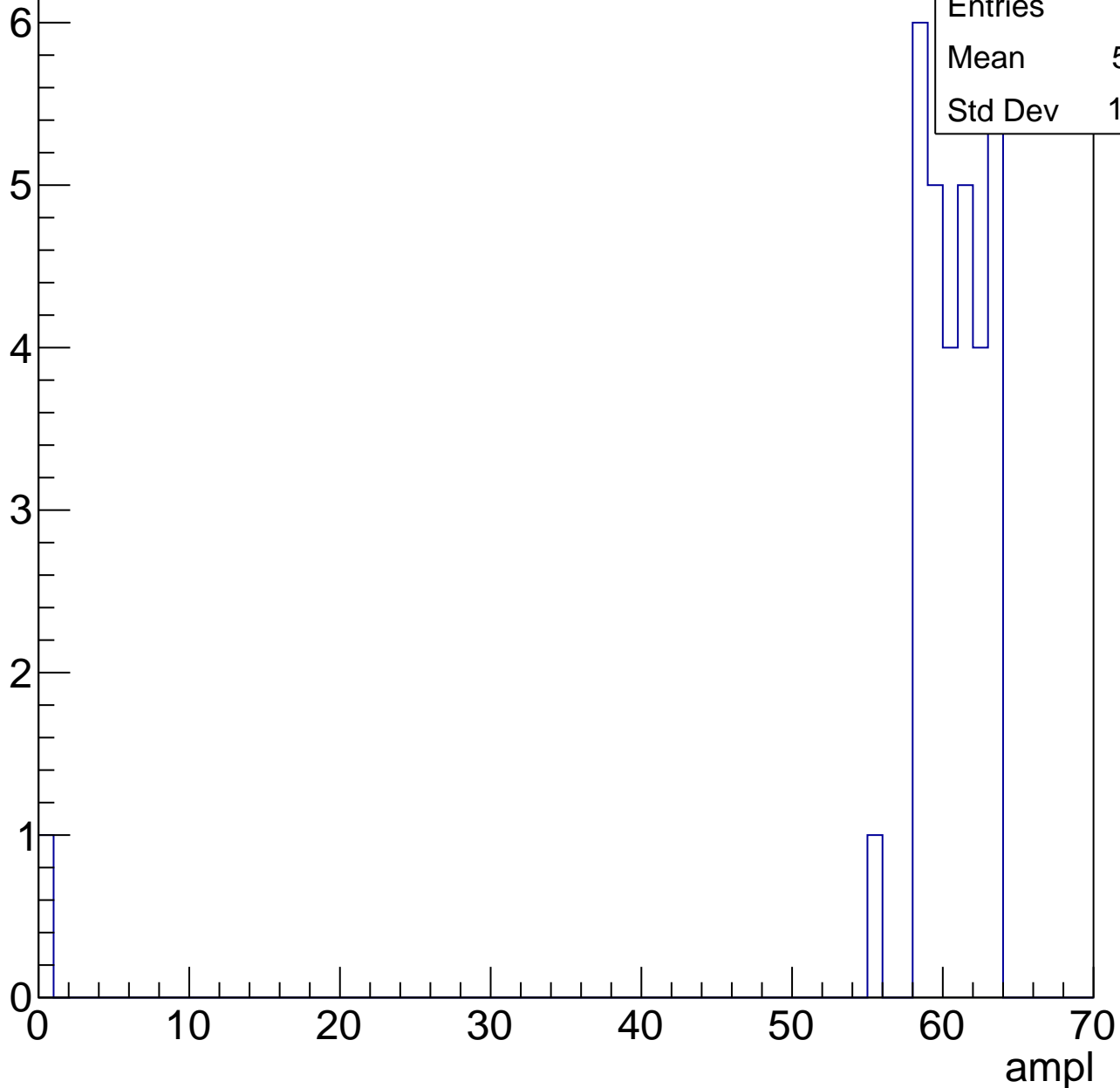


B1L100S, U25-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.41
Std Dev	10.68



B1L100S, U25-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch44, adc0

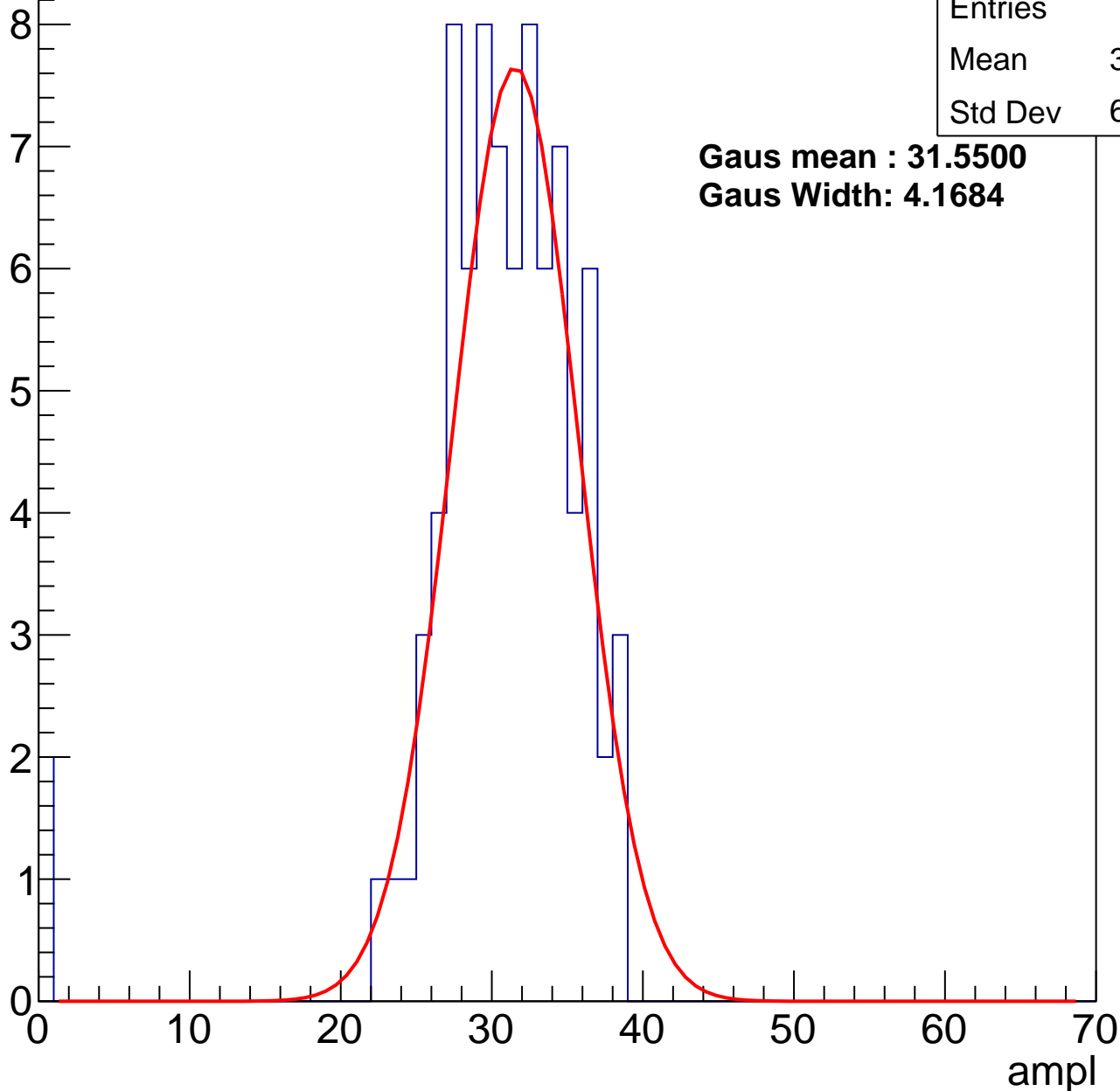
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	30.07
Std Dev	6.014

Gaus mean : 31.5500

Gaus Width: 4.1684



B1L100S, U25-ch44, adc1

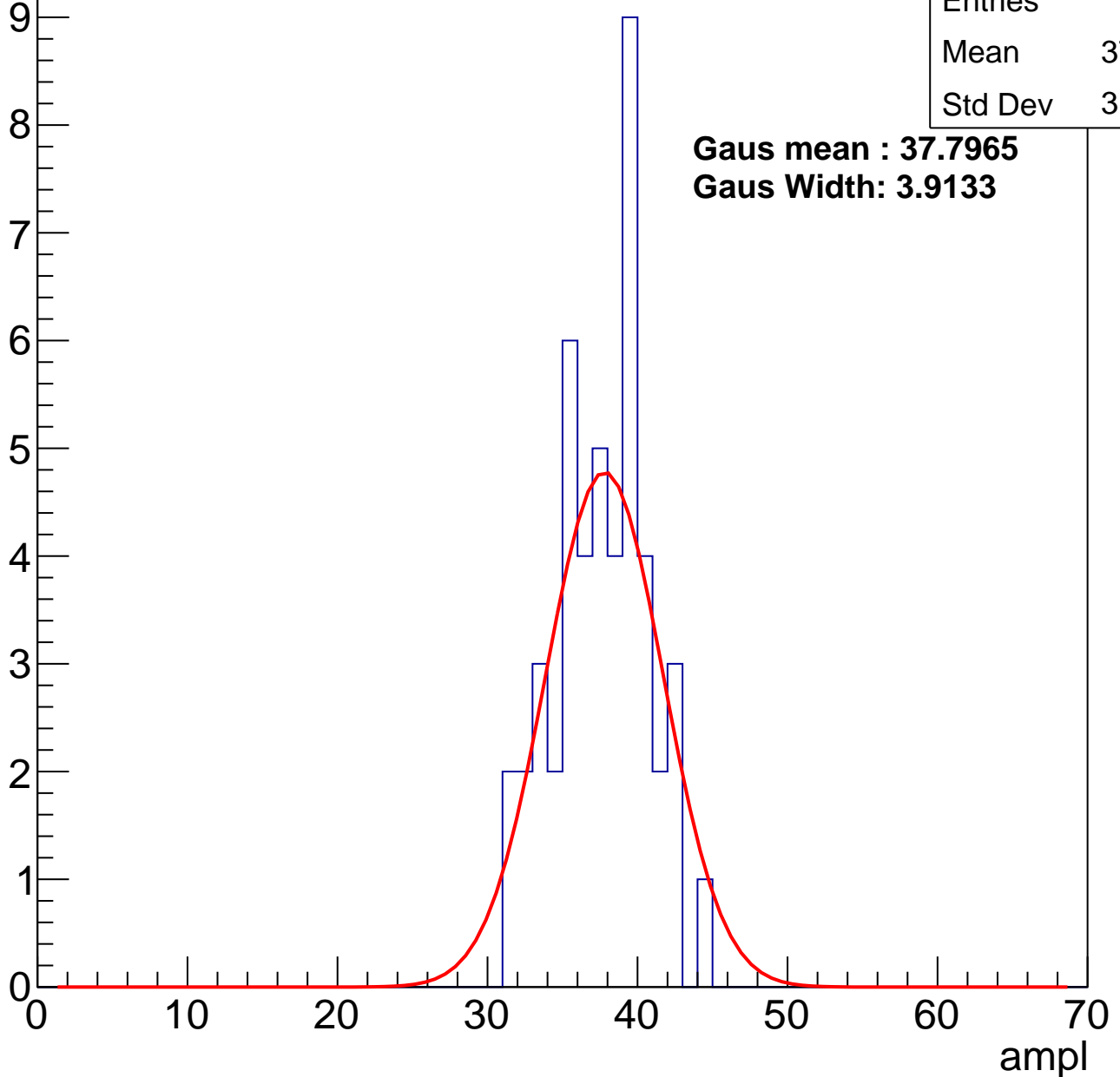
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	37.17
Std Dev	3.083

Gaus mean : 37.7965

Gaus Width: 3.9133



B1L100S, U25-ch44, adc2

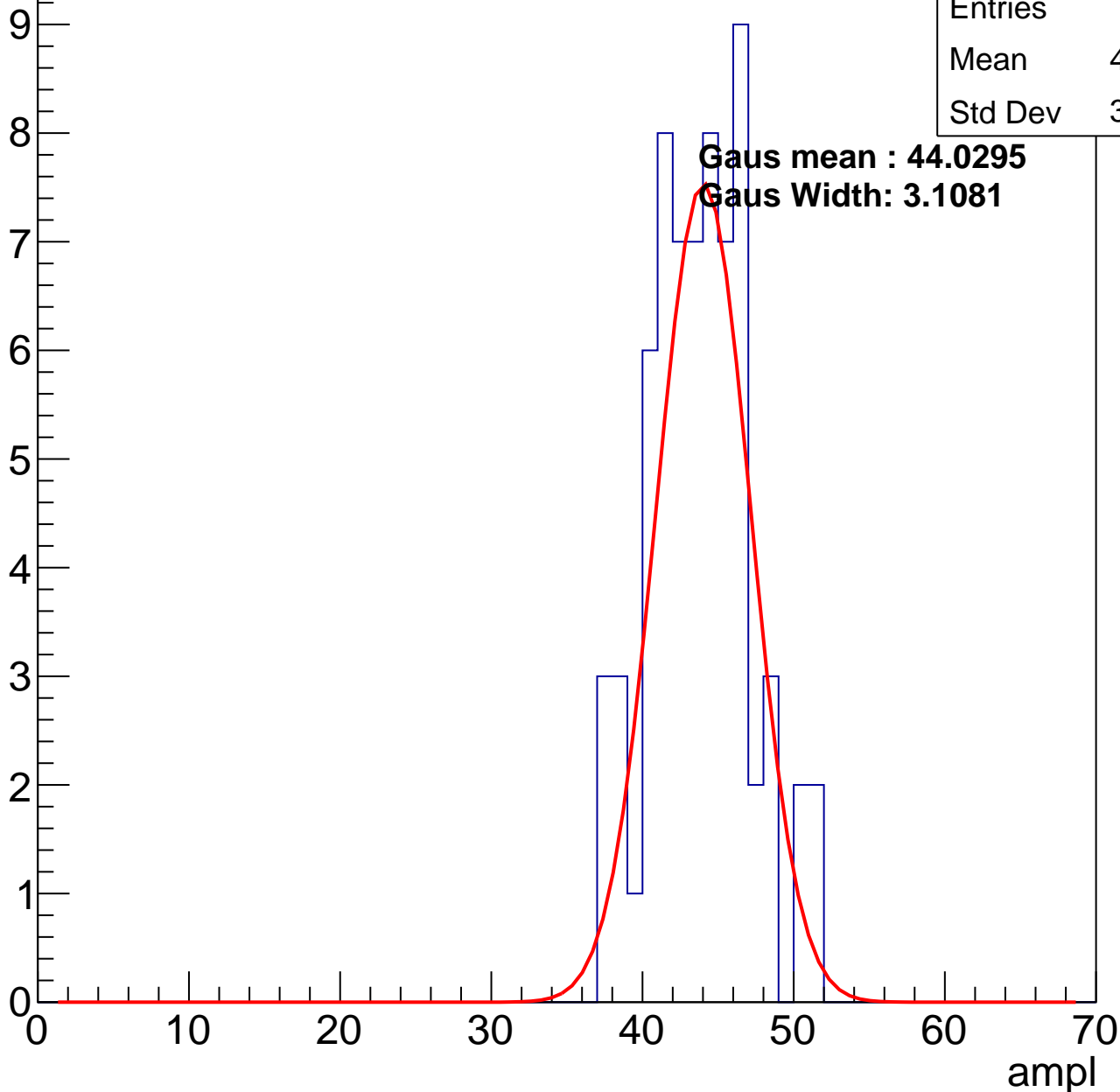
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.35
Std Dev	3.284

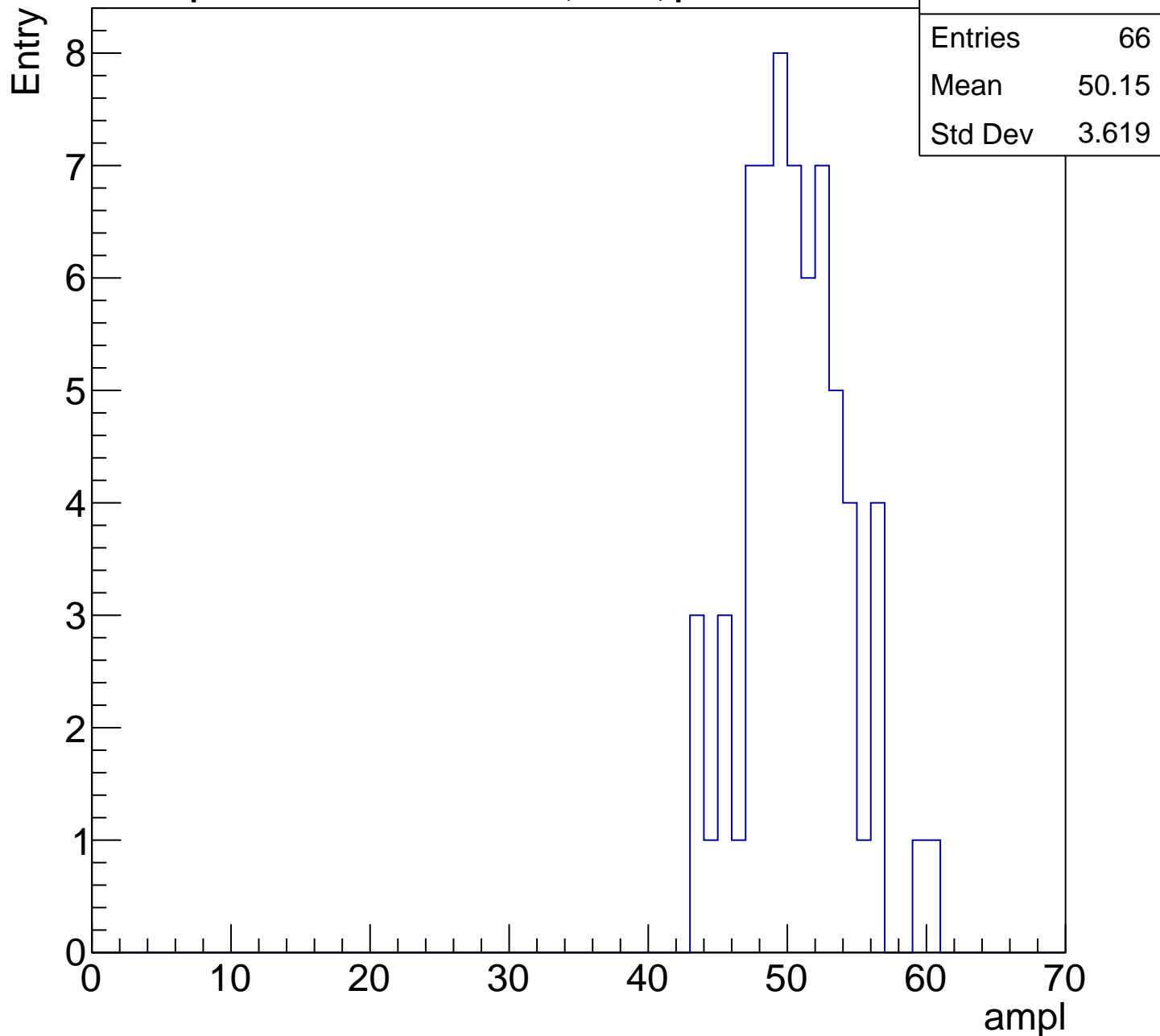
Gaus mean : 44.0295

Gaus Width: 3.1081



B1L100S, U25-ch44, adc3

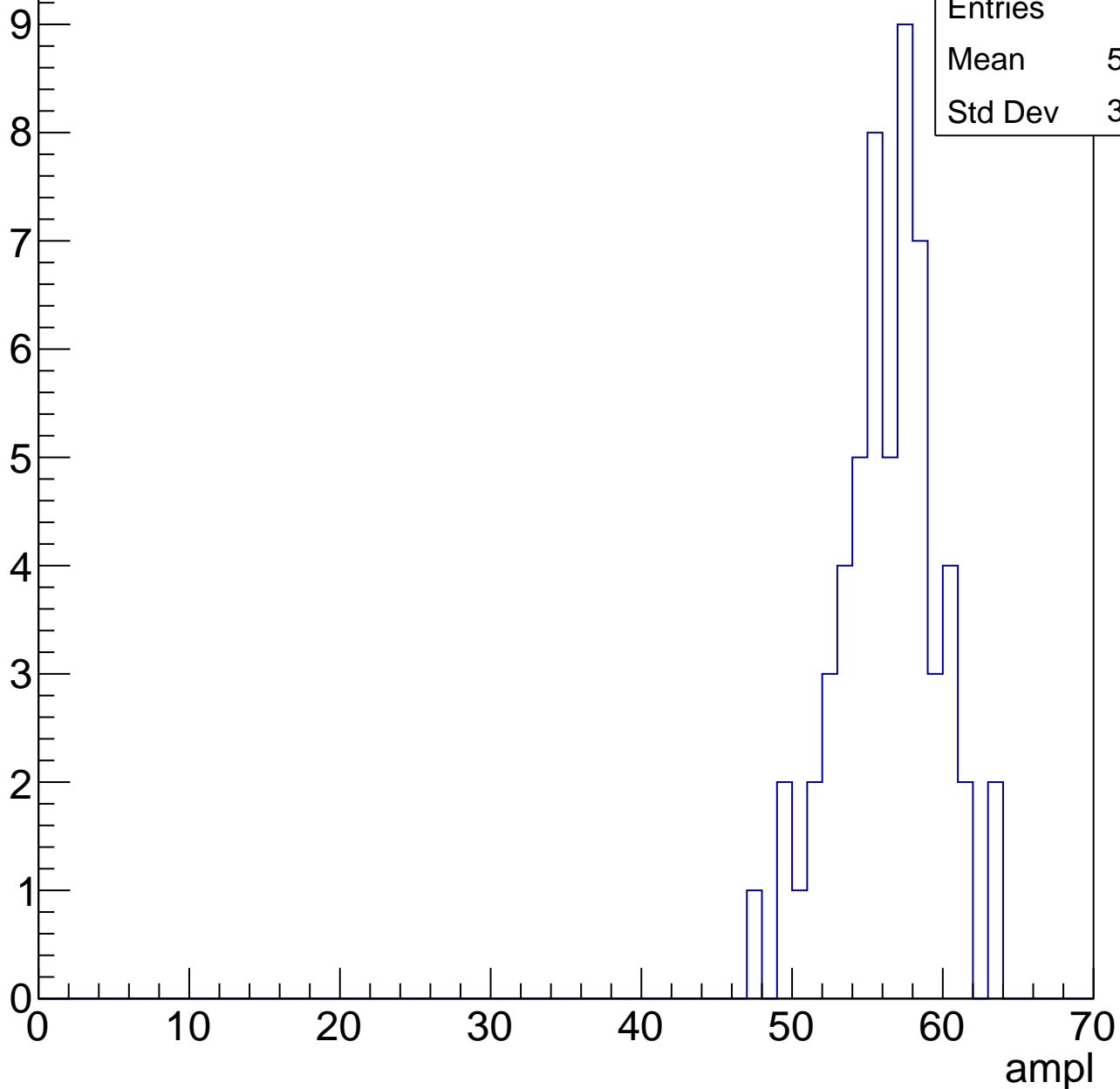
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

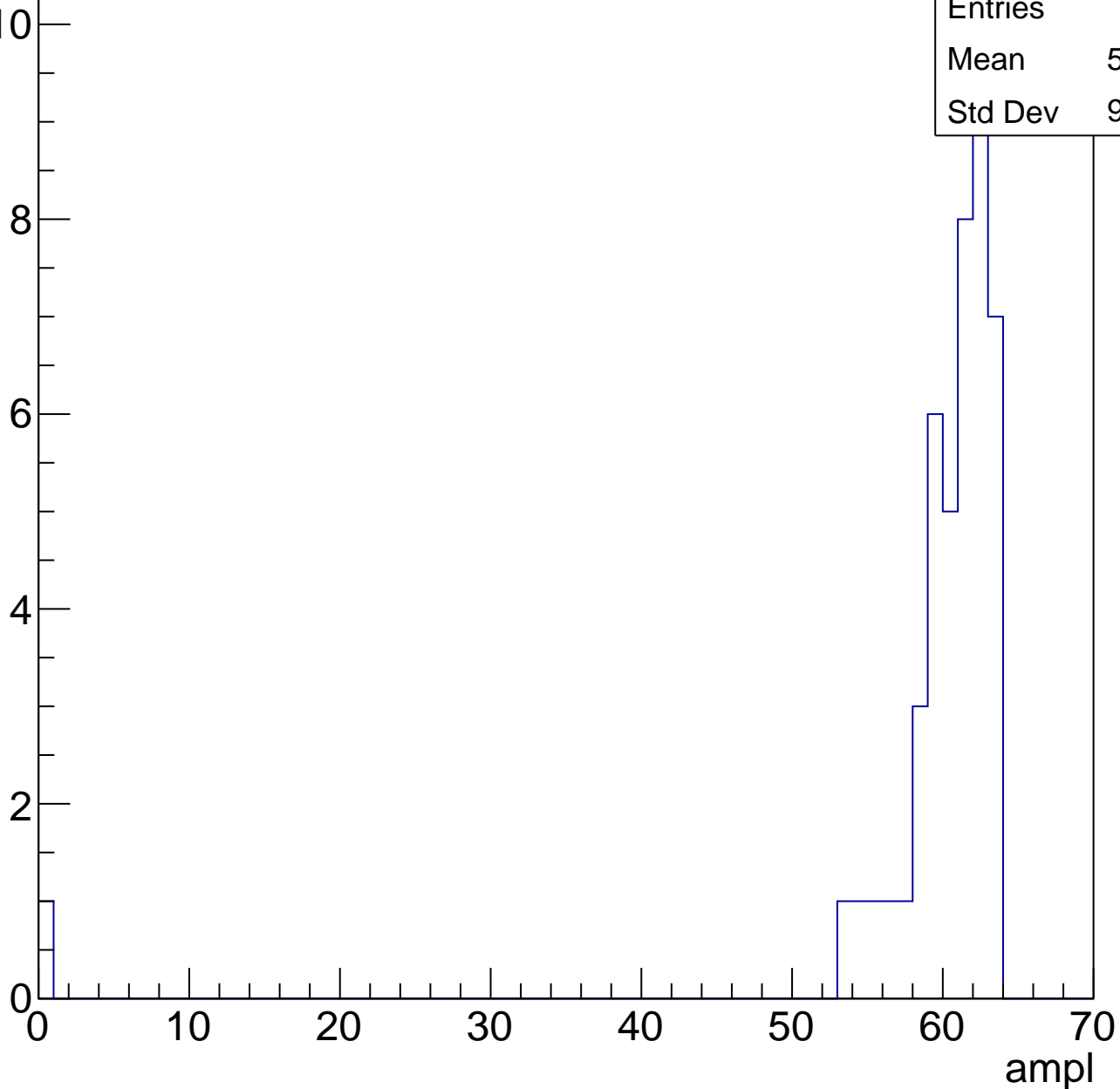


B1L100S, U25-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

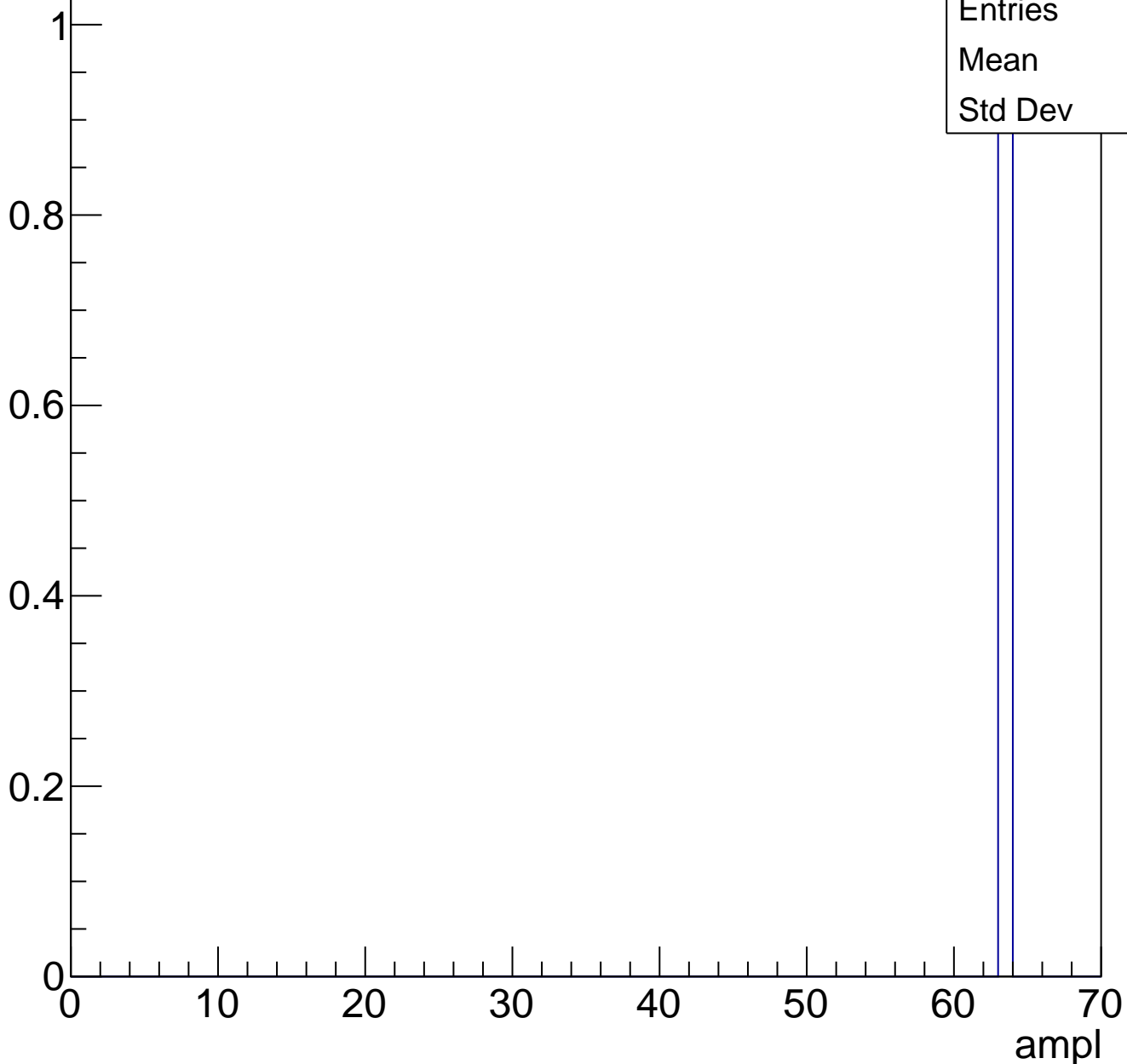
Entries	45
Mean	58.93
Std Dev	9.205



B1L100S, U25-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch45, adc0

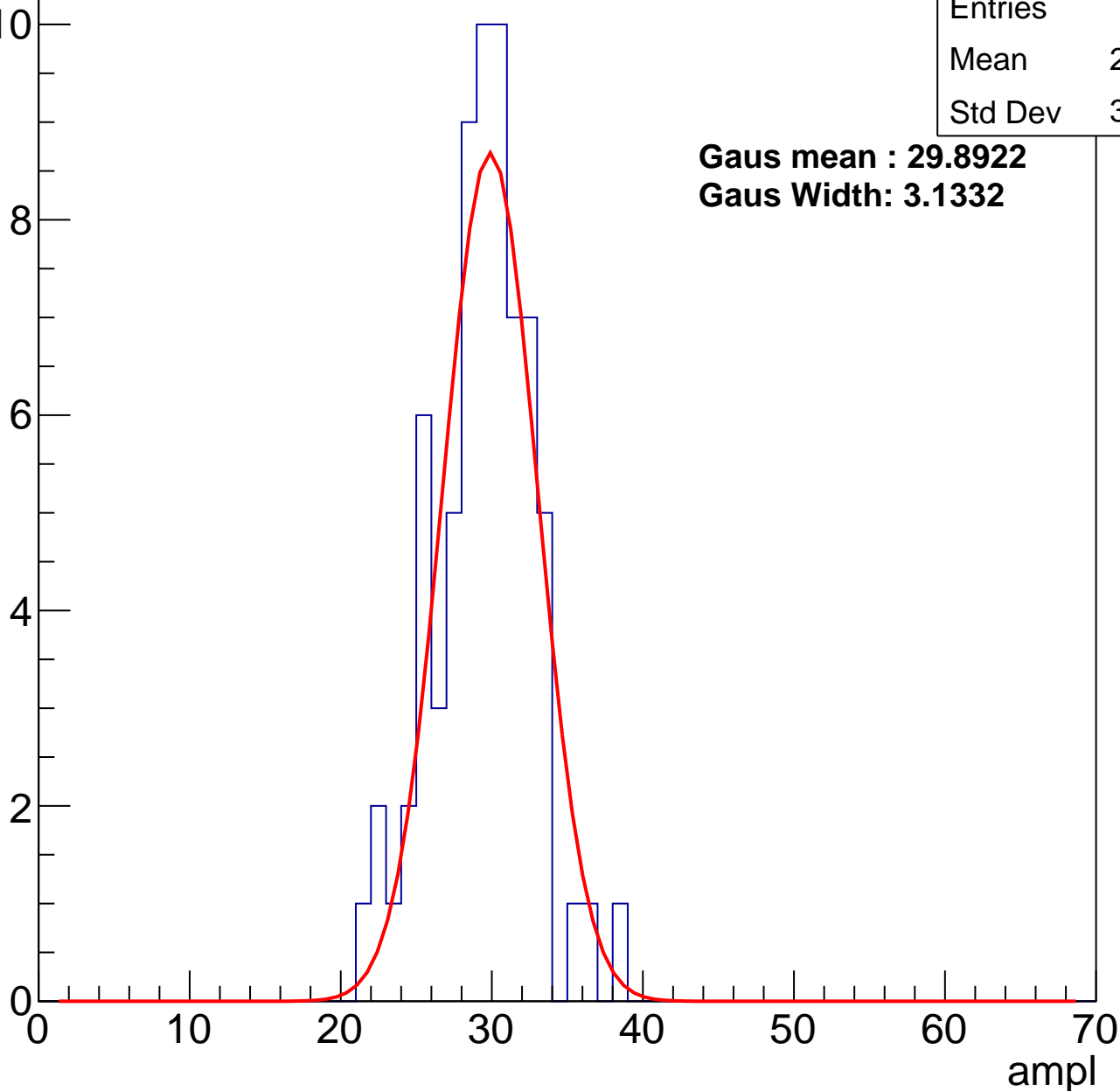
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	28.96
Std Dev	3.243

Gaus mean : 29.8922

Gaus Width: 3.1332



B1L100S, U25-ch45, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	35.78
Std Dev	2.933

Gaus mean : 36.3103

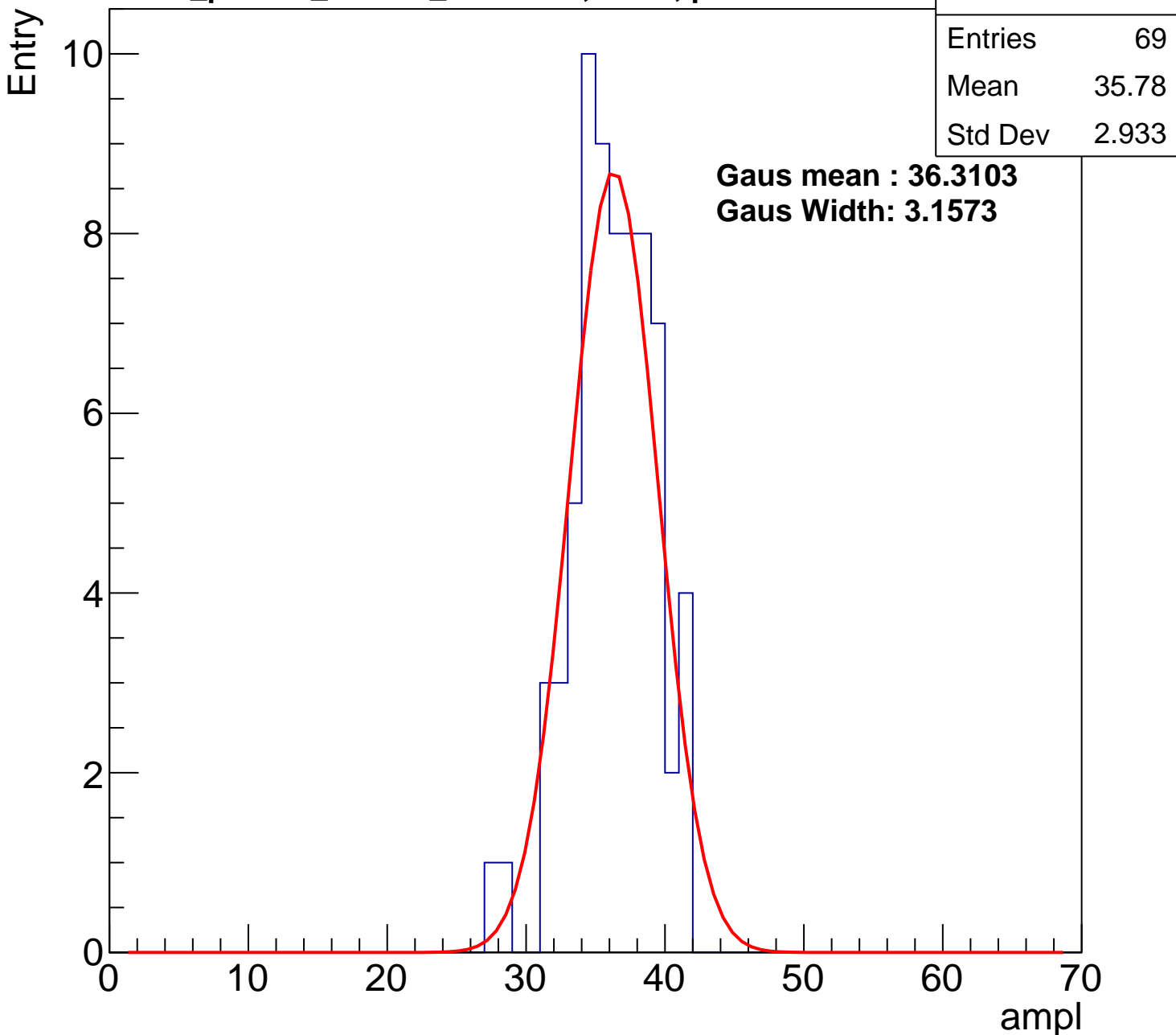
Gaus Width: 3.1573

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch45, adc2

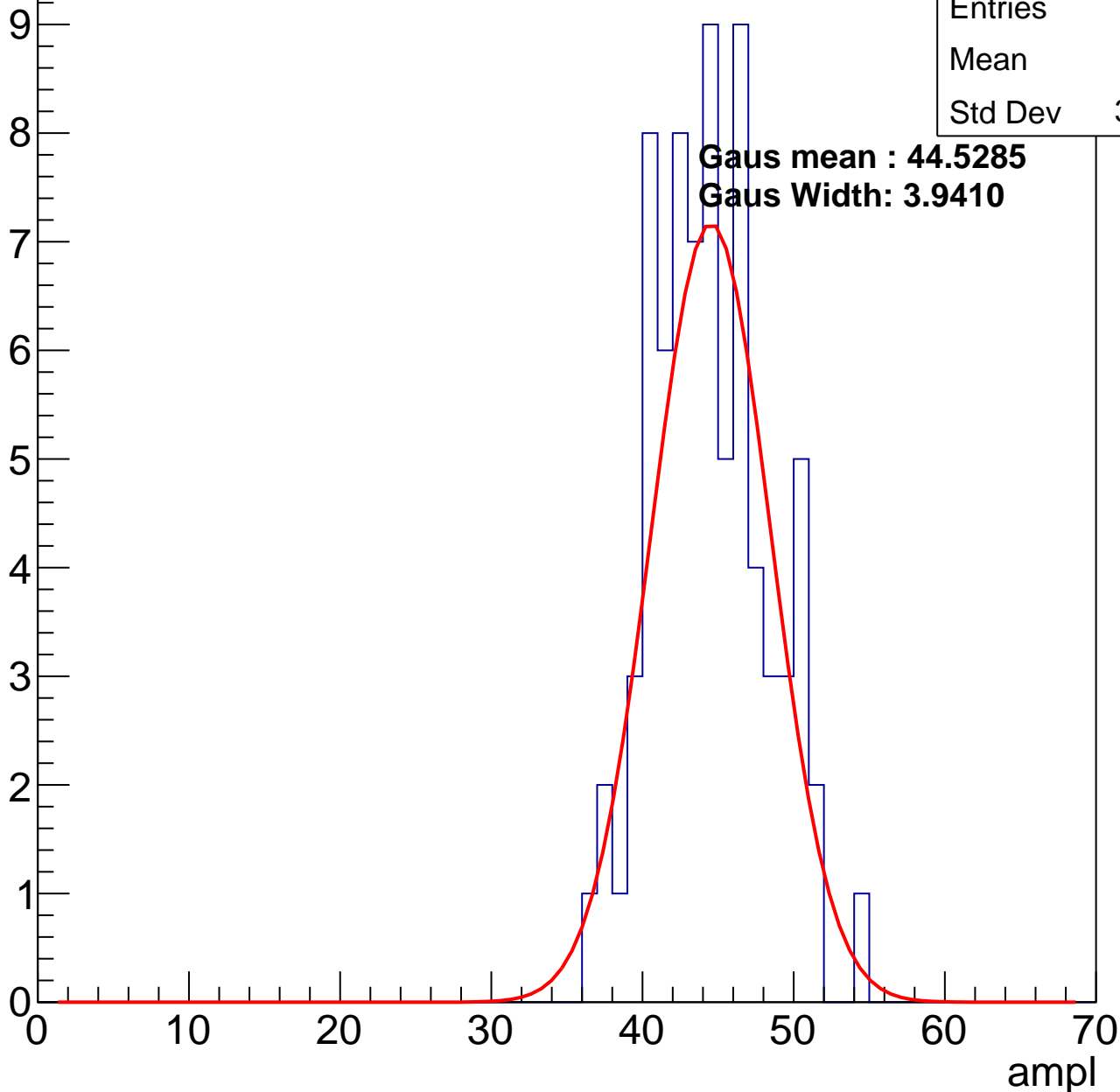
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	44
Std Dev	3.731

Gaus mean : 44.5285

Gaus Width: 3.9410

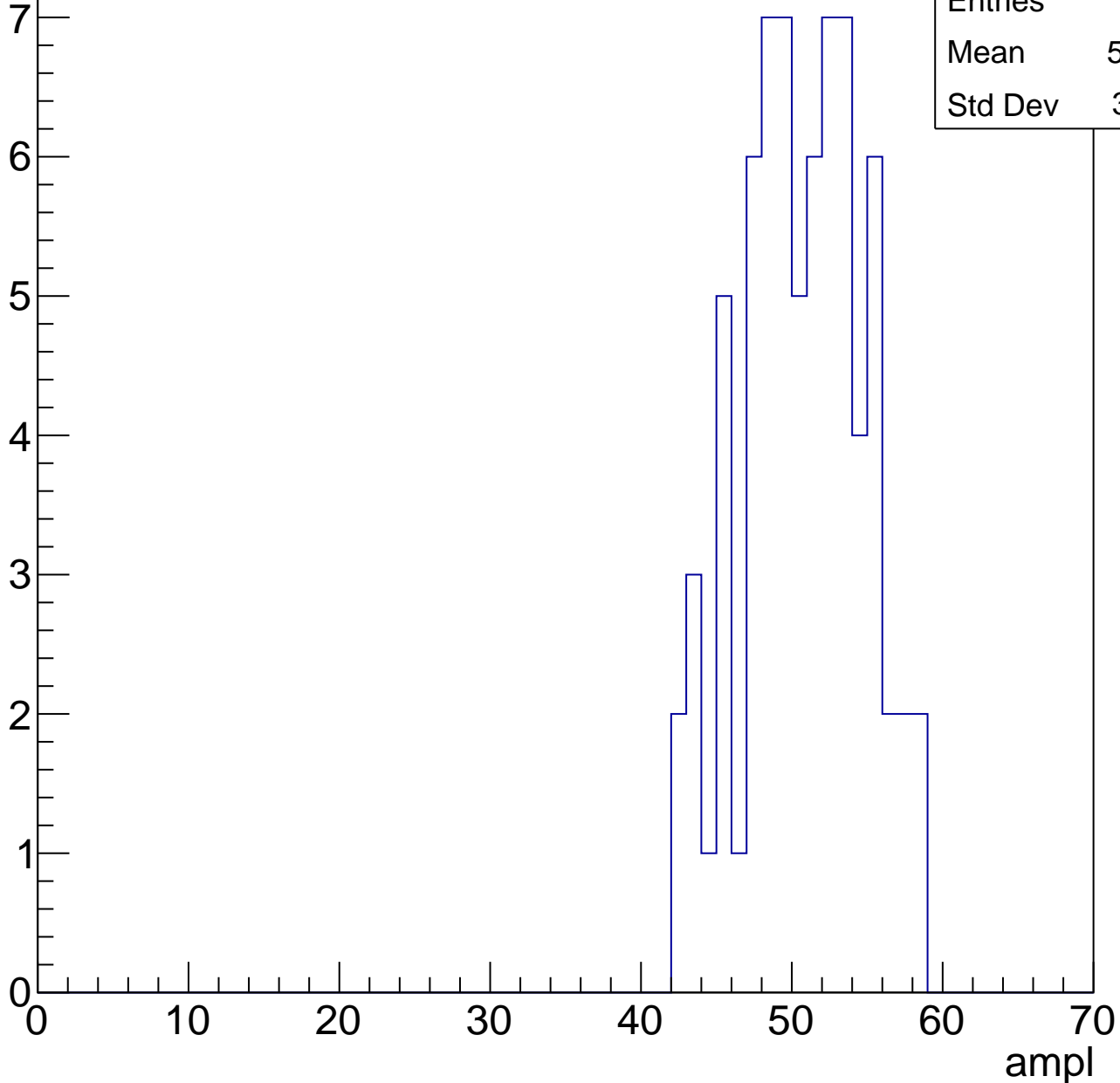


B1L100S, U25-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.25
Std Dev	3.961

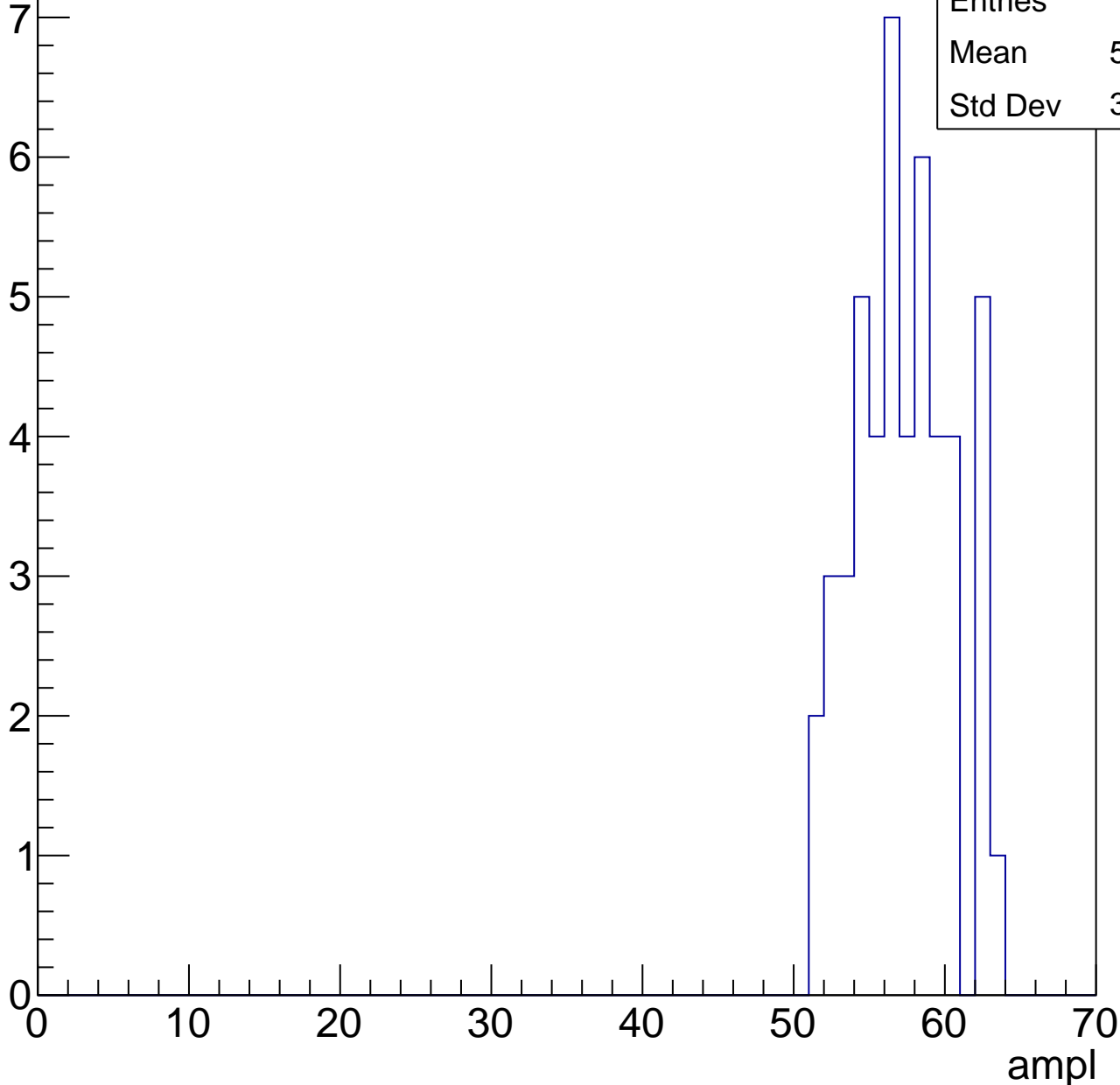


B1L100S, U25-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	56.75
Std Dev	3.159



B1L100S, U25-ch45, adc5

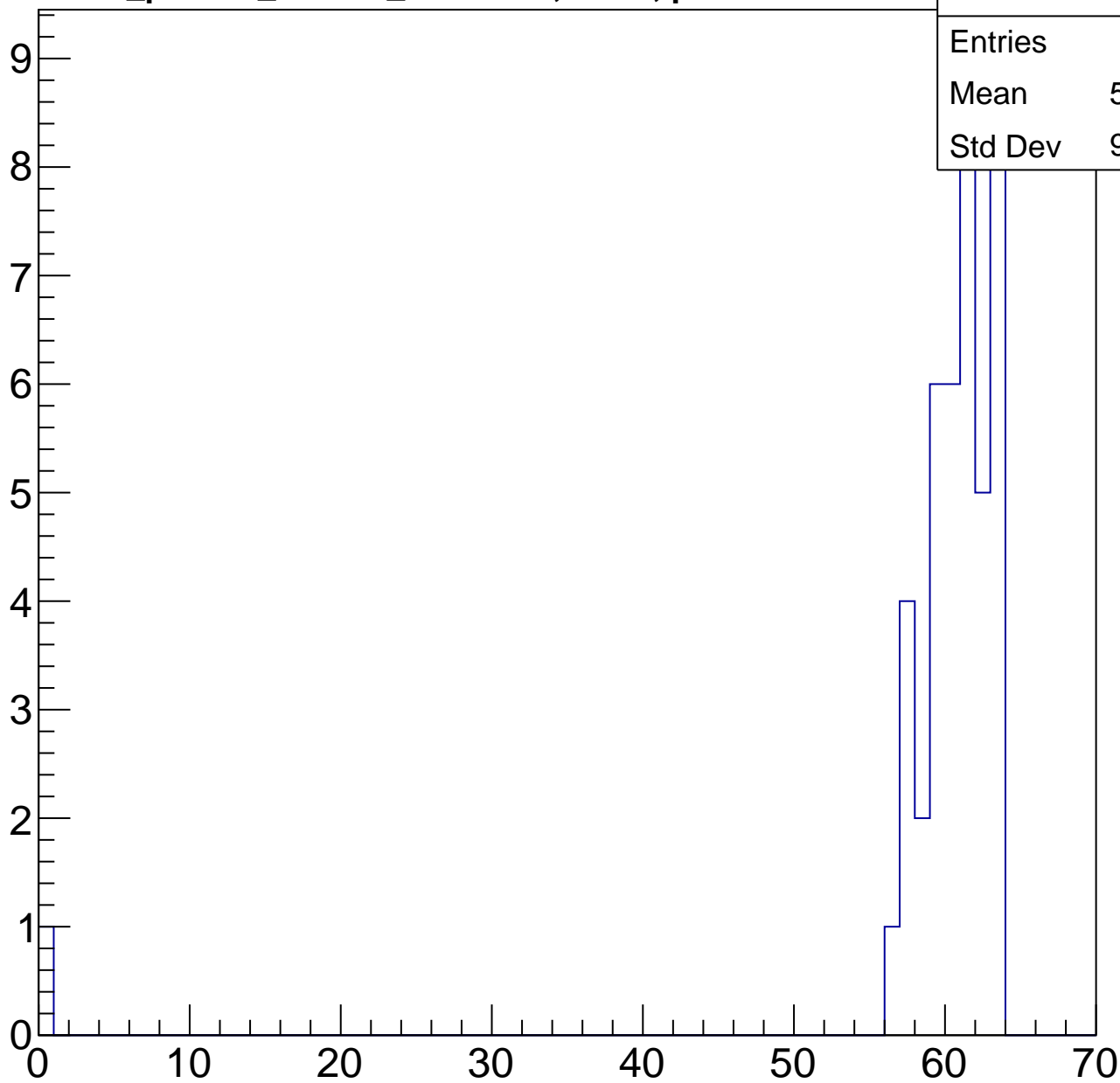
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.98
Std Dev	9.415

ampl



B1L100S, U25-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch46, adc0

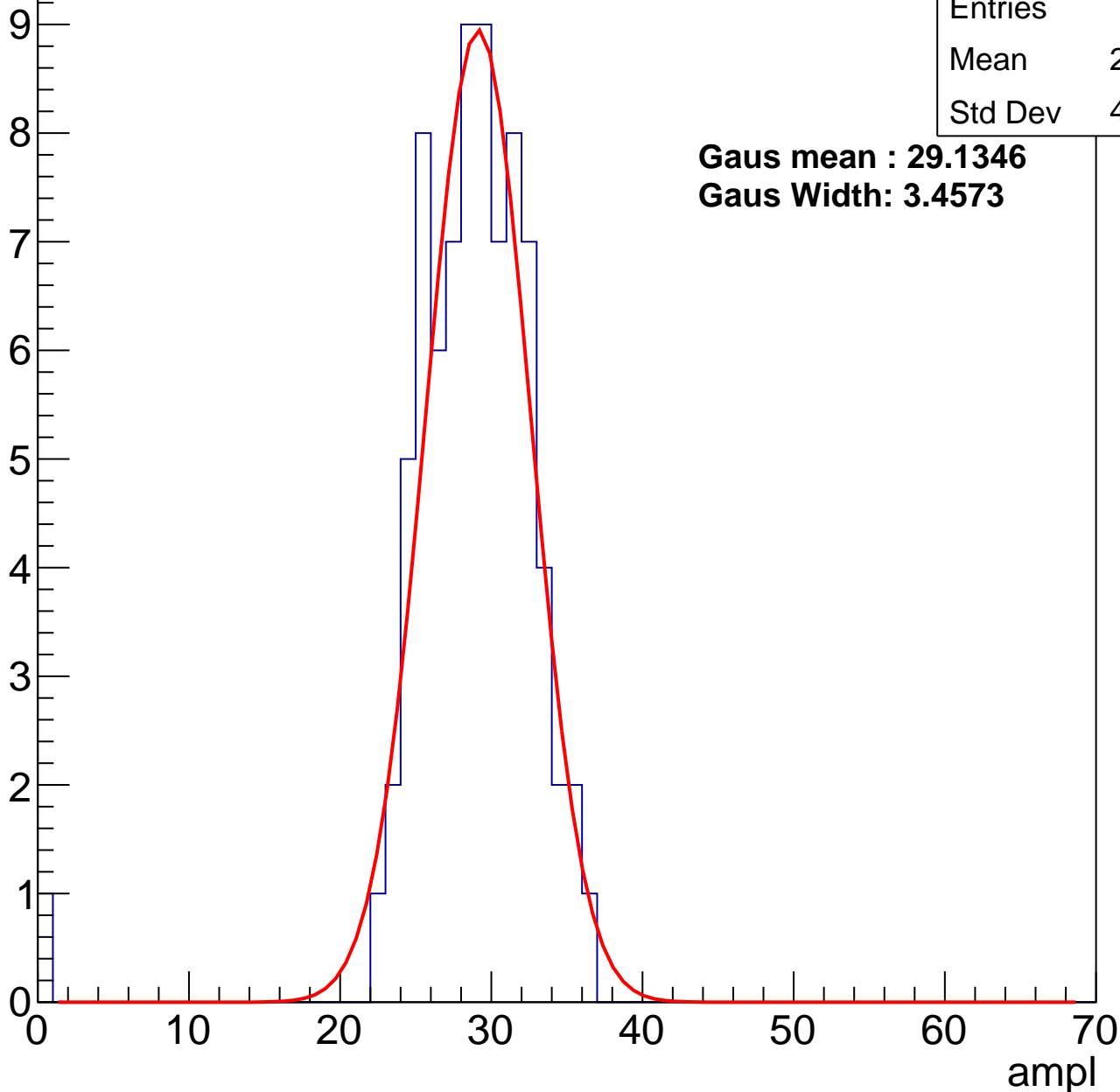
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	28.28
Std Dev	4.503

Gaus mean : 29.1346

Gaus Width: 3.4573



B1L100S, U25-ch46, adc1

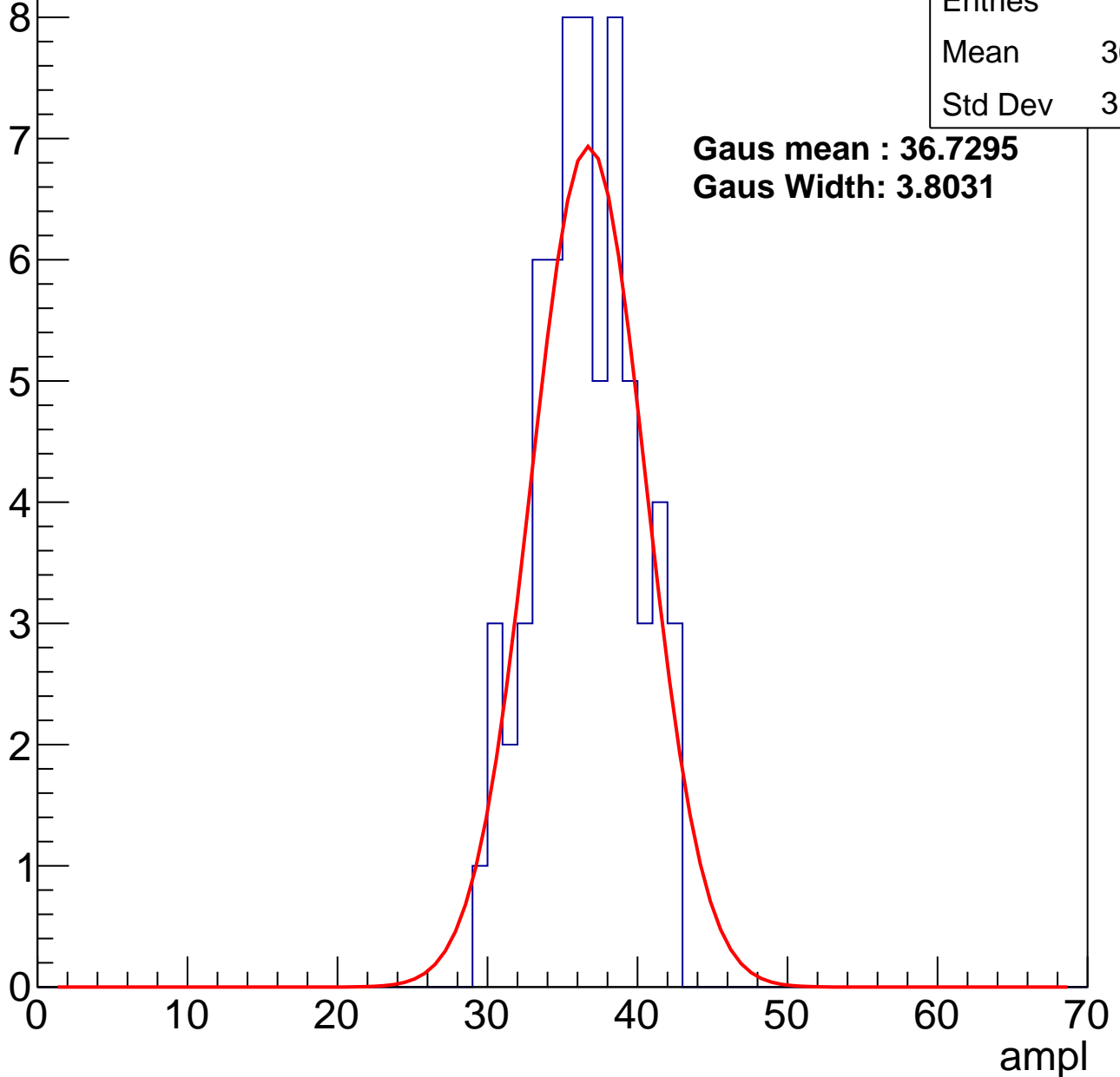
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.02
Std Dev	3.232

Gaus mean : 36.7295

Gaus Width: 3.8031



B1L100S, U25-ch46, adc2

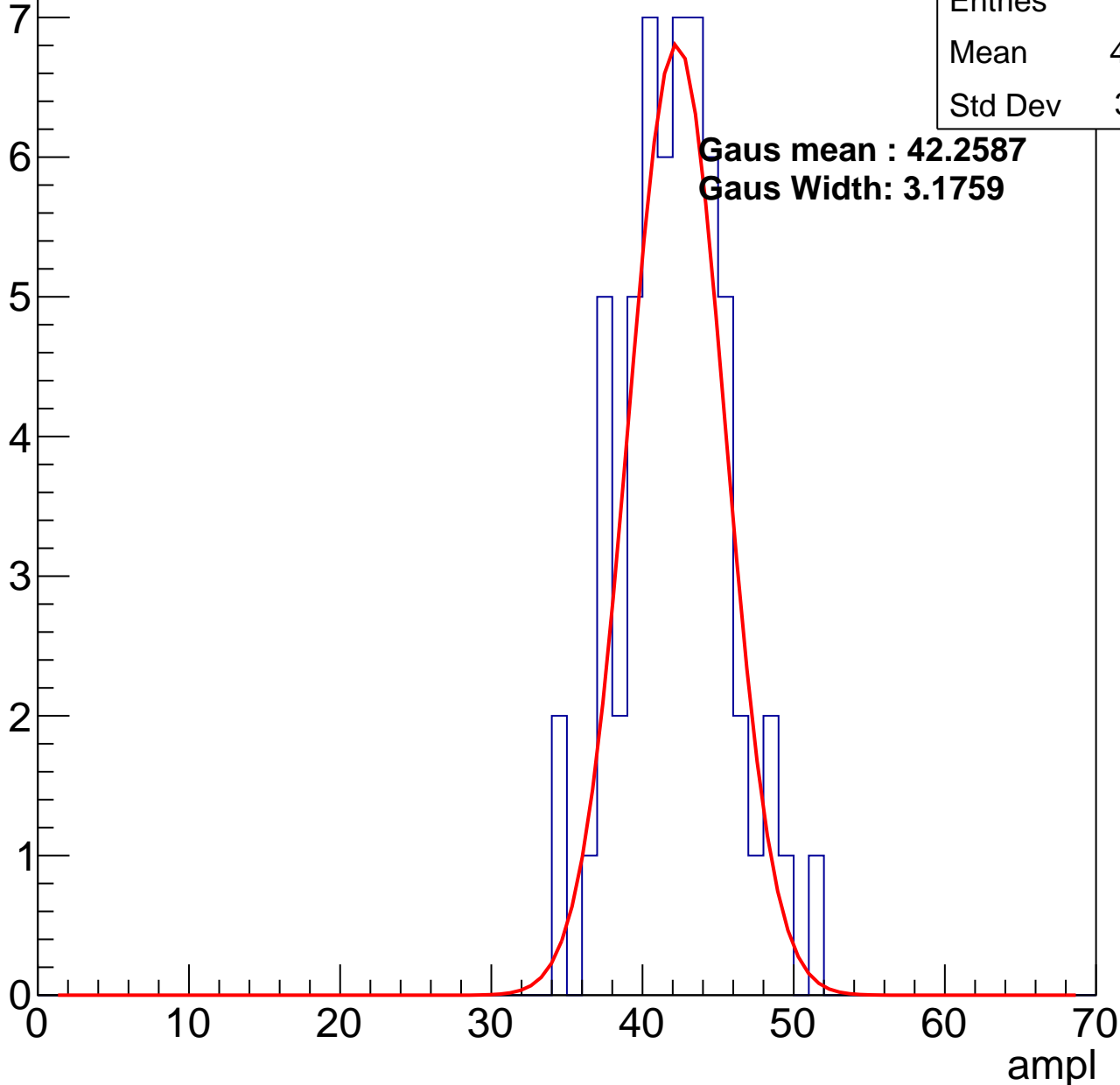
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	41.75
Std Dev	3.491

Gaus mean : 42.2587

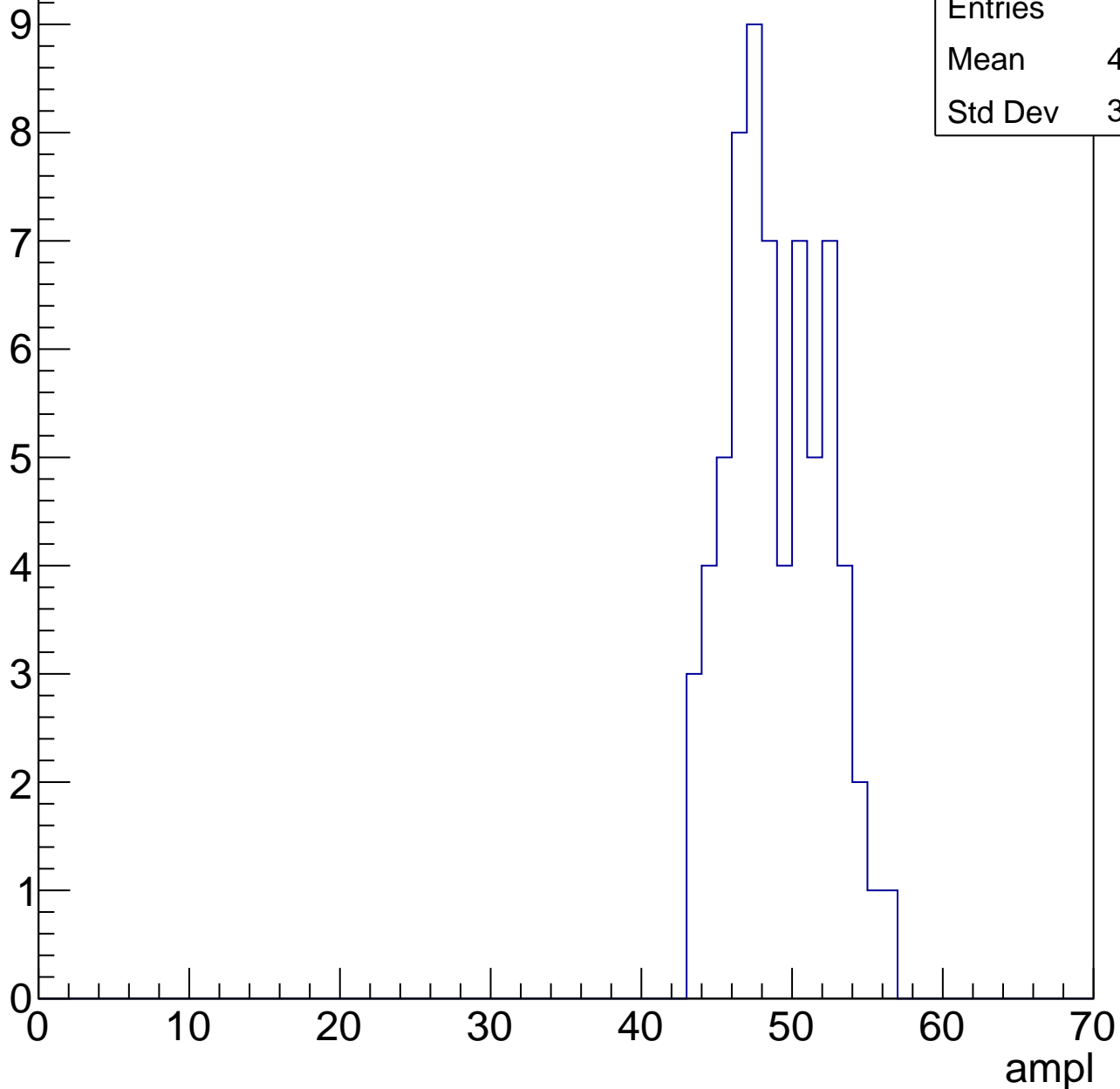
Gaus Width: 3.1759



B1L100S, U25-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

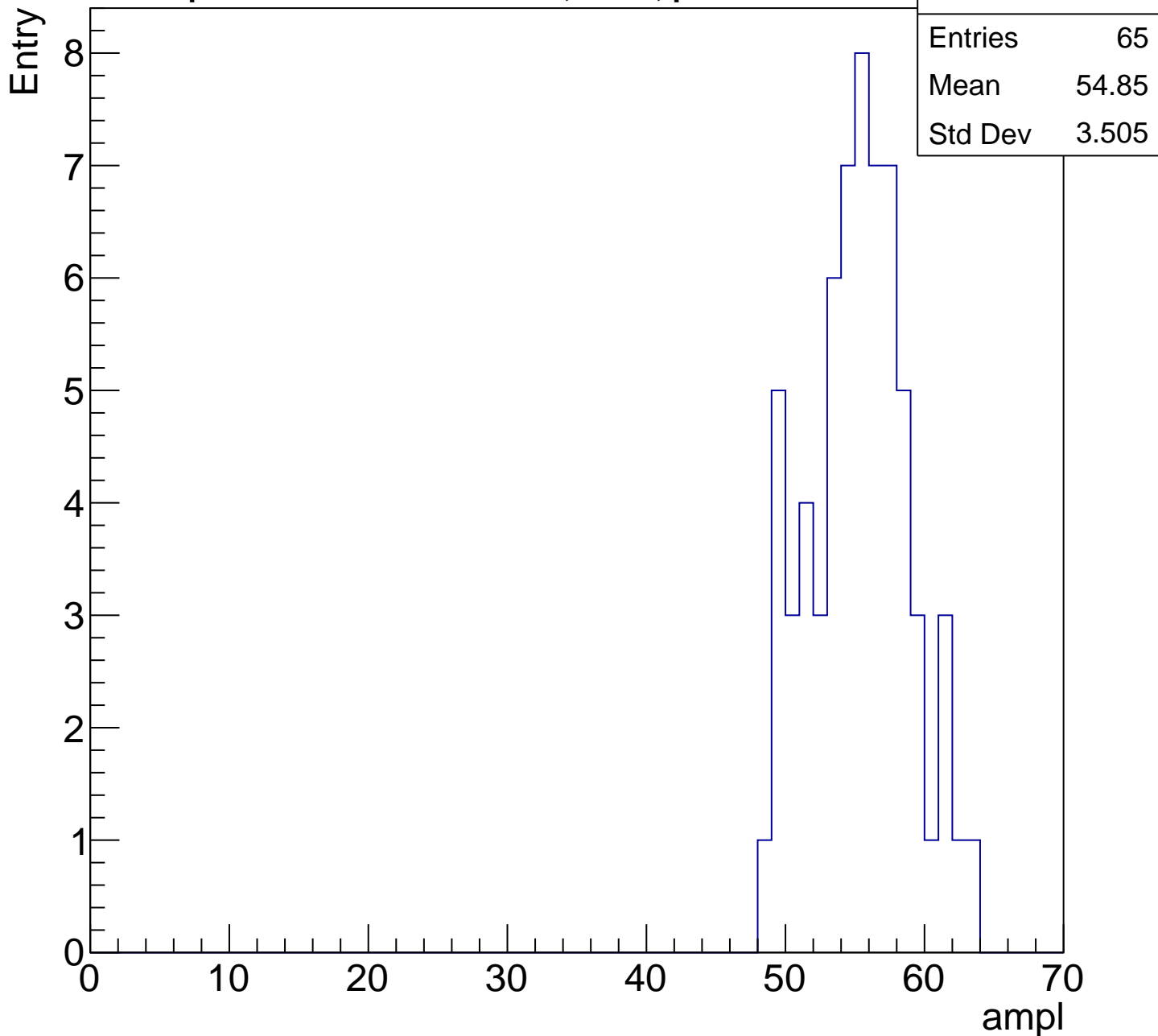
Entry



Entries	67
Mean	48.55
Std Dev	3.182

B1L100S, U25-ch46, adc4

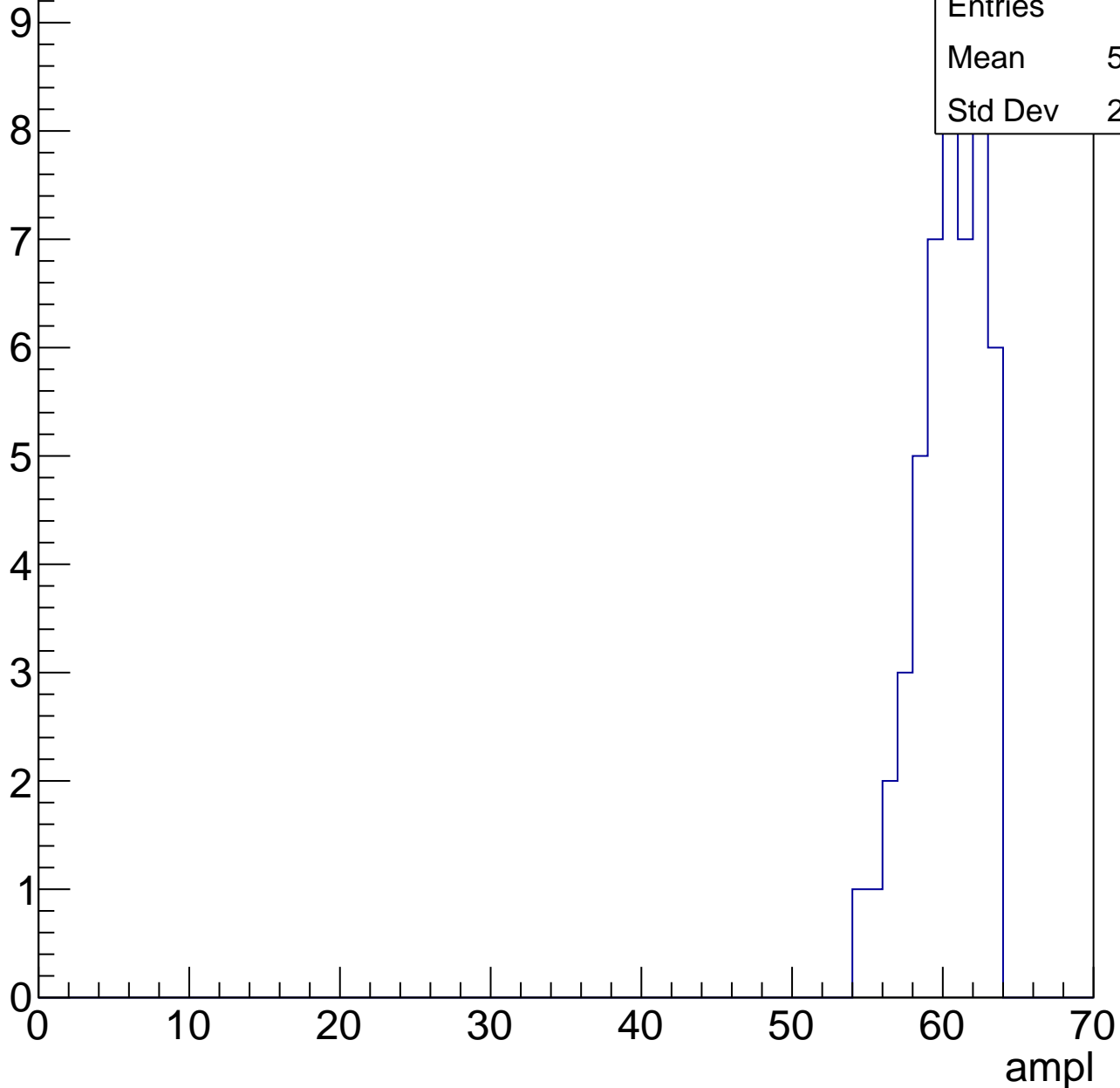
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

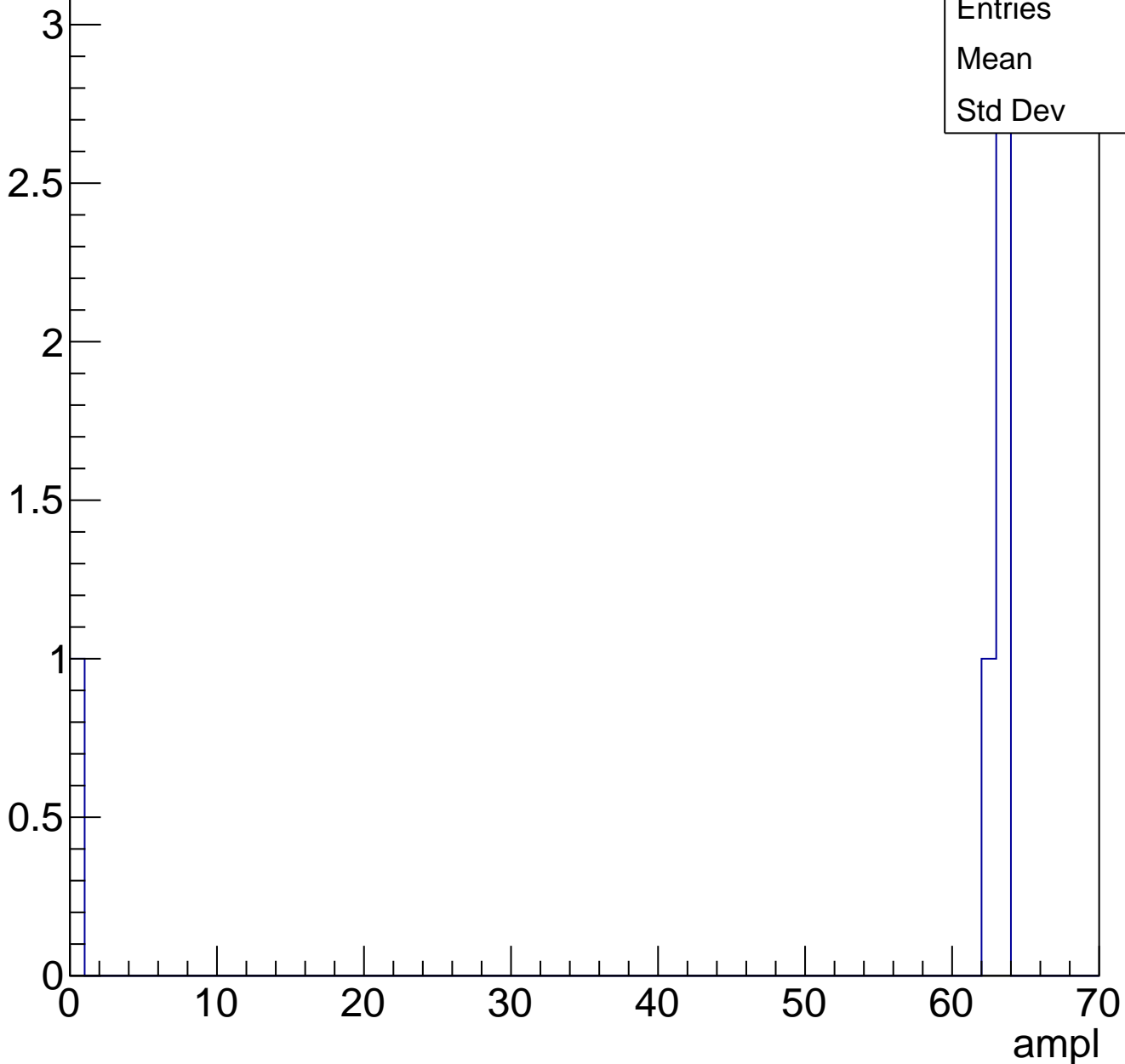
Entry



B1L100S, U25-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch47, adc0

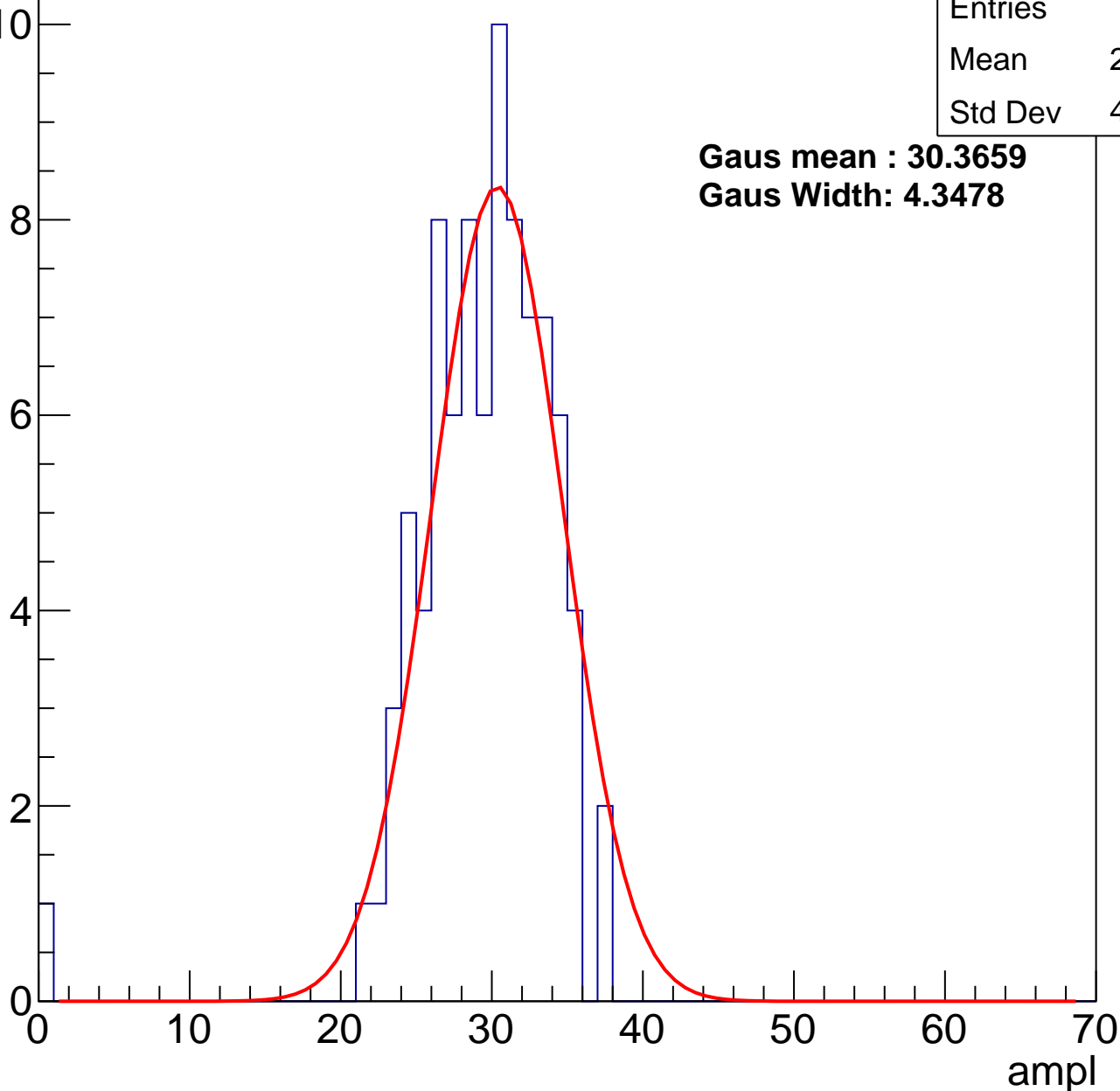
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	28.98
Std Dev	4.787

Gaus mean : 30.3659

Gaus Width: 4.3478



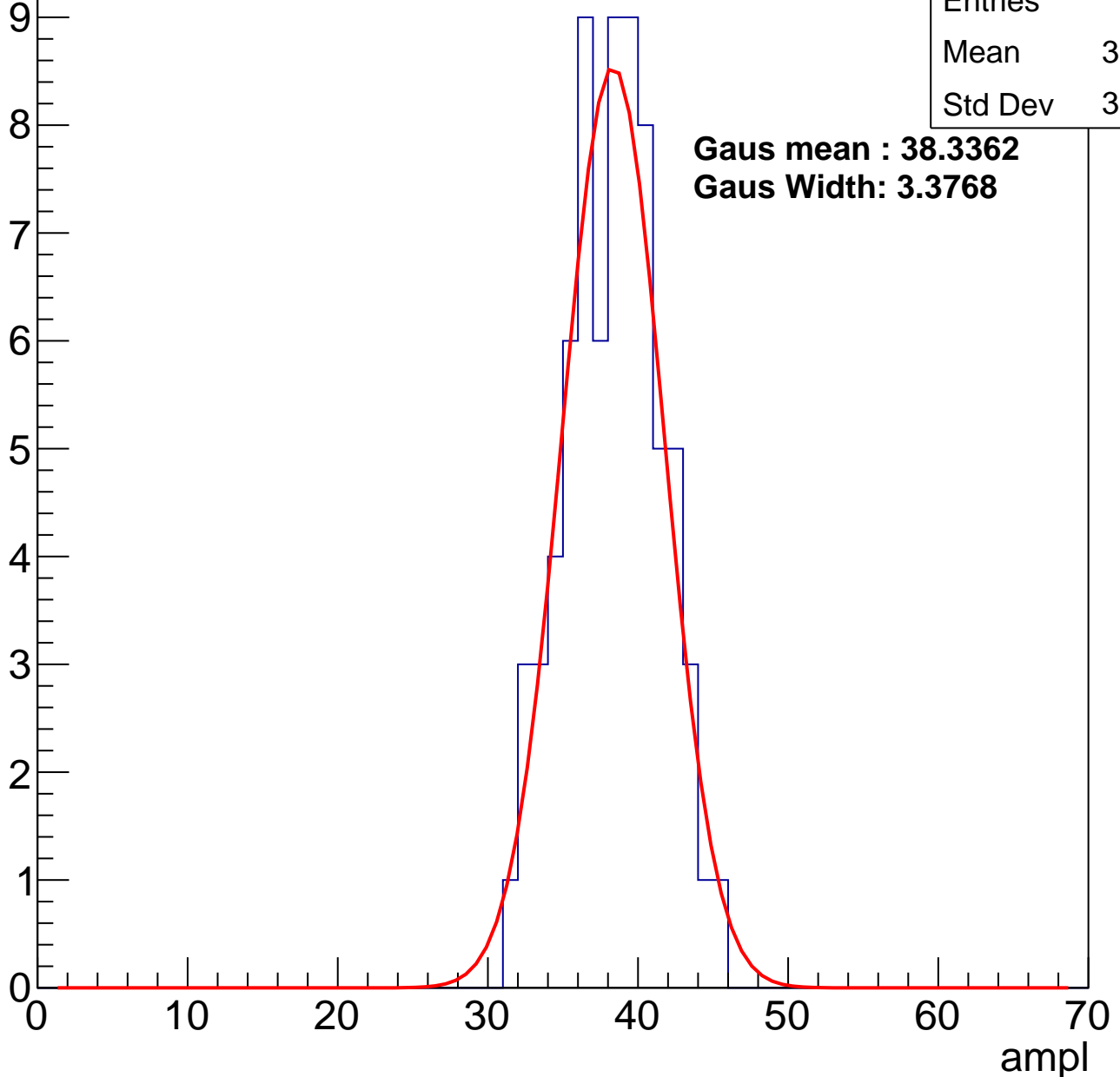
B1L100S, U25-ch47, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.86
Std Dev	3.133

Gaus mean : 38.3362
Gaus Width: 3.3768



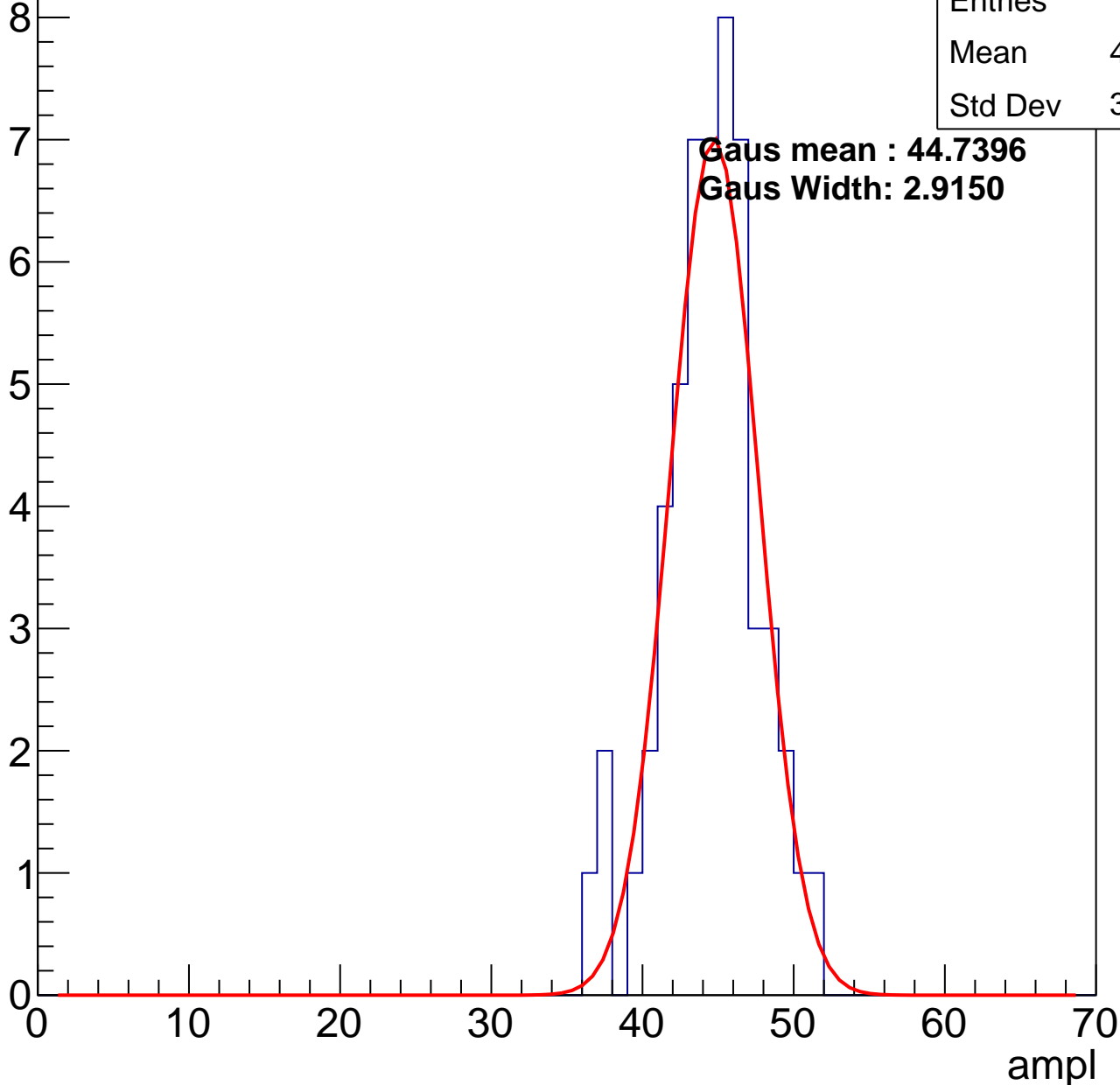
B1L100S, U25-ch47, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	44.04
Std Dev	3.127

Gaus mean : 44.7396
Gaus Width: 2.9150



B1L100S, U25-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	51.17
Std Dev	3.863

Entry

10

8

6

4

2

0

0

10

20

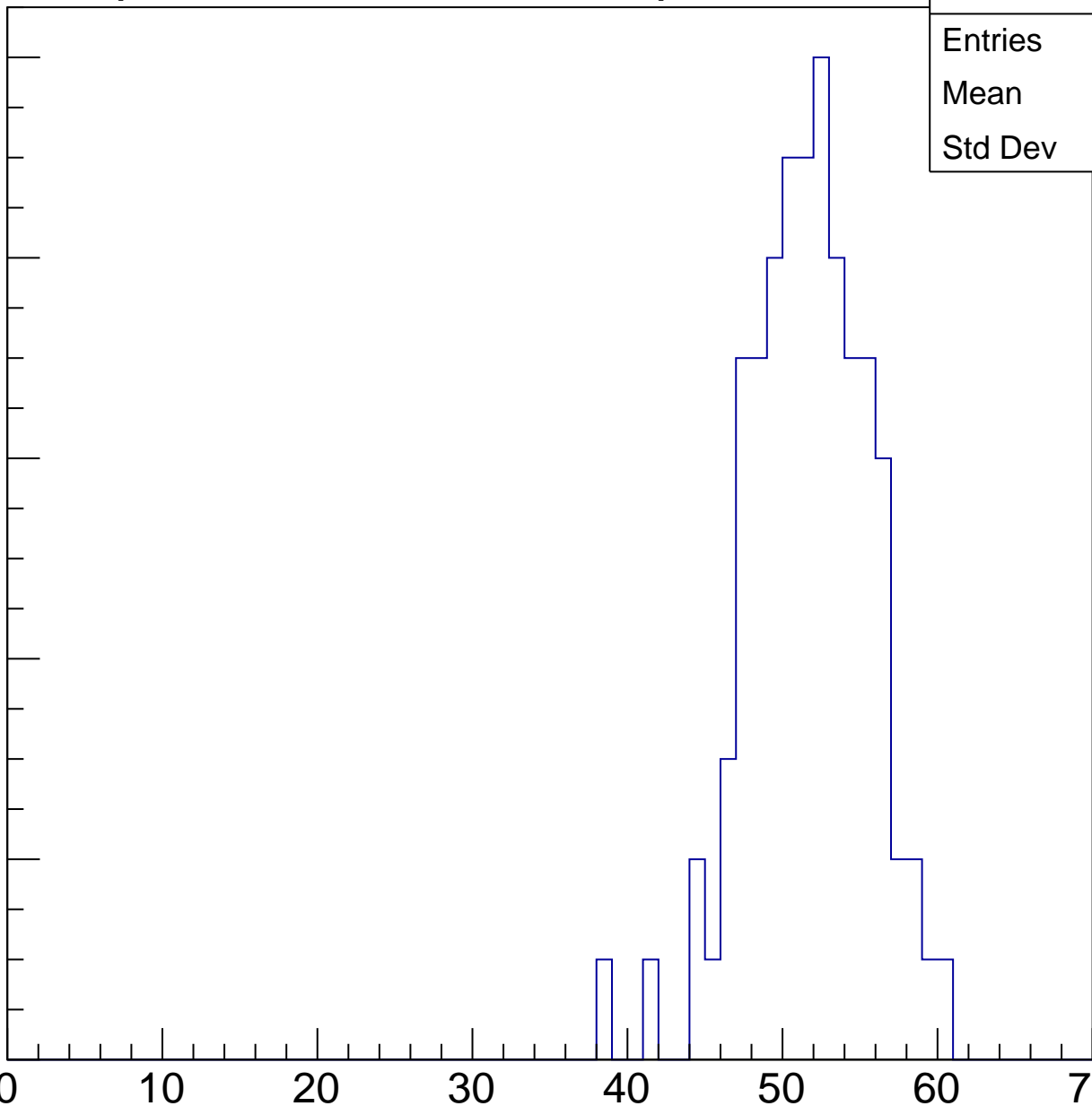
30

40

50

60

ampl

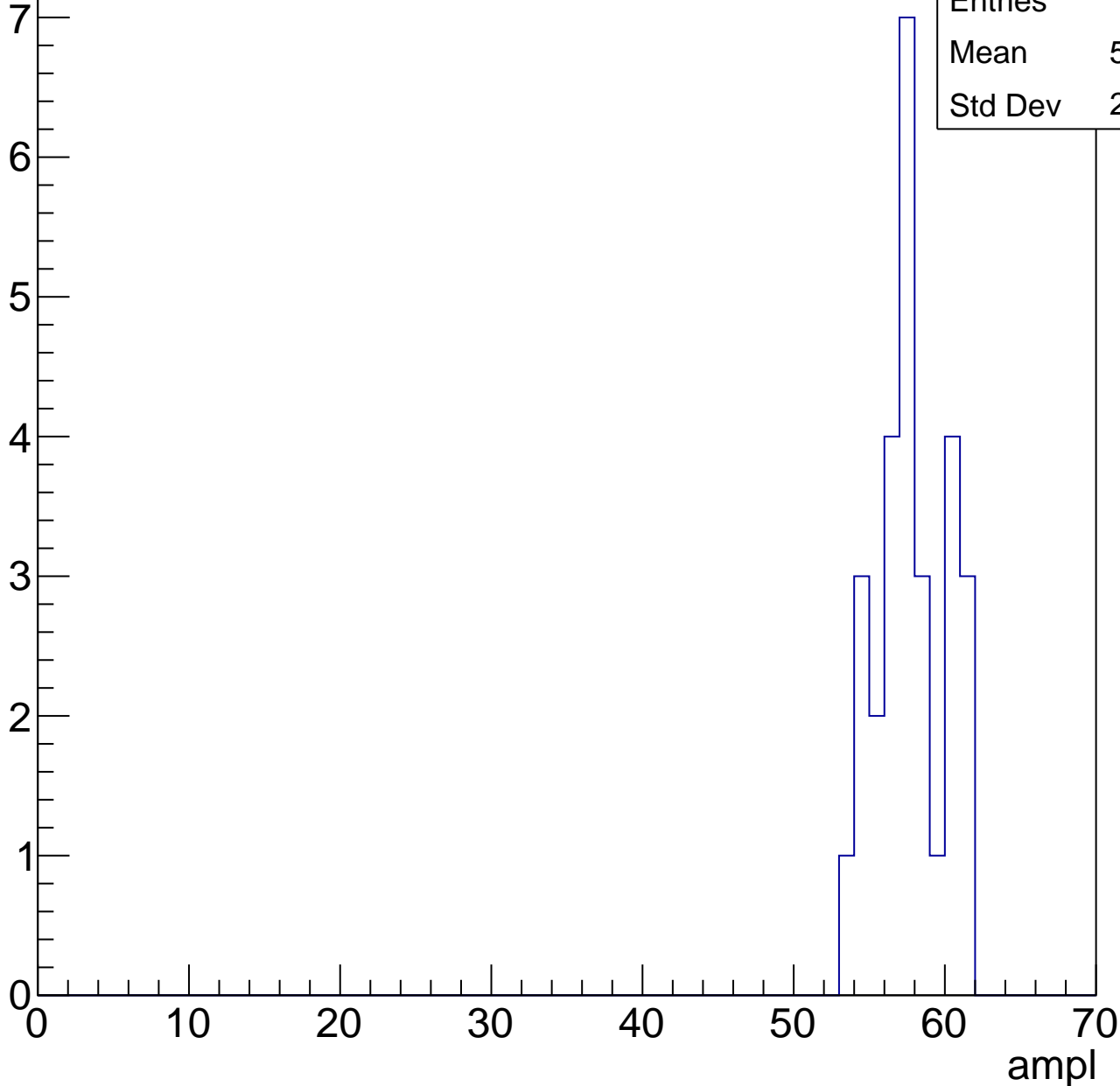


B1L100S, U25-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

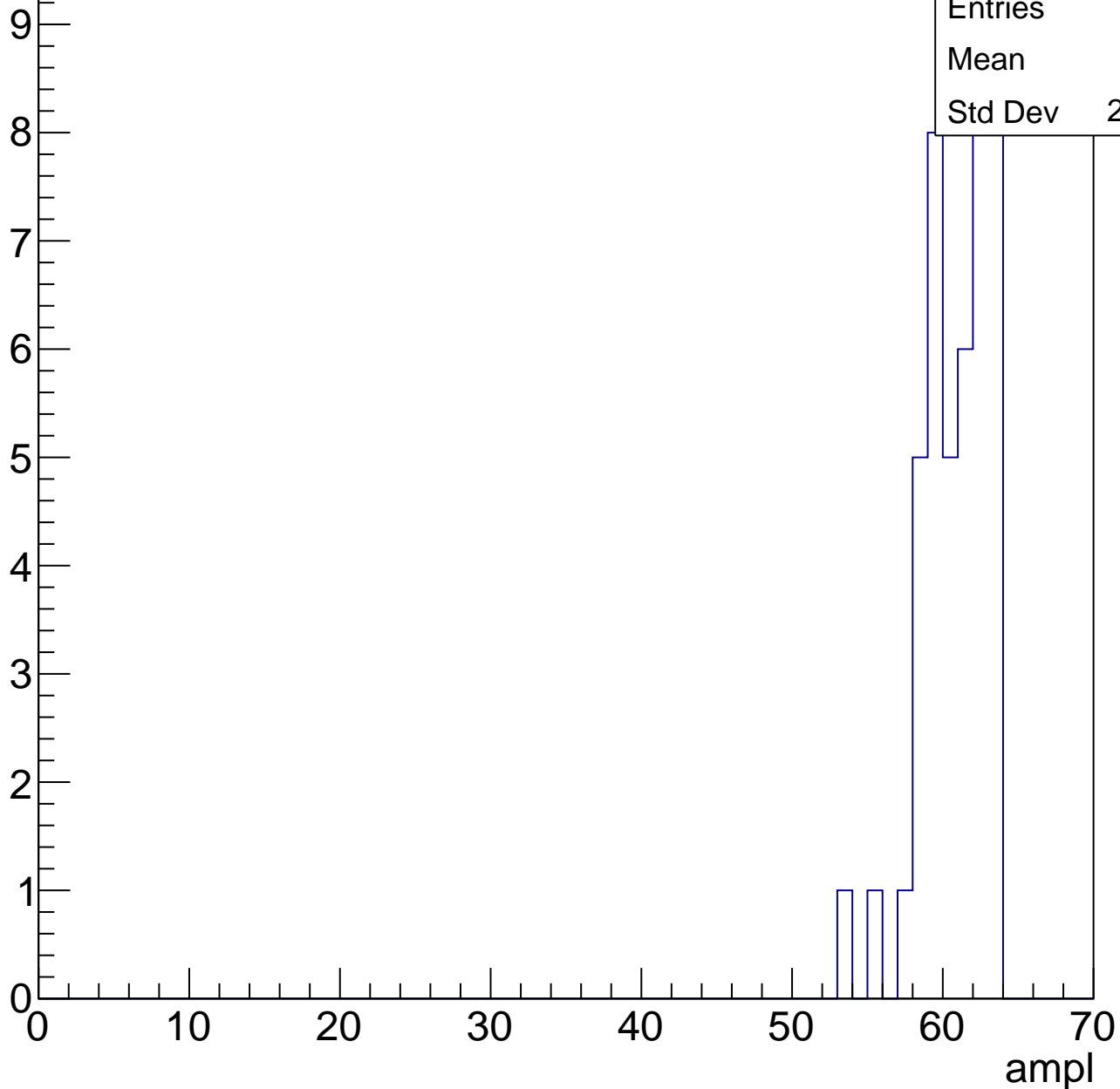
Entries	28
Mean	57.29
Std Dev	2.266



B1L100S, U25-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	45
Mean	60.4
Std Dev	2.245

B1L100S, U25-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch48, adc0

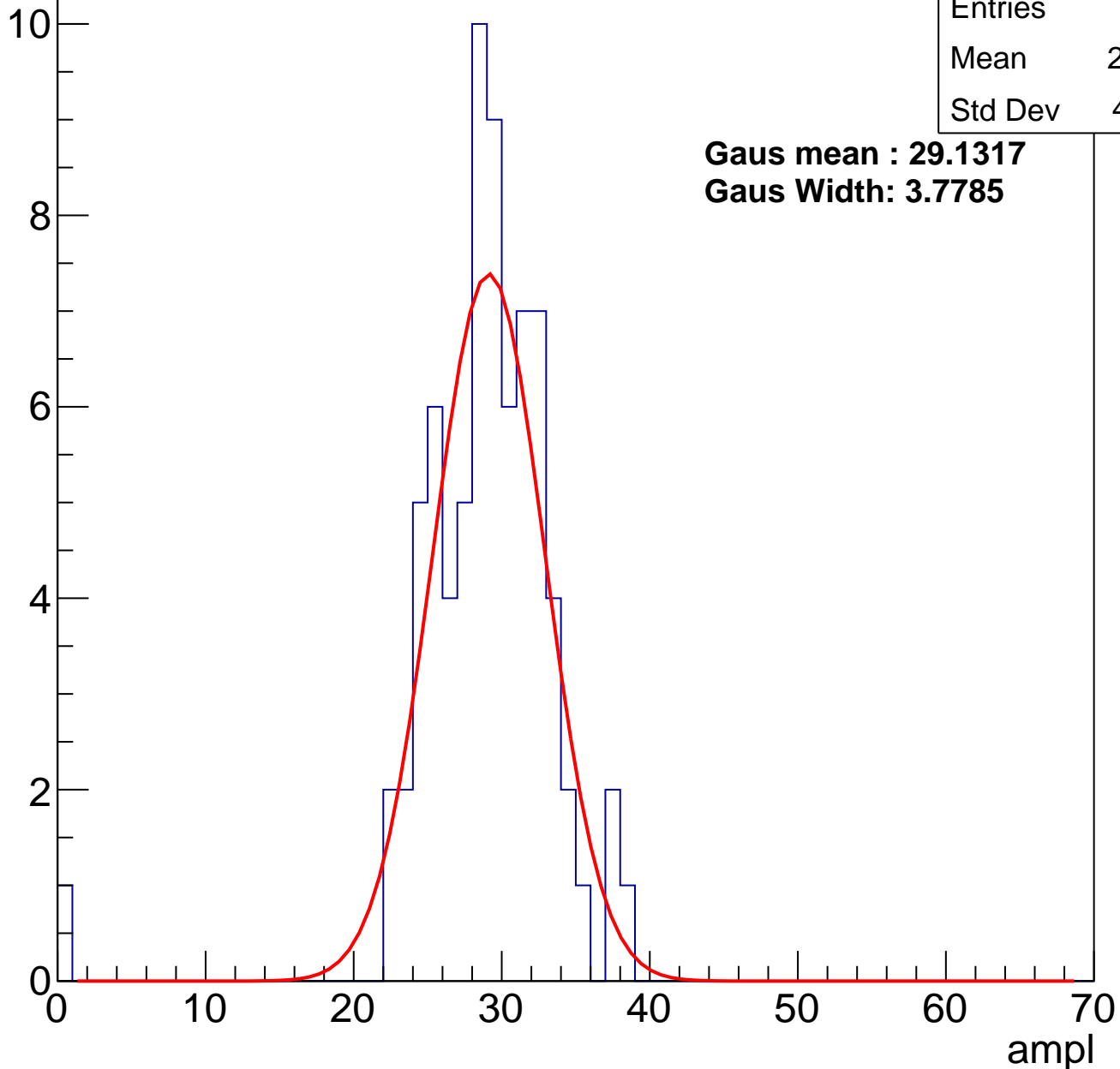
calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	28.49
Std Dev	4.841

Gaus mean : 29.1317

Gaus Width: 3.7785

Entry



B1L100S, U25-ch48, adc1

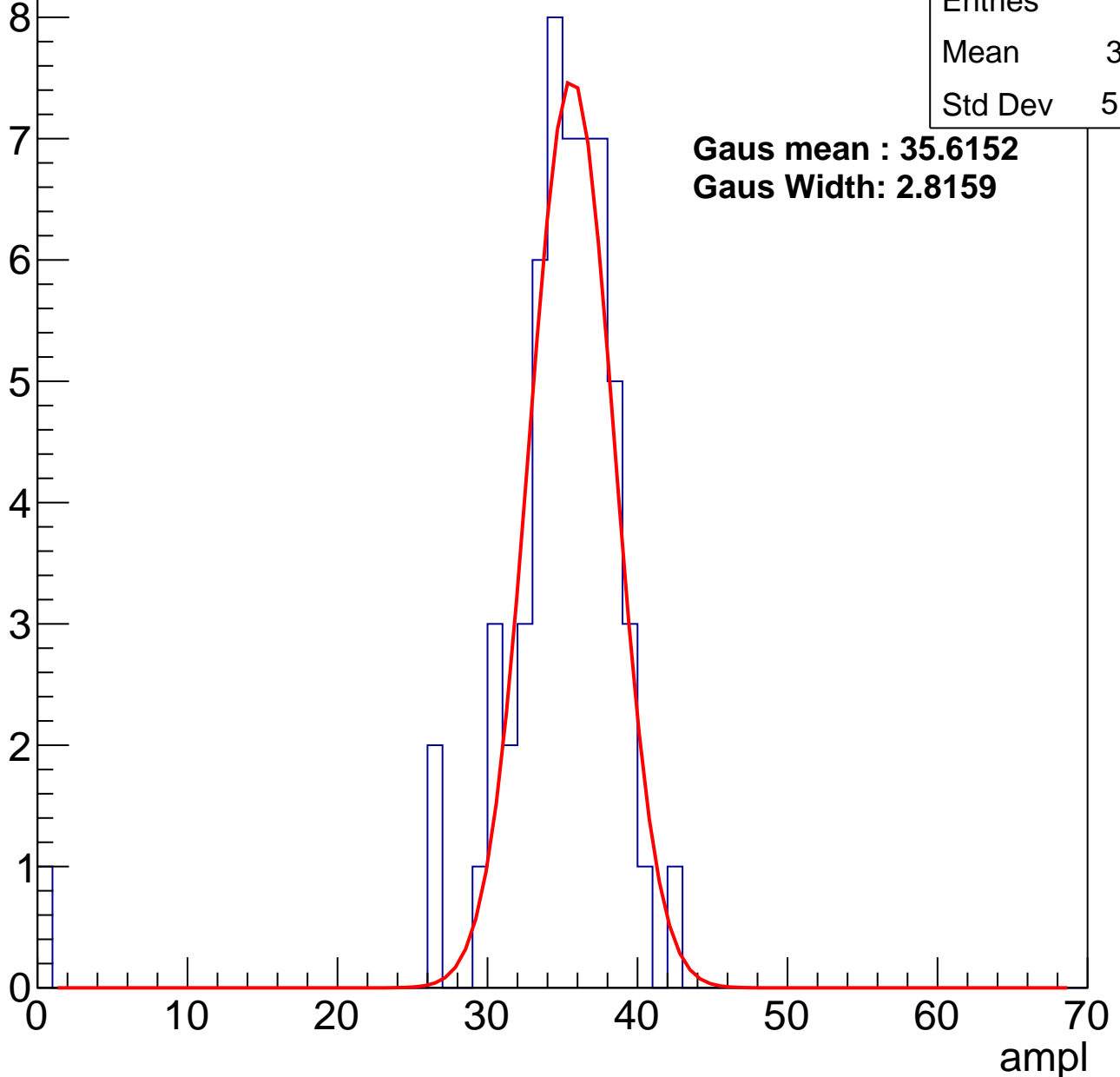
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.11
Std Dev	5.534

Gaus mean : 35.6152

Gaus Width: 2.8159



B1L100S, U25-ch48, adc2

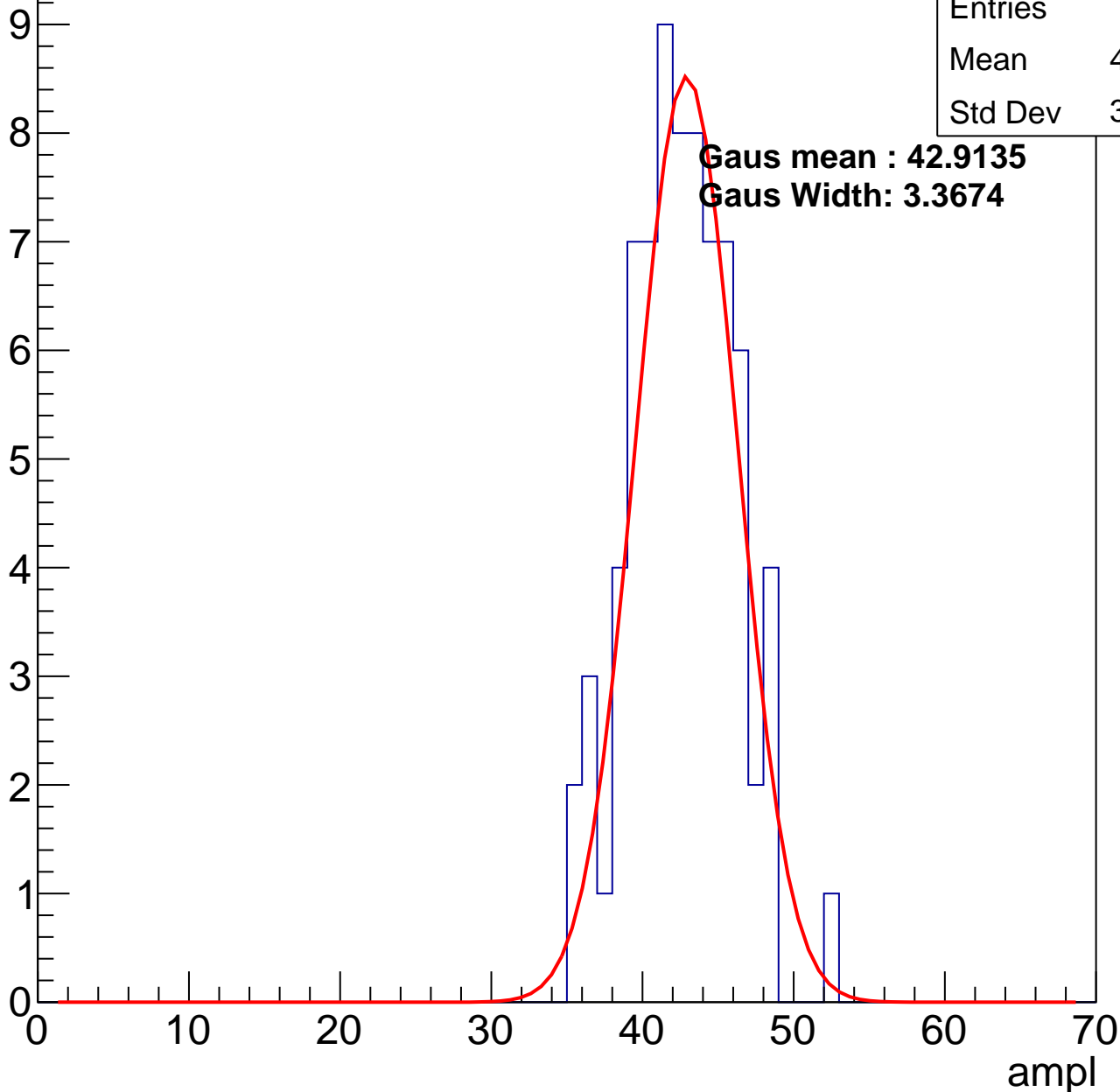
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.18
Std Dev	3.406

Gaus mean : 42.9135

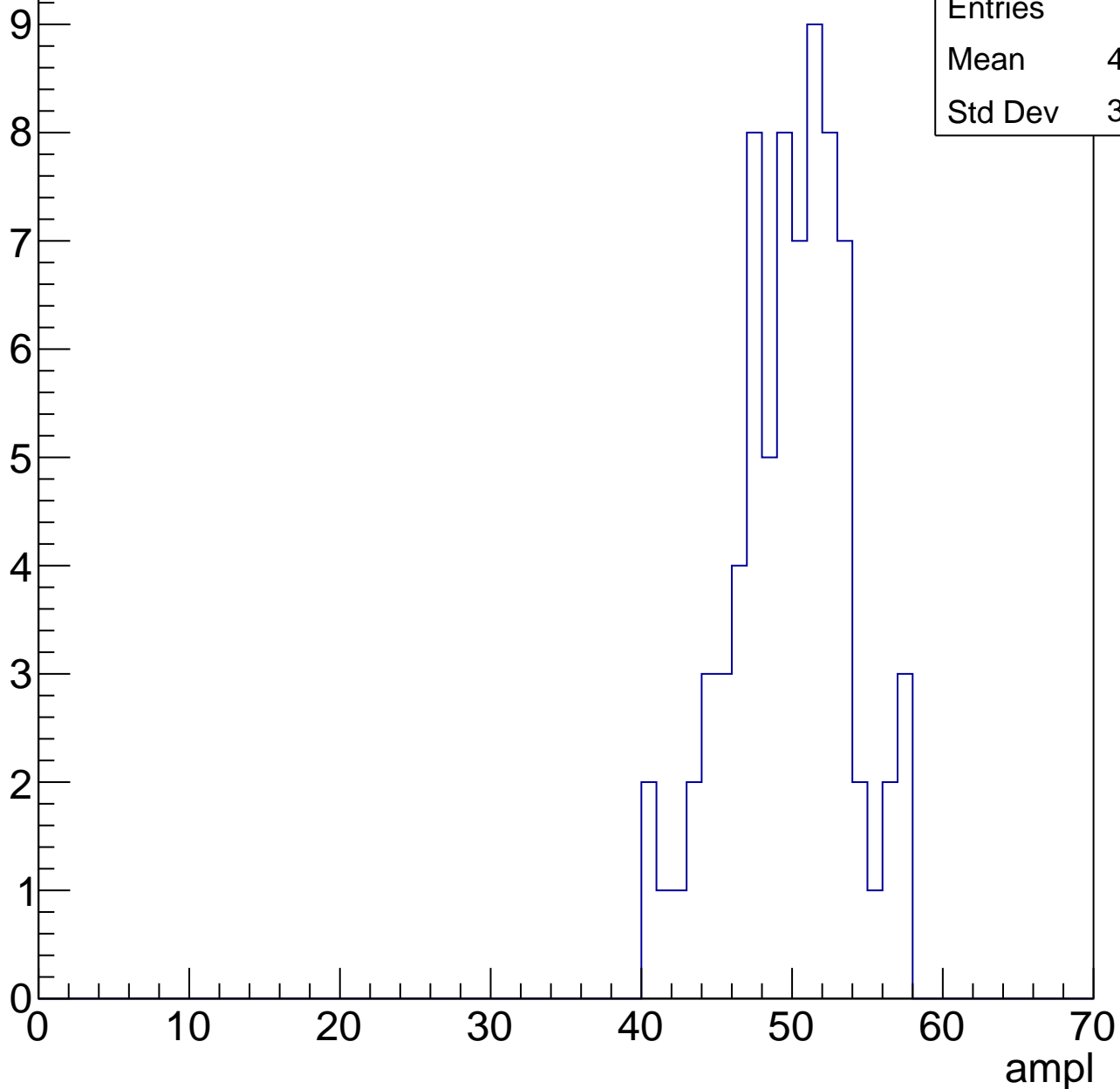
Gaus Width: 3.3674



B1L100S, U25-ch48, adc3

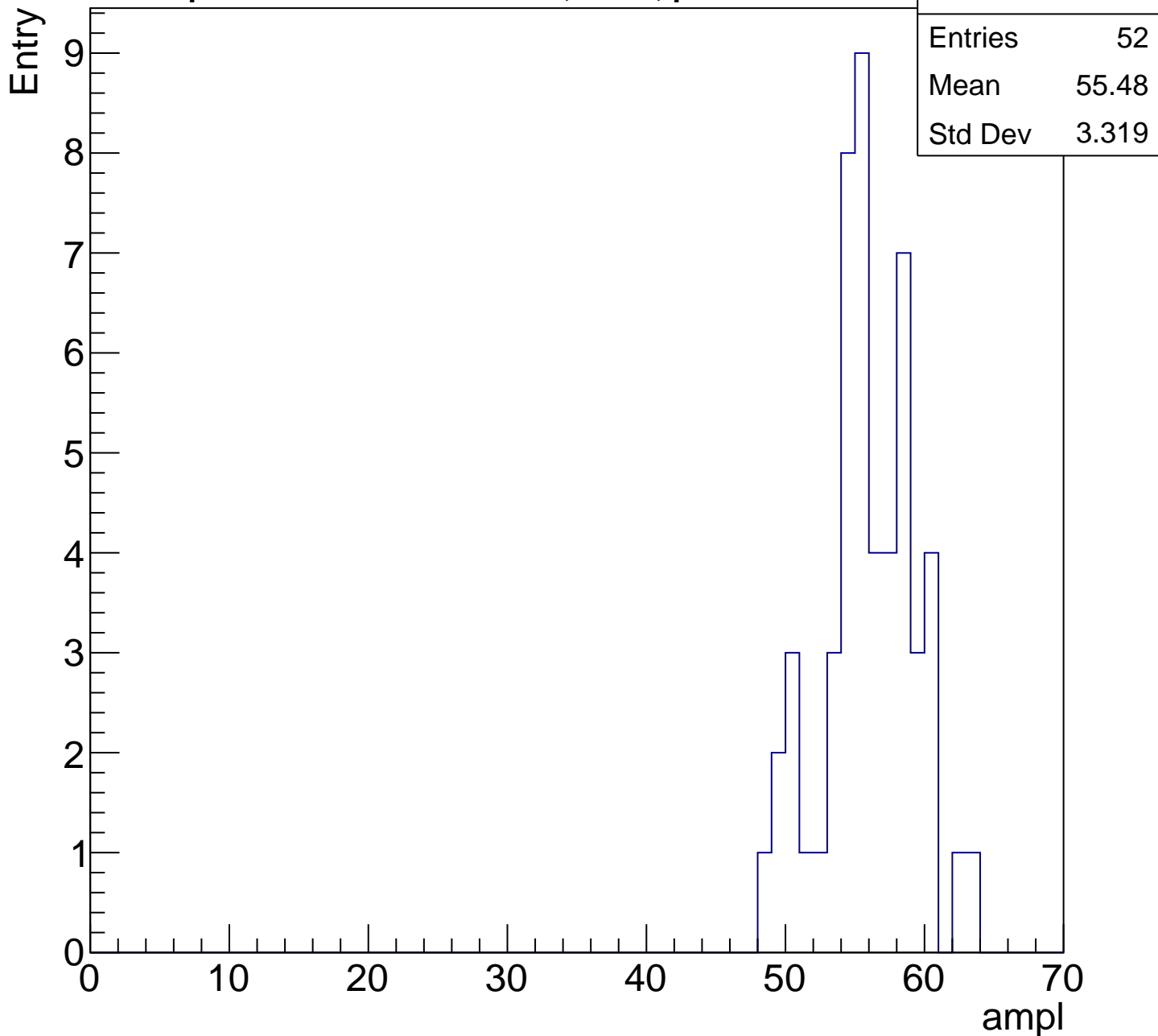
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch48, adc4

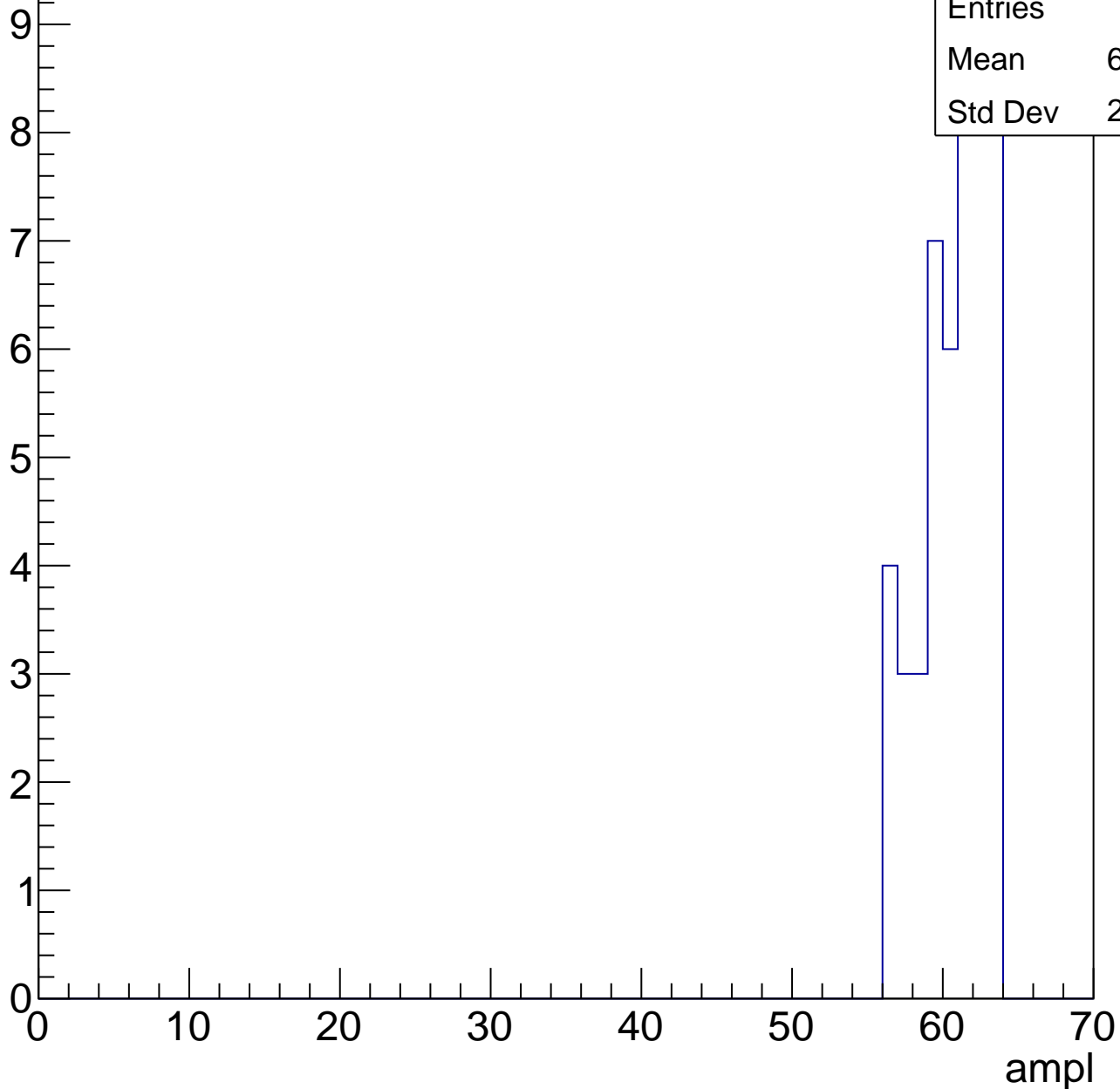
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

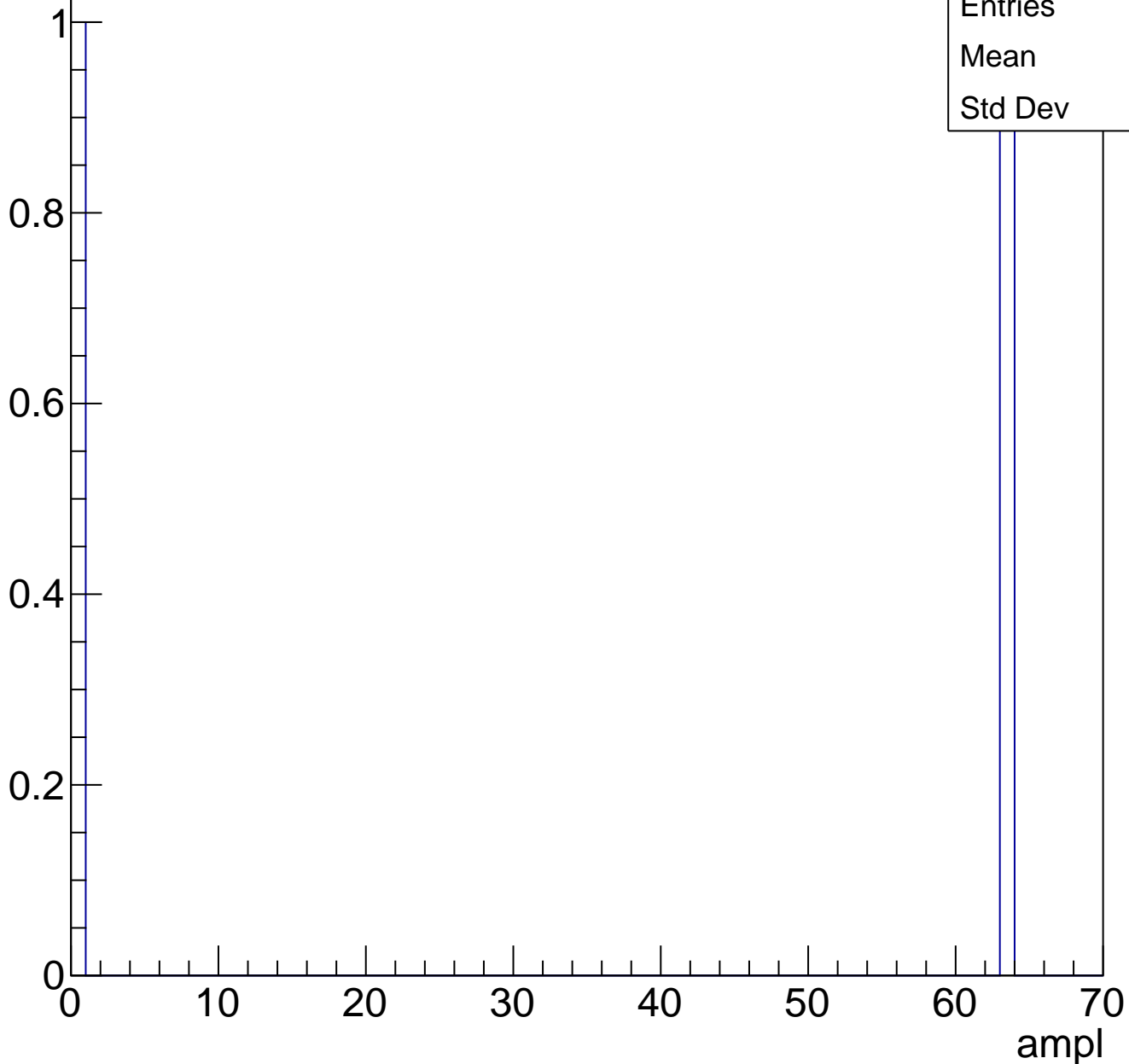
Entry



B1L100S, U25-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch49, adc0

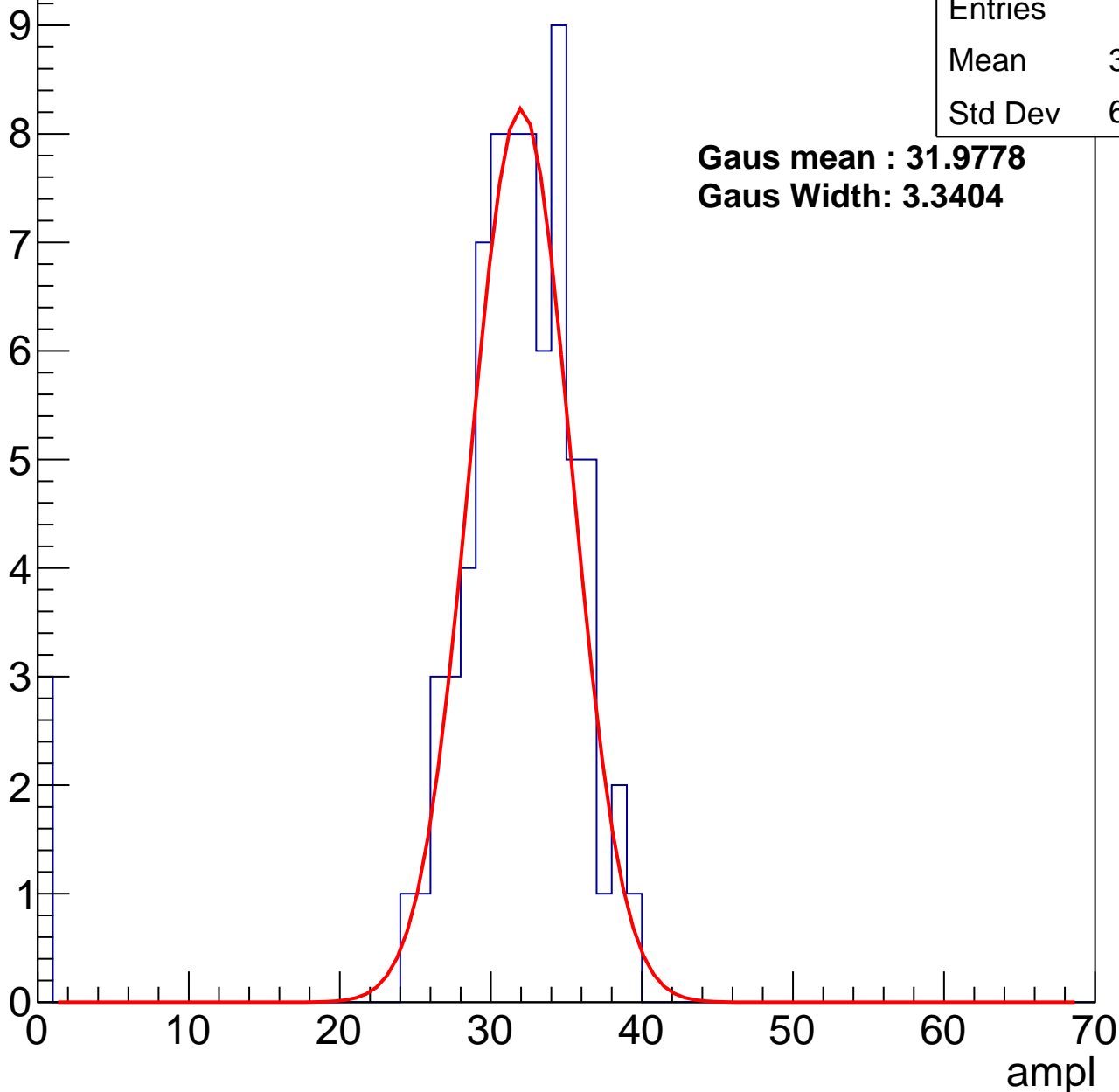
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.37
Std Dev	6.976

Gaus mean : 31.9778

Gaus Width: 3.3404



B1L100S, U25-ch49, adc1

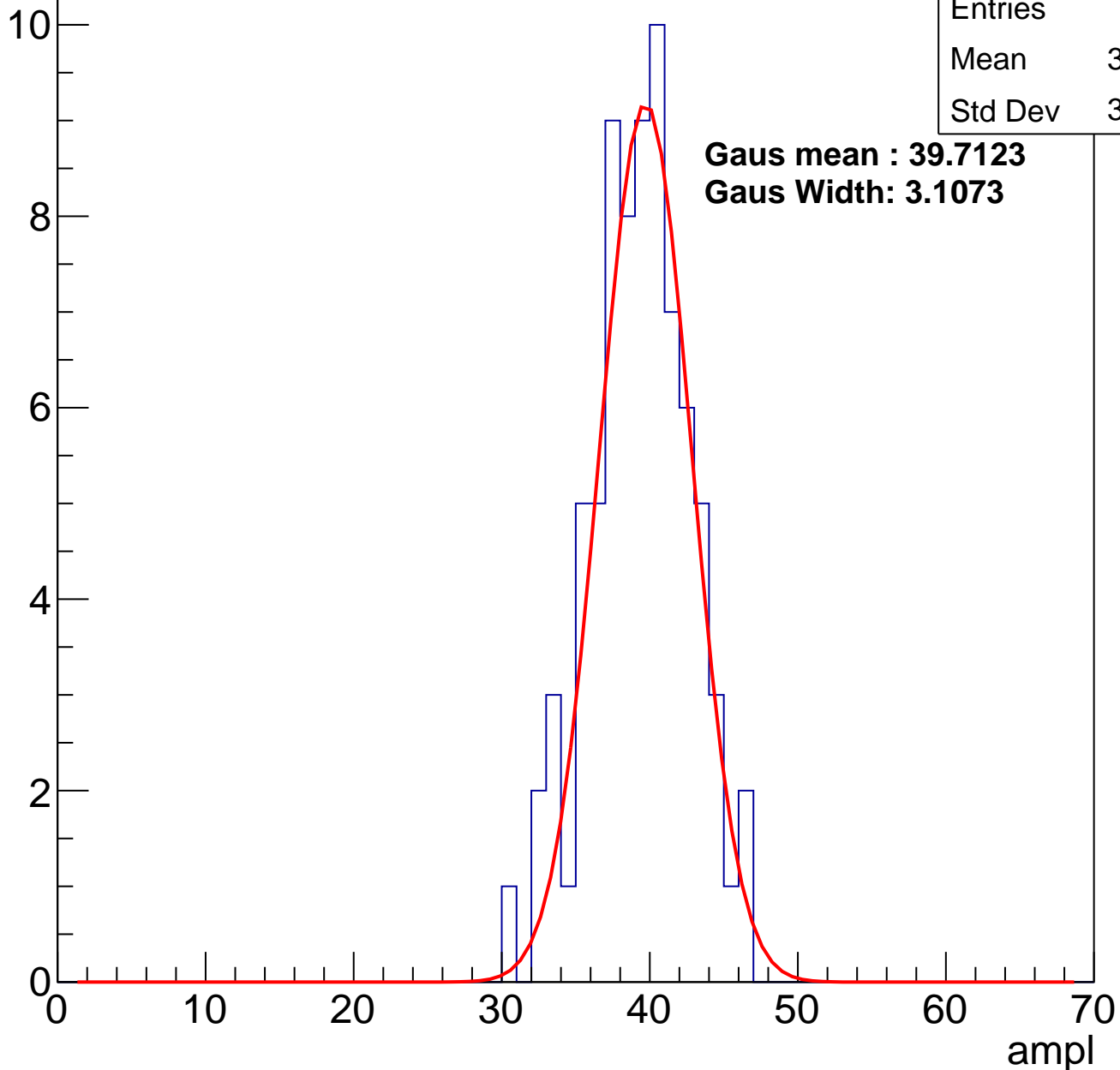
calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	38.87
Std Dev	3.343

Gaus mean : 39.7123

Gaus Width: 3.1073

Entry



B1L100S, U25-ch49, adc2

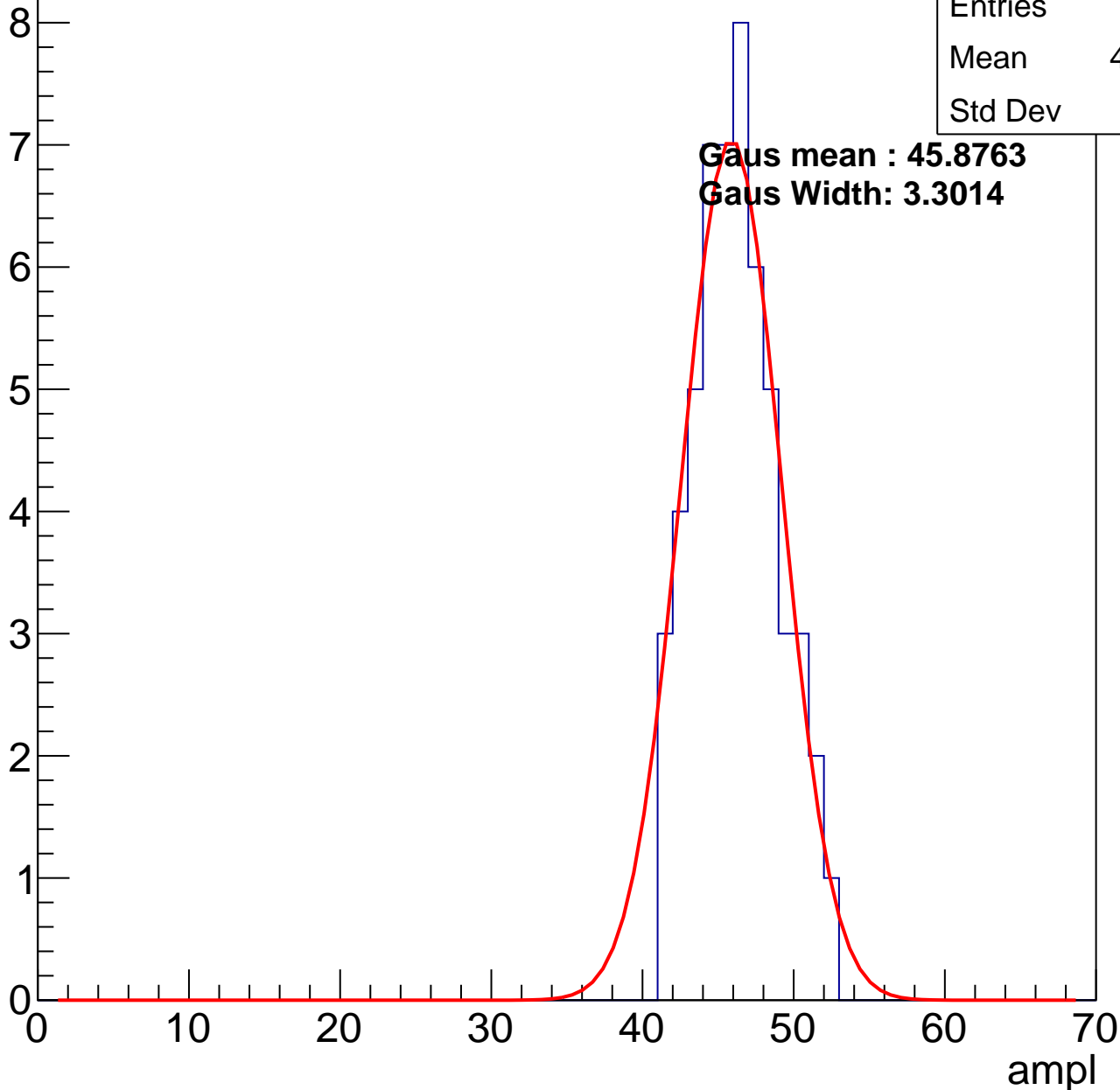
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	45.74
Std Dev	2.73

Gaus mean : 45.8763

Gaus Width: 3.3014

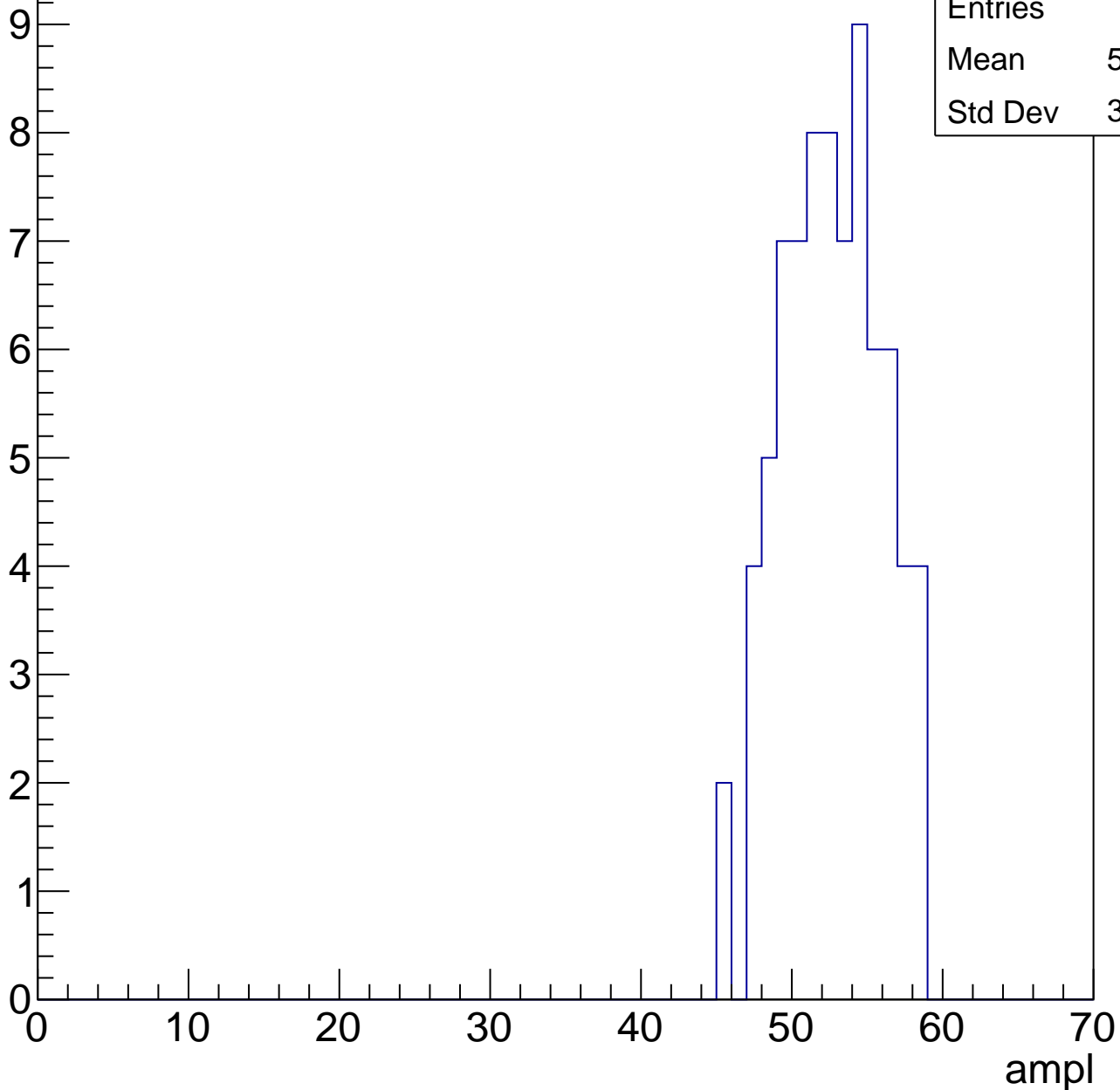


B1L100S, U25-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	52.18
Std Dev	3.246

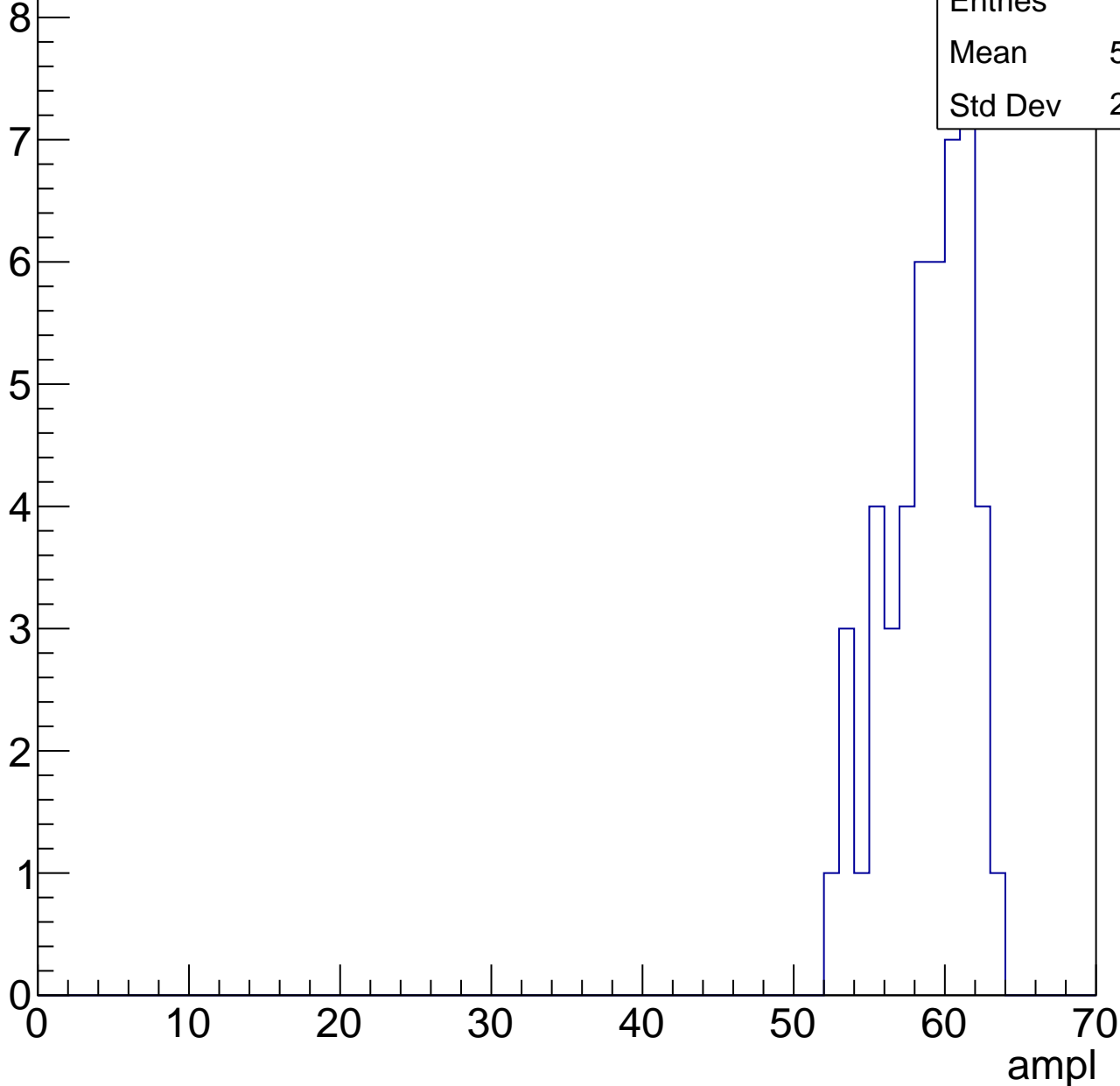


B1L100S, U25-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.38
Std Dev	2.766

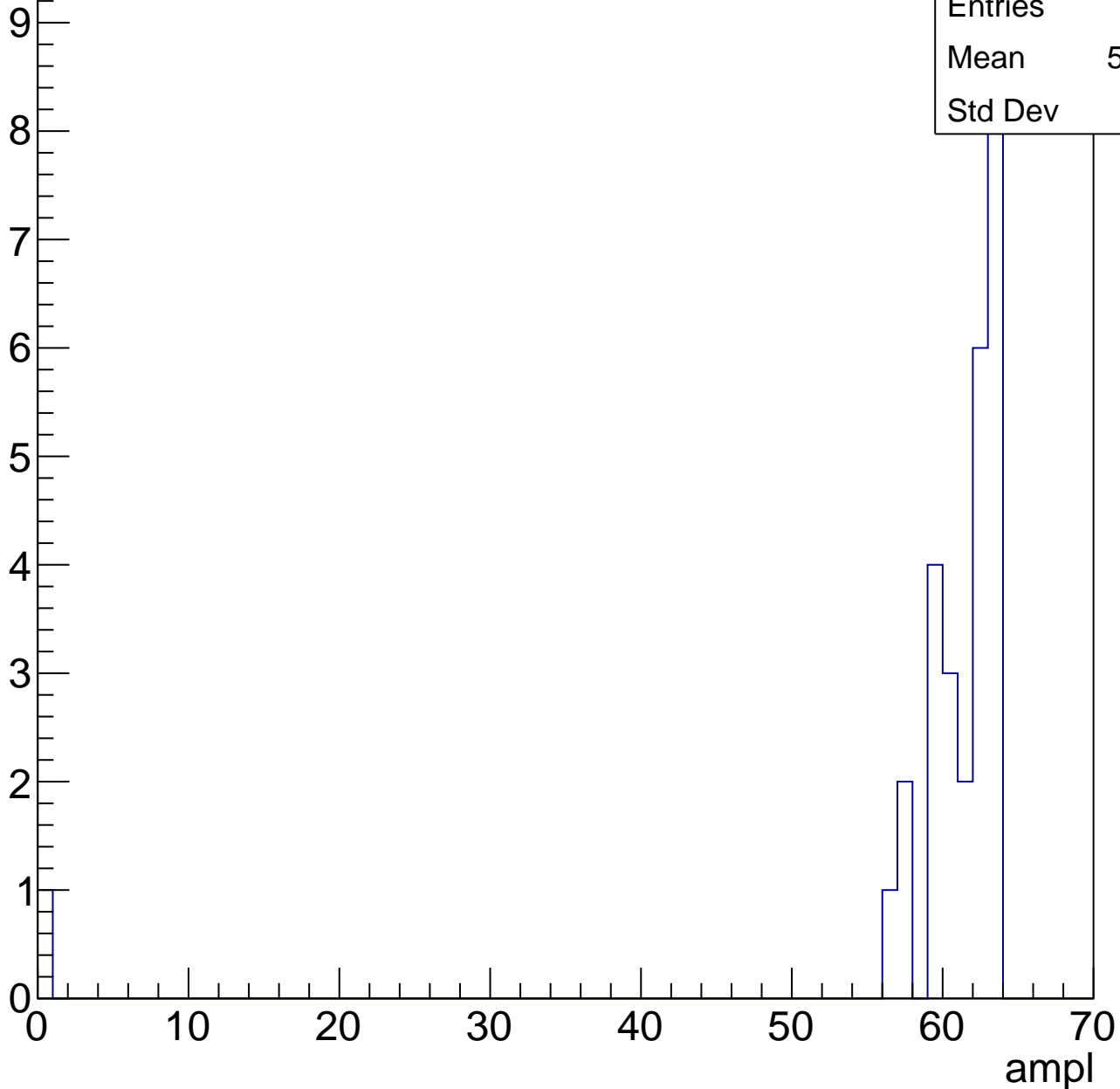


B1L100S, U25-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	58.82
Std Dev	11.5



B1L100S, U25-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch50, adc0

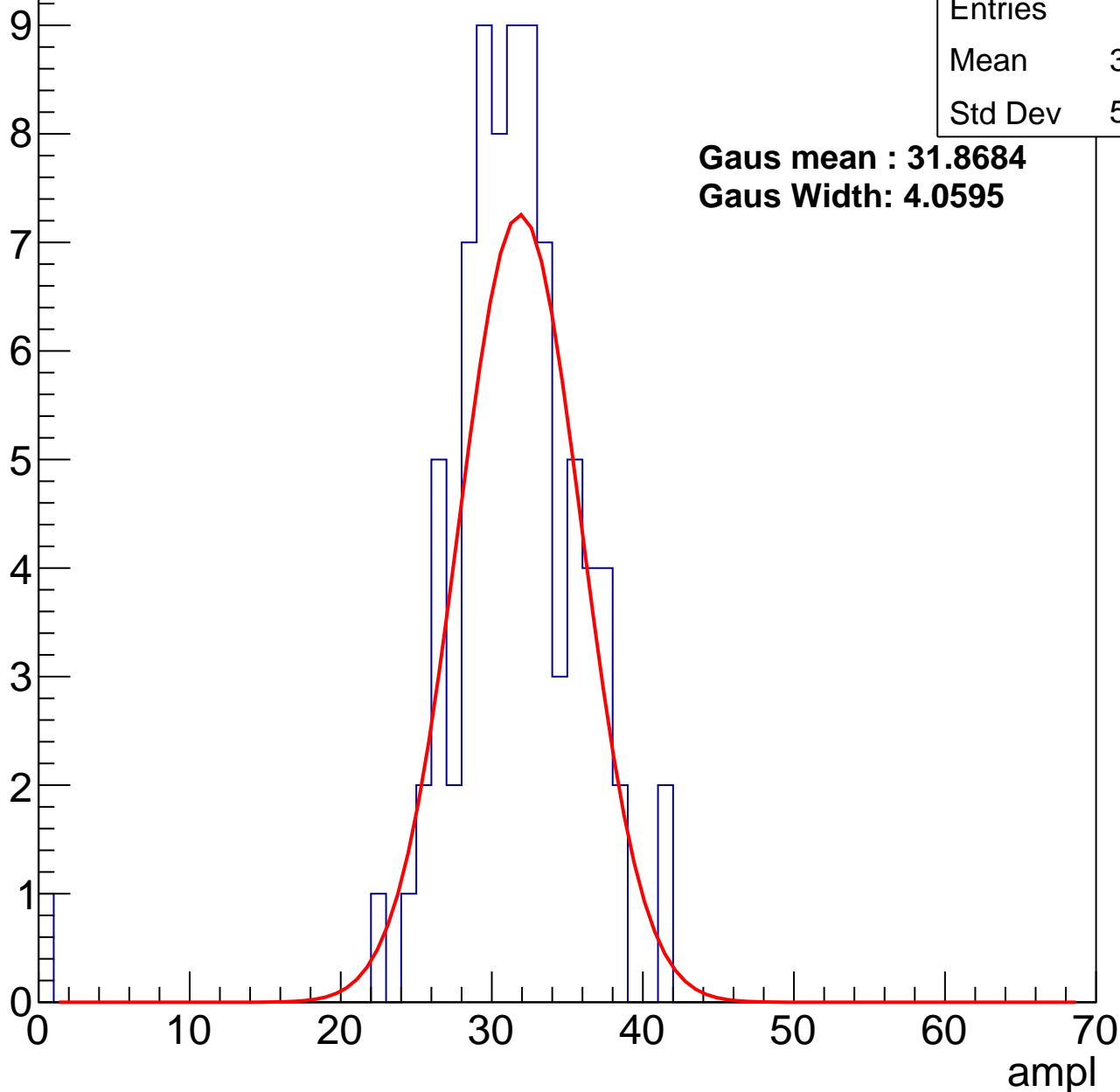
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	30.89
Std Dev	5.106

Gaus mean : 31.8684

Gaus Width: 4.0595



B1L100S, U25-ch50, adc1

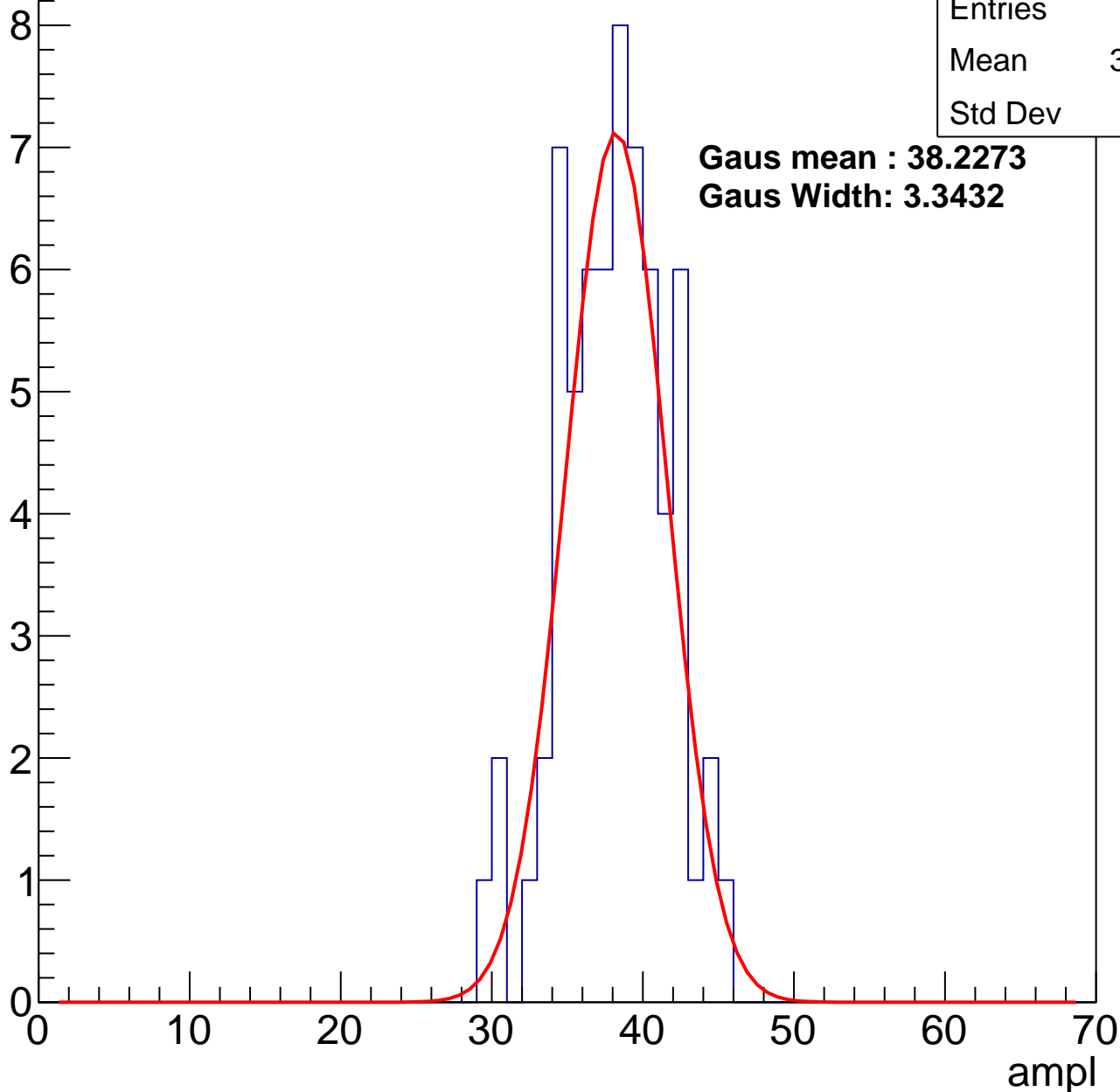
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.65
Std Dev	3.47

Gaus mean : 38.2273

Gaus Width: 3.3432



B1L100S, U25-ch50, adc2

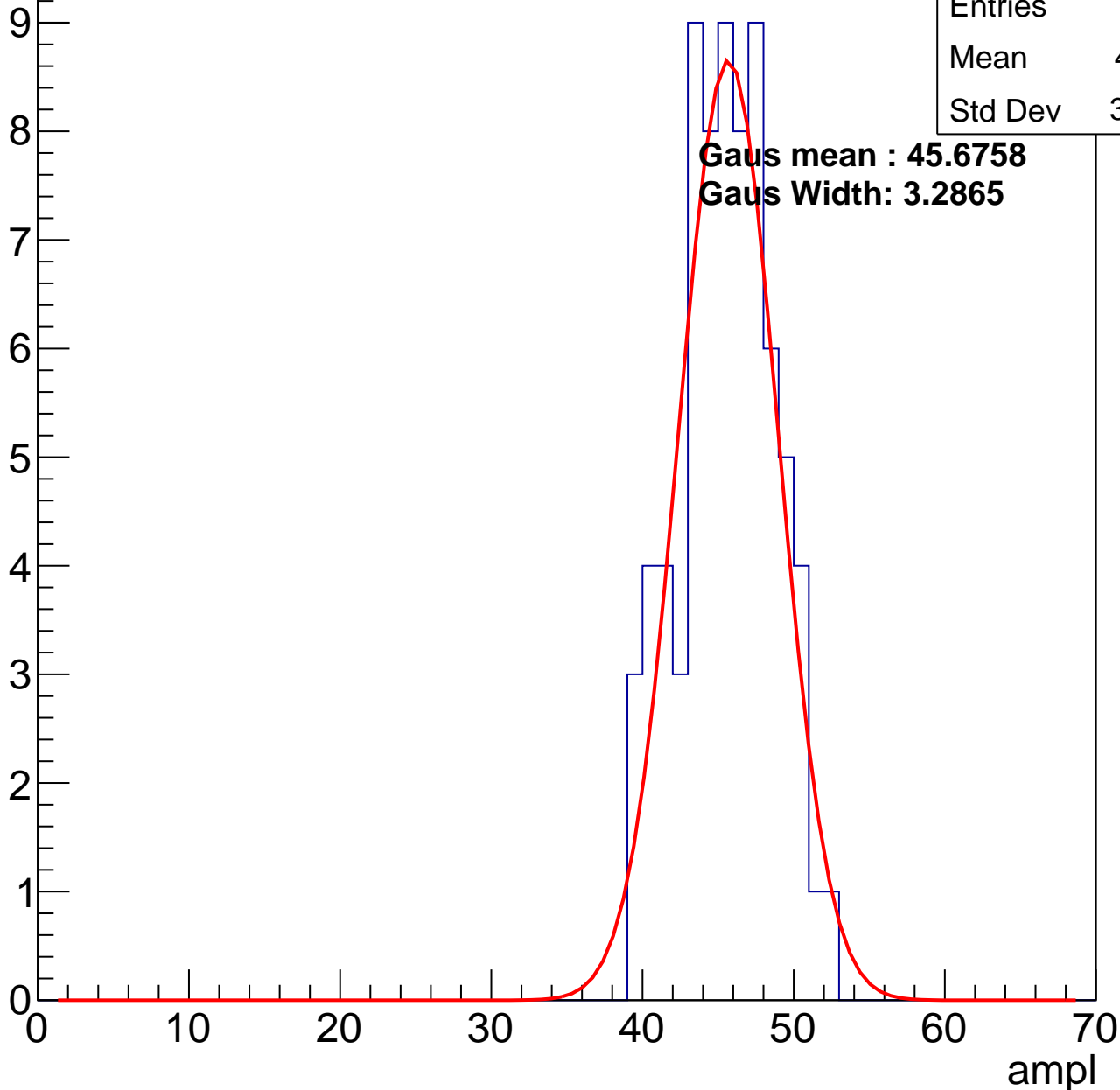
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	45.11
Std Dev	3.087

Gaus mean : 45.6758

Gaus Width: 3.2865

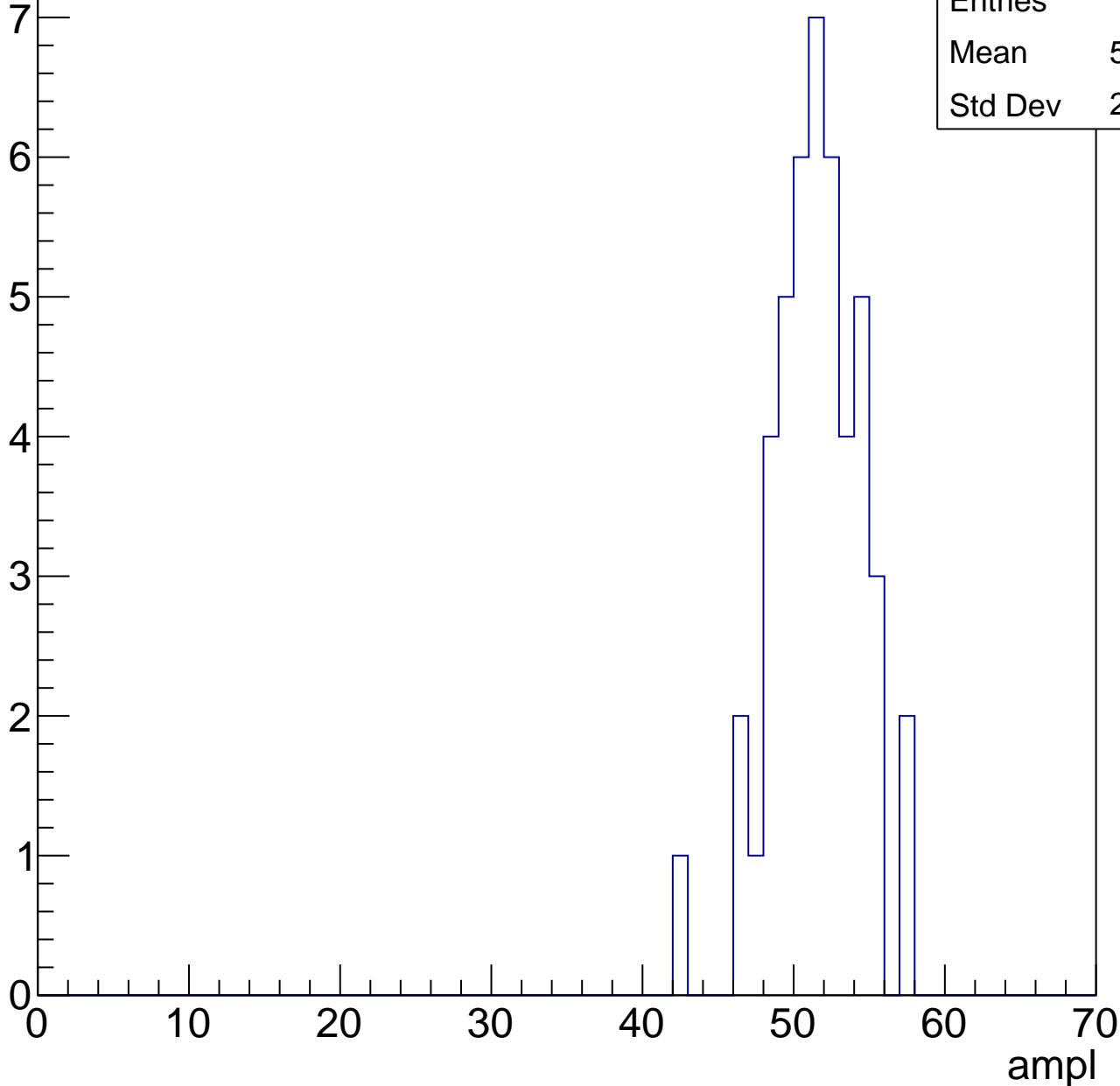


B1L100S, U25-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	51.04
Std Dev	2.934

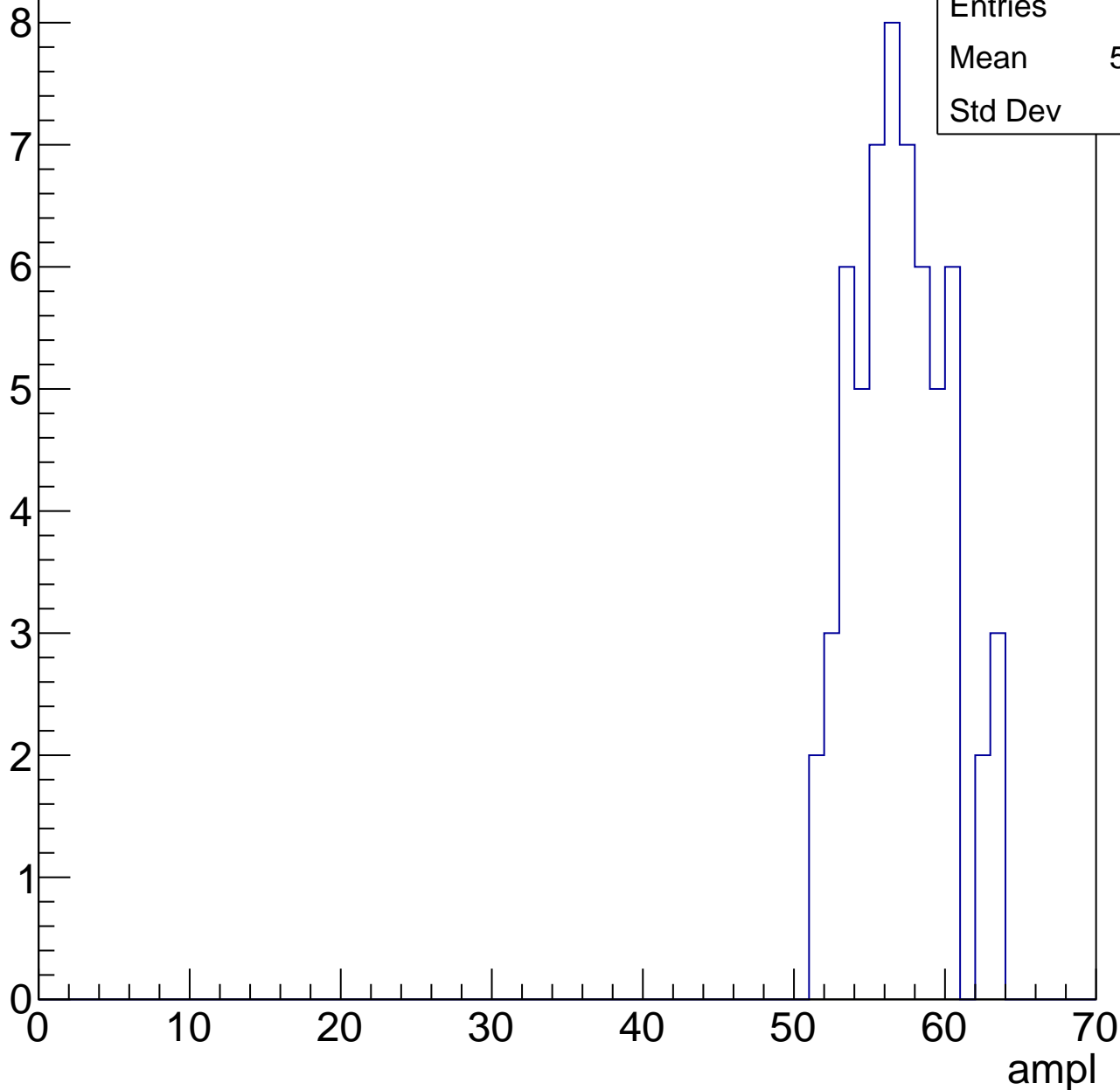


B1L100S, U25-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

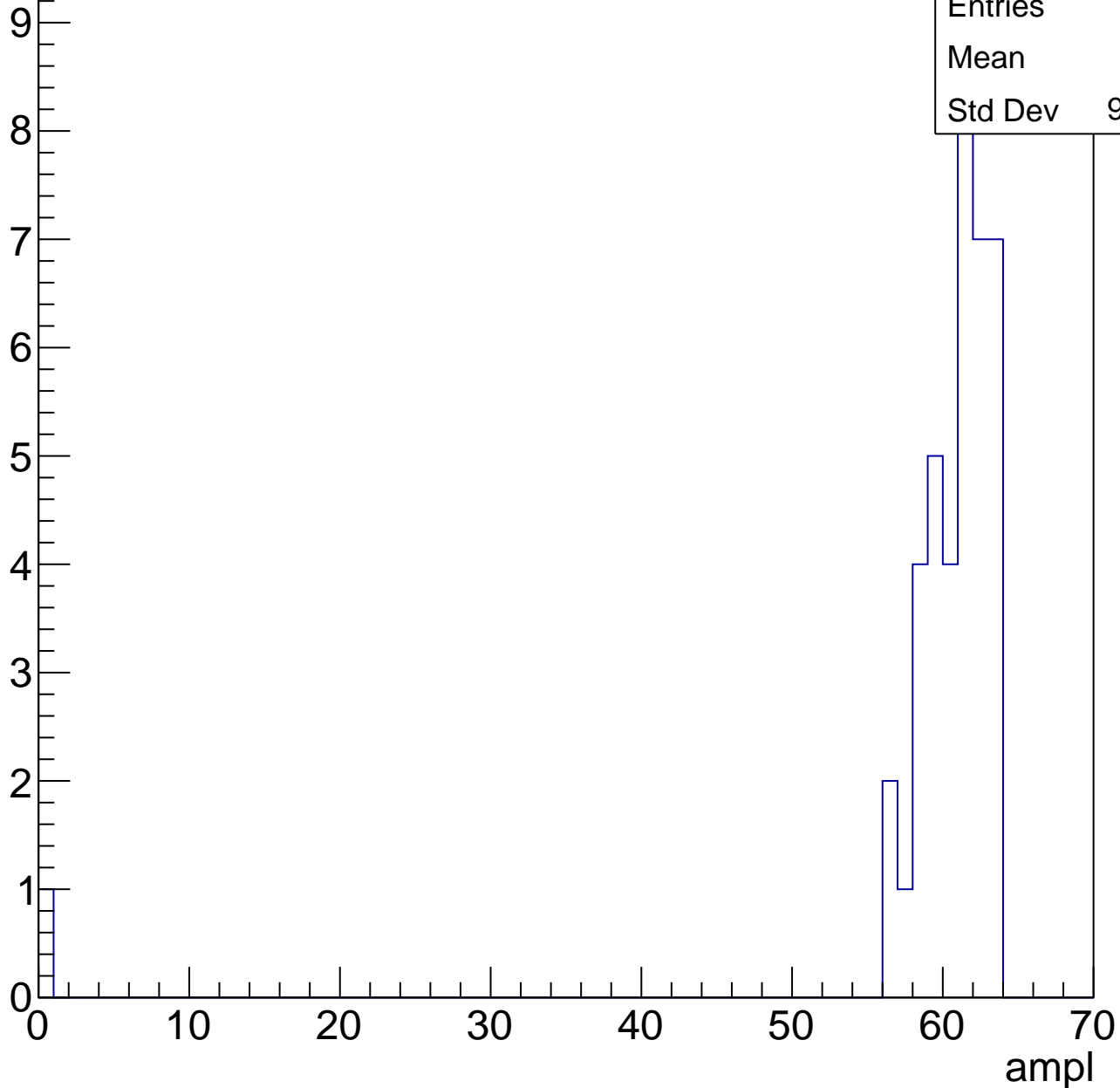
Entries	60
Mean	56.57
Std Dev	3.03



B1L100S, U25-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch51, adc0

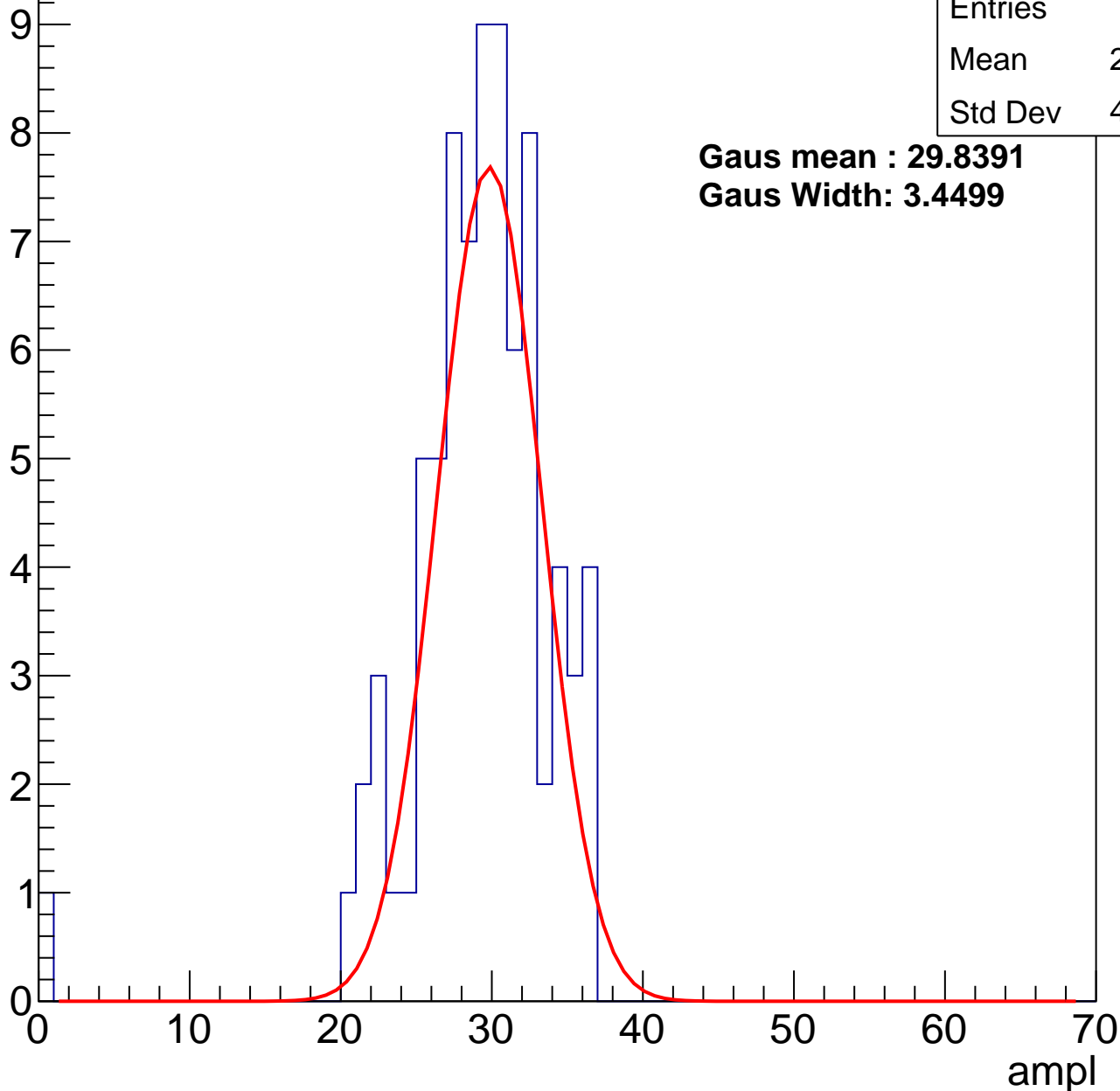
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	28.68
Std Dev	4.975

Gaus mean : 29.8391

Gaus Width: 3.4499



B1L100S, U25-ch51, adc1

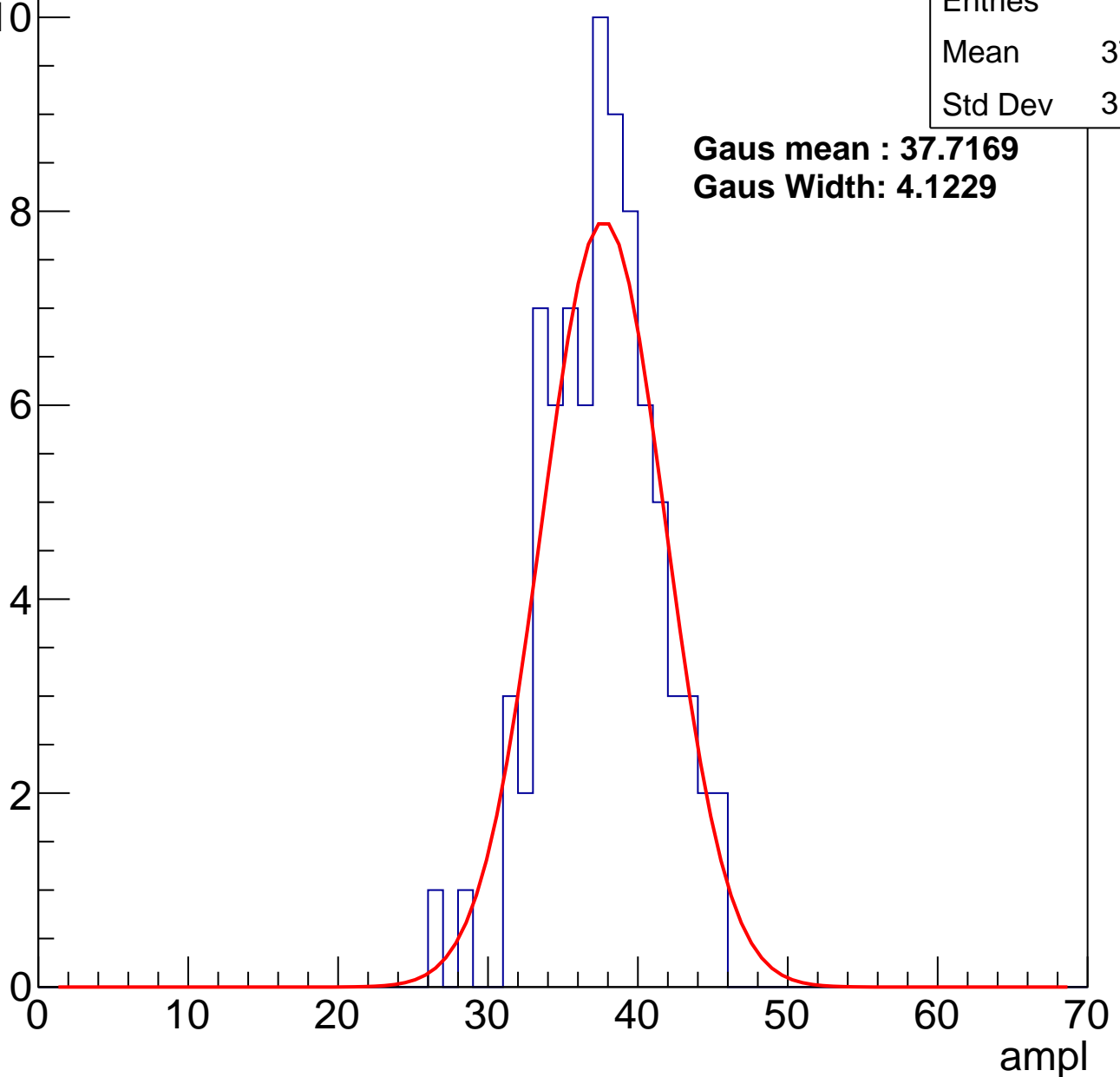
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	37.15
Std Dev	3.755

Gaus mean : 37.7169

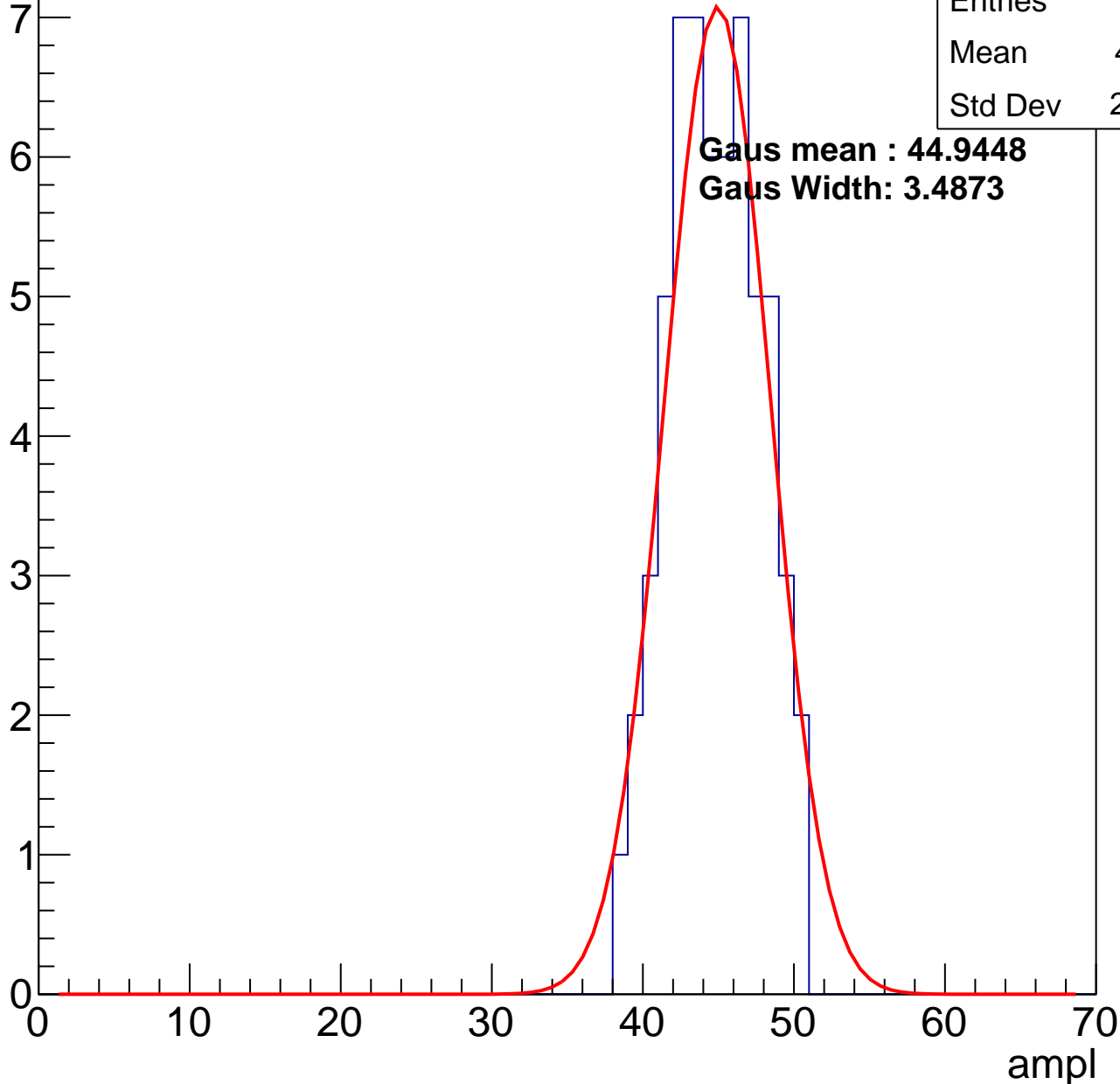
Gaus Width: 4.1229



B1L100S, U25-ch51, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

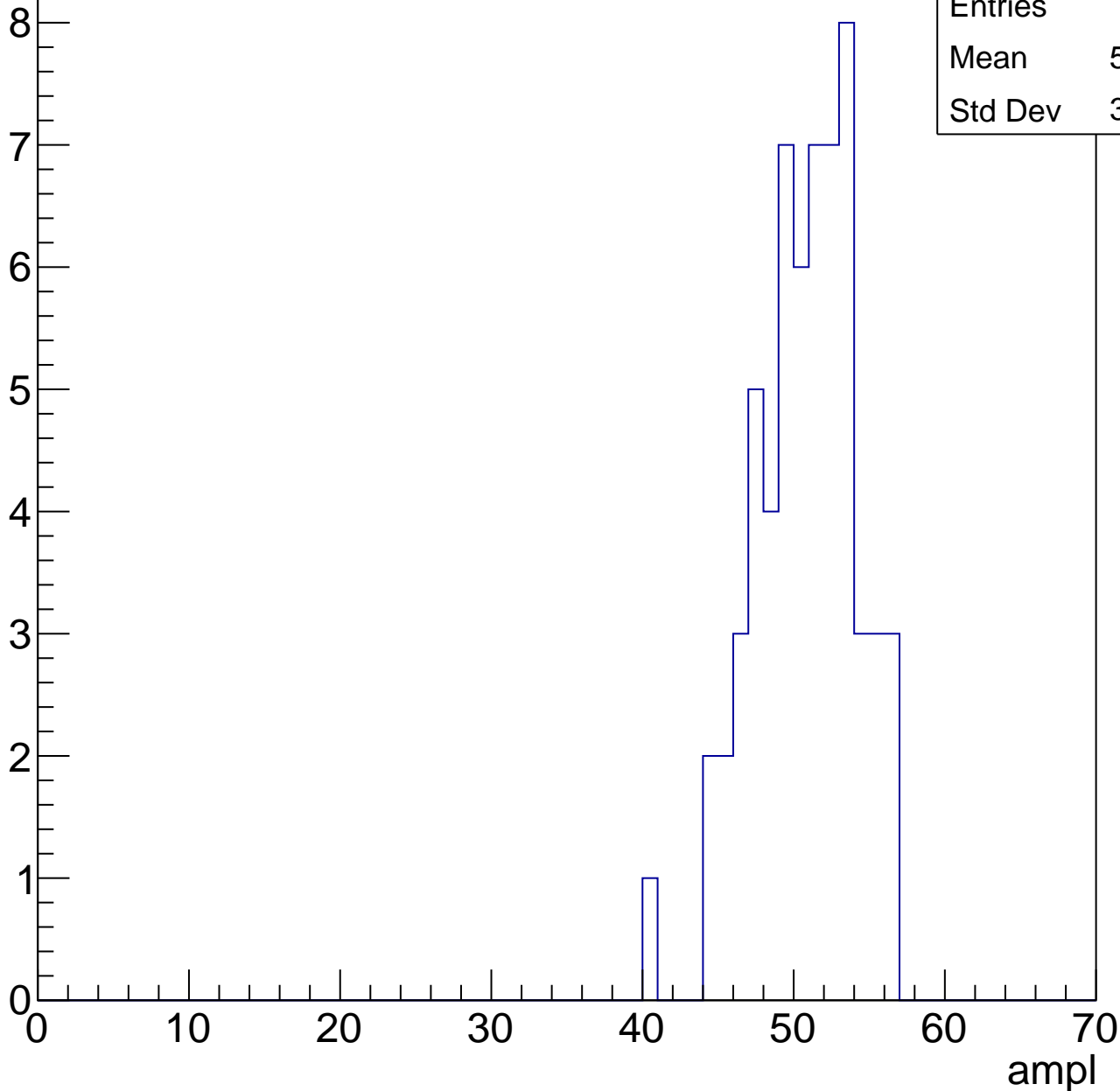


B1L100S, U25-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.26
Std Dev	3.319

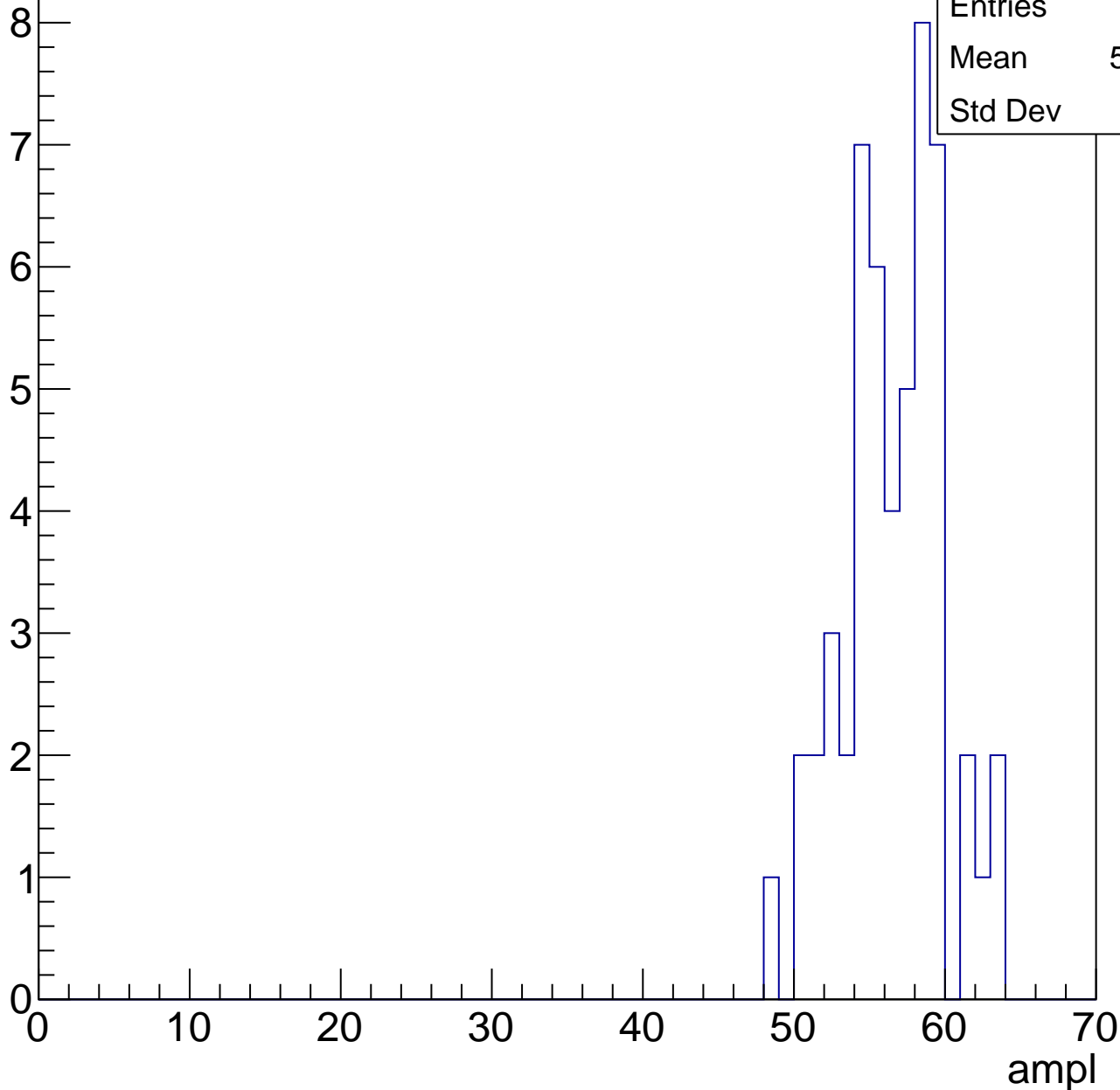


B1L100S, U25-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.08
Std Dev	3.31

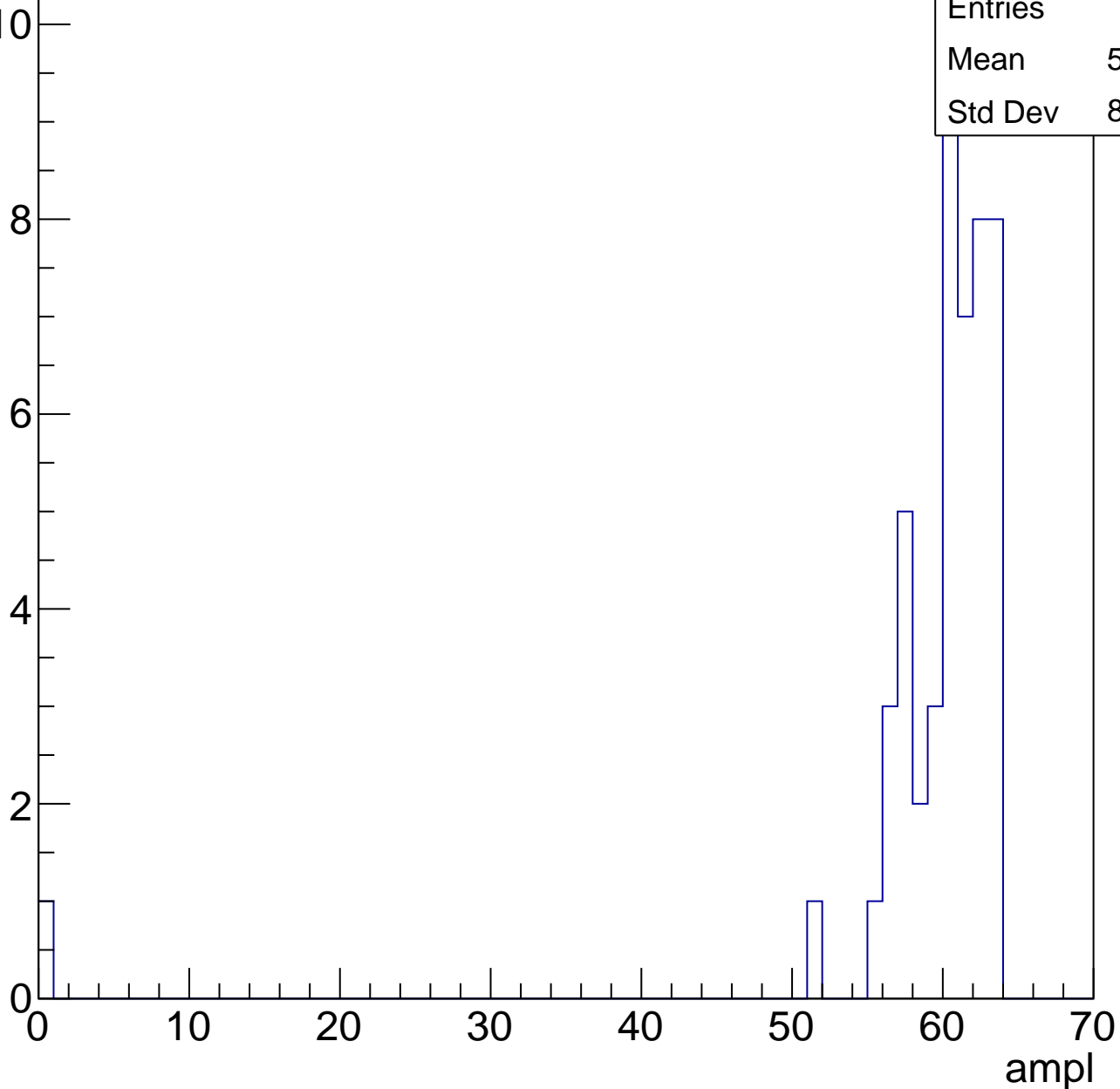


B1L100S, U25-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.76
Std Dev	8.858



B1L100S, U25-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

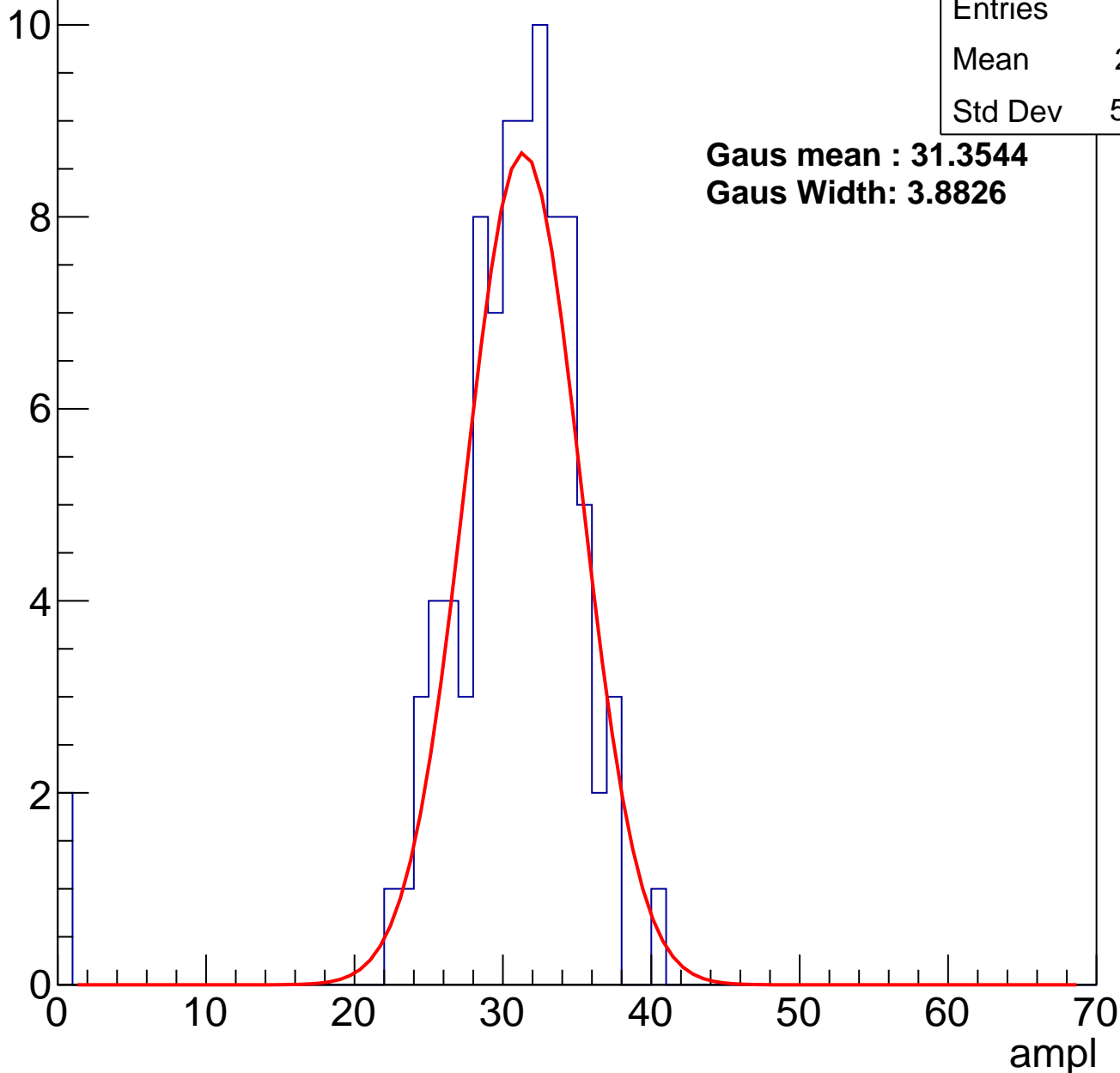
B1L100S, U25-ch52, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	29.91
Std Dev	5.783

Gaus mean : 31.3544
Gaus Width: 3.8826

Entry



B1L100S, U25-ch52, adc1

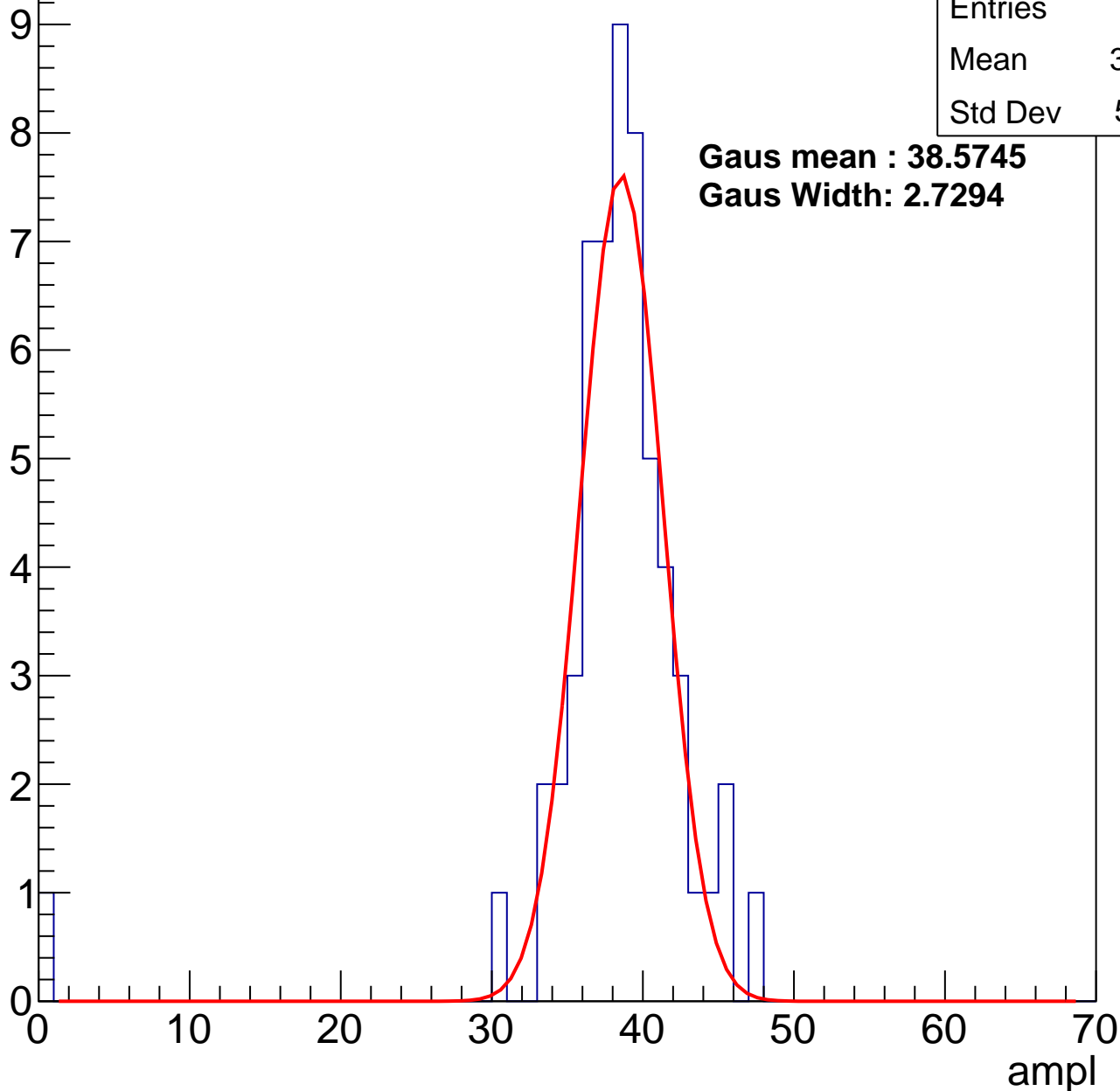
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	37.68
Std Dev	5.921

Gaus mean : 38.5745

Gaus Width: 2.7294



B1L100S, U25-ch52, adc2

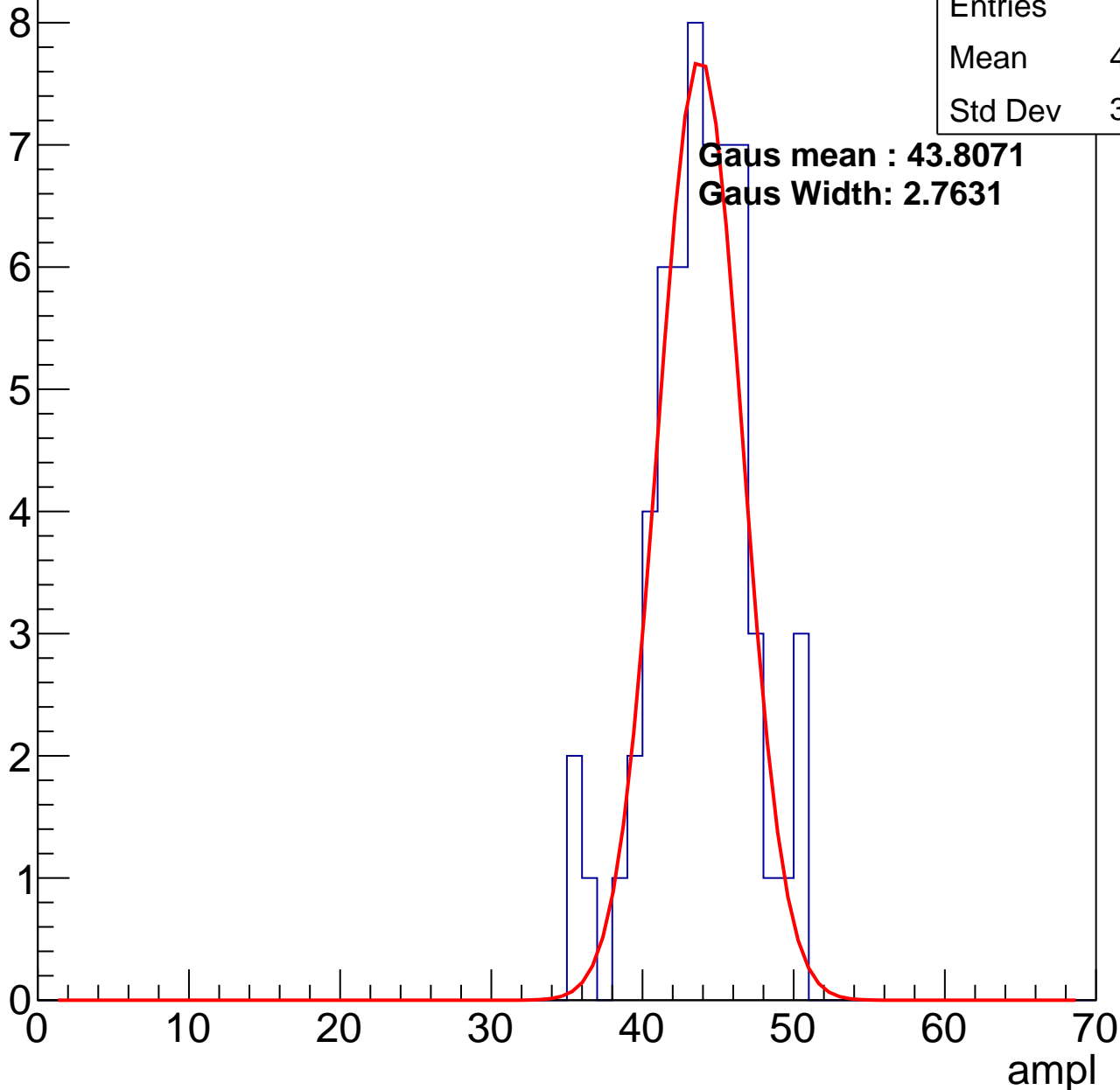
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.34
Std Dev	3.312

Gaus mean : 43.8071

Gaus Width: 2.7631

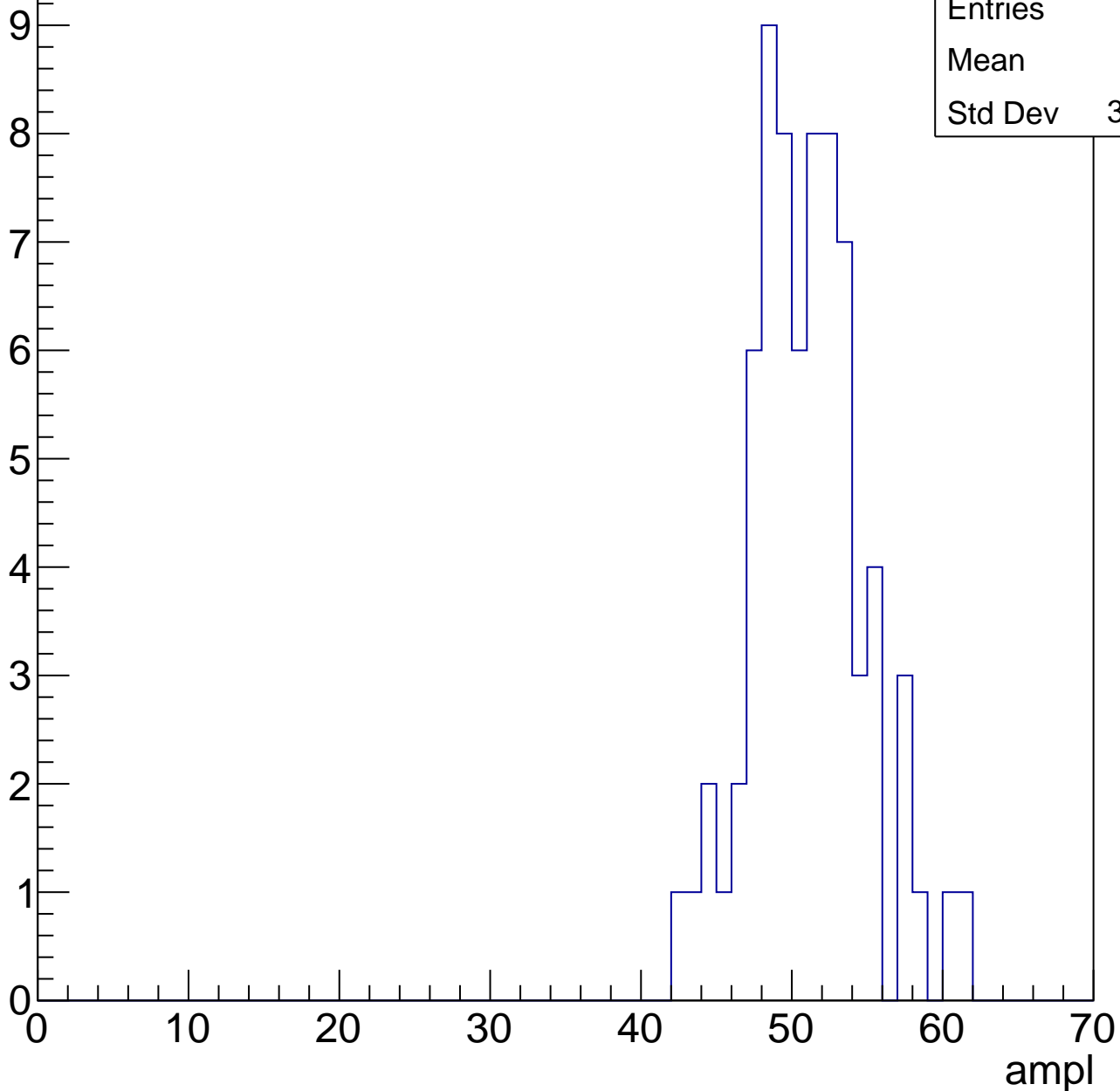


B1L100S, U25-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	50.6
Std Dev	3.722



B1L100S, U25-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	56.81
Std Dev	3.66

Entry

10

8

6

4

2

0

0

10

20

30

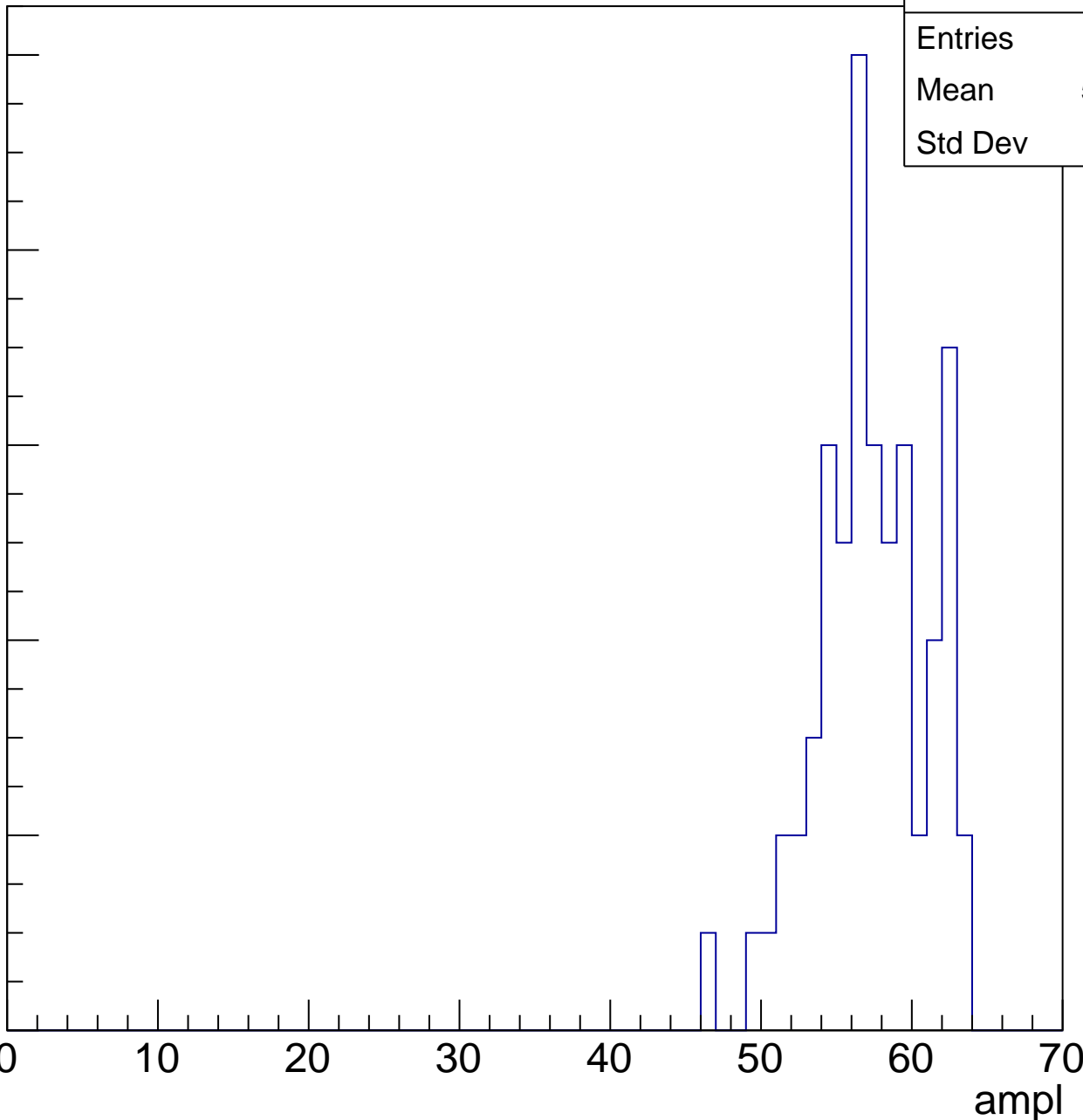
40

50

60

70

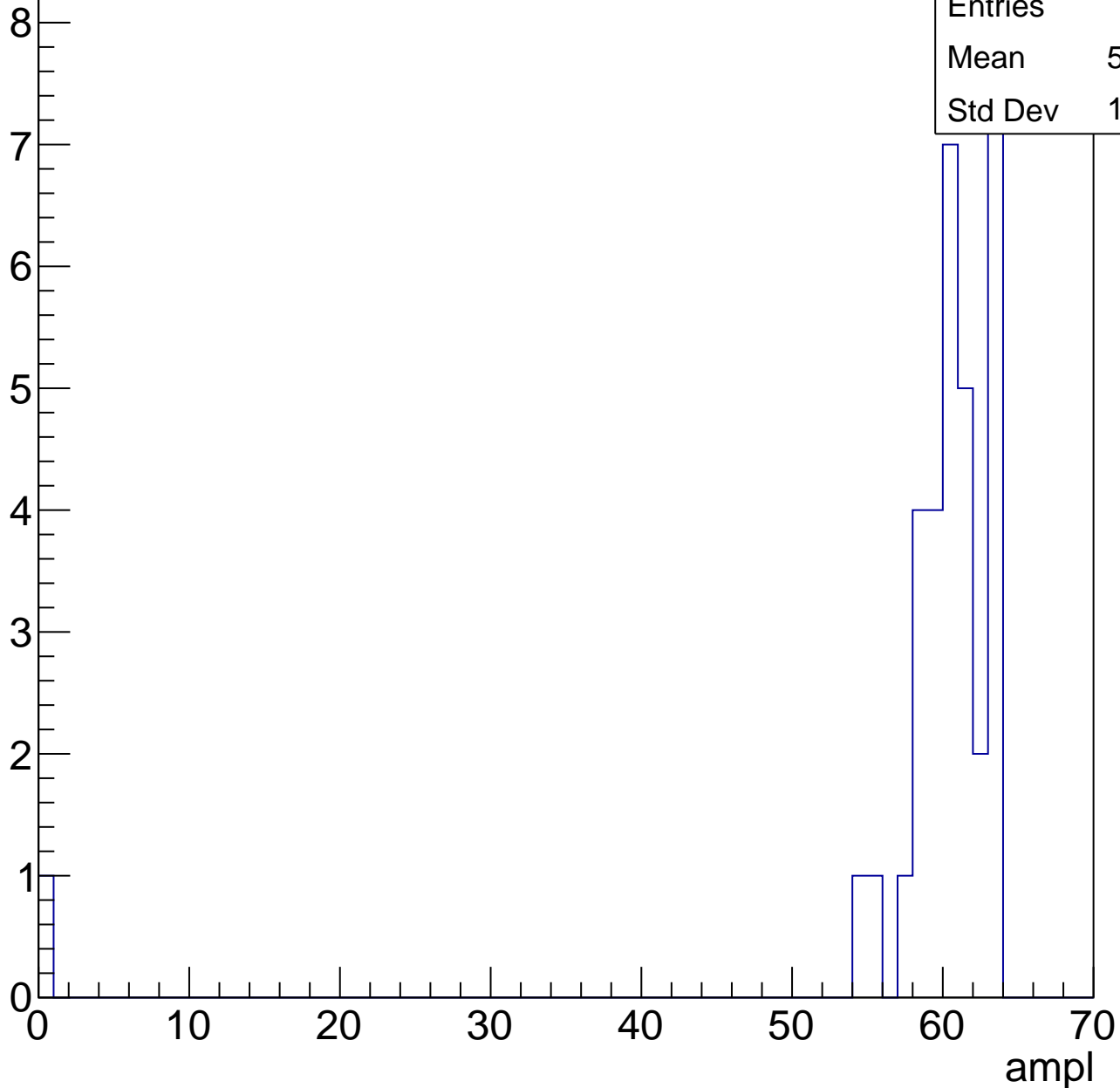
ampl



B1L100S, U25-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U25-ch53, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	30.16
Std Dev	5.134

Gaus mean : 30.9968

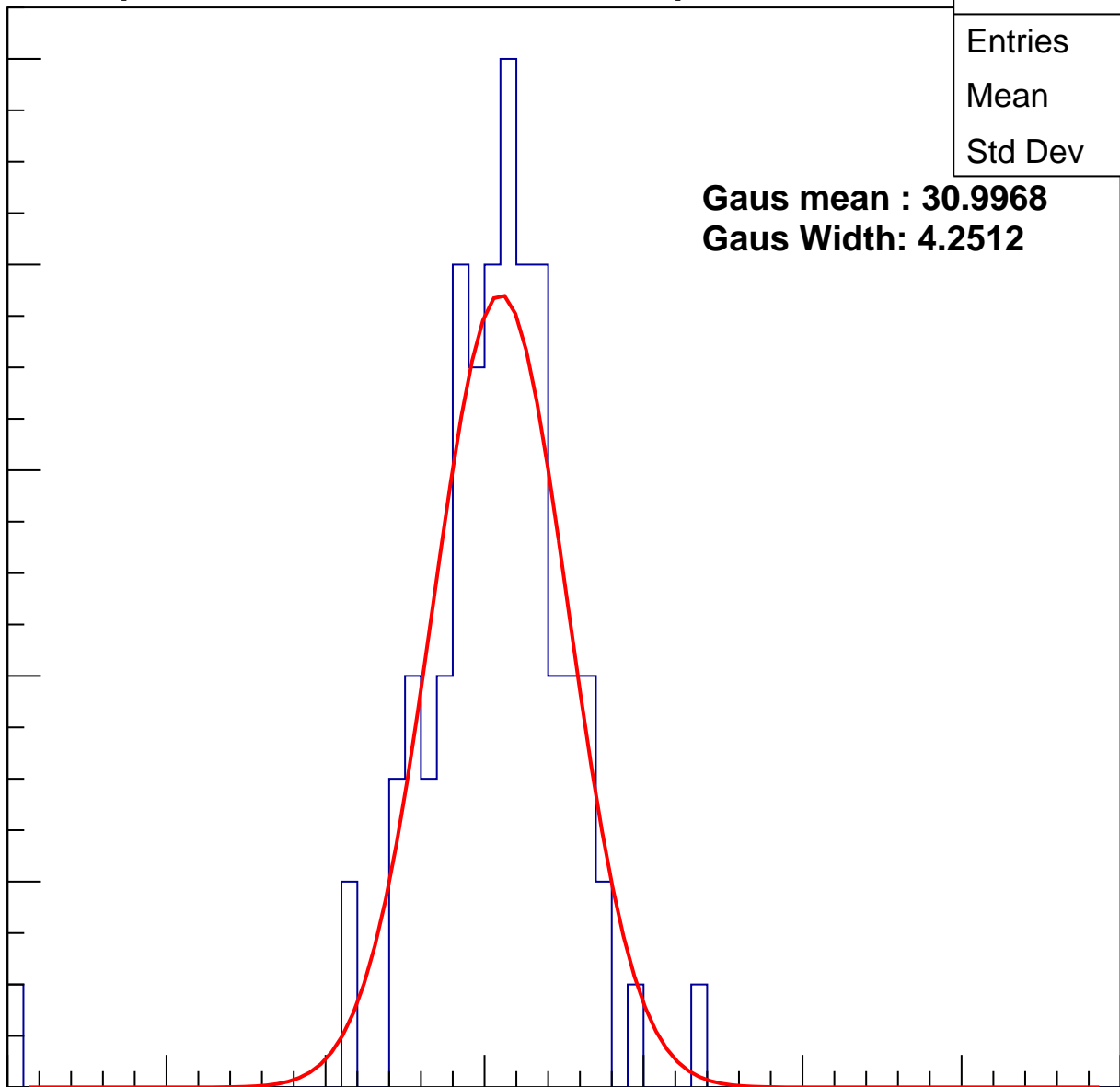
Gaus Width: 4.2512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



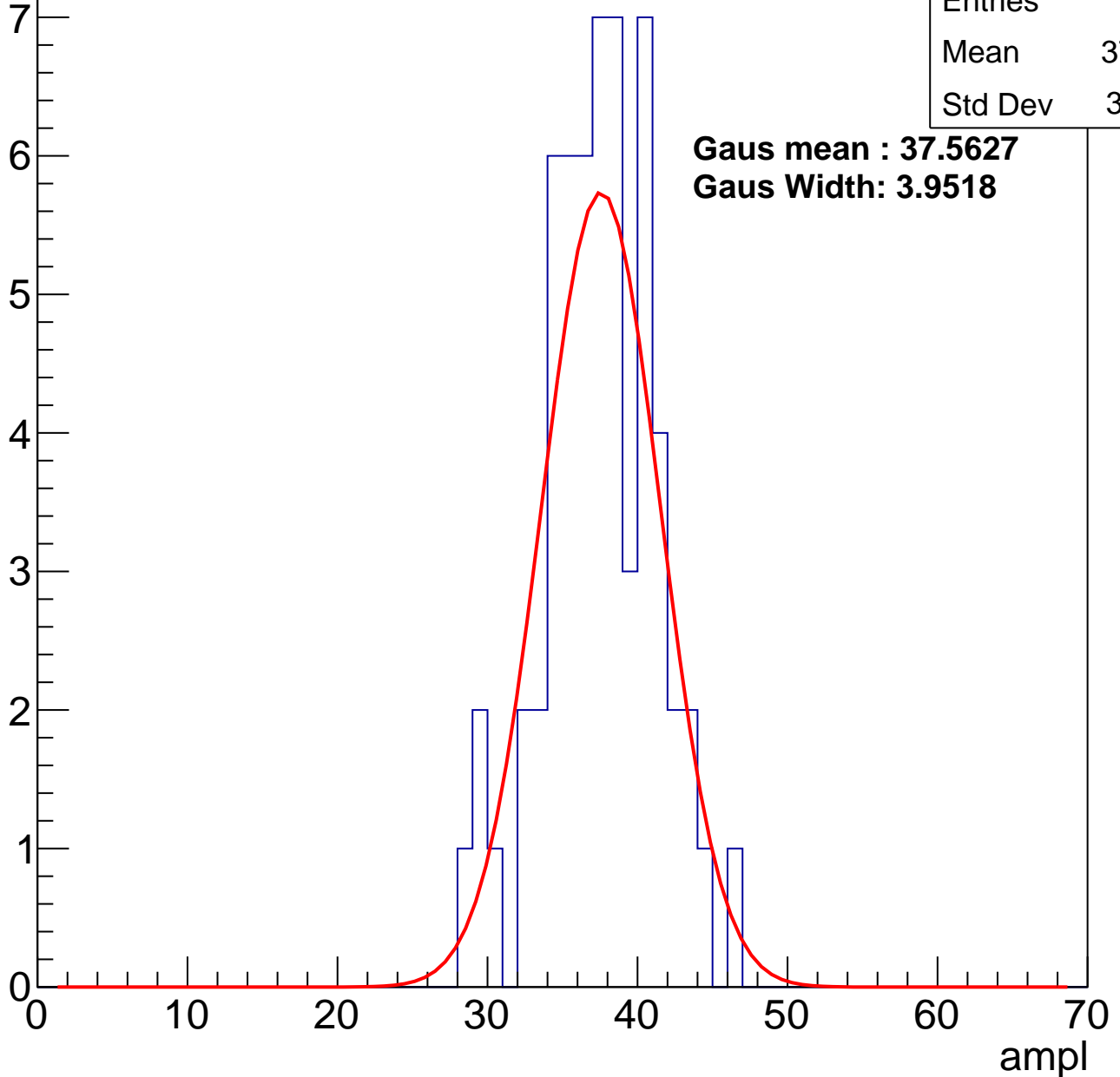
B1L100S, U25-ch53, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

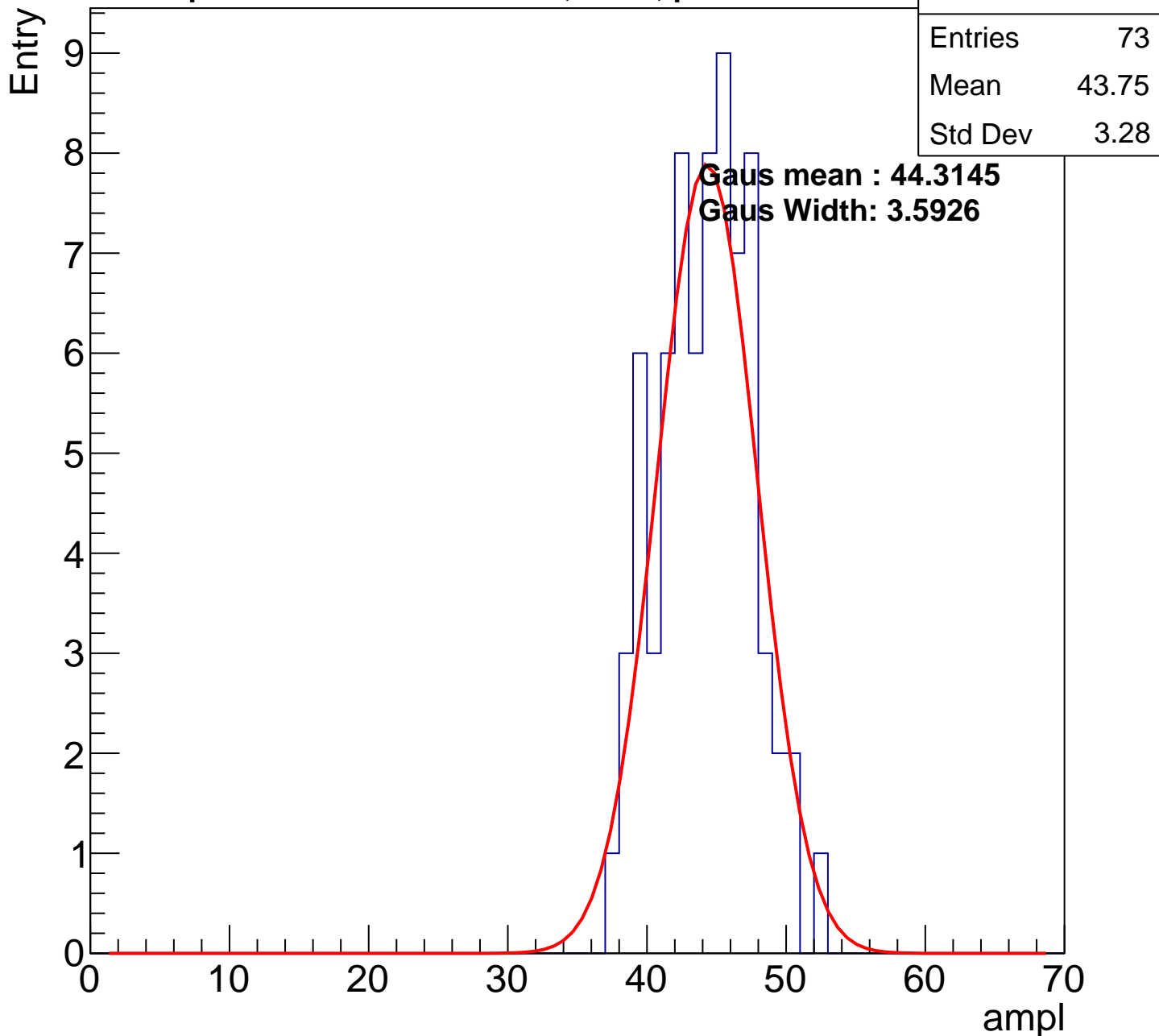
Entries	60
Mean	37.03
Std Dev	3.701

Gaus mean : 37.5627
Gaus Width: 3.9518



B1L100S, U25-ch53, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	51.16
Std Dev	3.212

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

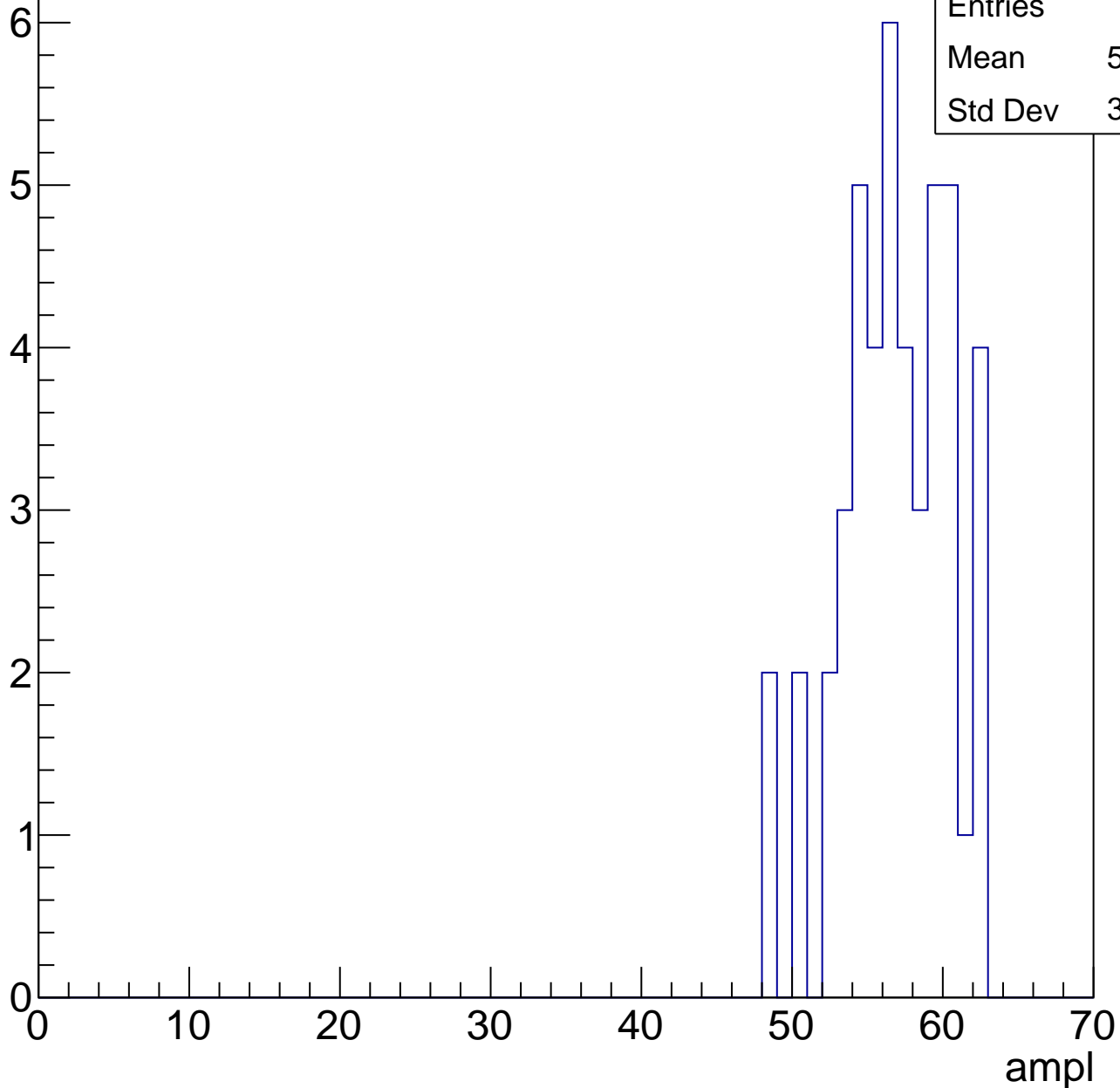
ampl

70

B1L100S, U25-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

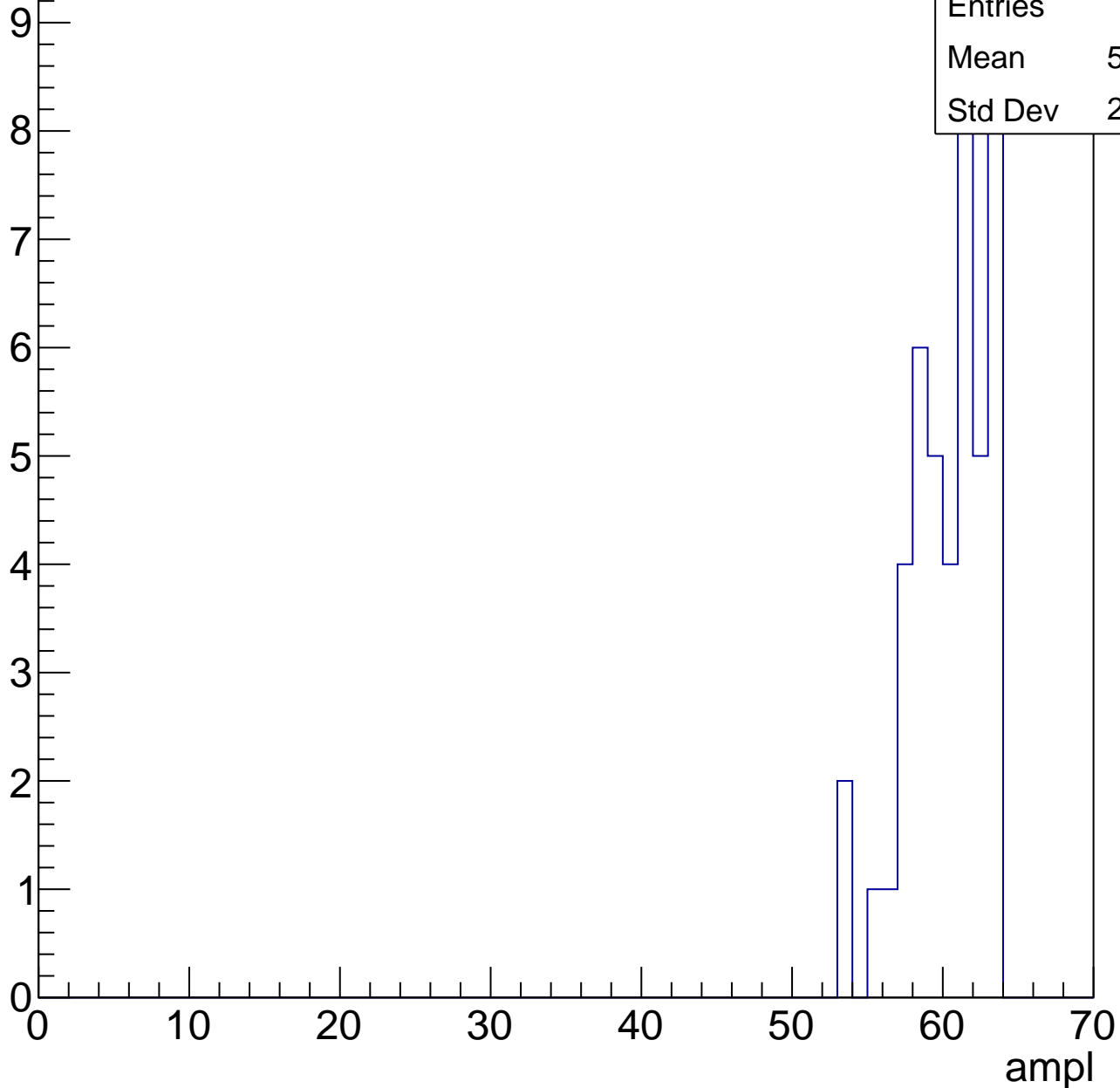


Entries	46
Mean	56.33
Std Dev	3.576

B1L100S, U25-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

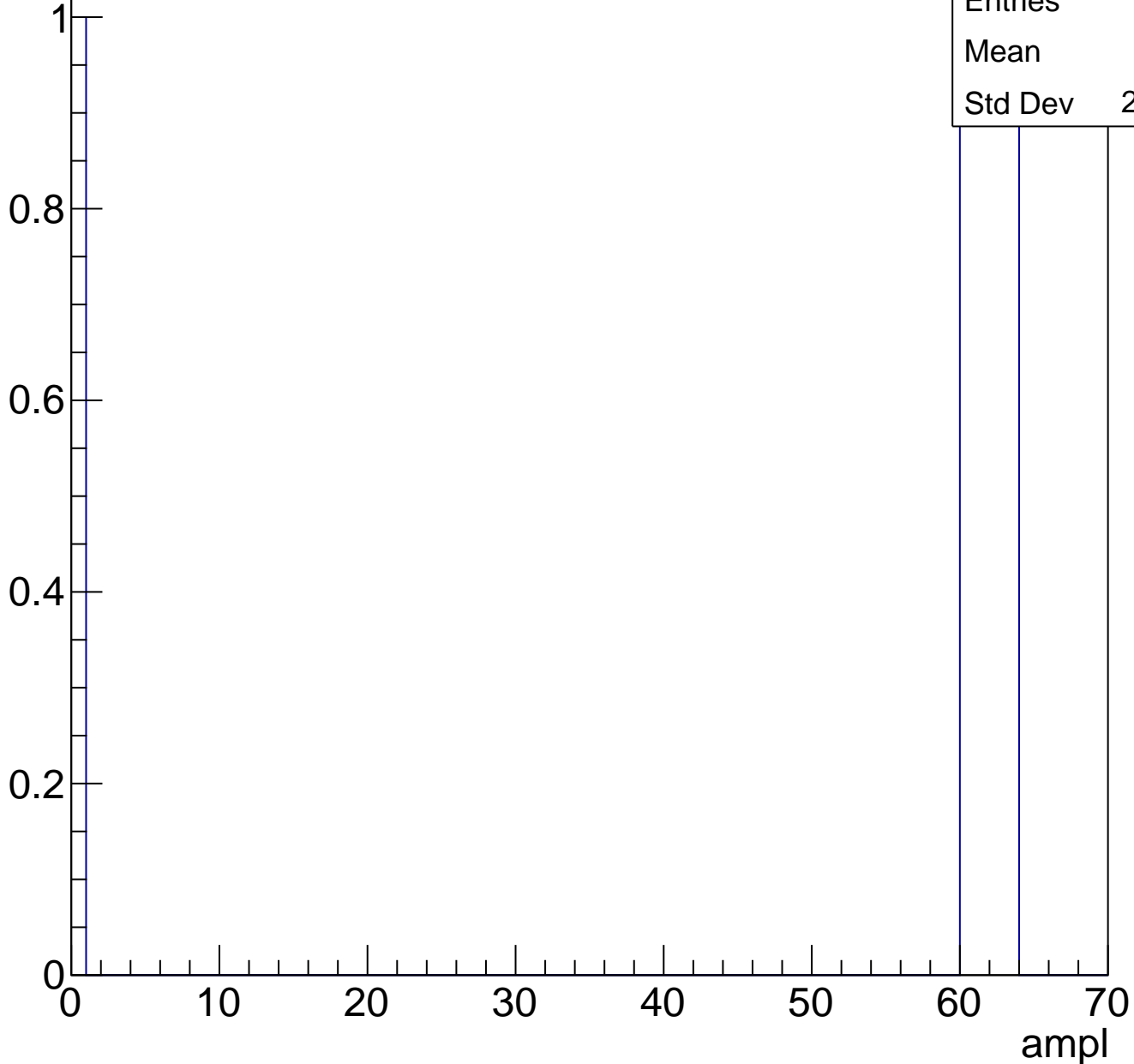
Entry



B1L100S, U25-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch54, adc0

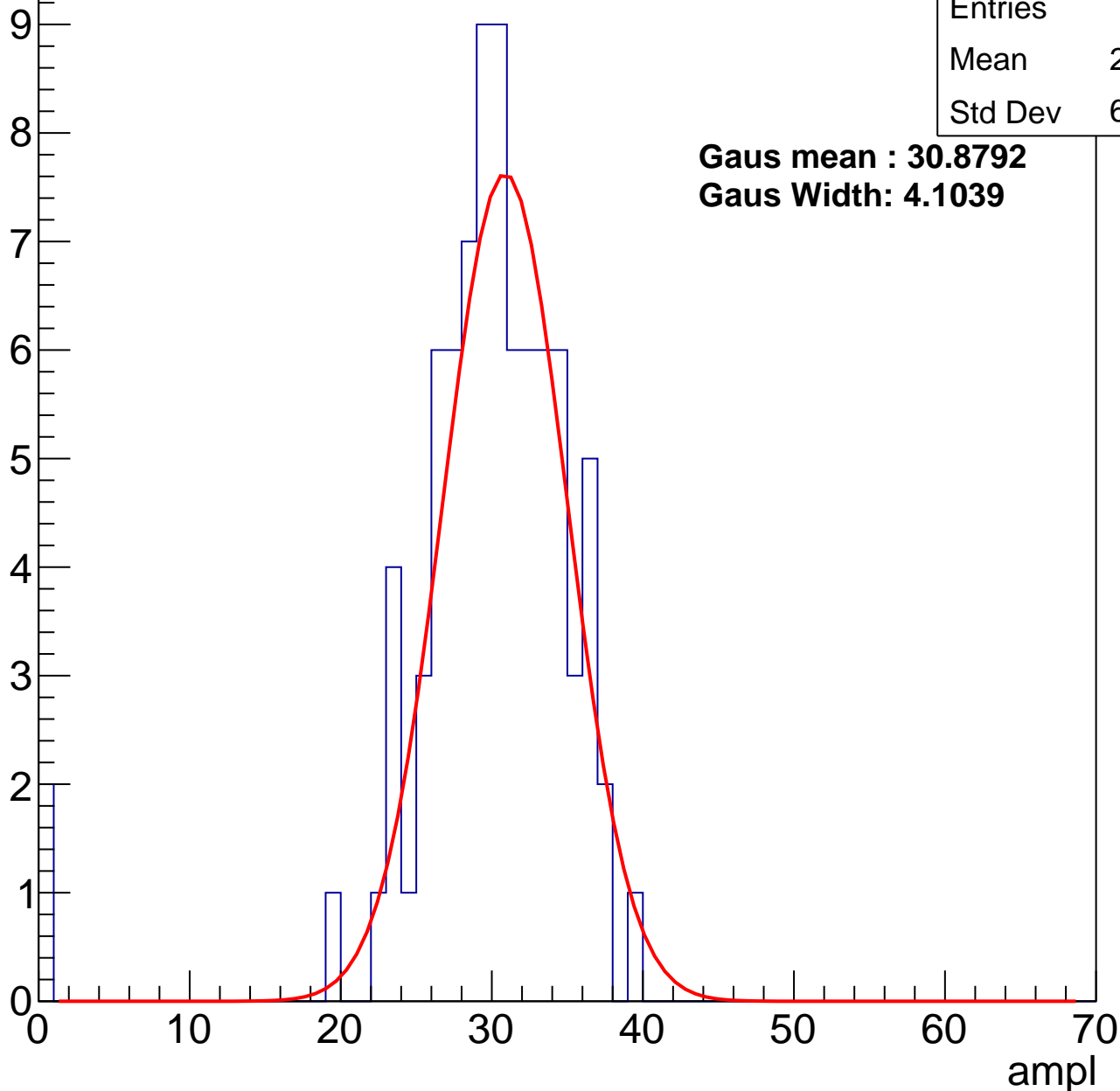
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	29.23
Std Dev	6.019

Gaus mean : 30.8792

Gaus Width: 4.1039



B1L100S, U25-ch54, adc1

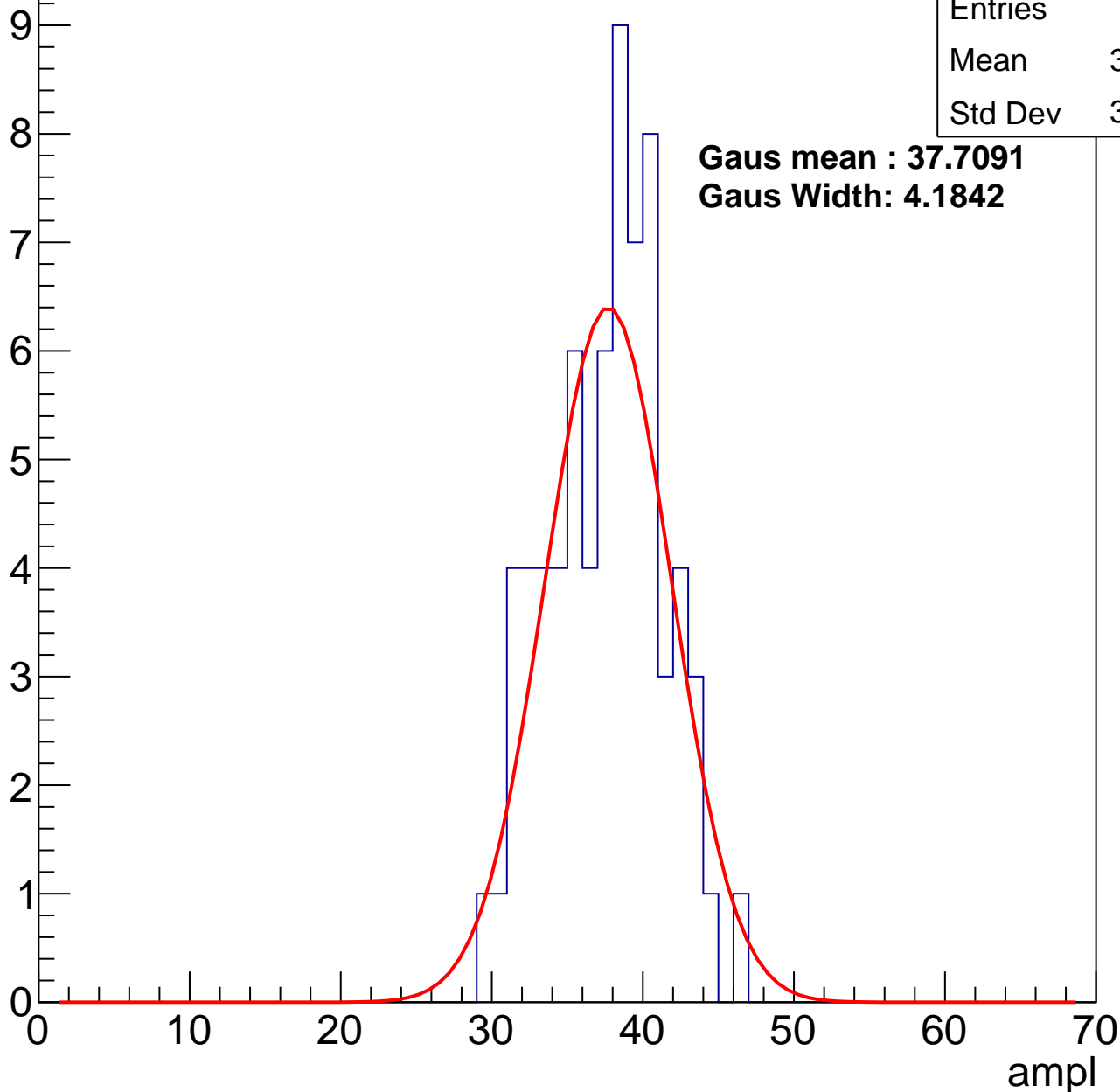
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.14
Std Dev	3.739

Gaus mean : 37.7091

Gaus Width: 4.1842



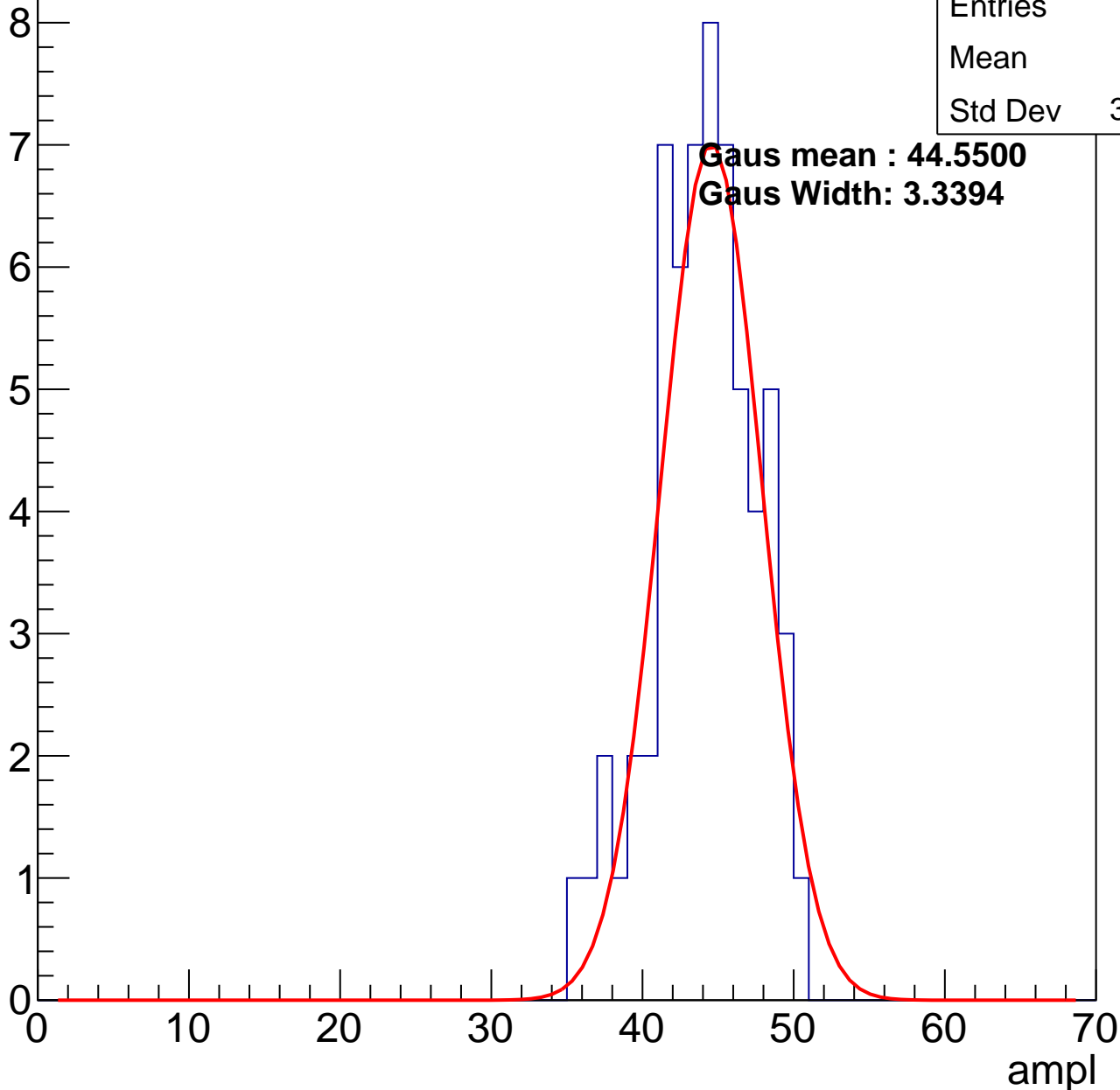
B1L100S, U25-ch54, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.6
Std Dev	3.358

Gaus mean : 44.5500
Gaus Width: 3.3394

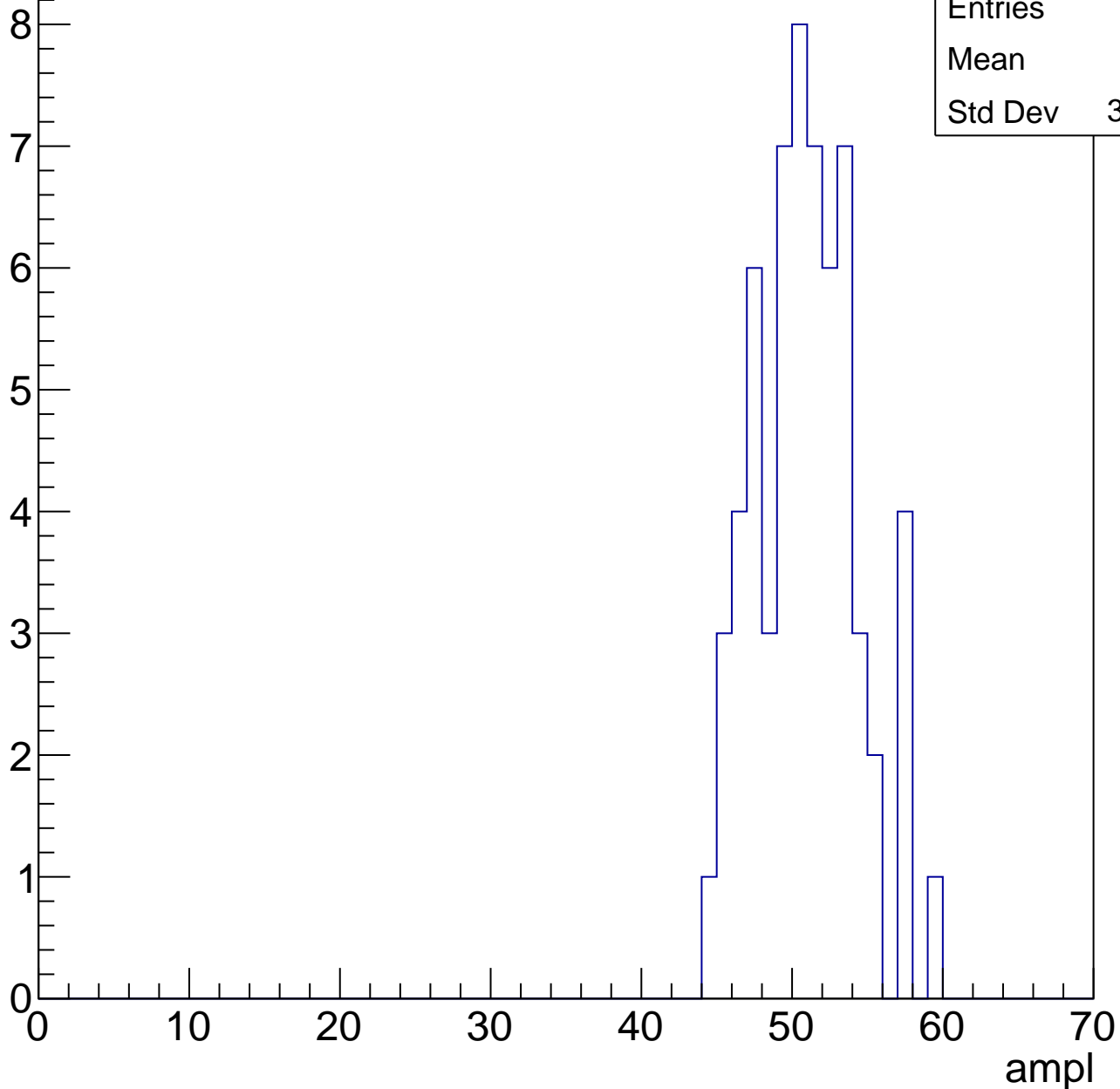


B1L100S, U25-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

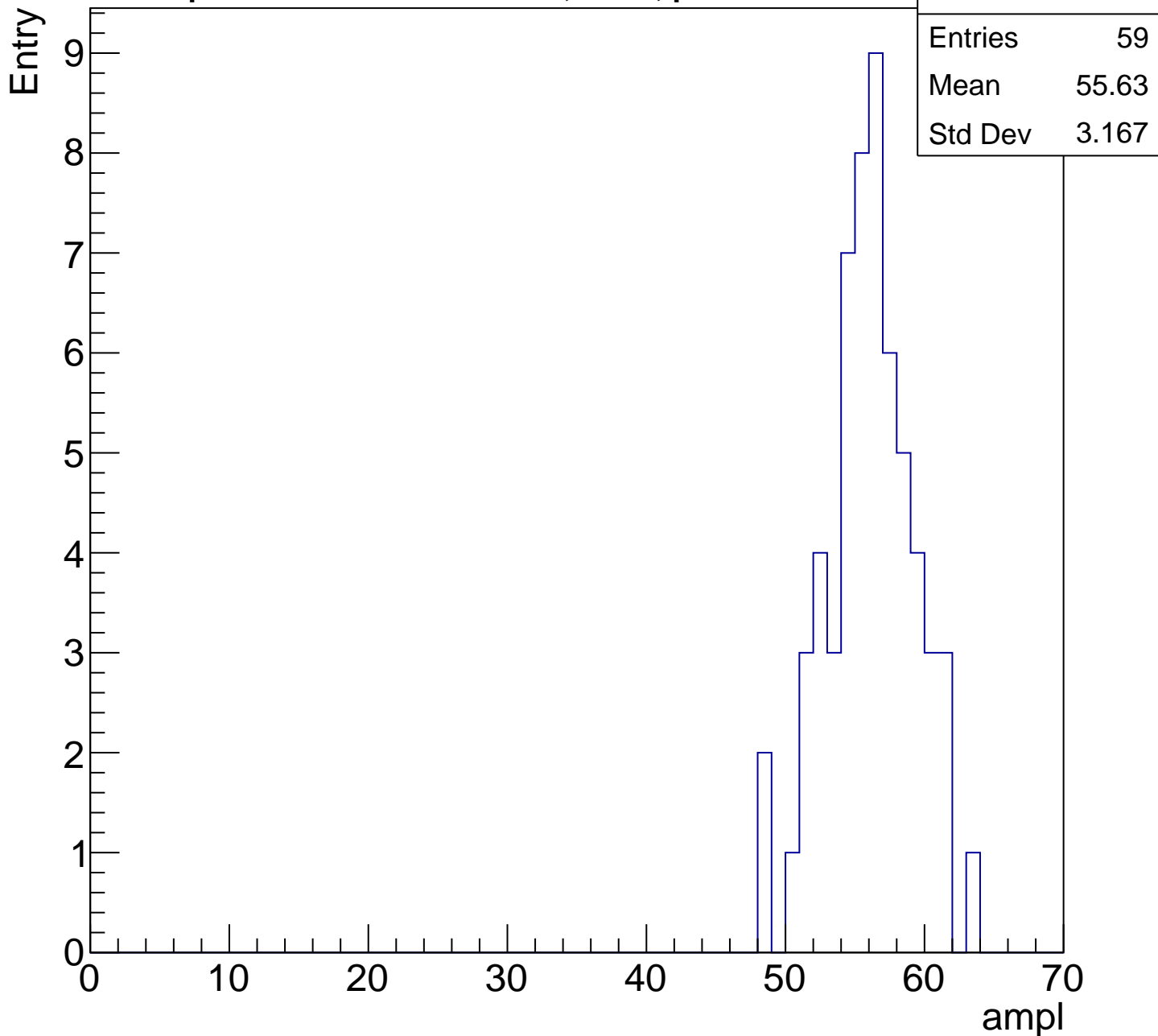
Entry

Entries	62
Mean	50.5
Std Dev	3.364



B1L100S, U25-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

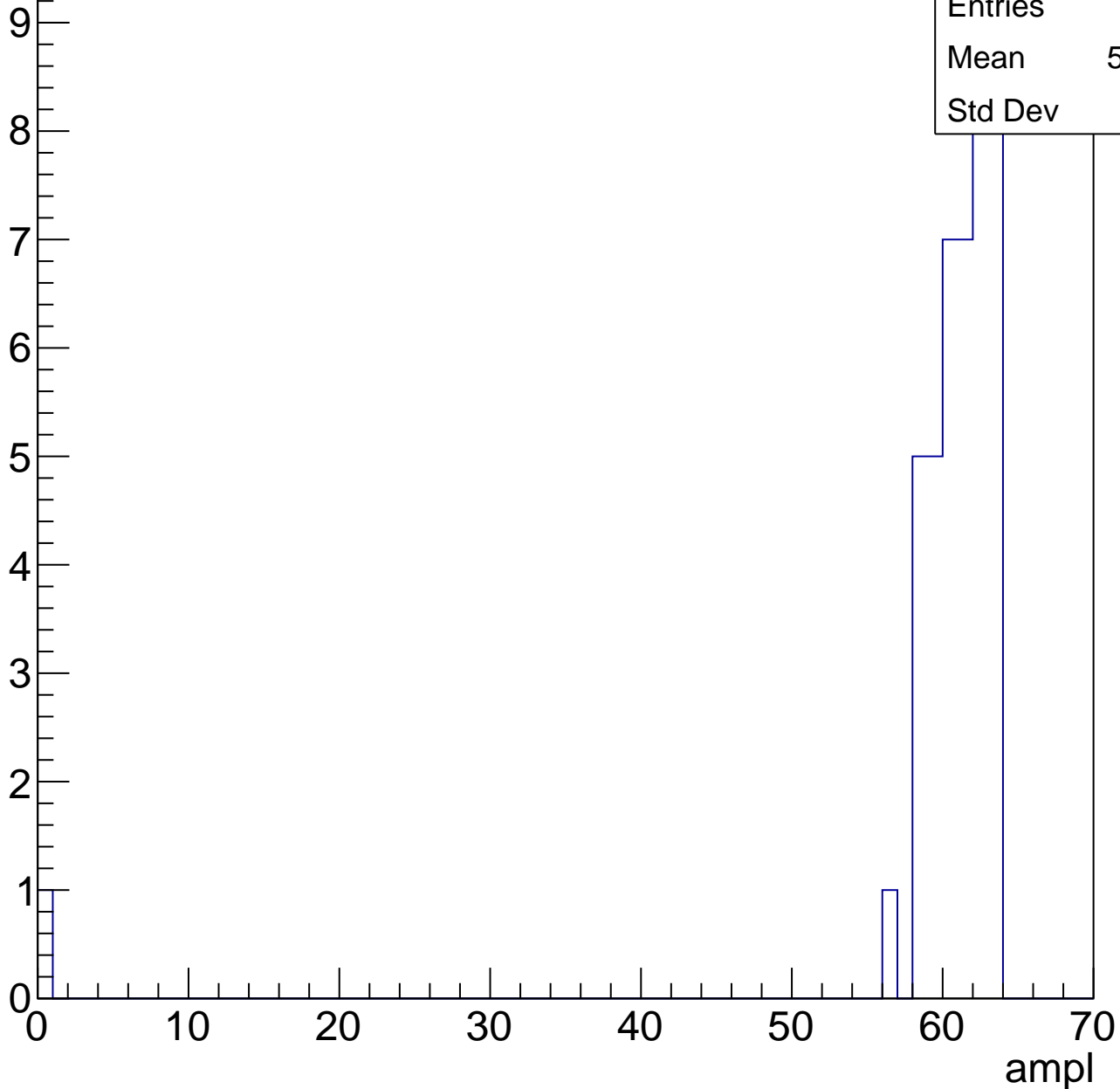


B1L100S, U25-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	59.39
Std Dev	9.23



B1L100S, U25-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U25-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch55, adc0

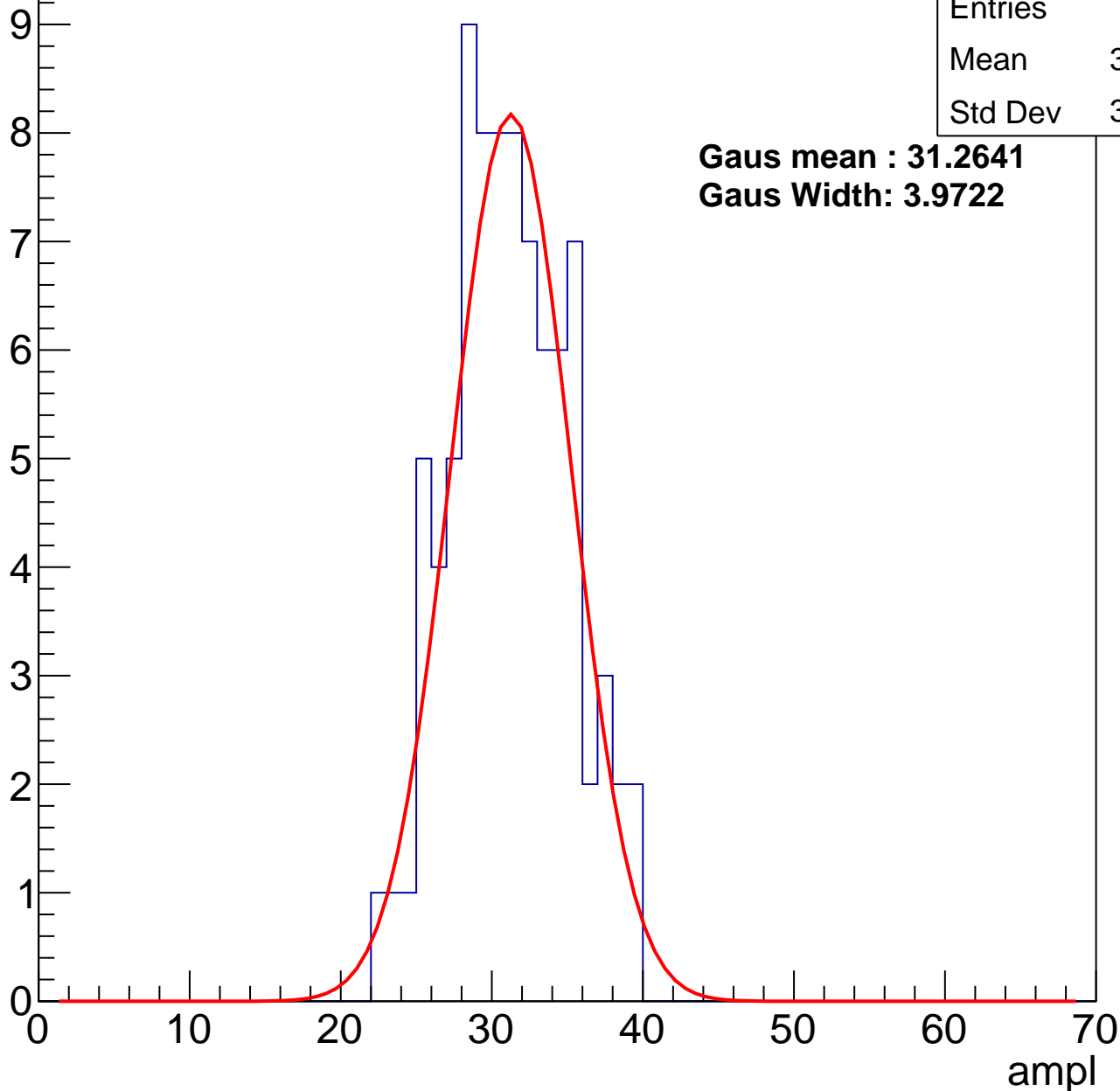
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.74
Std Dev	3.835

Gaus mean : 31.2641

Gaus Width: 3.9722



B1L100S, U25-ch55, adc1

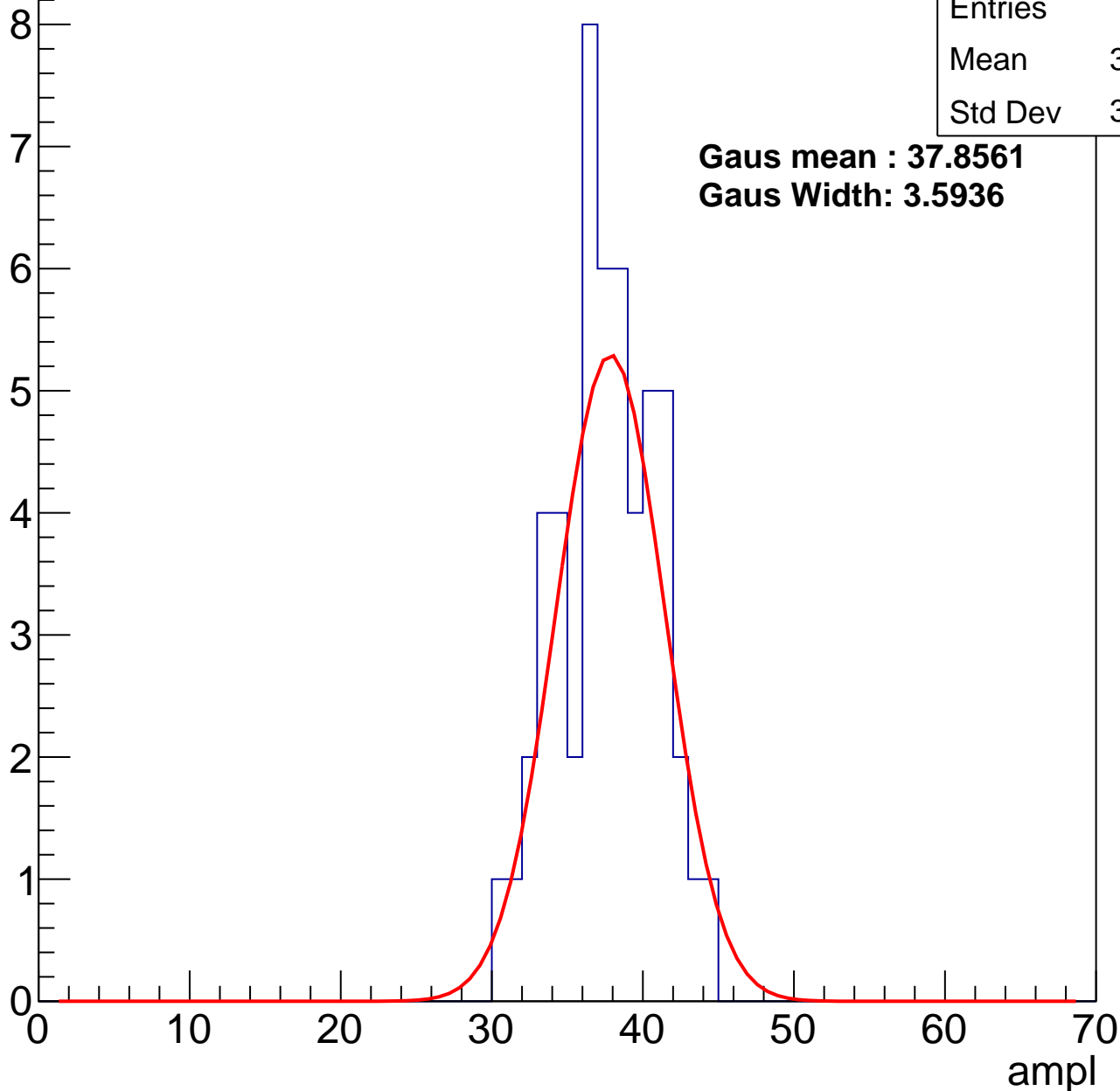
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	37.17
Std Dev	3.197

Gaus mean : 37.8561

Gaus Width: 3.5936



B1L100S, U25-ch55, adc2

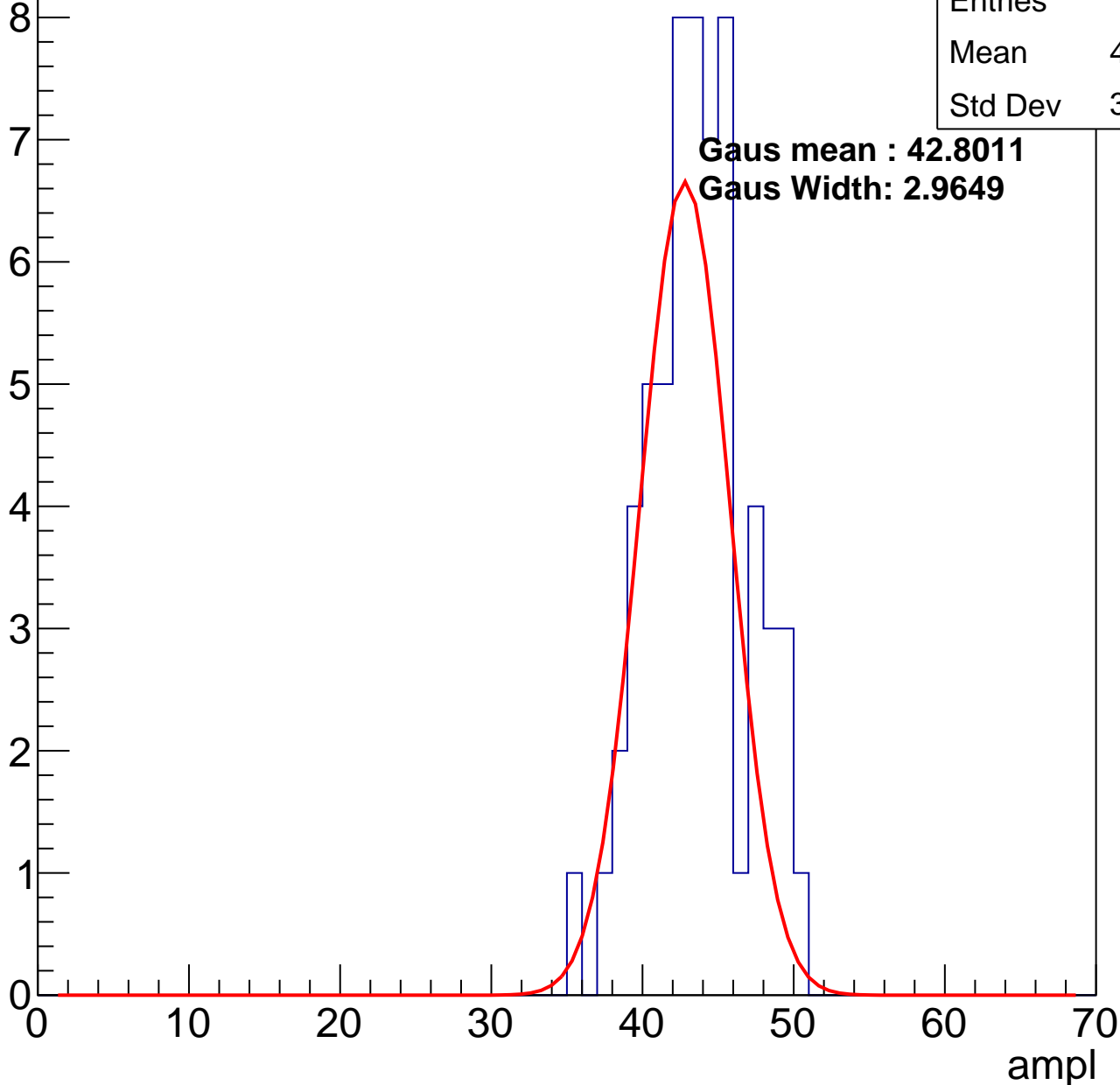
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.15
Std Dev	3.213

Gaus mean : 42.8011

Gaus Width: 2.9649

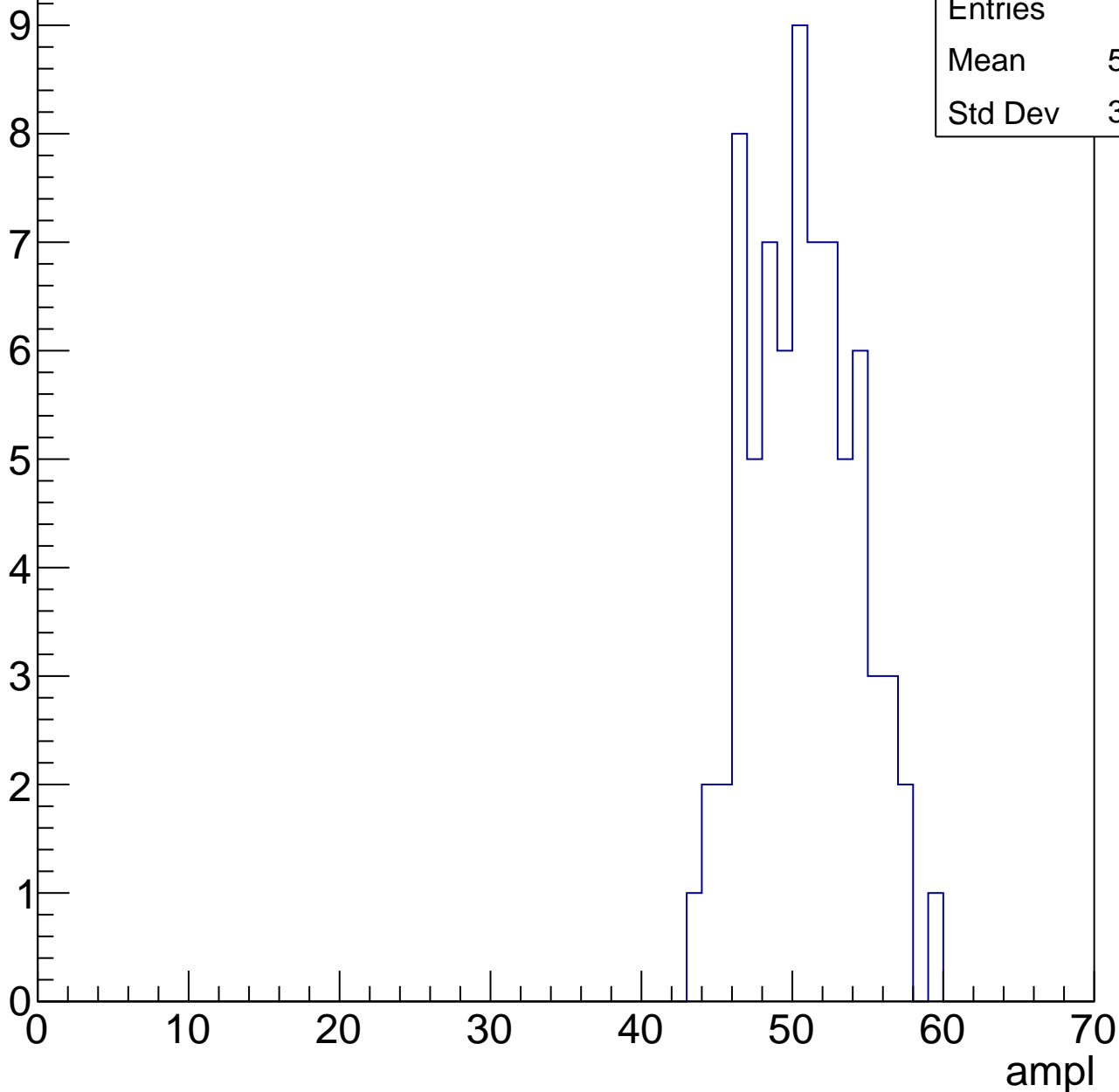


B1L100S, U25-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.27
Std Dev	3.508

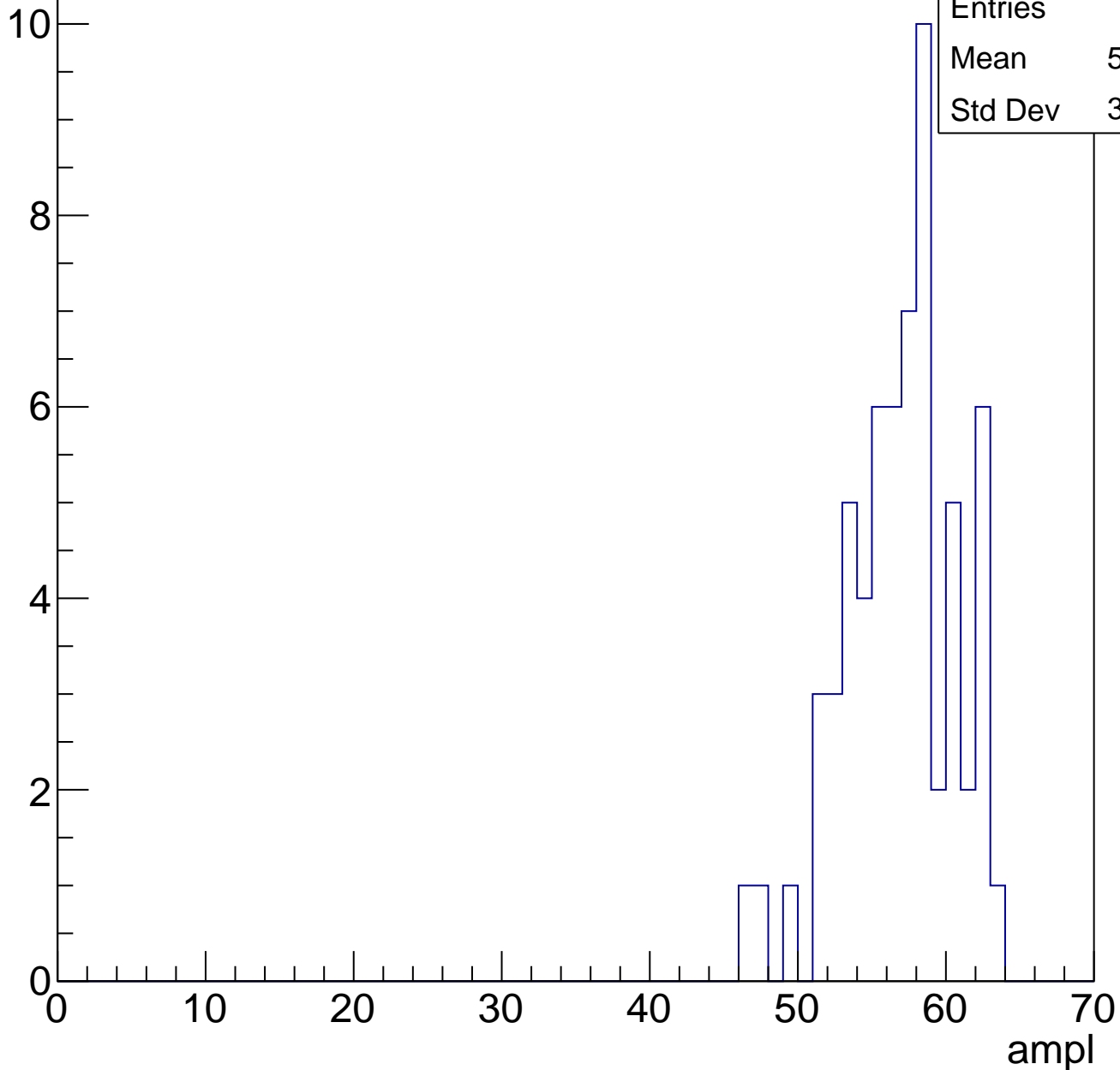


B1L100S, U25-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	56.38
Std Dev	3.718

Entry

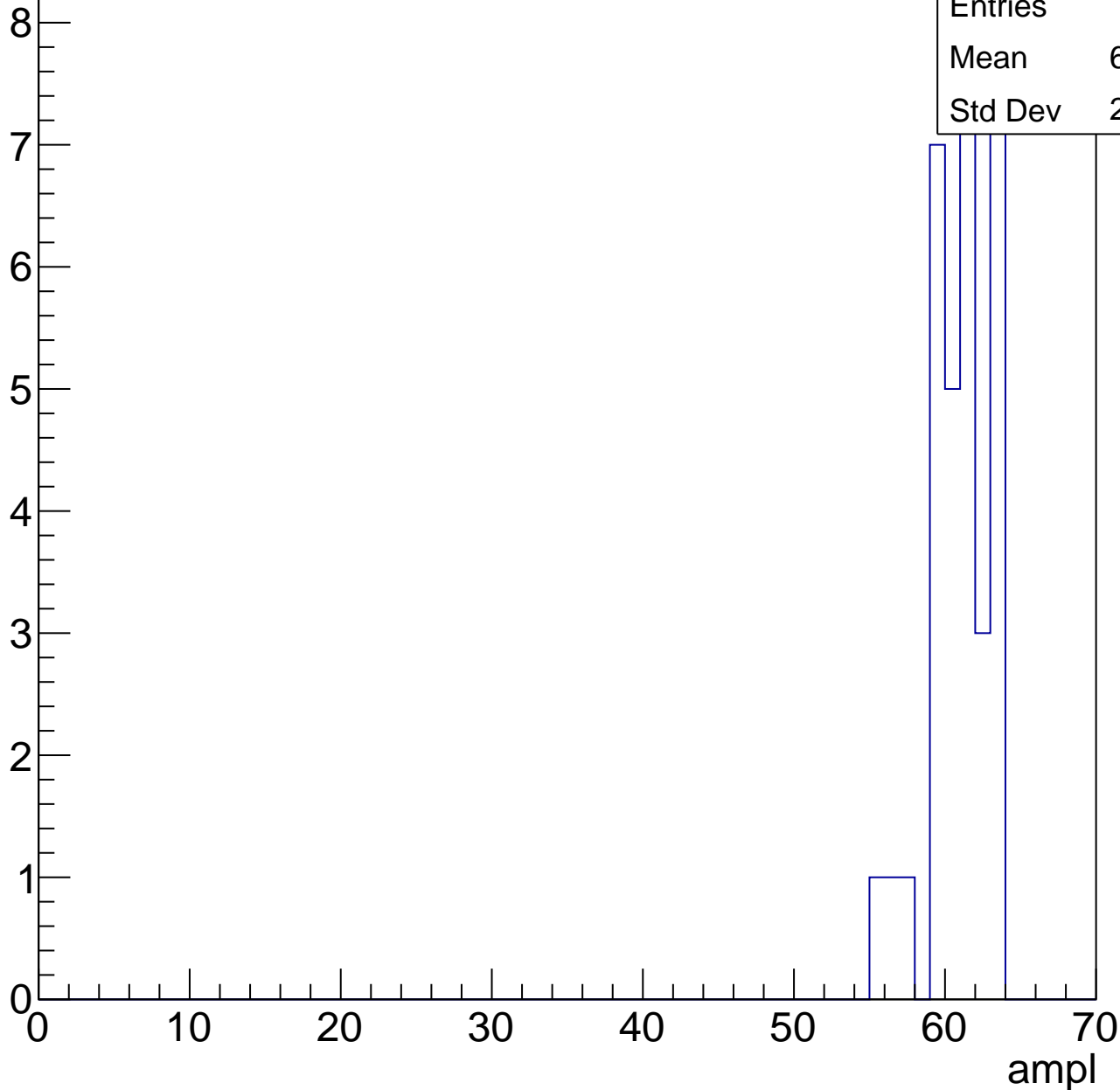


B1L100S, U25-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	60.56
Std Dev	2.017



B1L100S, U25-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch56, adc0

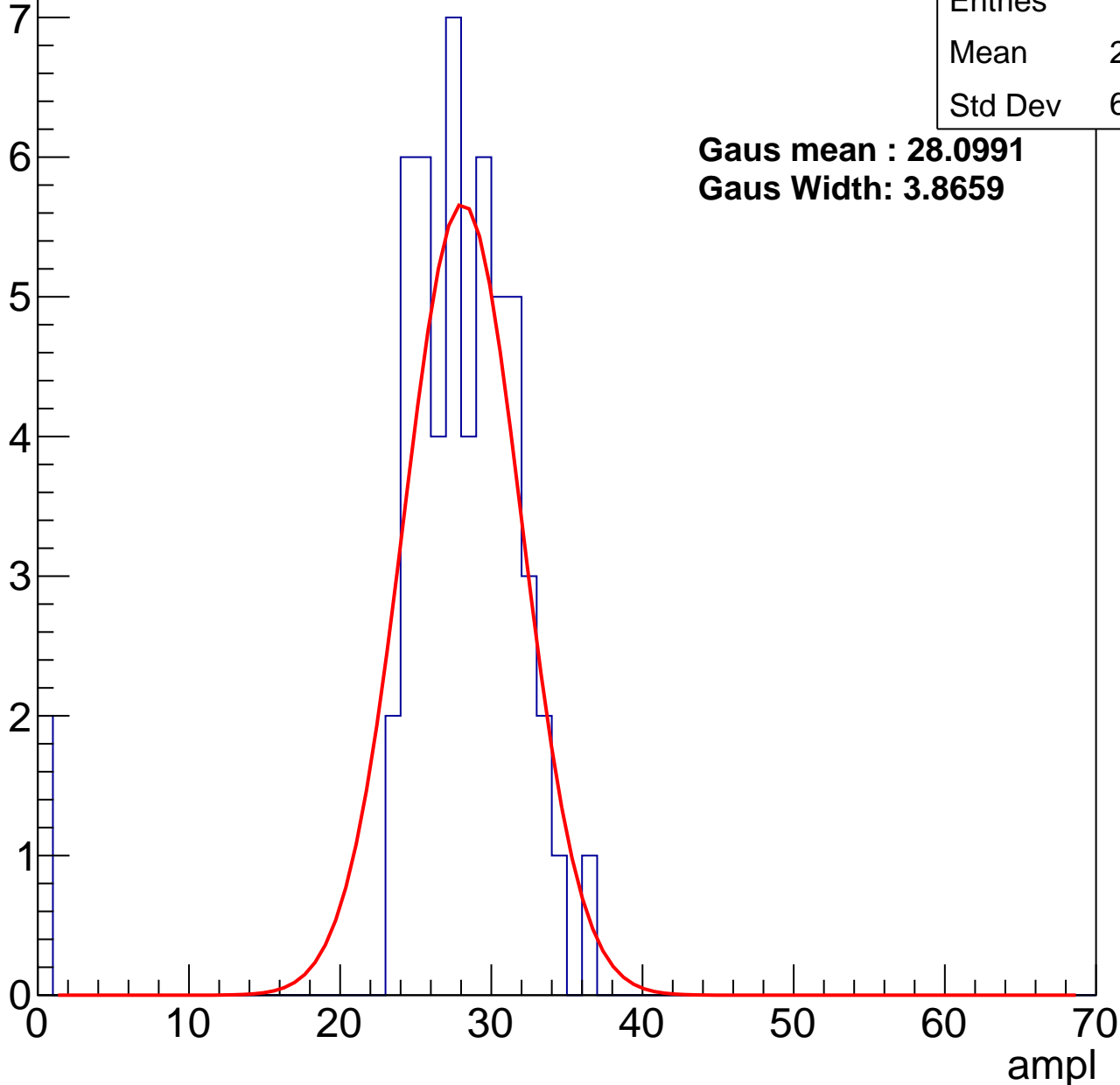
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	26.96
Std Dev	6.089

Gaus mean : 28.0991

Gaus Width: 3.8659



B1L100S, U25-ch56, adc1

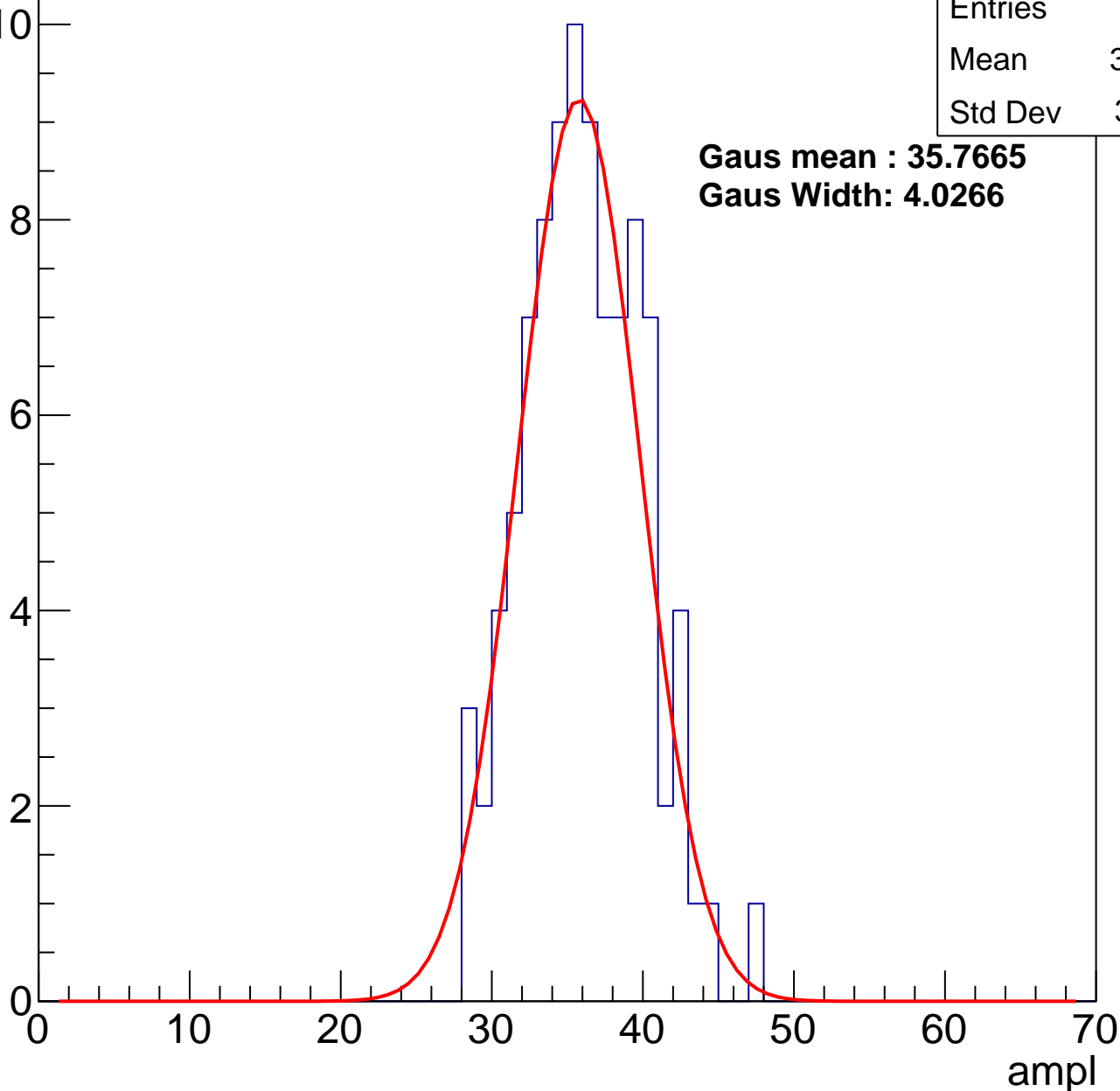
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	95
Mean	35.64
Std Dev	3.871

Gaus mean : 35.7665

Gaus Width: 4.0266



B1L100S, U25-ch56, adc2

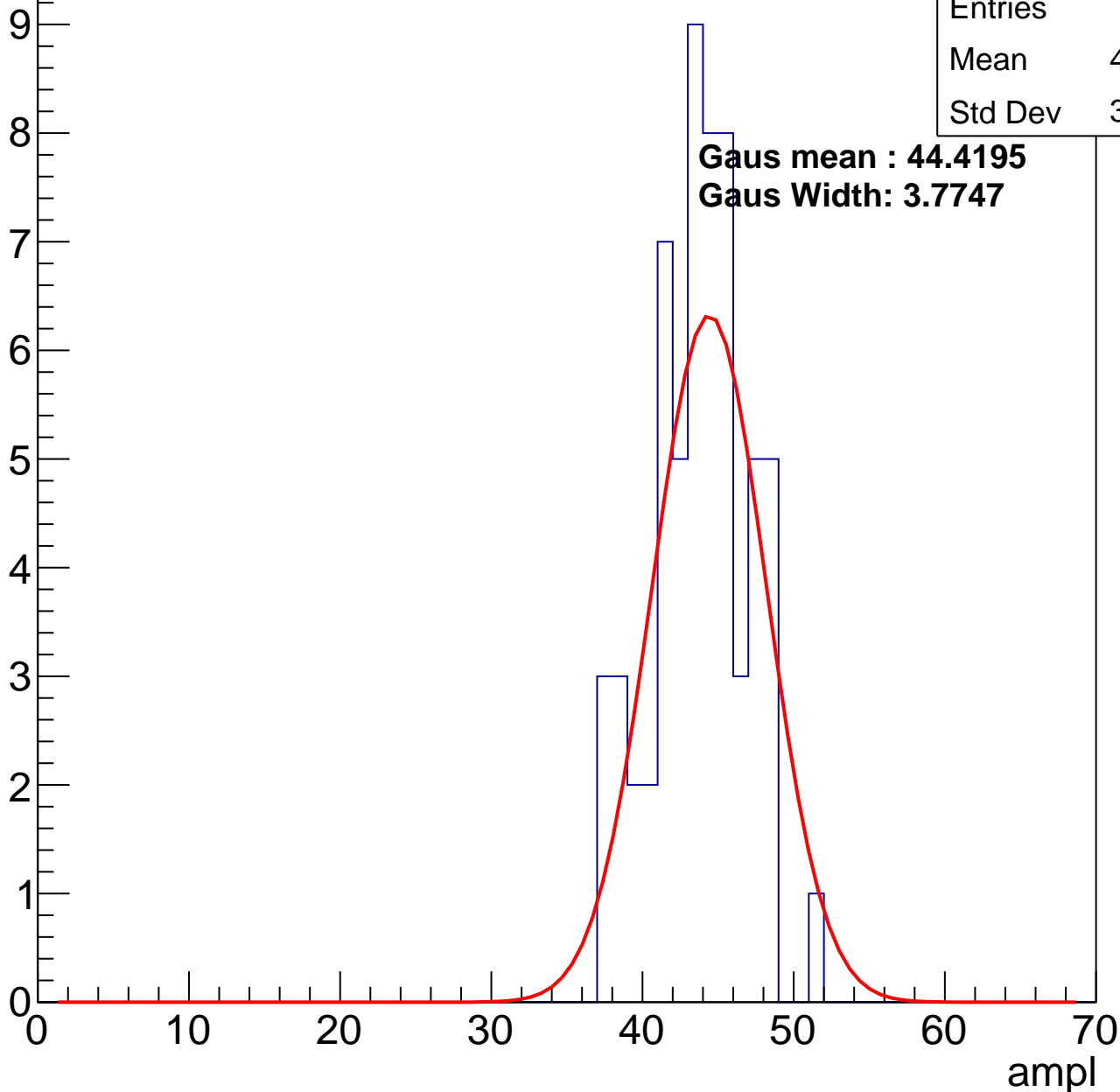
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.33
Std Dev	3.124

Gaus mean : 44.4195

Gaus Width: 3.7747

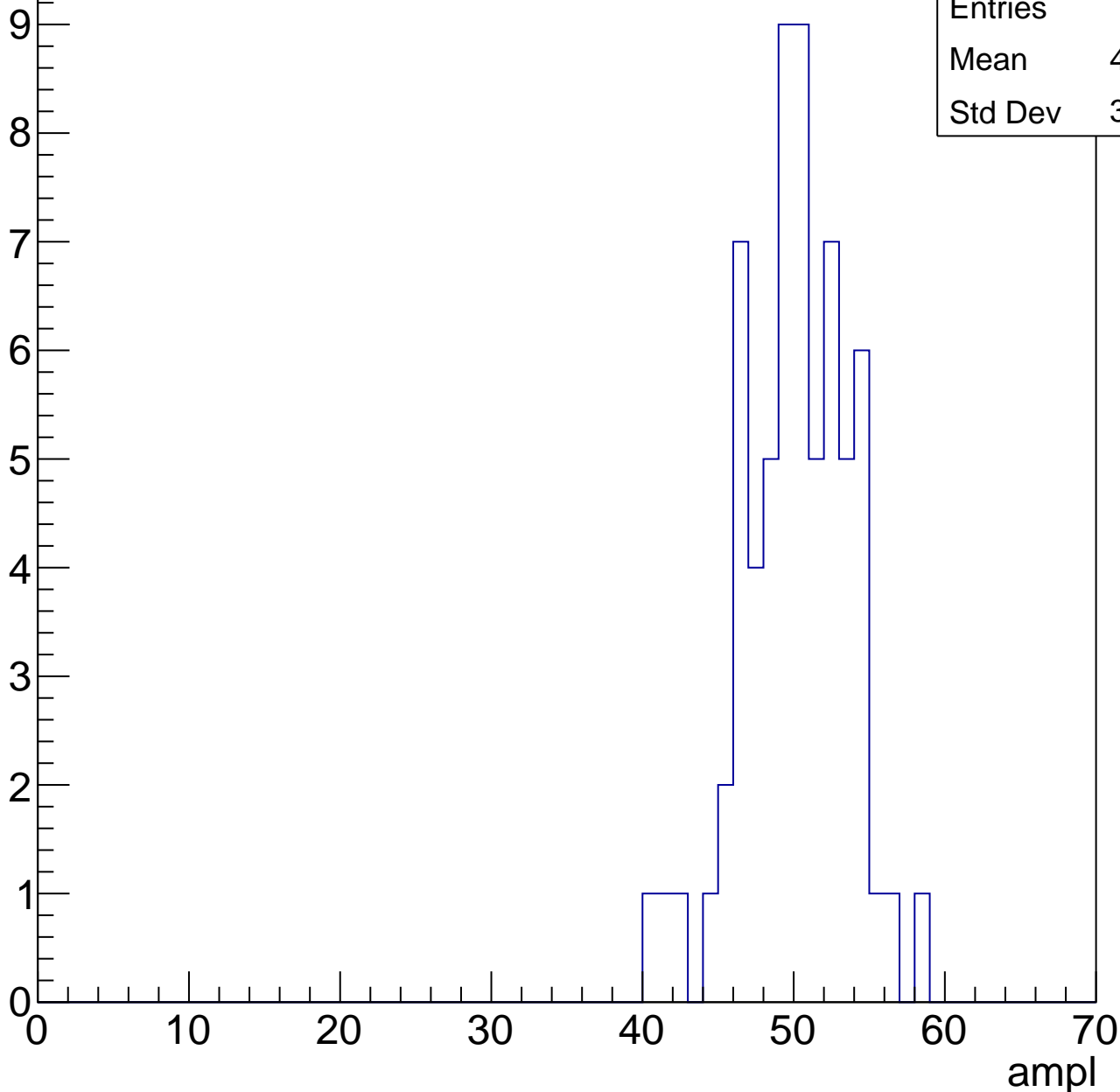


B1L100S, U25-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

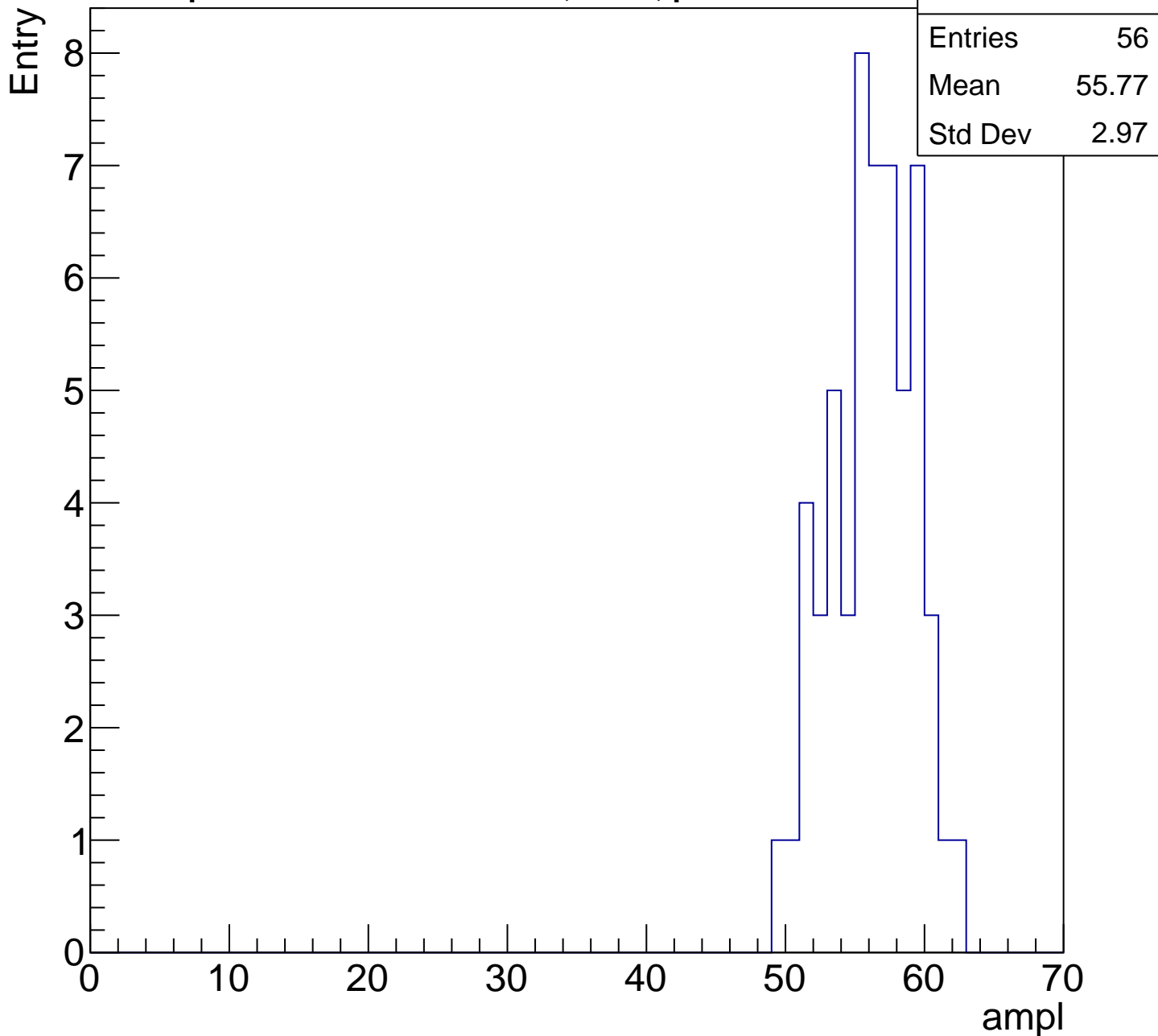
Entry

Entries	66
Mean	49.62
Std Dev	3.476



B1L100S, U25-ch56, adc4

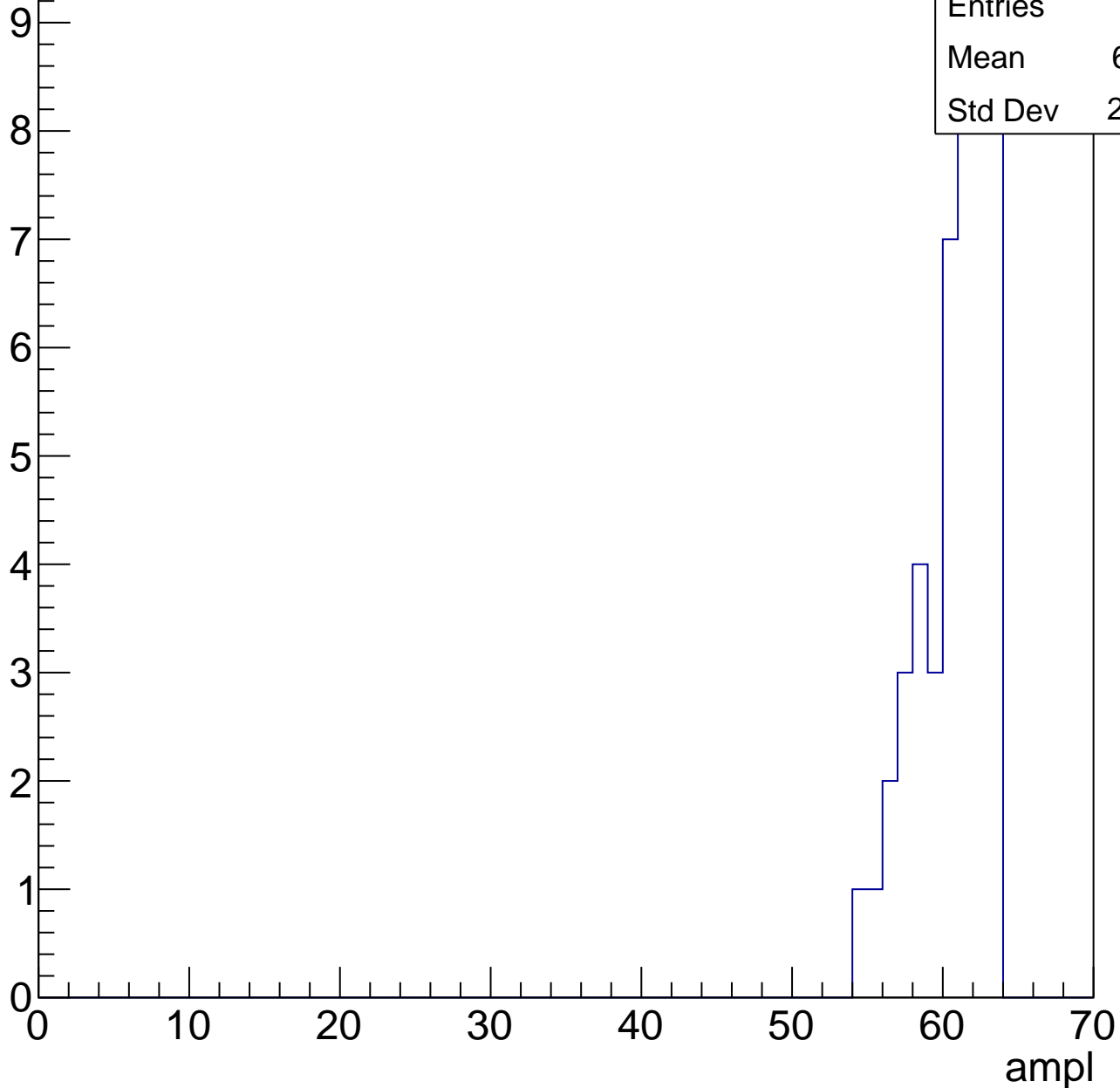
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L100S, U25-ch57, adc0

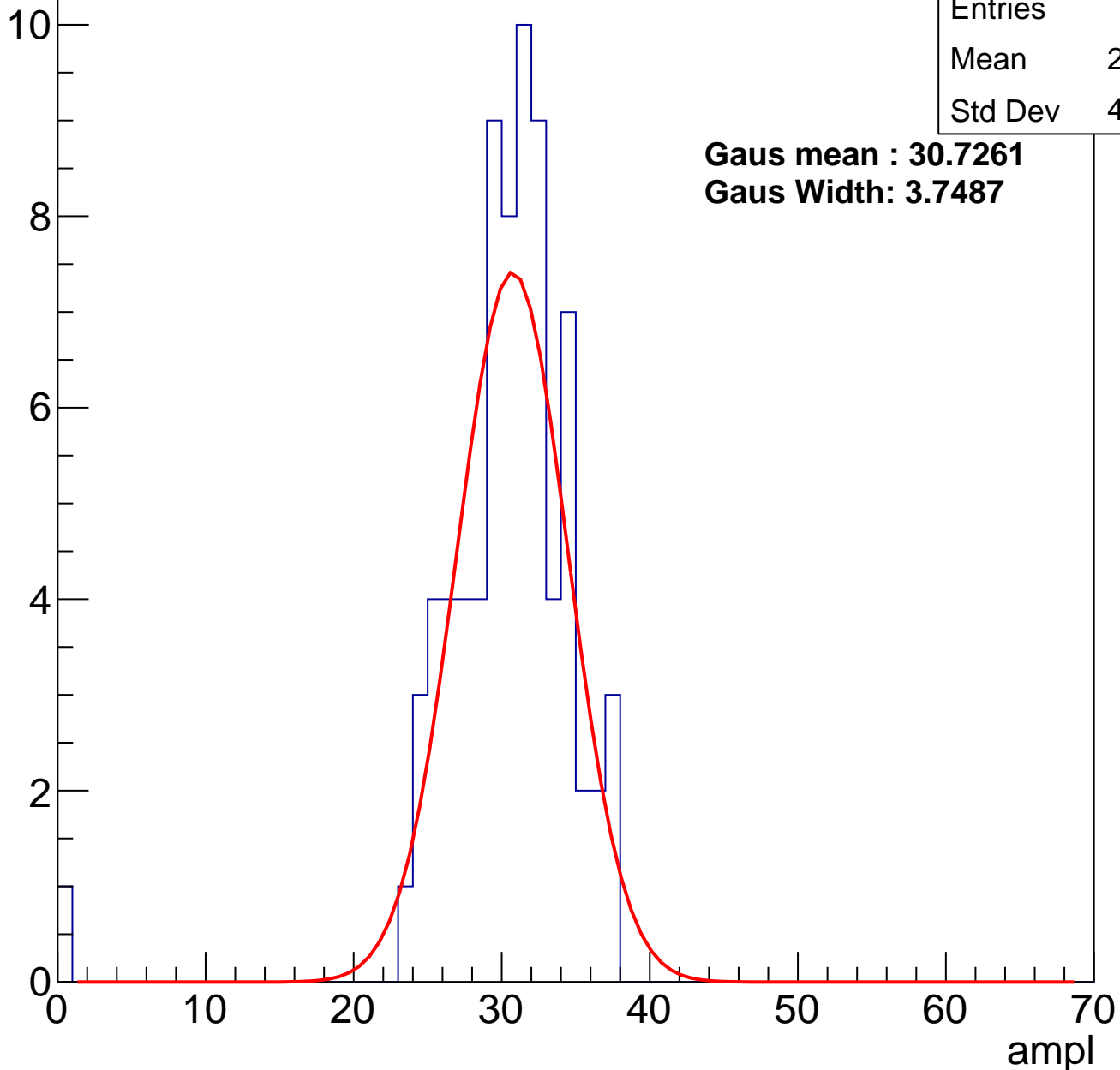
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	29.88
Std Dev	4.822

Gaus mean : 30.7261

Gaus Width: 3.7487

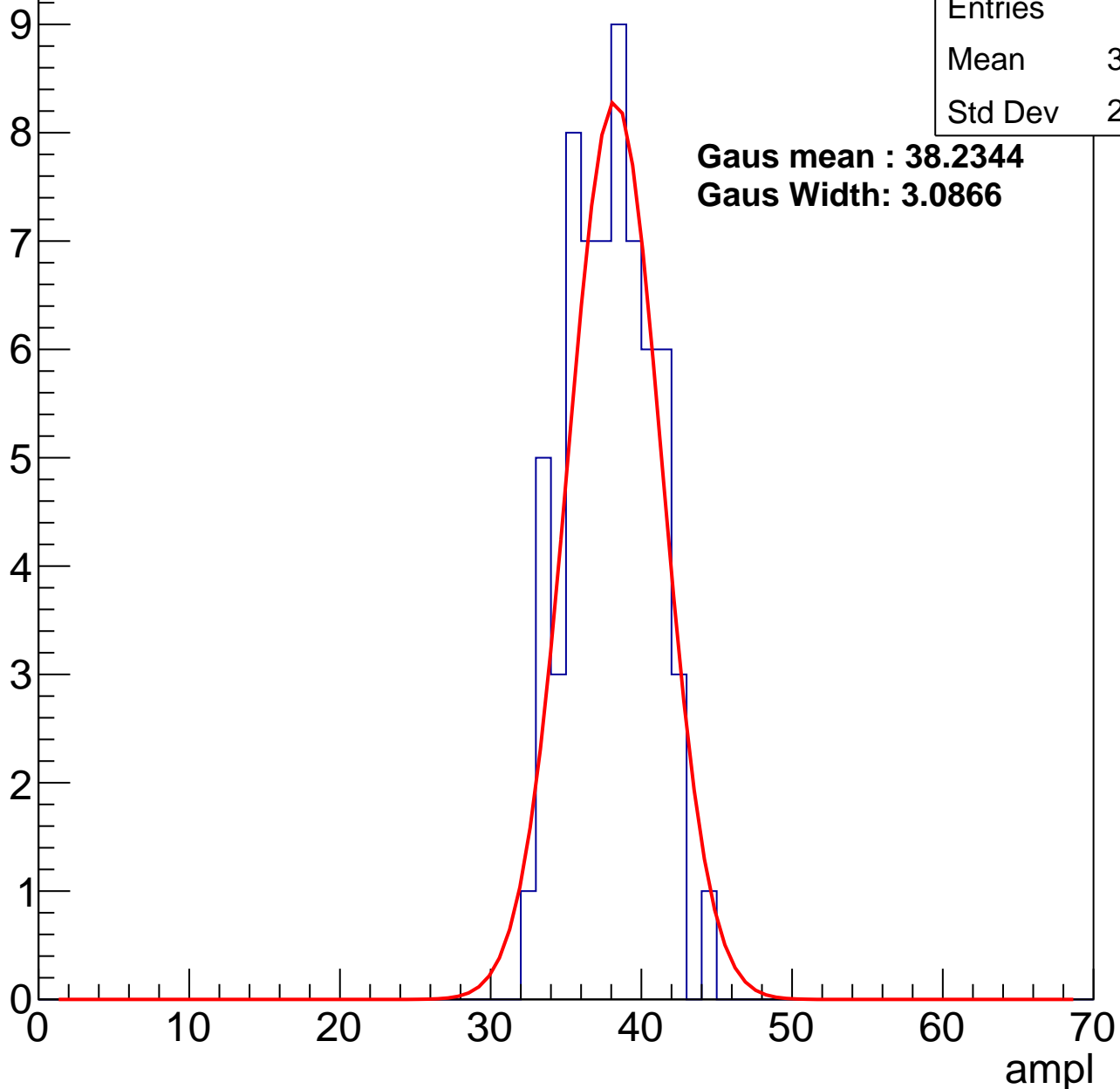
Entry



B1L100S, U25-ch57, adc1

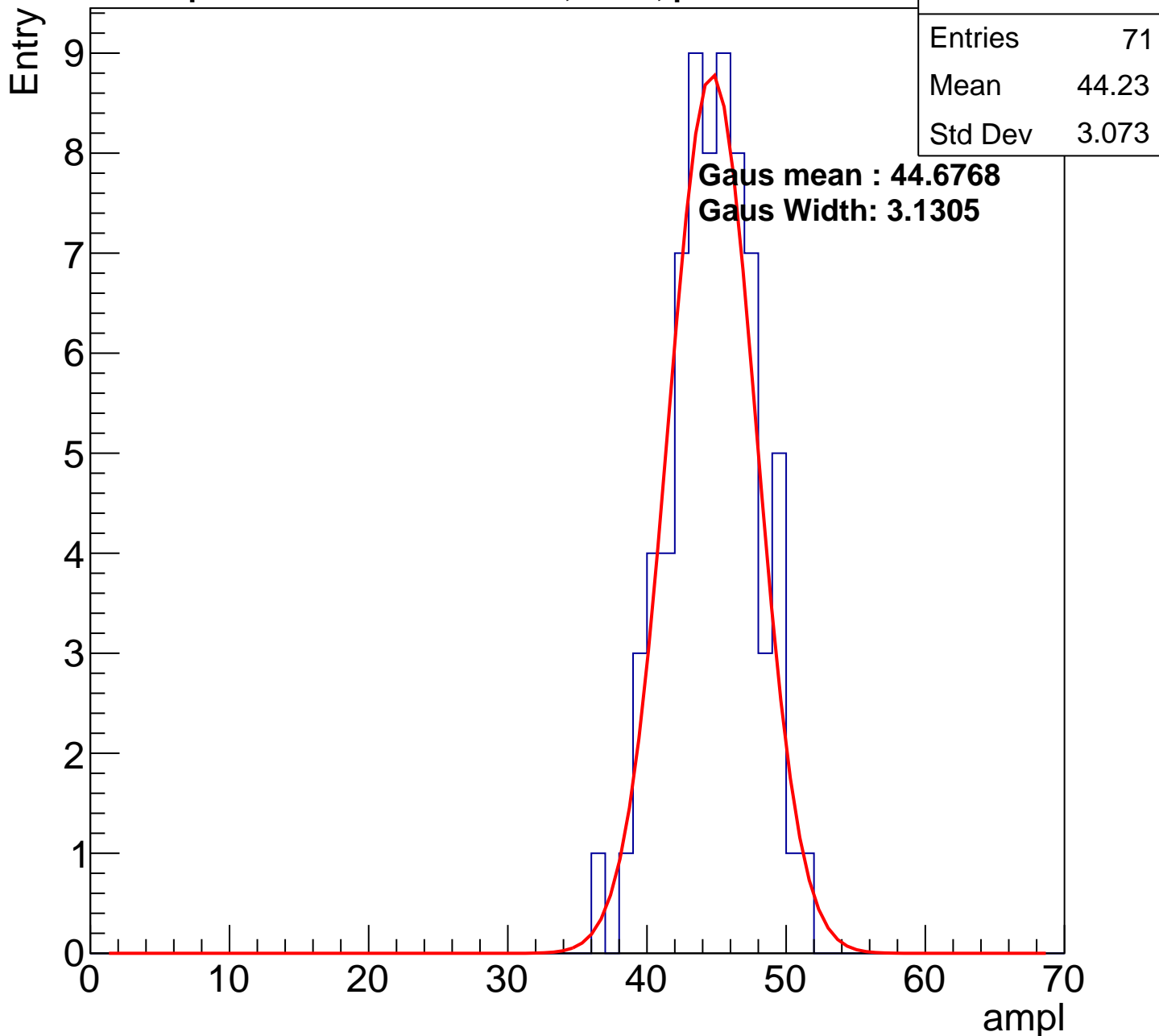
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch57, adc2

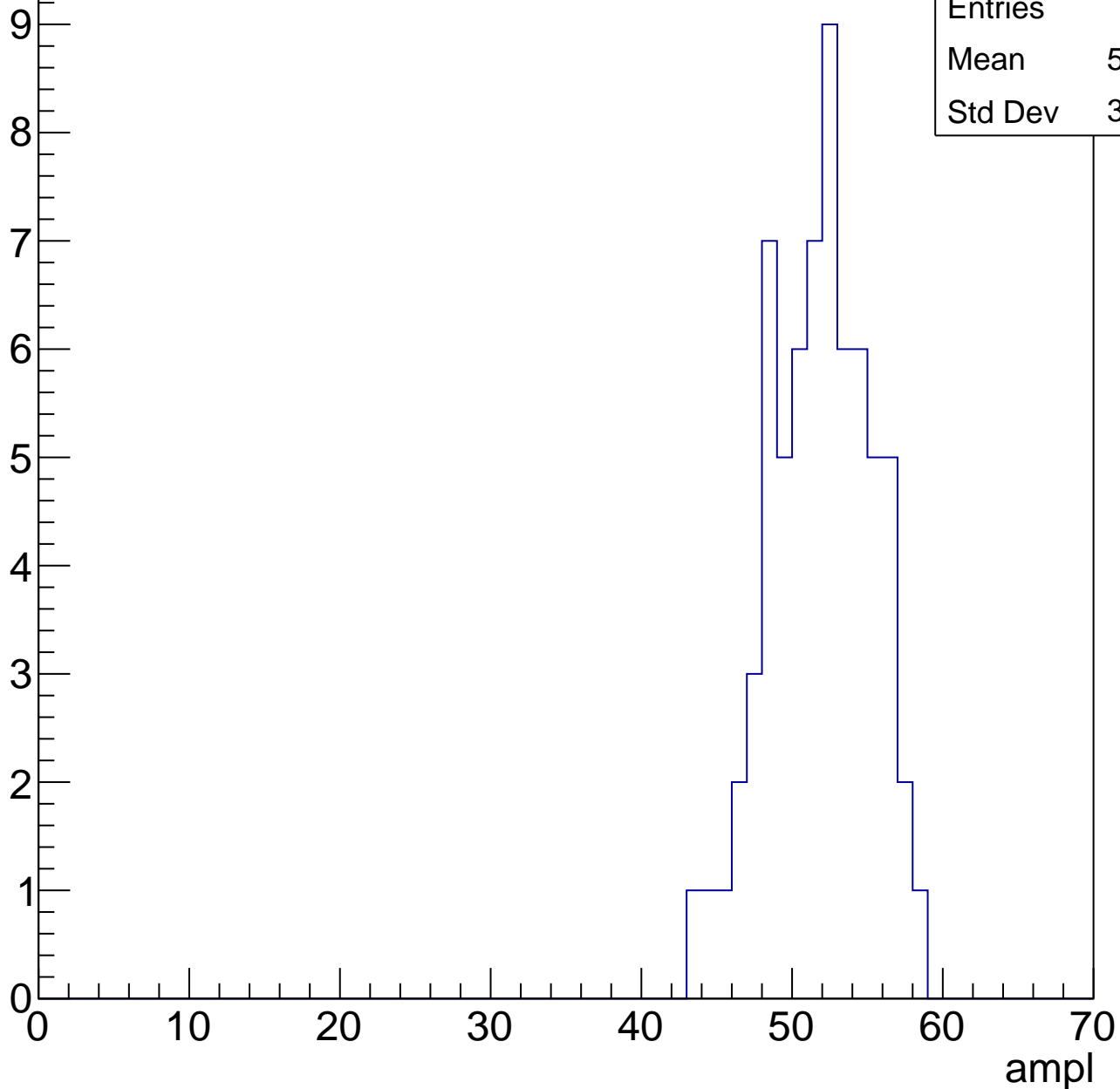
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

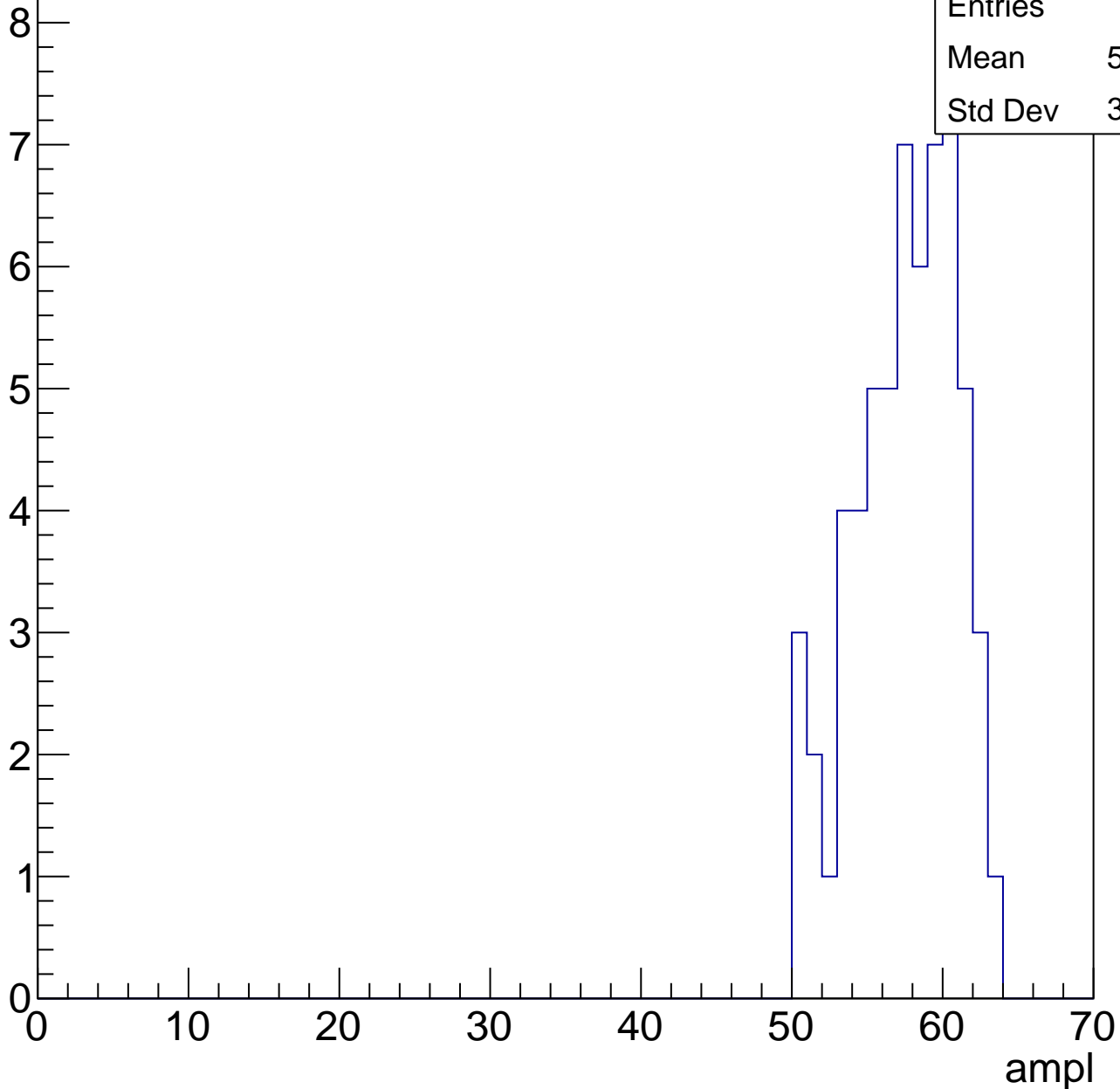


B1L100S, U25-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.07
Std Dev	3.304

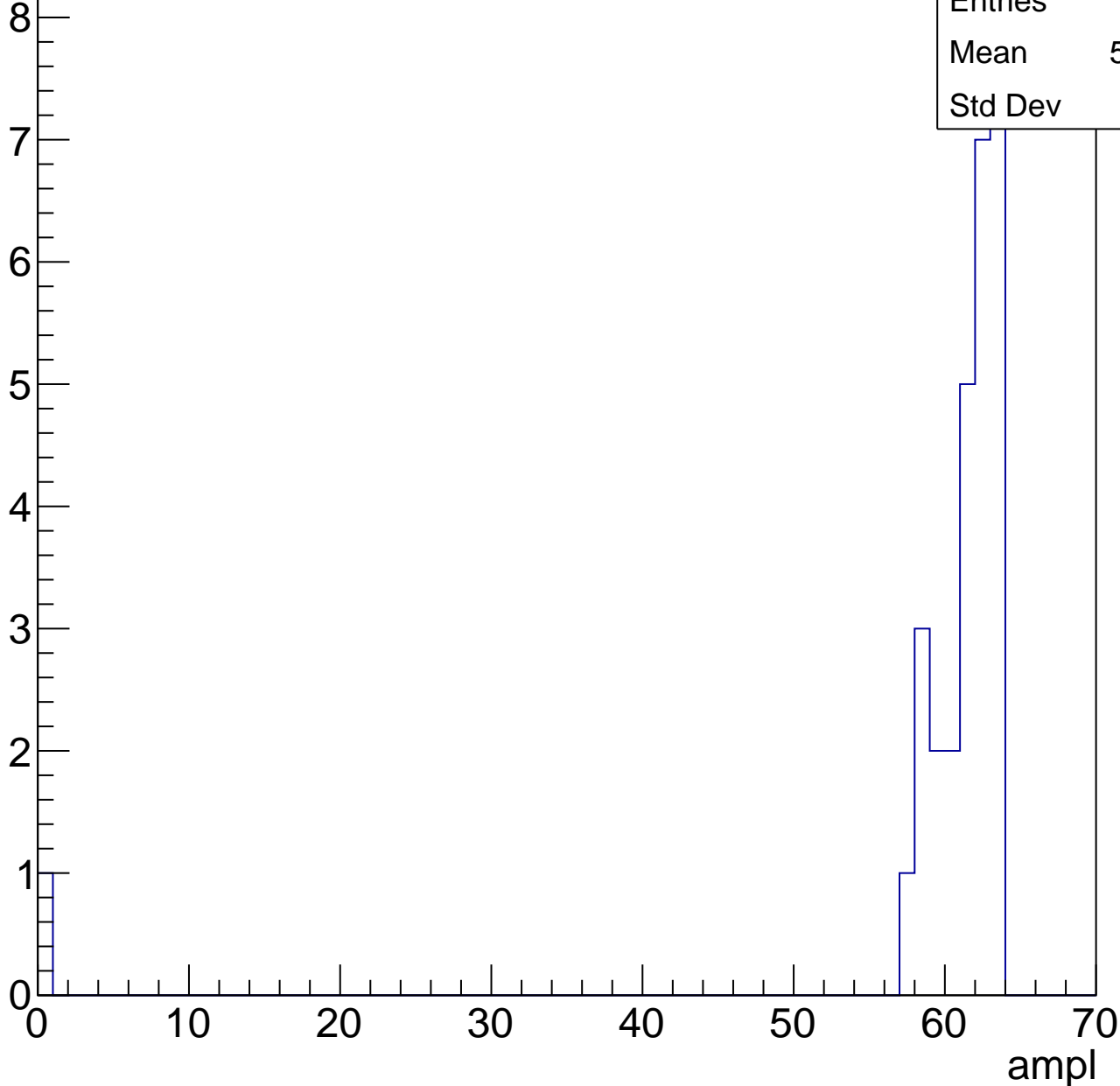


B1L100S, U25-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	59.03
Std Dev	11.3



B1L100S, U25-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch58, adc0

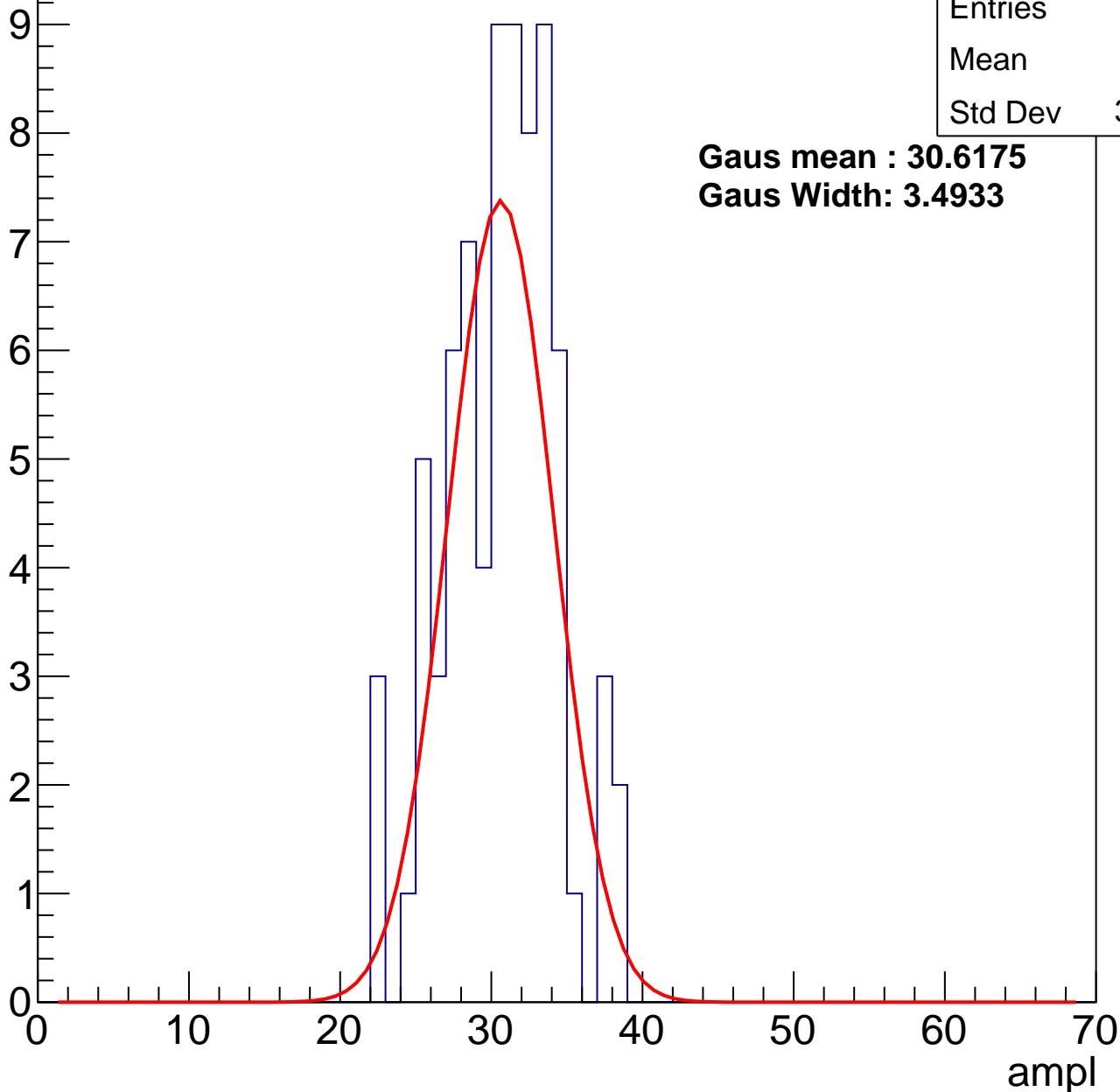
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.2
Std Dev	3.631

Gaus mean : 30.6175

Gaus Width: 3.4933



B1L100S, U25-ch58, adc1

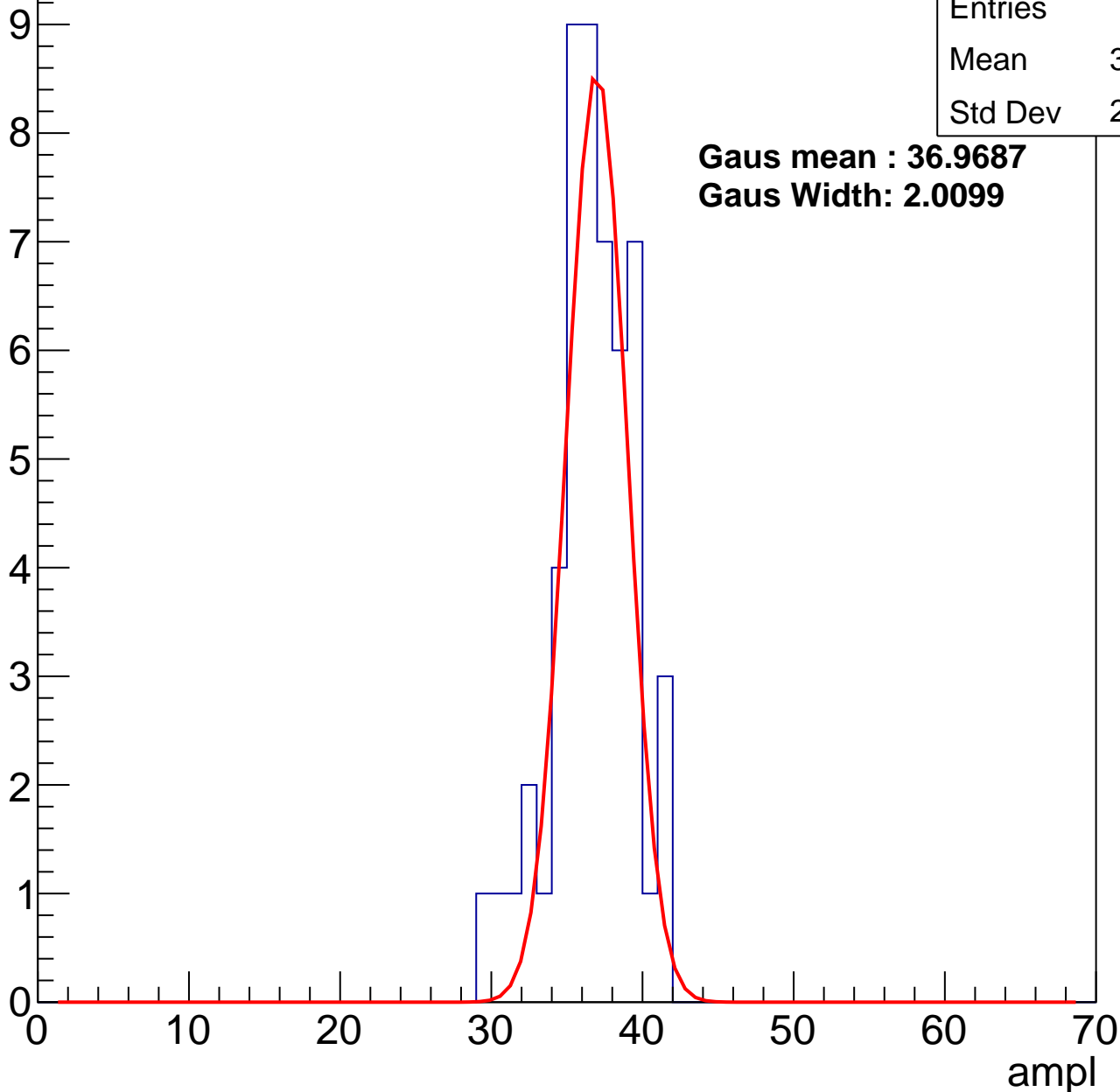
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	36.25
Std Dev	2.623

Gaus mean : 36.9687

Gaus Width: 2.0099



B1L100S, U25-ch58, adc2

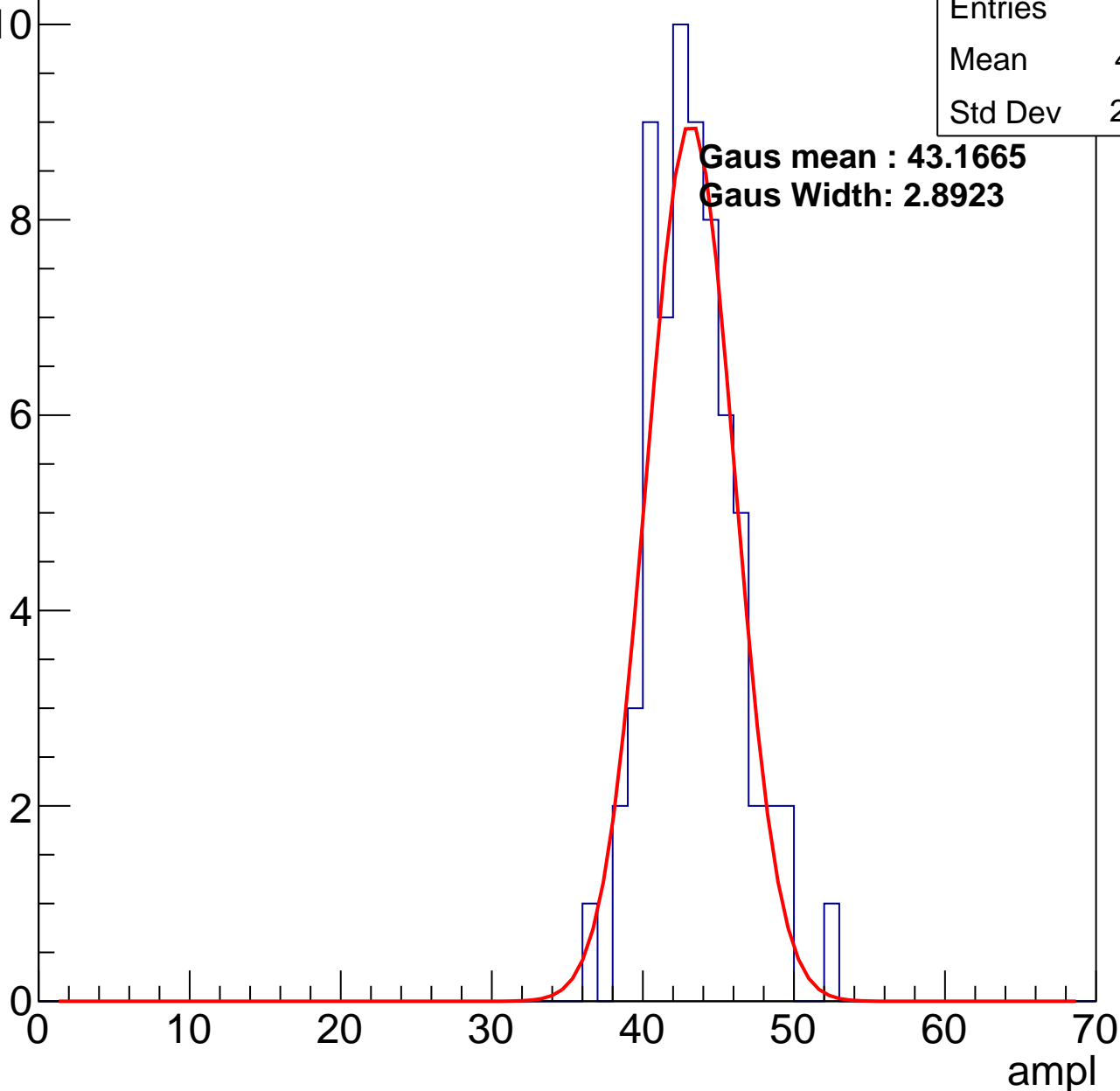
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.91
Std Dev	2.936

Gaus mean : 43.1665

Gaus Width: 2.8923



B1L100S, U25-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	50.63
Std Dev	3.645

Entry

10

8

6

4

2

0

0

10

20

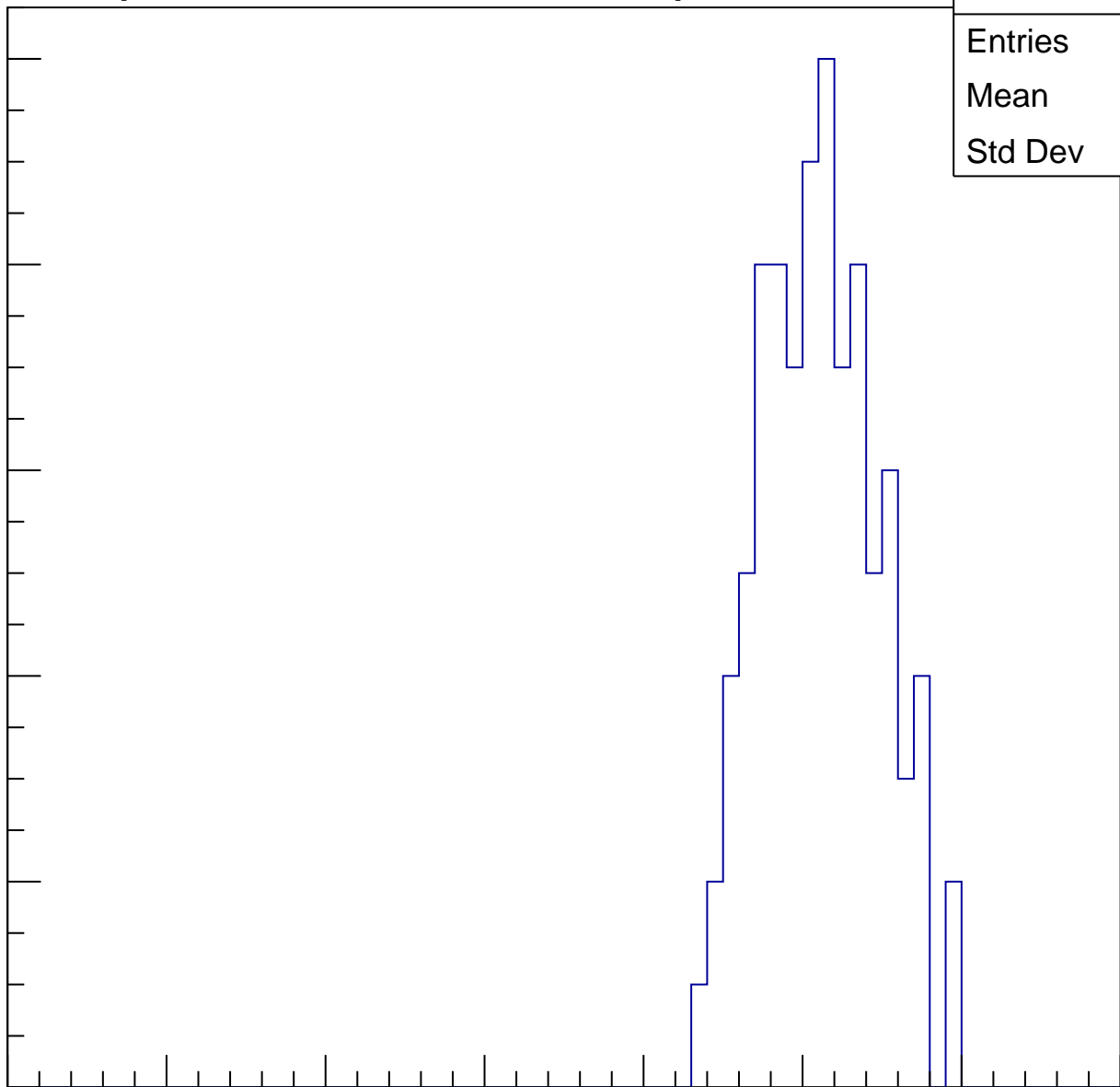
30

40

50

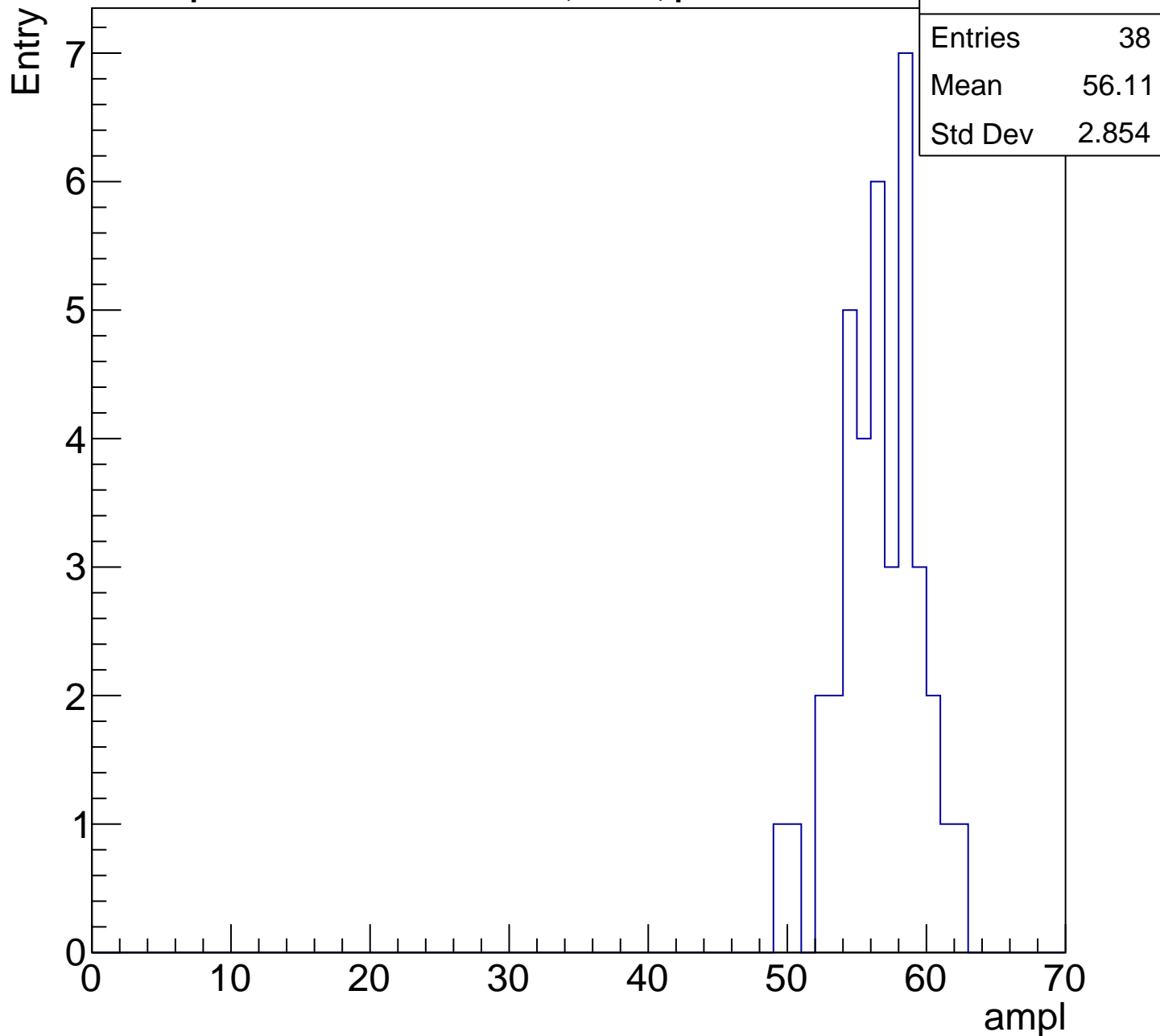
60

ampl



B1L100S, U25-ch58, adc4

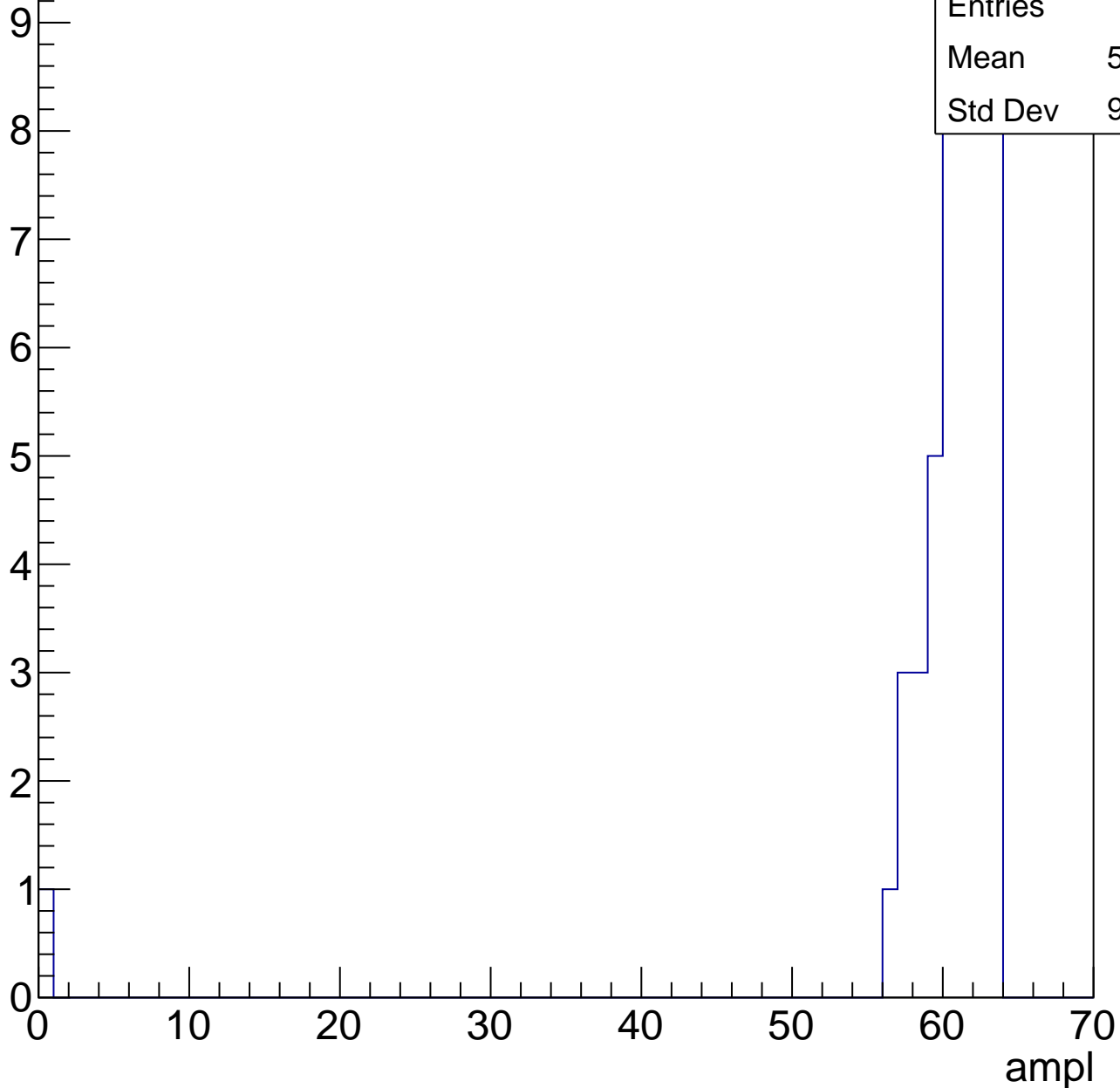
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch59, adc0

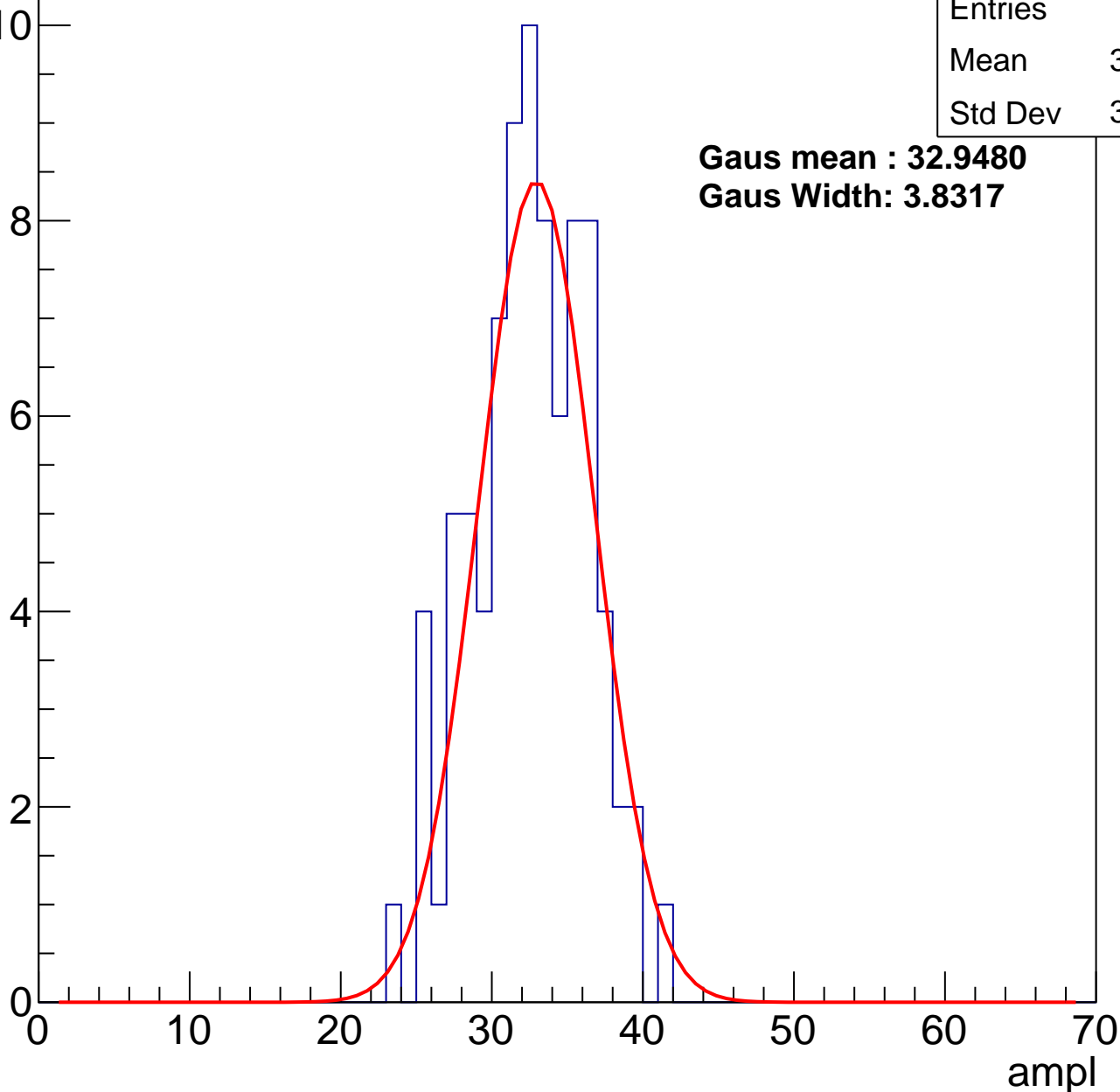
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	32.09
Std Dev	3.715

Gaus mean : 32.9480

Gaus Width: 3.8317



B1L100S, U25-ch59, adc1

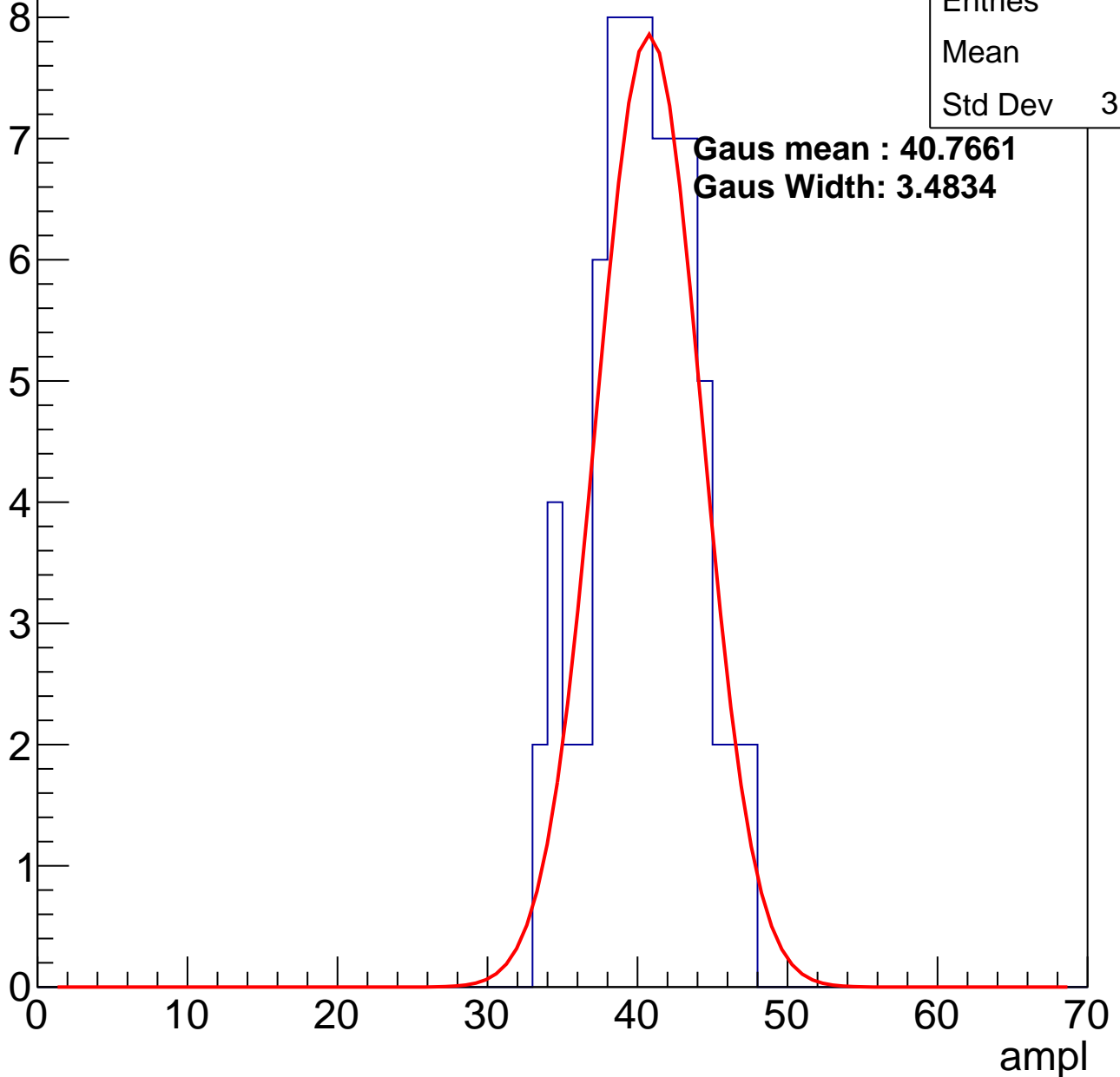
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	40
Std Dev	3.367

Gaus mean : 40.7661

Gaus Width: 3.4834



B1L100S, U25-ch59, adc2

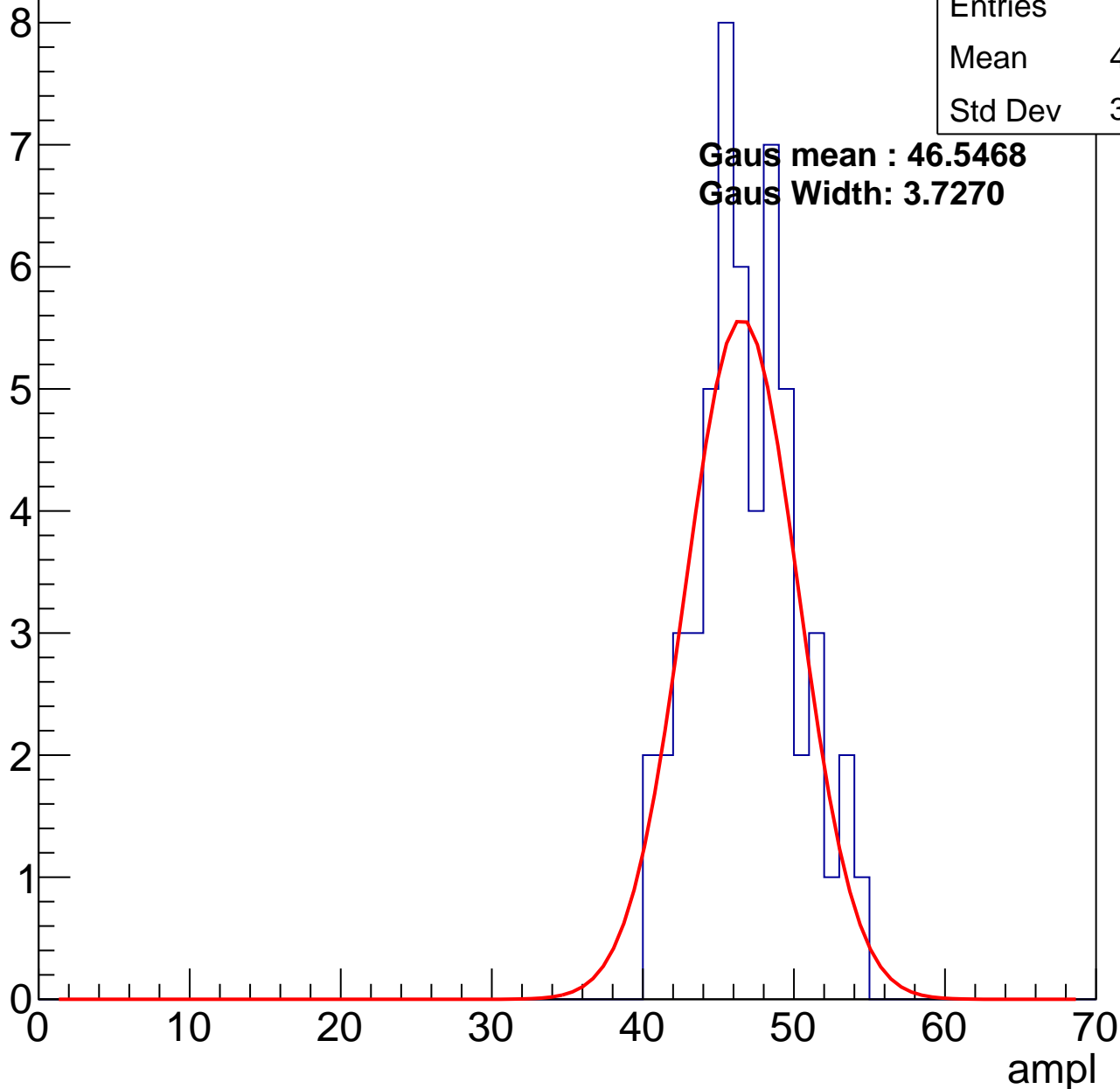
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	46.43
Std Dev	3.326

Gaus mean : 46.5468

Gaus Width: 3.7270

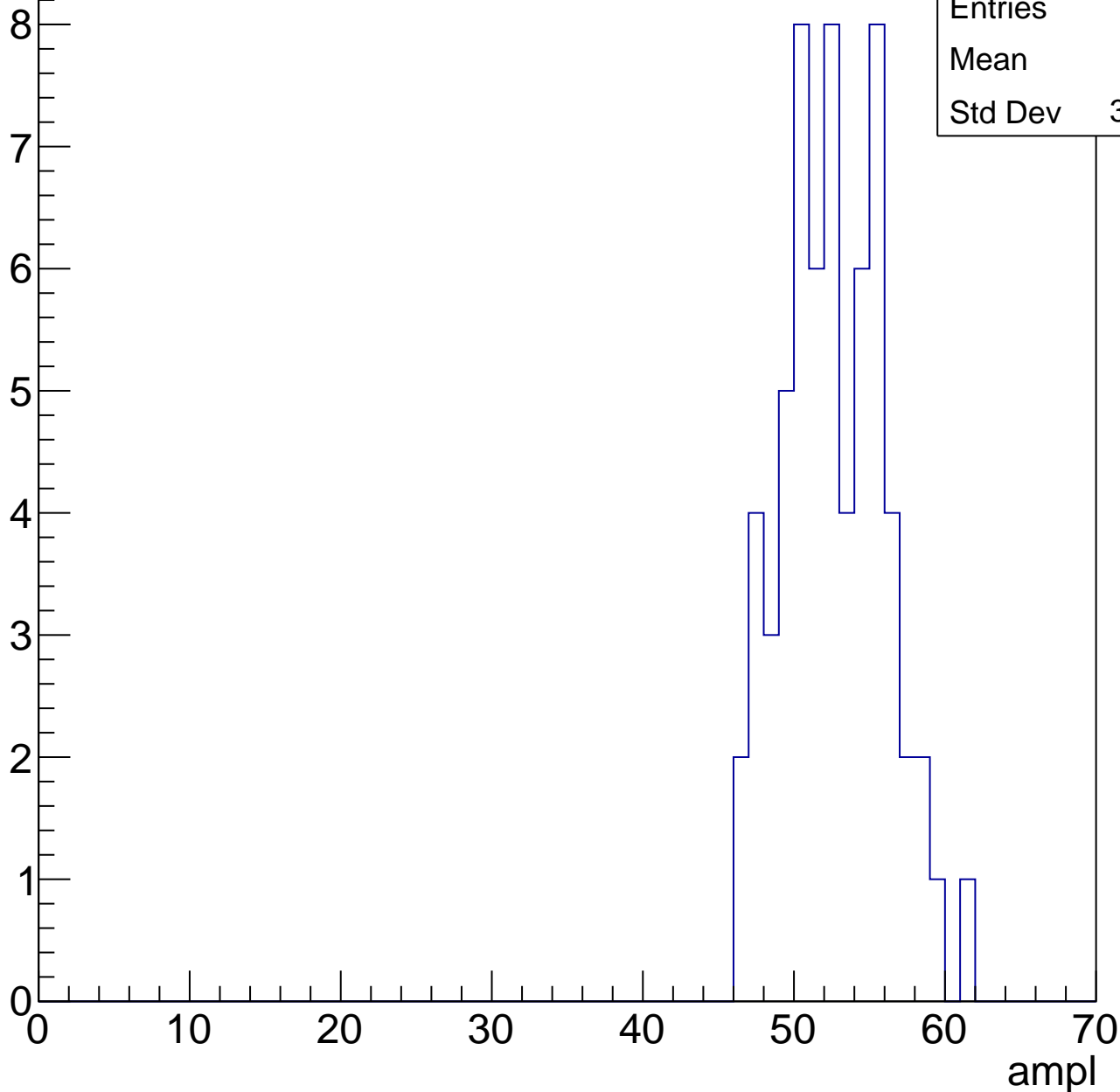


B1L100S, U25-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

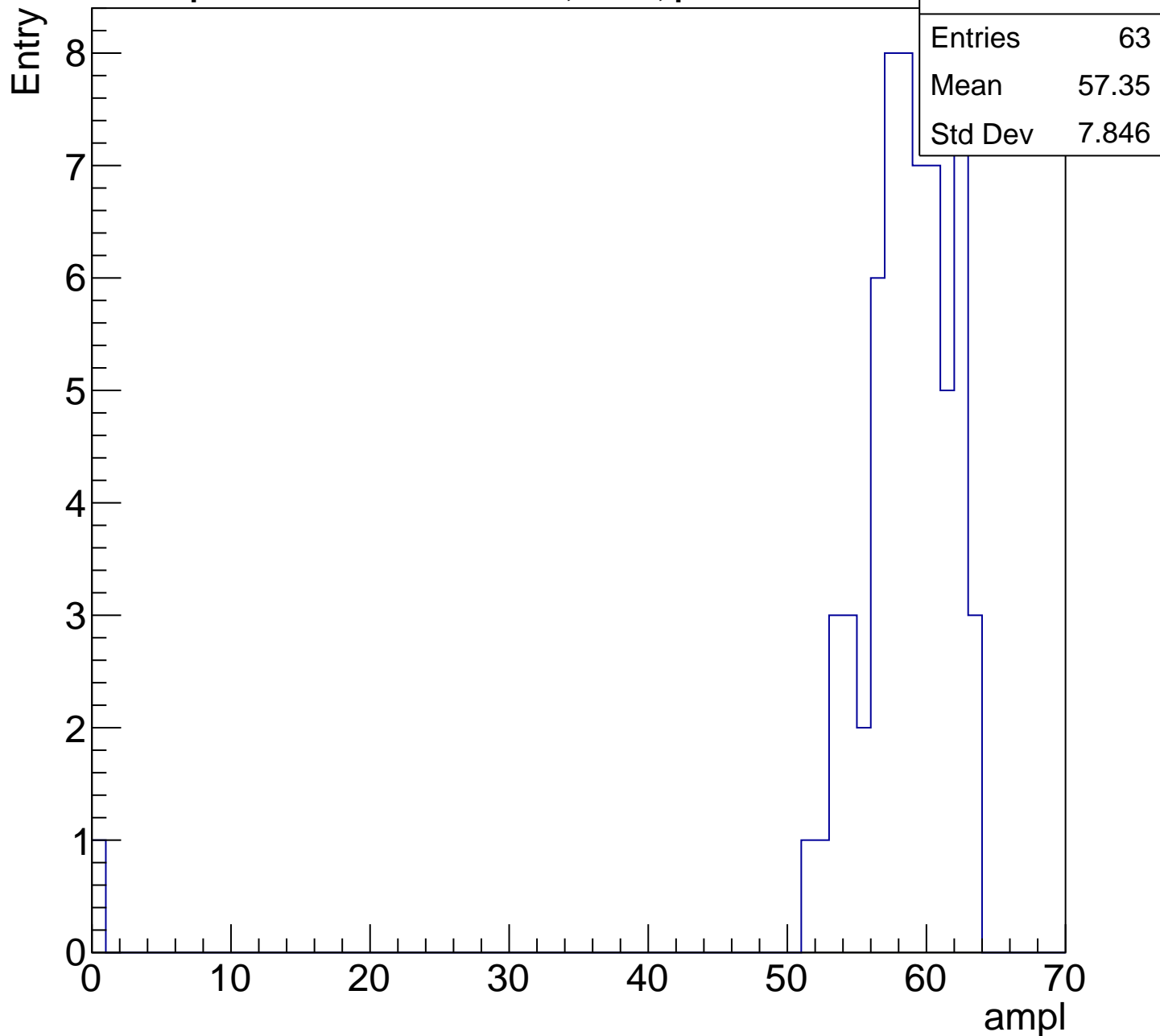
Entry

Entries	64
Mean	52.2
Std Dev	3.346



B1L100S, U25-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

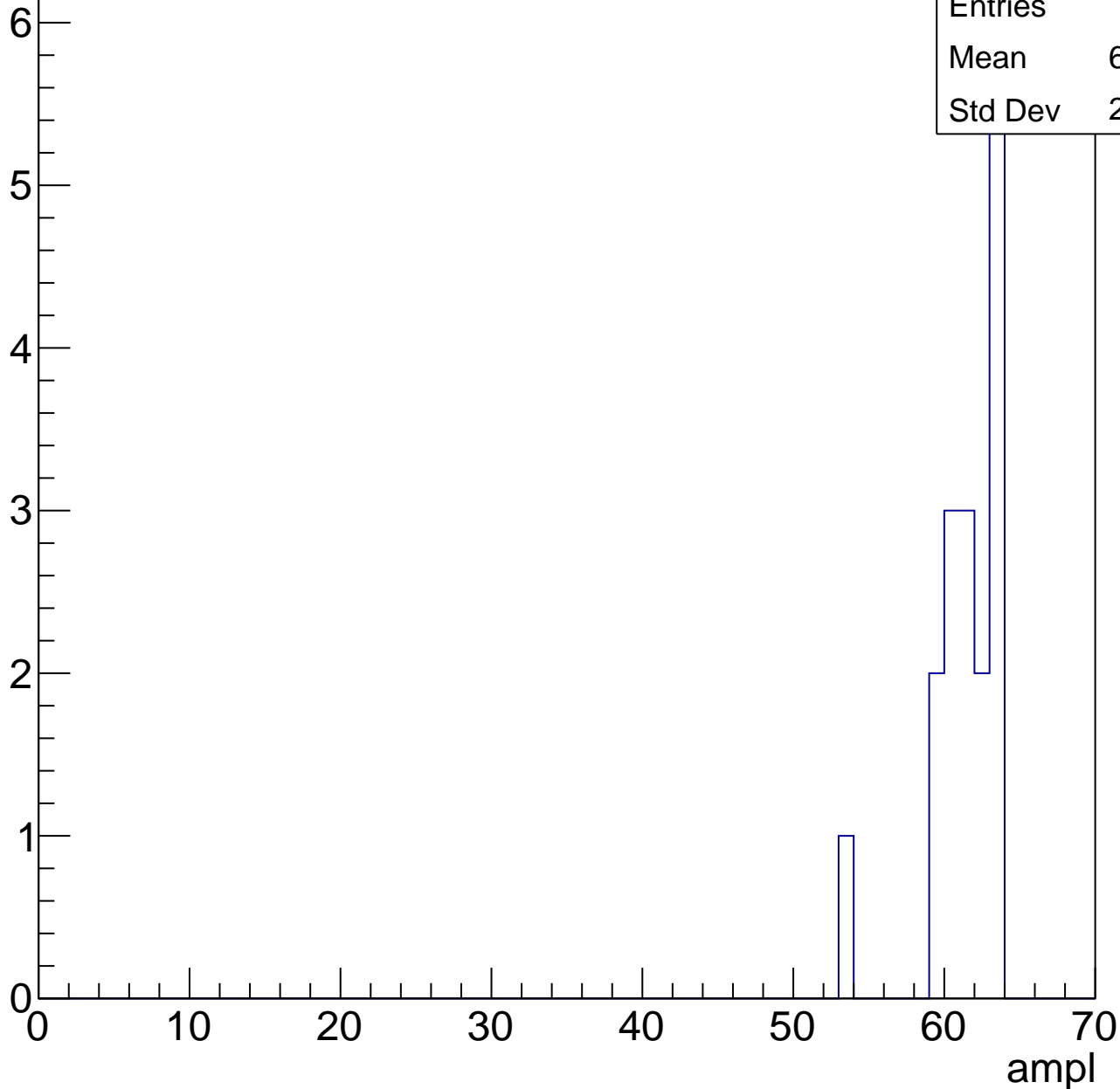


B1L100S, U25-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	60.94
Std Dev	2.437



B1L100S, U25-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch60, adc0

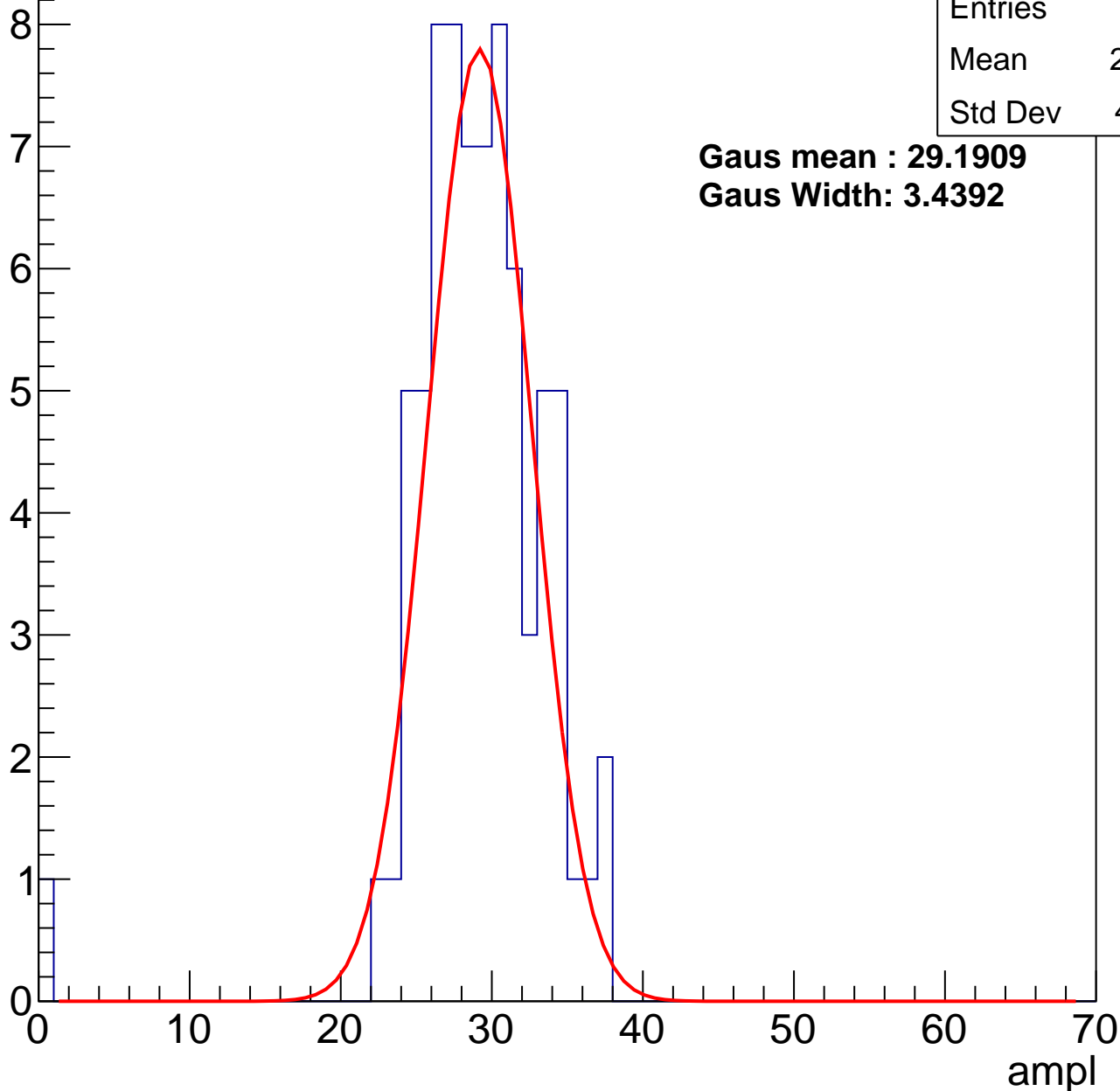
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.58
Std Dev	4.801

Gaus mean : 29.1909

Gaus Width: 3.4392



B1L100S, U25-ch60, adc1

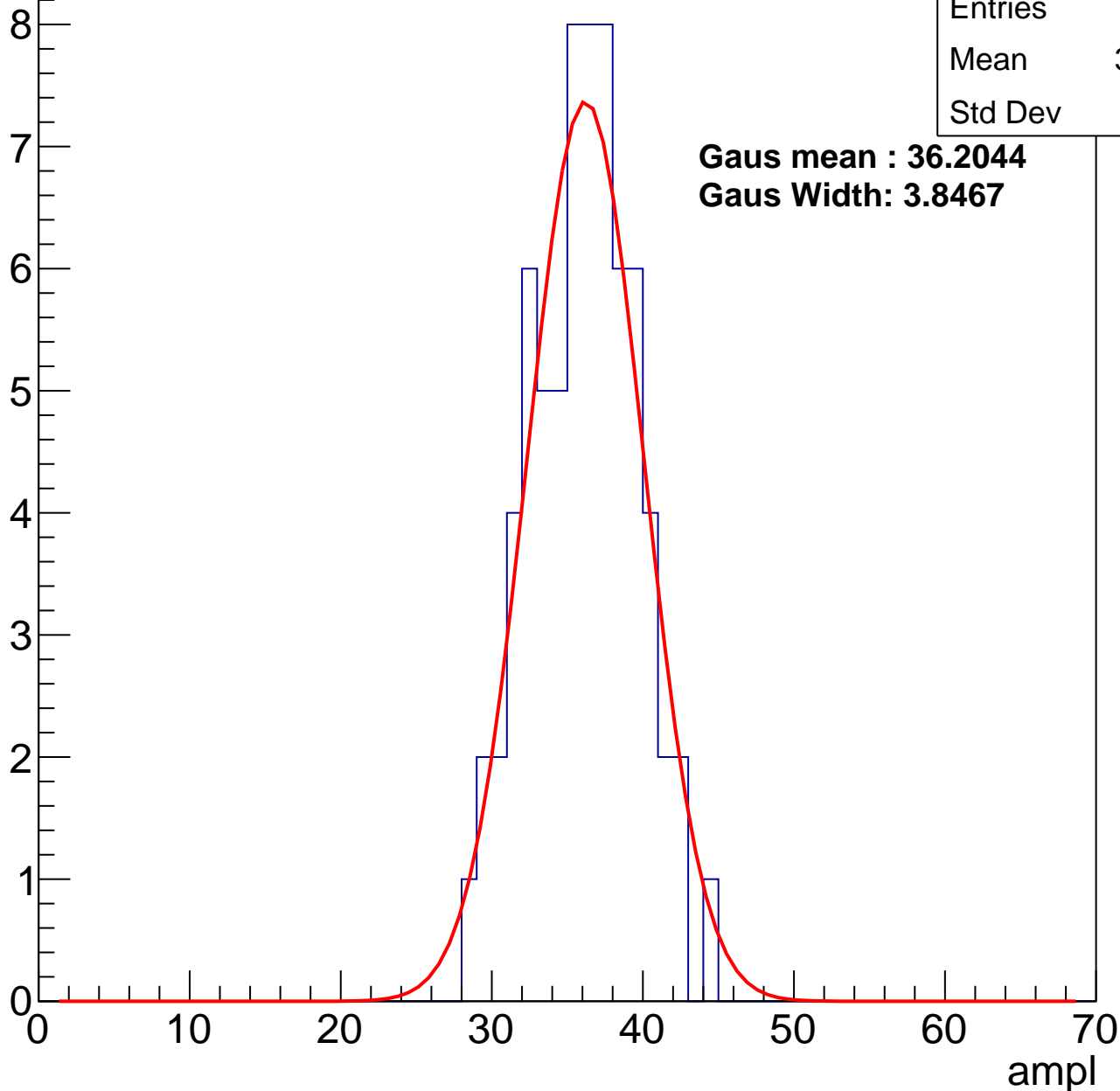
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.61
Std Dev	3.44

Gaus mean : 36.2044

Gaus Width: 3.8467



B1L100S, U25-ch60, adc2

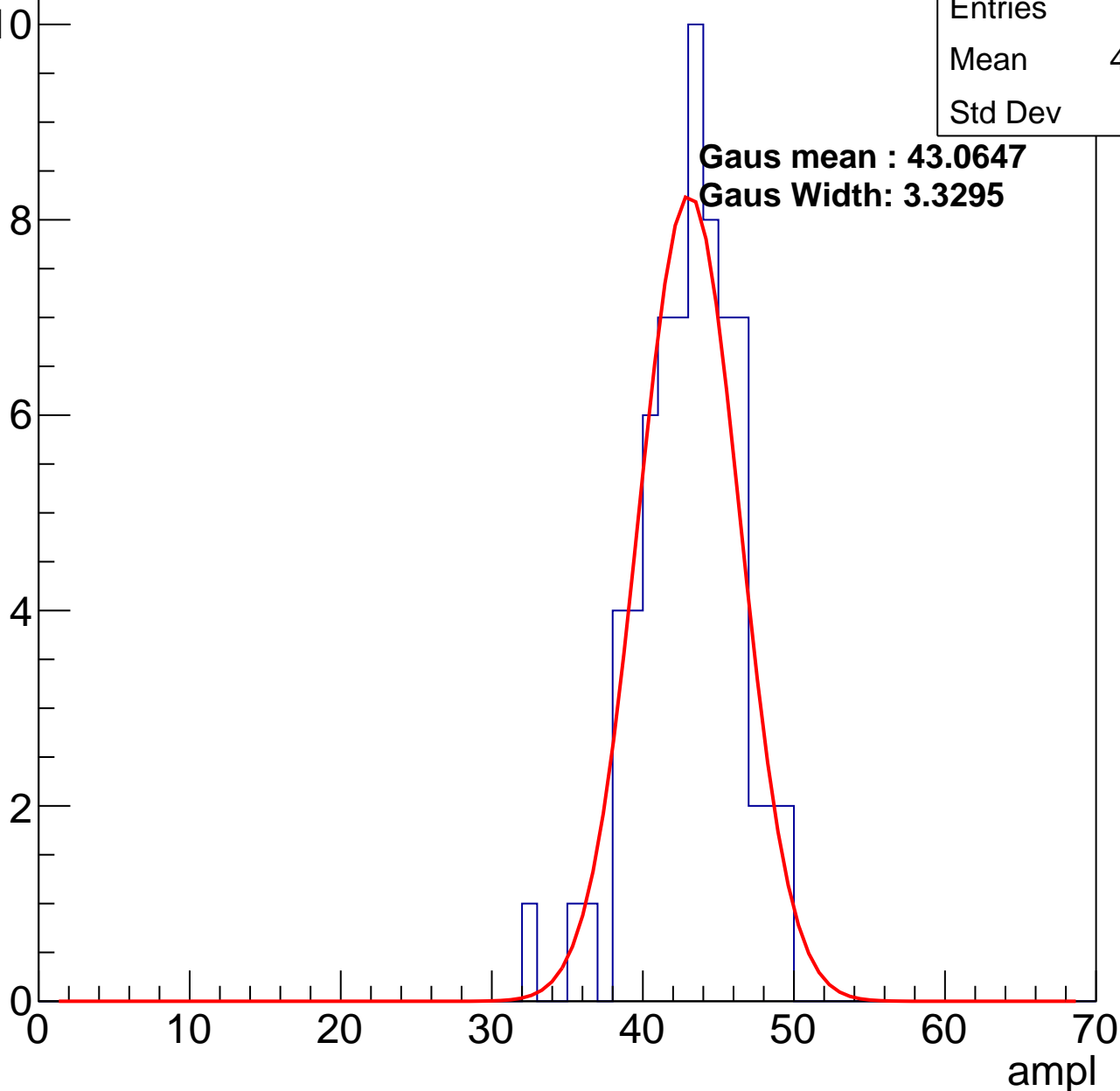
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.59
Std Dev	3.25

Gaus mean : 43.0647

Gaus Width: 3.3295

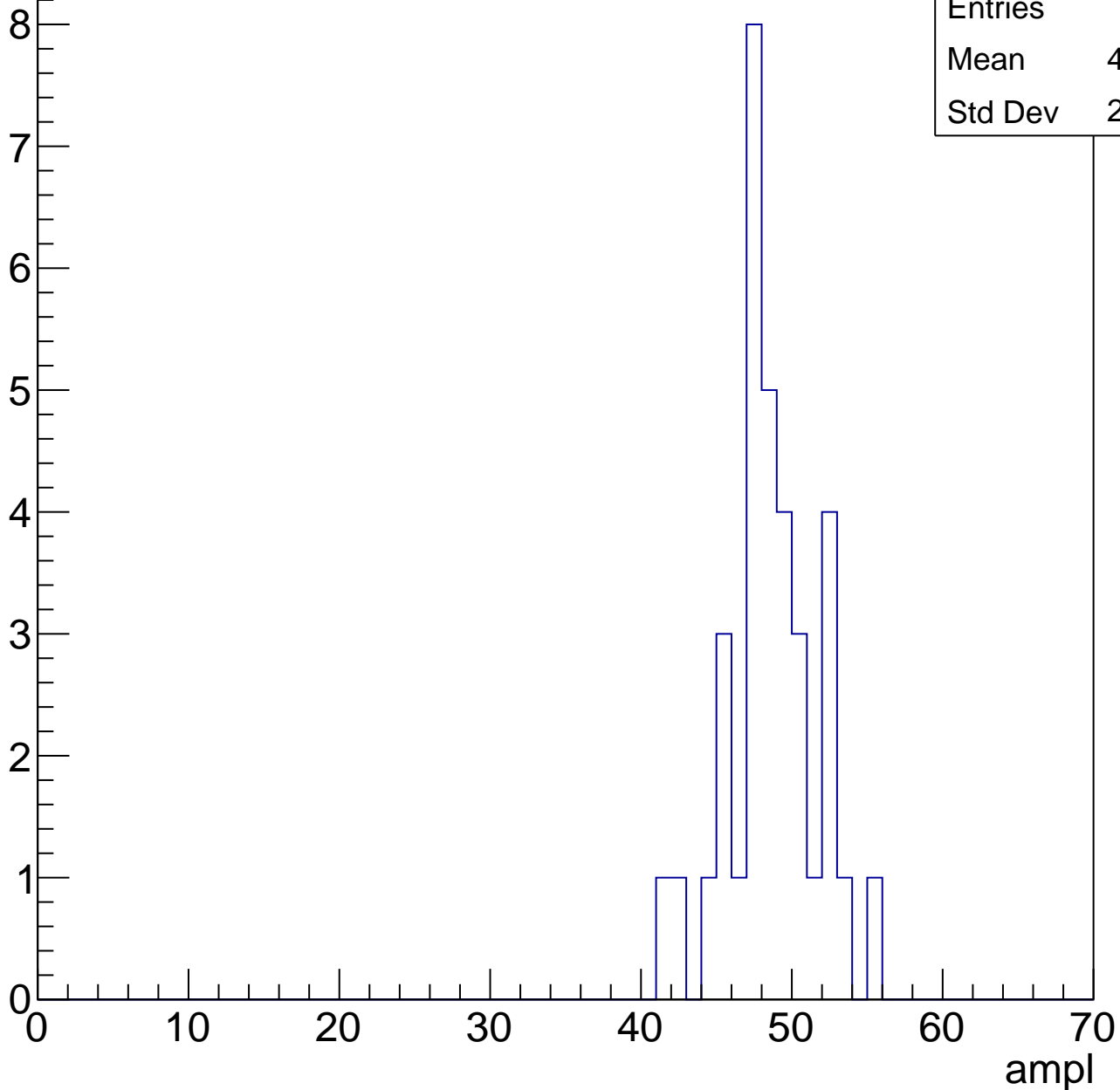


B1L100S, U25-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	48.15
Std Dev	2.982



B1L100S, U25-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	53.33
Std Dev	3.141

Entry

10

8

6

4

2

0

0

10

20

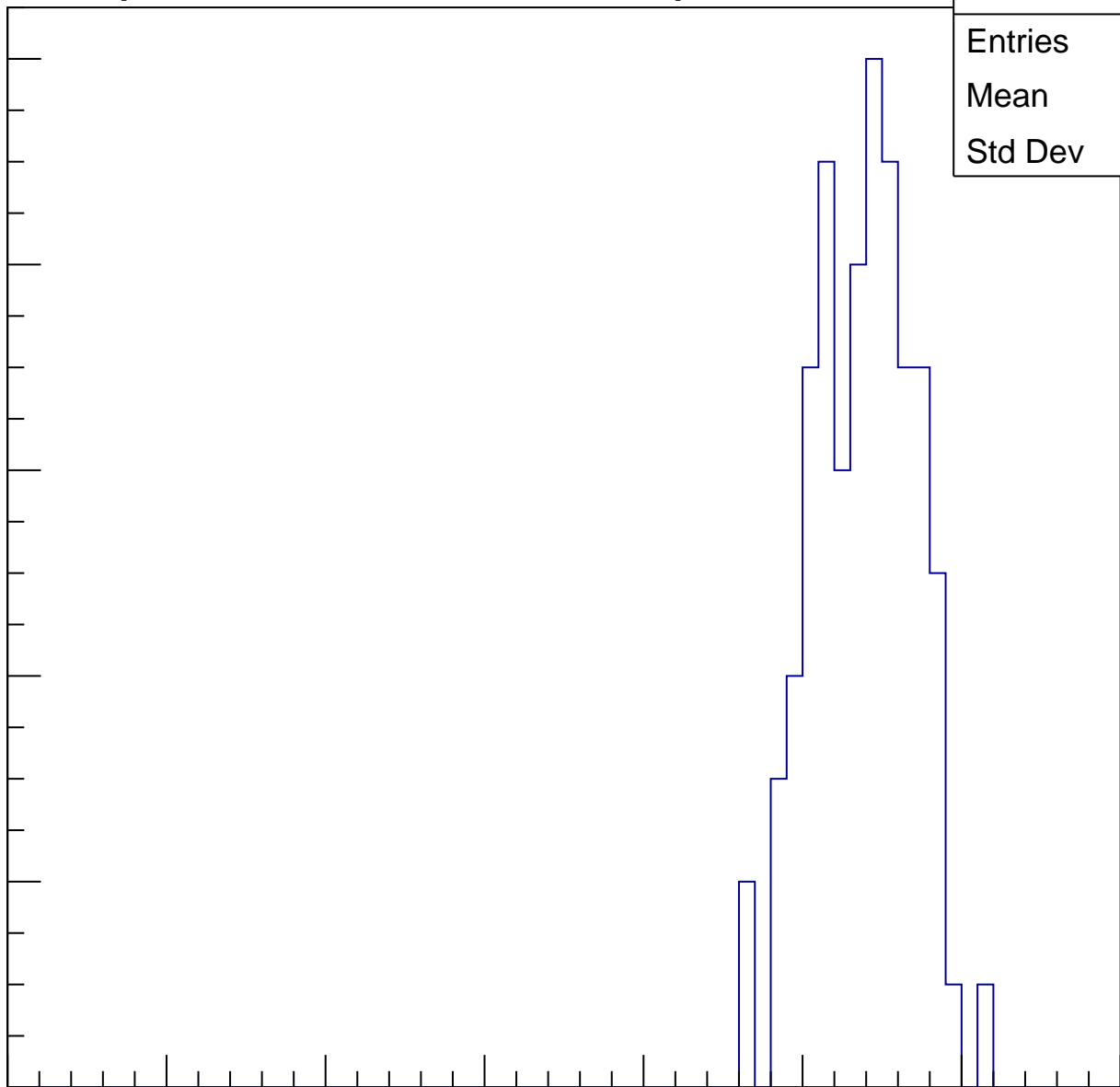
30

40

50

60

ampl



B1L100S, U25-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	58
Mean	59.12
Std Dev	8.139

0

2

4

6

8

10

B1L100S, U25-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch61, adc0

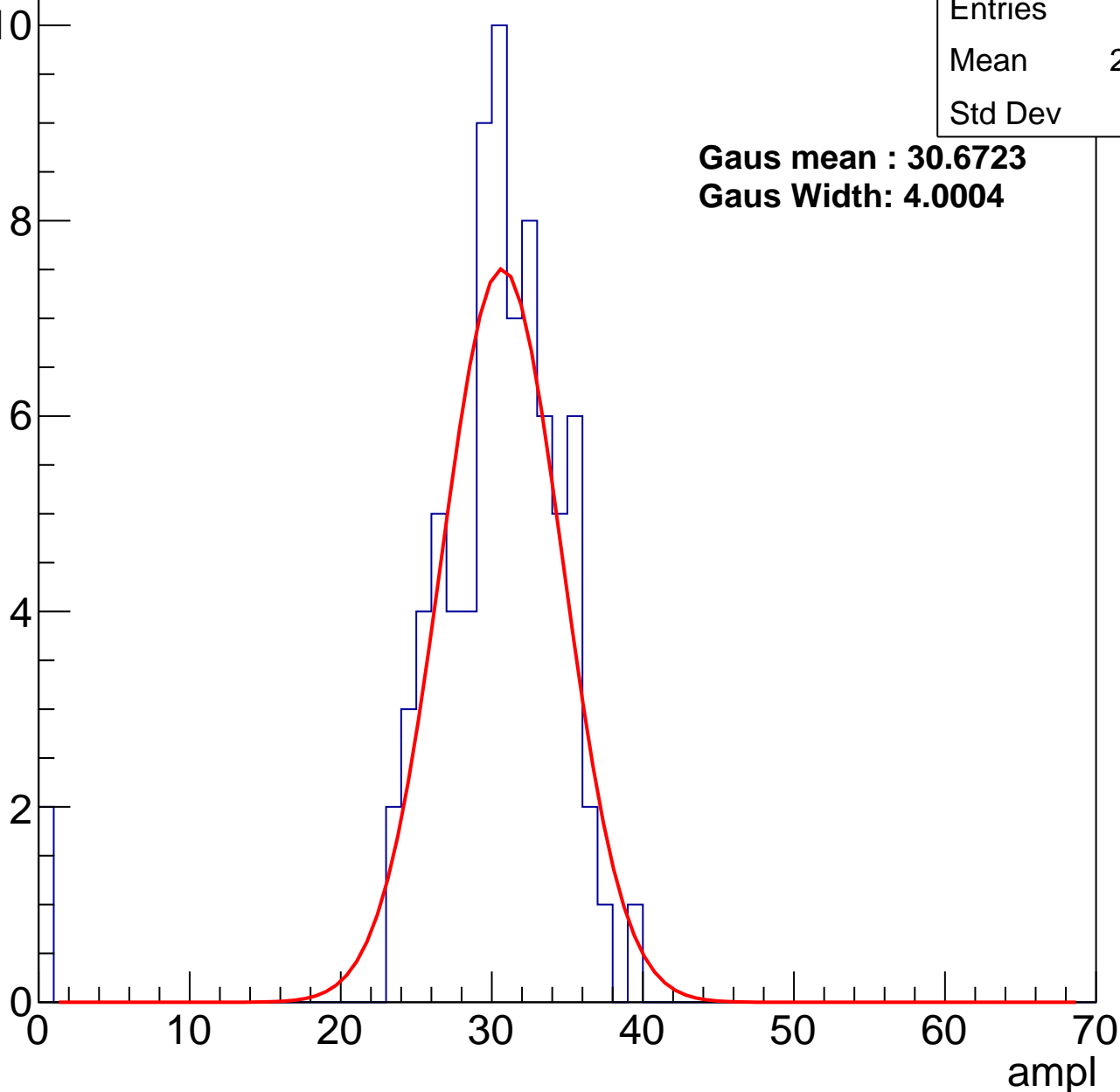
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	29.47
Std Dev	5.9

Gaus mean : 30.6723

Gaus Width: 4.0004



B1L100S, U25-ch61, adc1

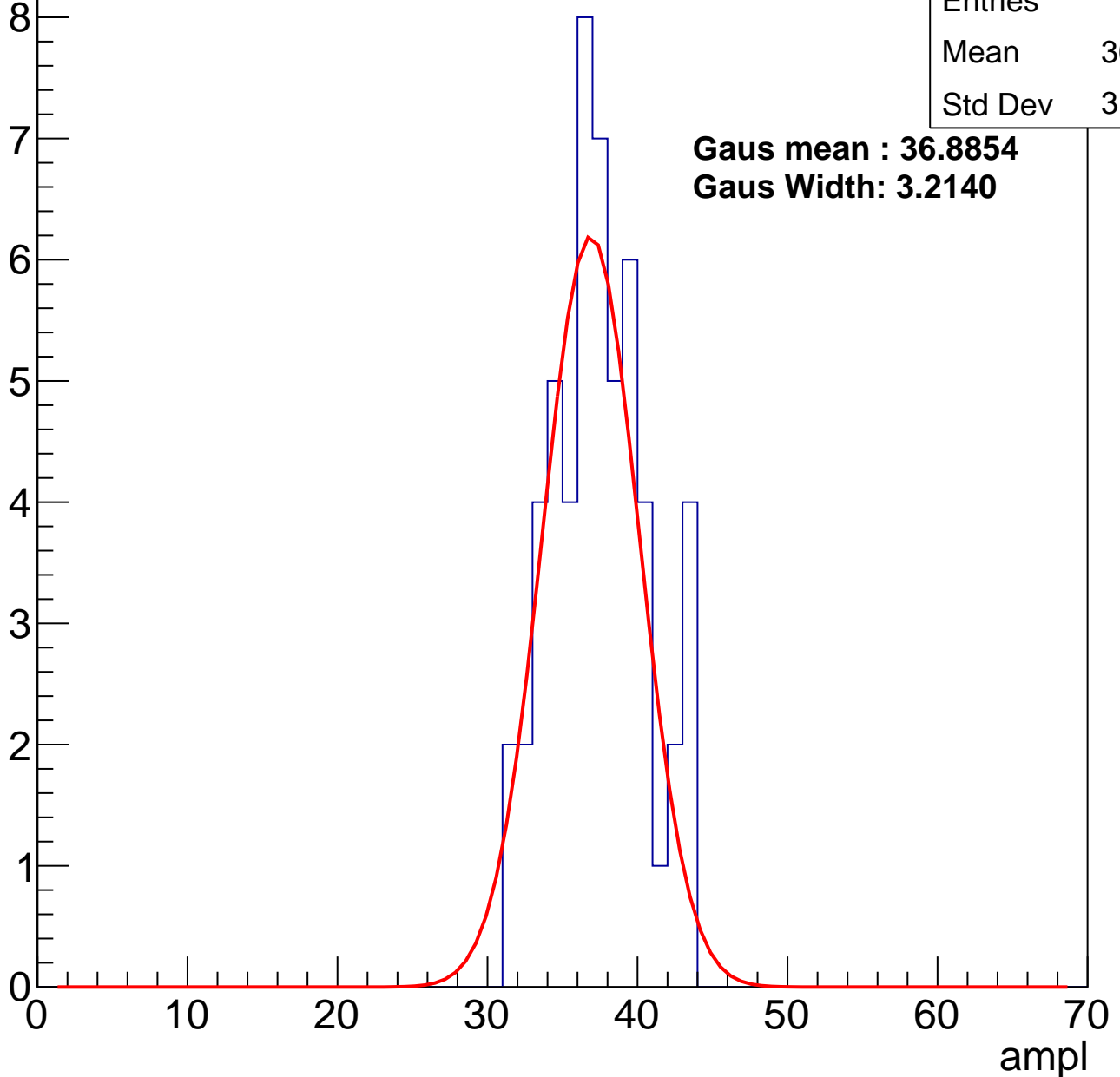
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.96
Std Dev	3.133

Gaus mean : 36.8854

Gaus Width: 3.2140



B1L100S, U25-ch61, adc2

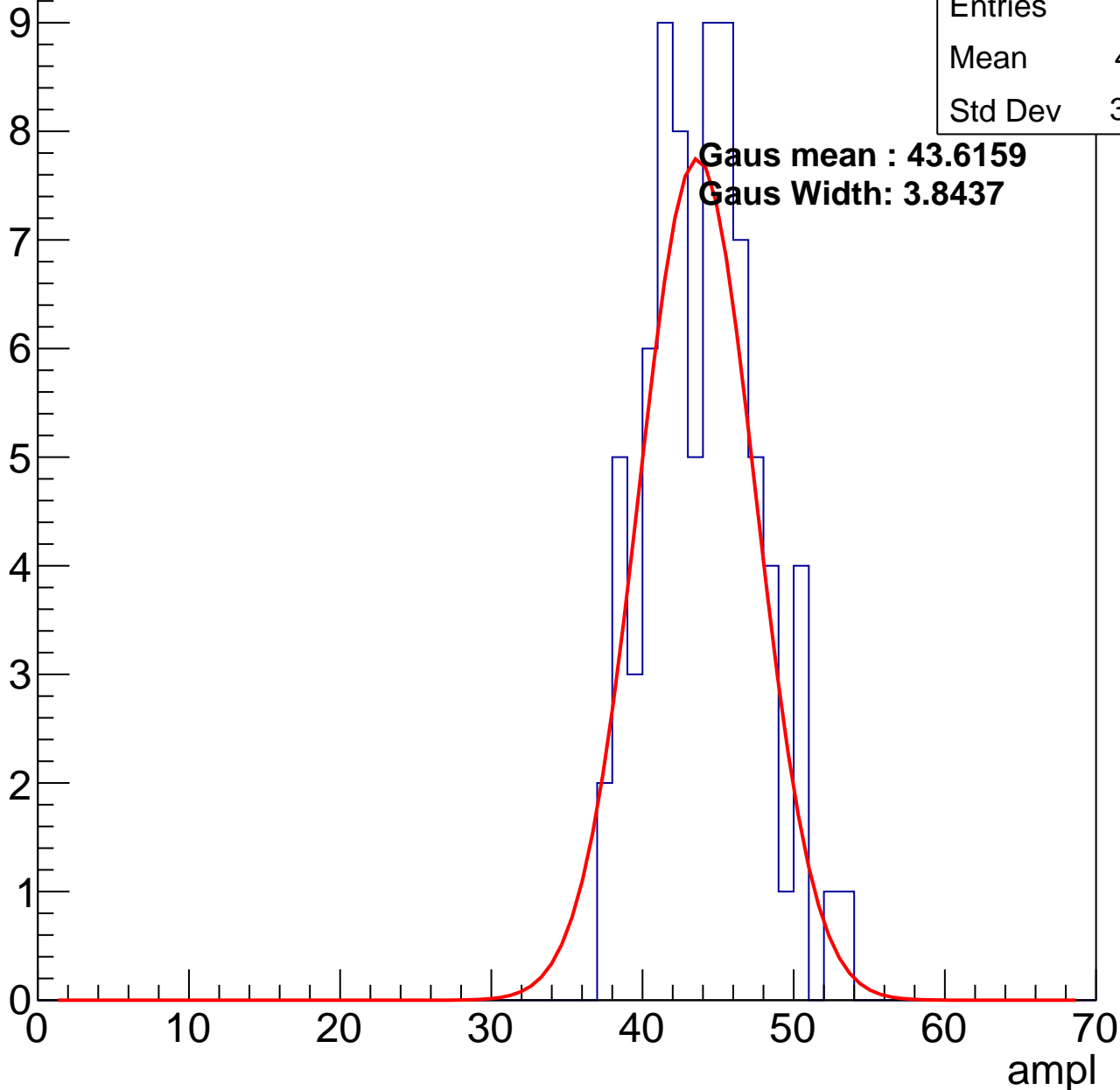
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	43.61
Std Dev	3.584

Gaus mean : 43.6159

Gaus Width: 3.8437

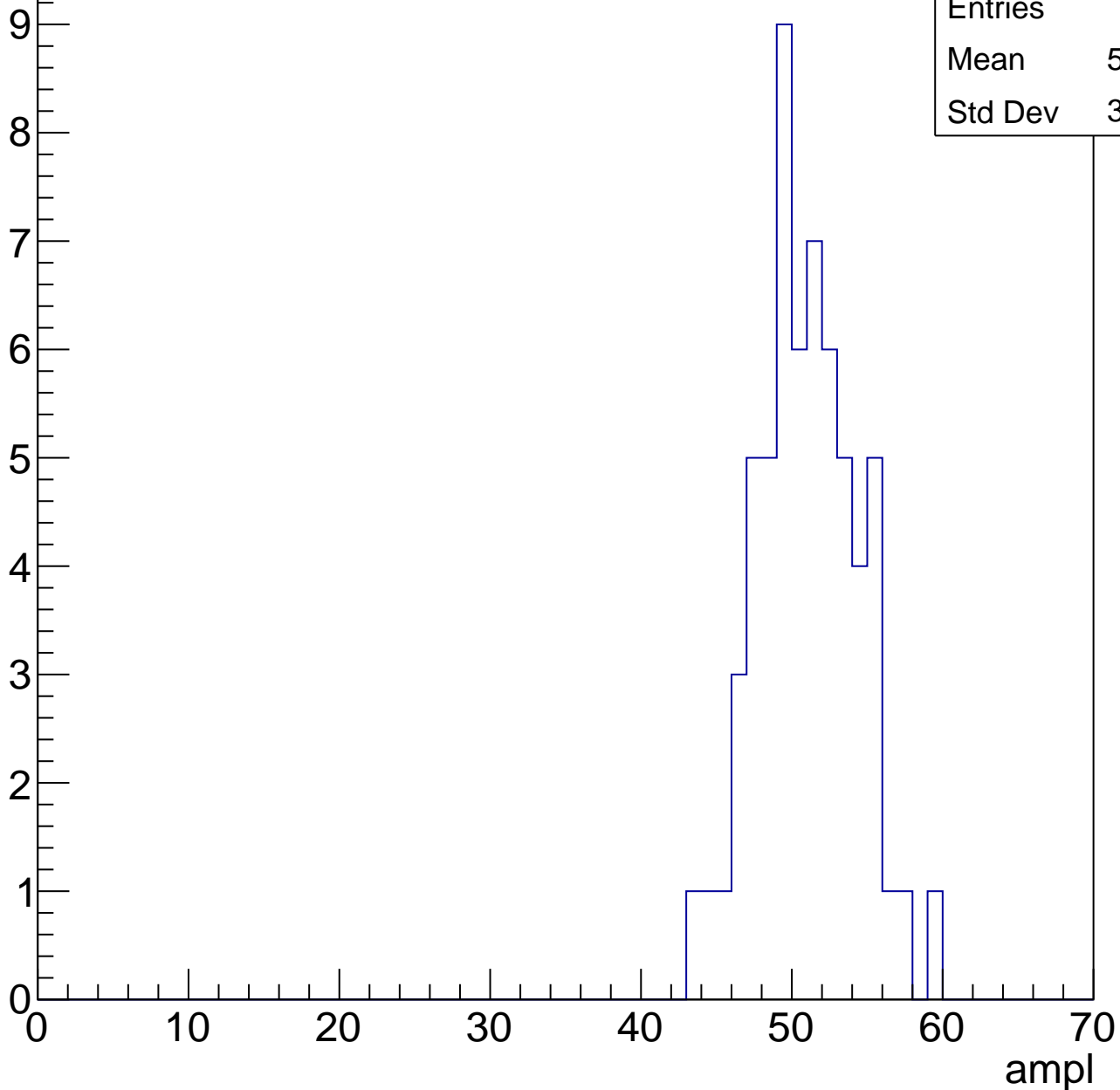


B1L100S, U25-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

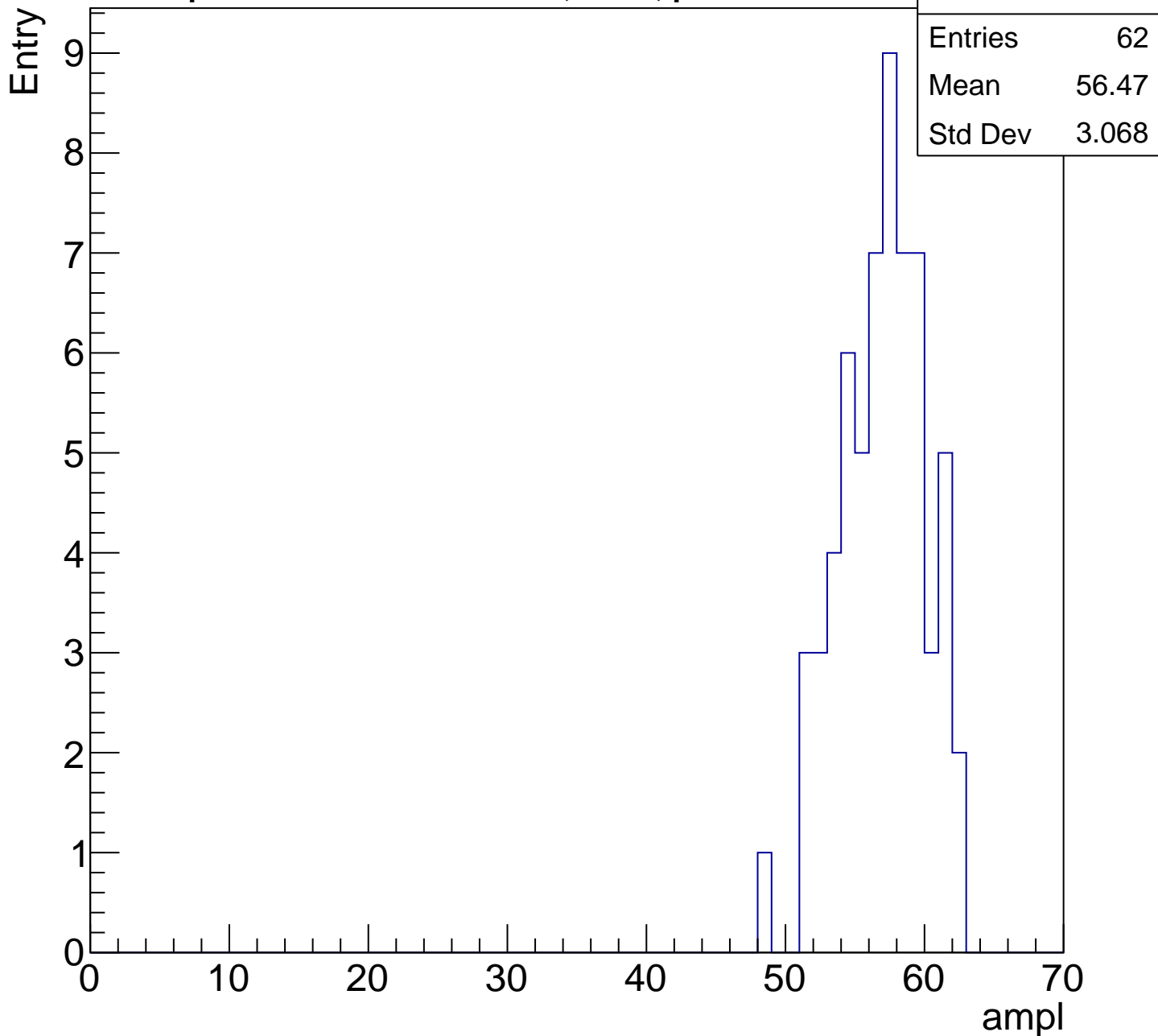
Entry

Entries	61
Mean	50.54
Std Dev	3.252



B1L100S, U25-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

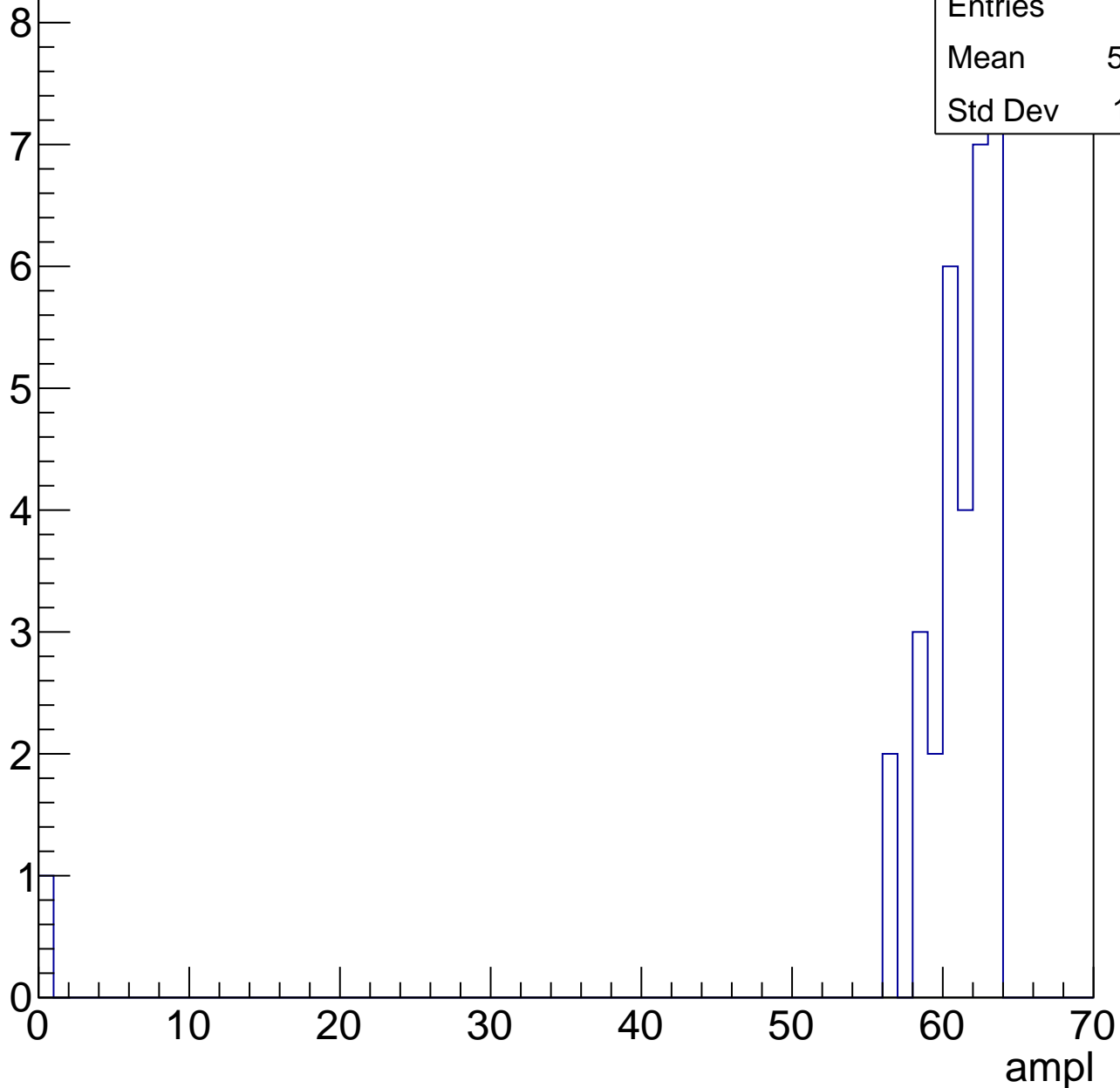


B1L100S, U25-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.97
Std Dev	10.61



B1L100S, U25-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch62, adc0

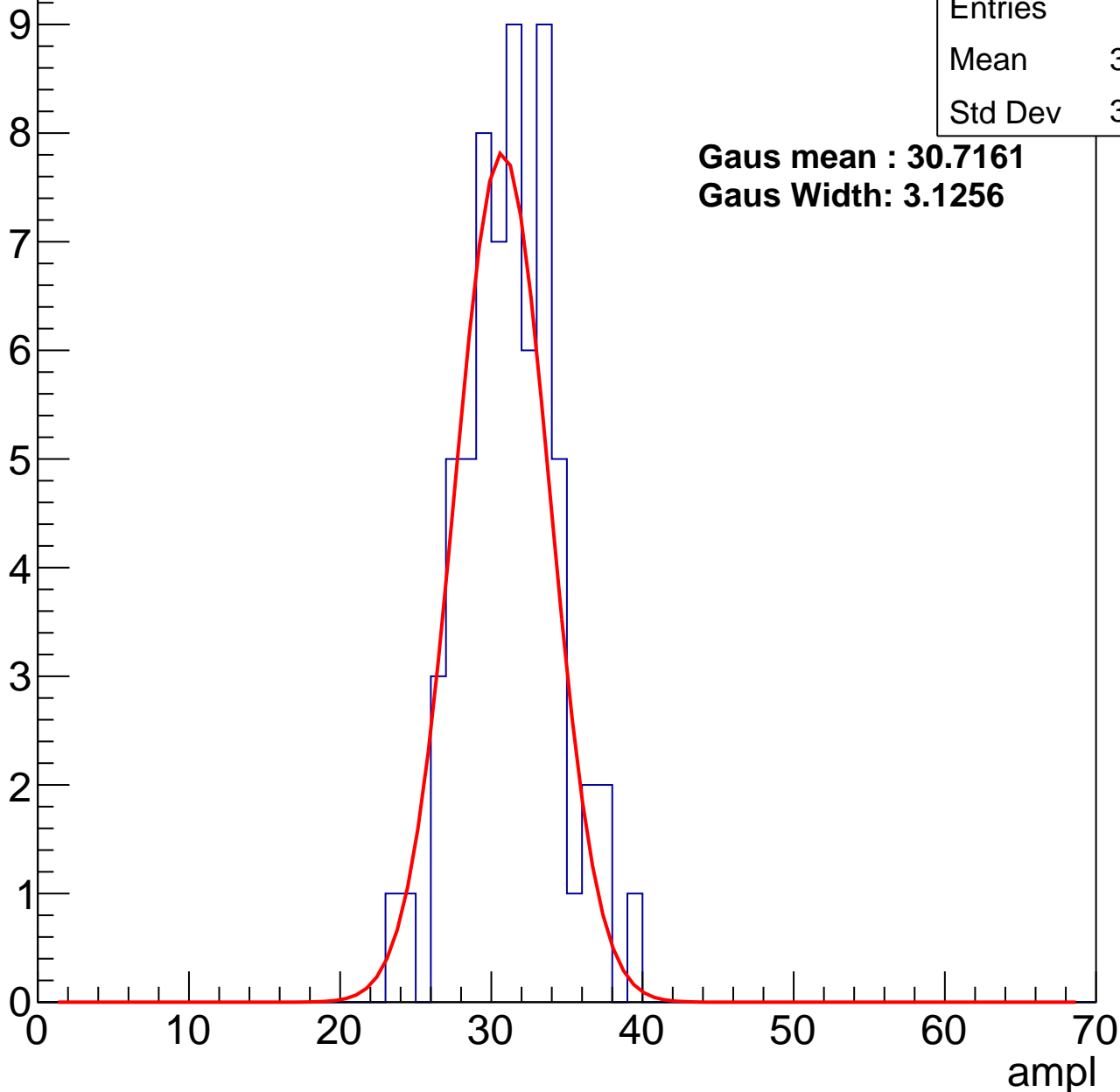
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	30.77
Std Dev	3.132

Gaus mean : 30.7161

Gaus Width: 3.1256



B1L100S, U25-ch62, adc1

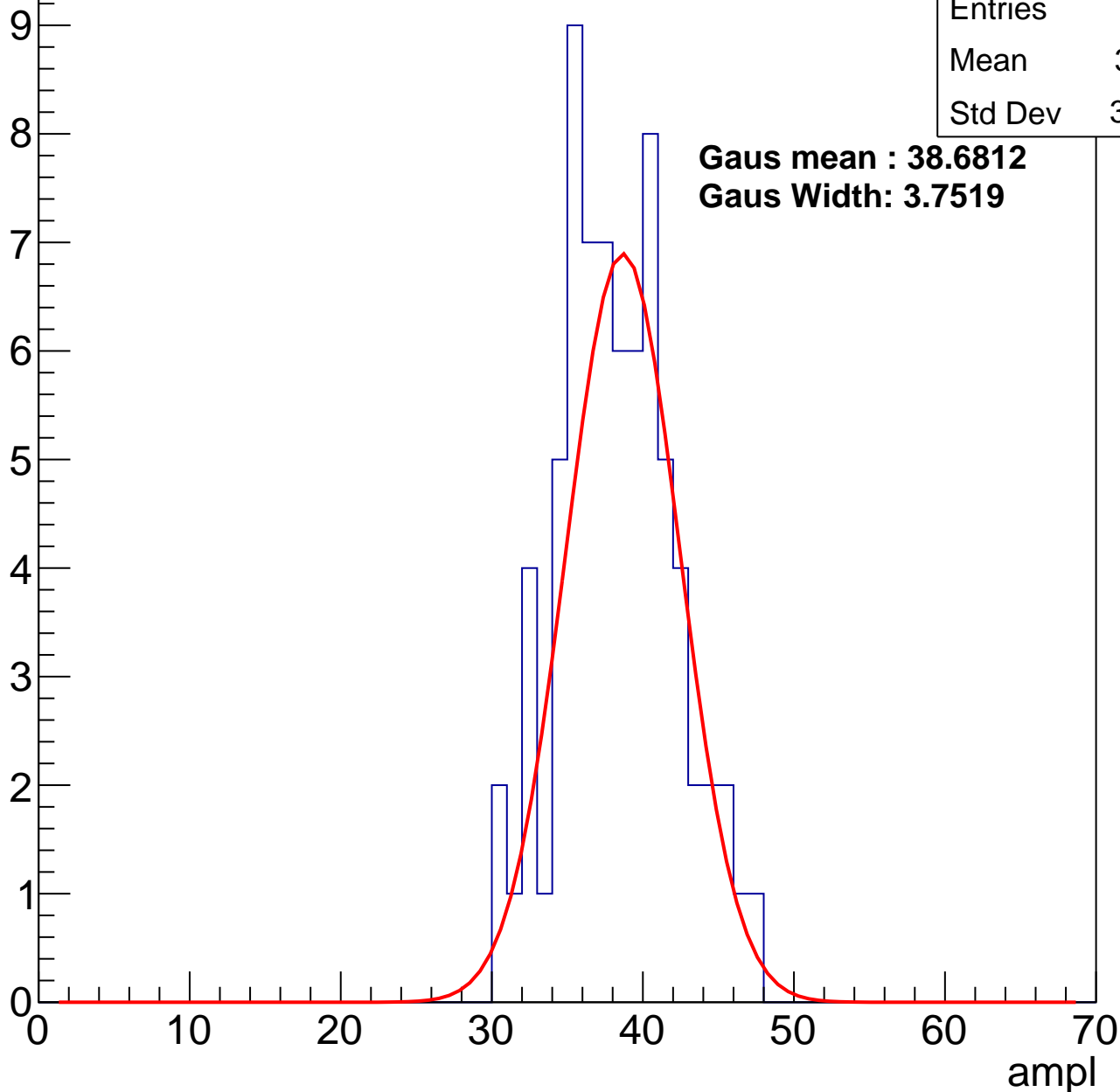
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.81
Std Dev	3.795

Gaus mean : 38.6812

Gaus Width: 3.7519



B1L100S, U25-ch62, adc2

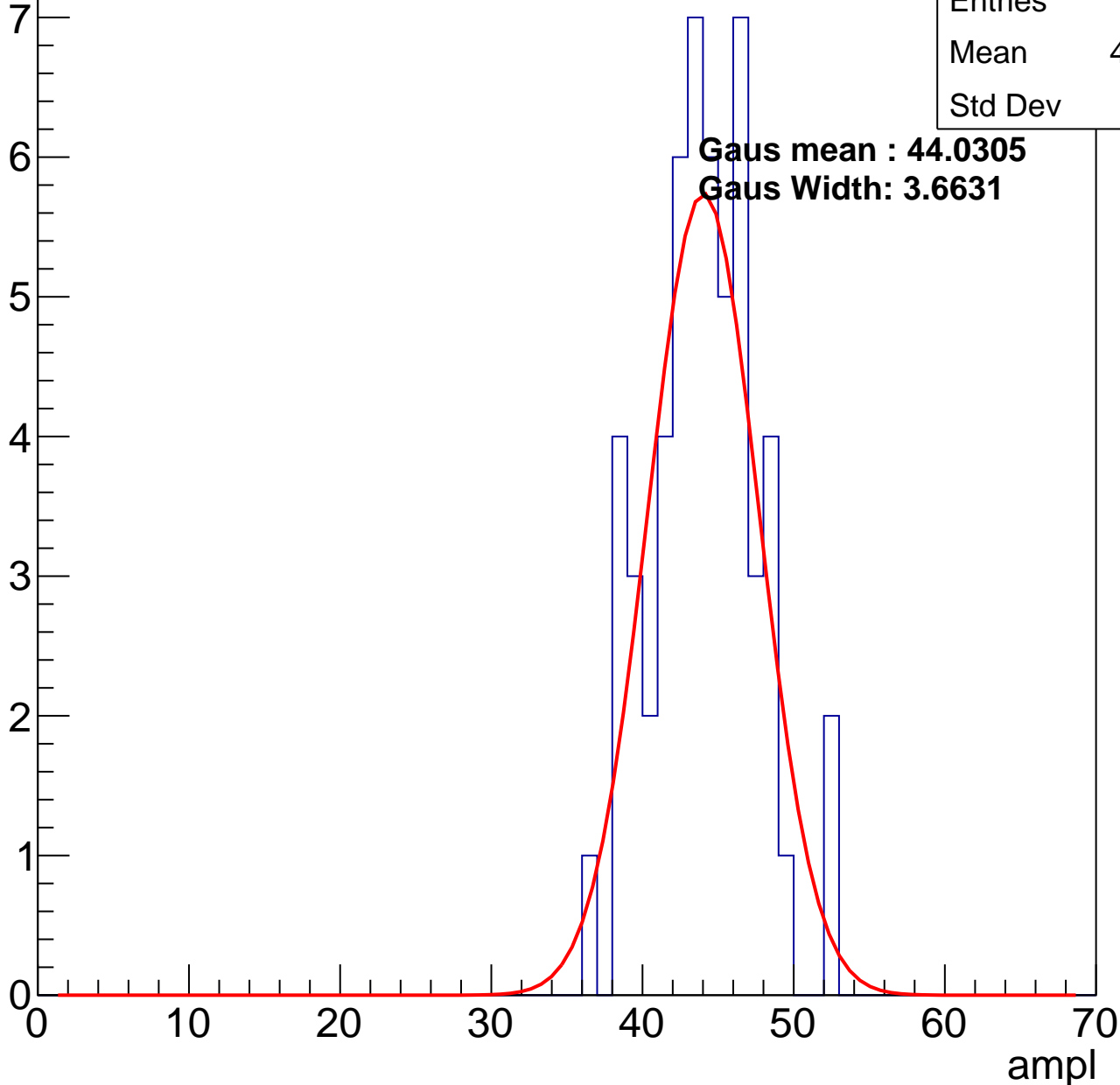
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.62
Std Dev	3.44

Gaus mean : 44.0305

Gaus Width: 3.6631

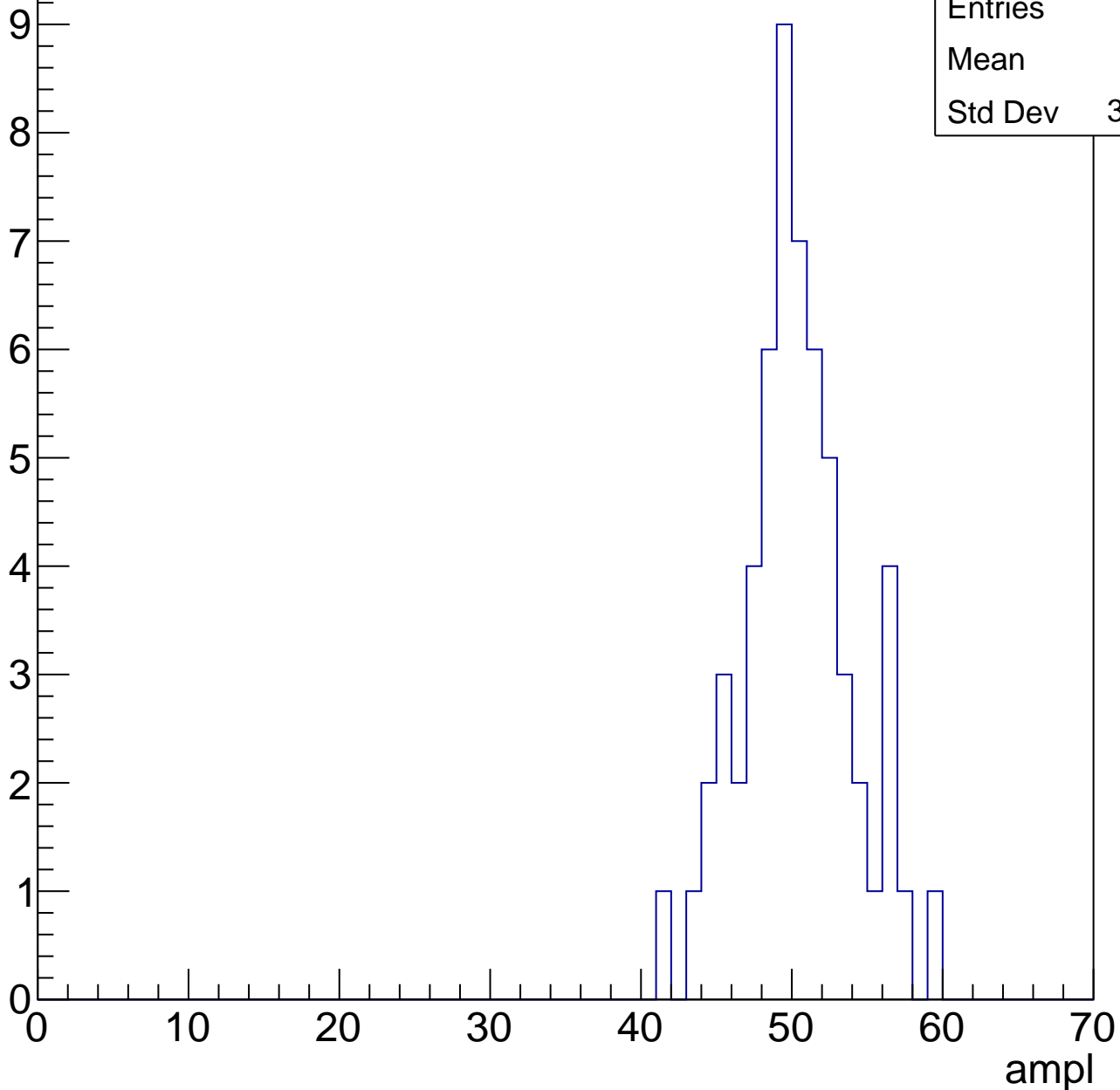


B1L100S, U25-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.9
Std Dev	3.623

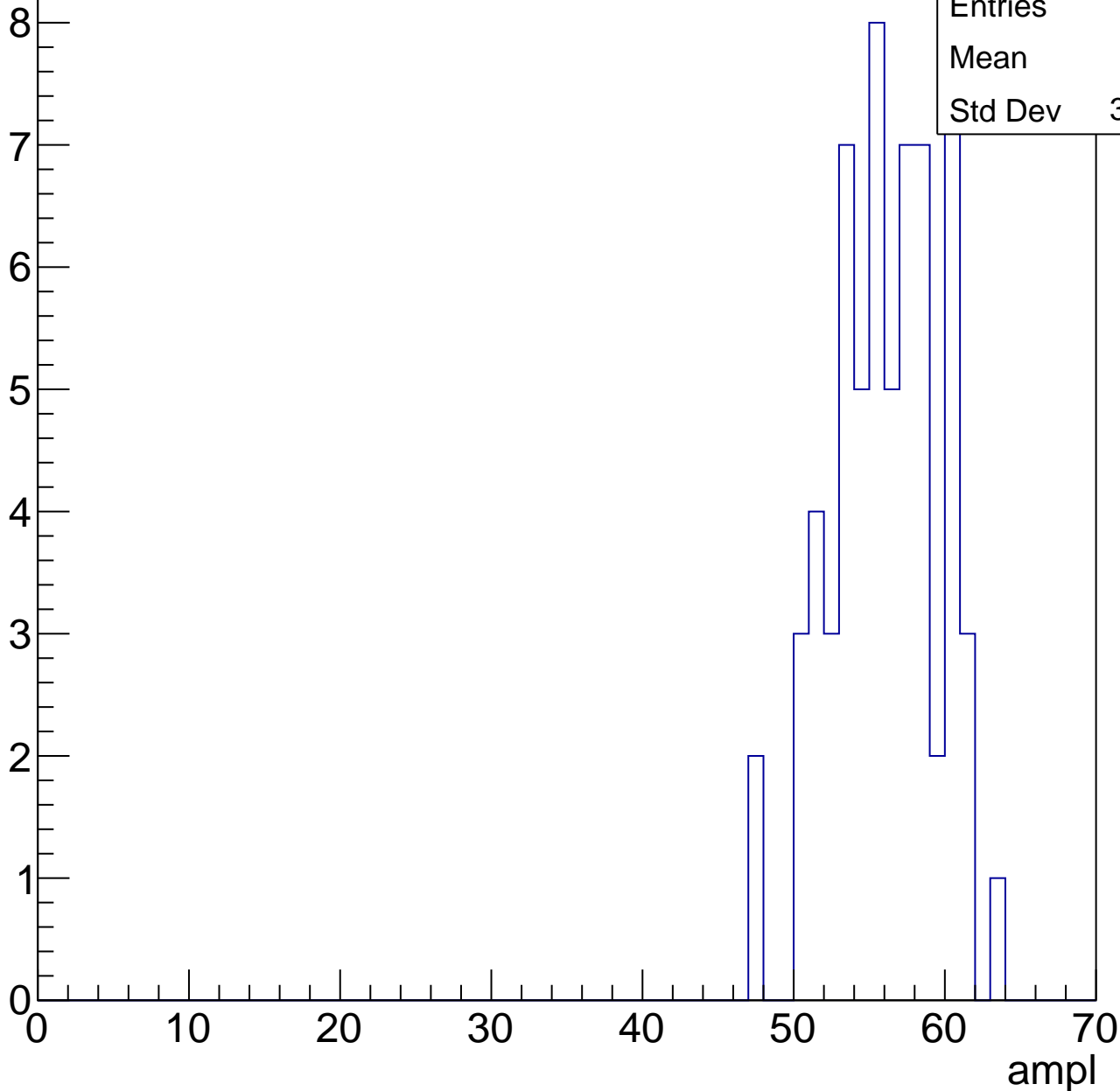


B1L100S, U25-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	55.6
Std Dev	3.516

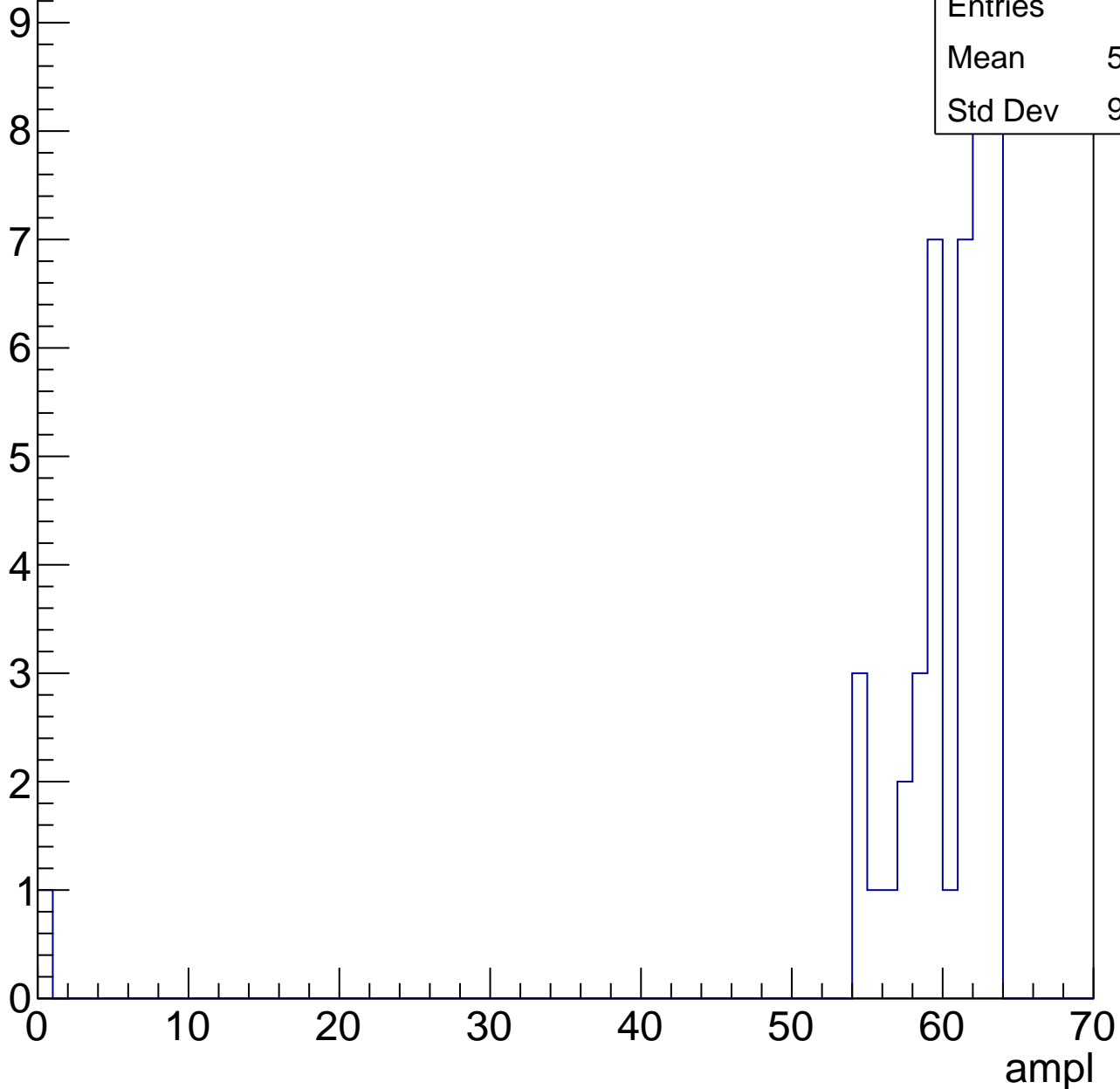


B1L100S, U25-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

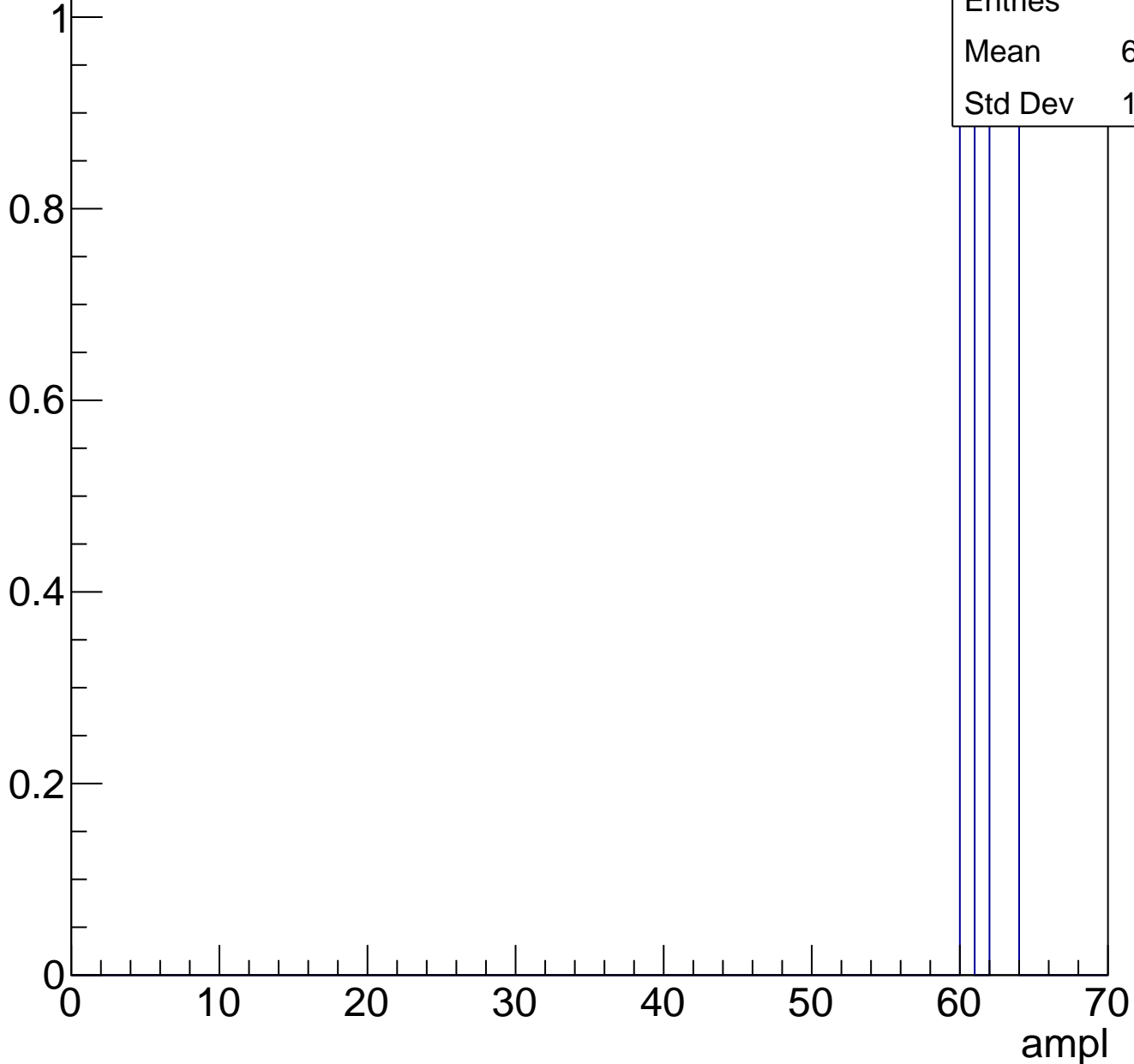
Entries	43
Mean	58.67
Std Dev	9.432



B1L100S, U25-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch63, adc0

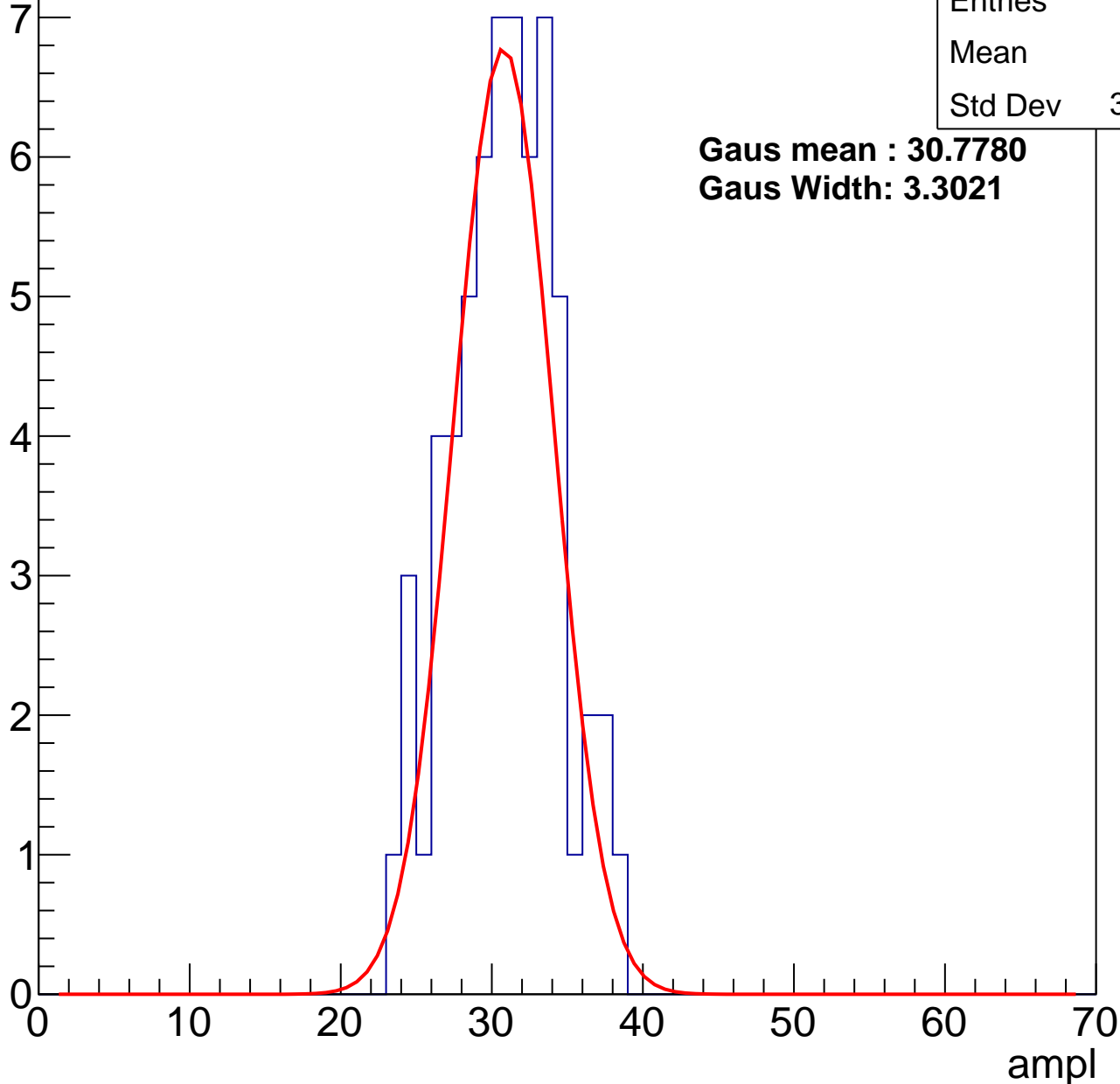
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.4
Std Dev	3.434

Gaus mean : 30.7780

Gaus Width: 3.3021



B1L100S, U25-ch63, adc1

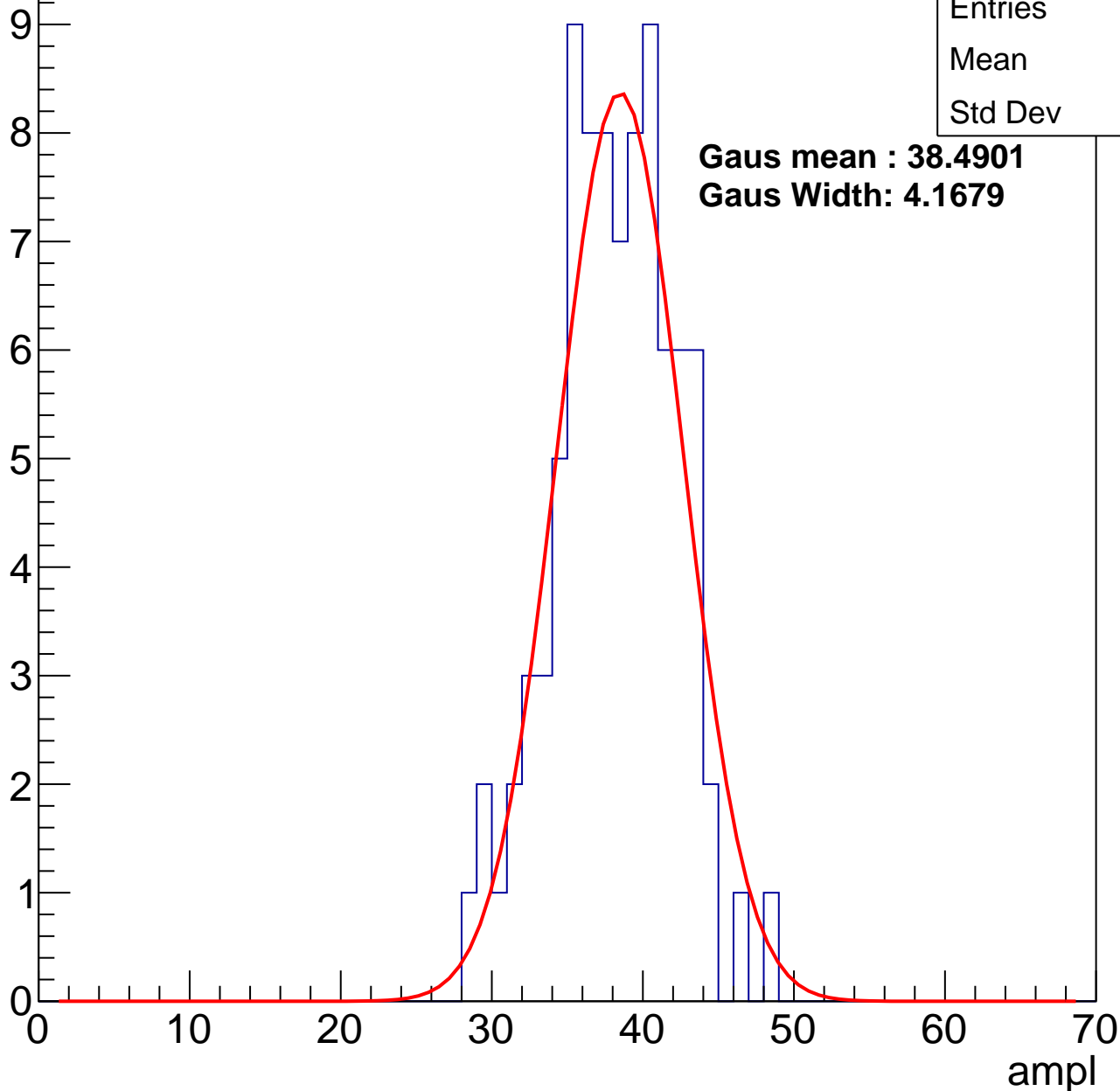
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	37.7
Std Dev	3.96

Gaus mean : 38.4901

Gaus Width: 4.1679



B1L100S, U25-ch63, adc2

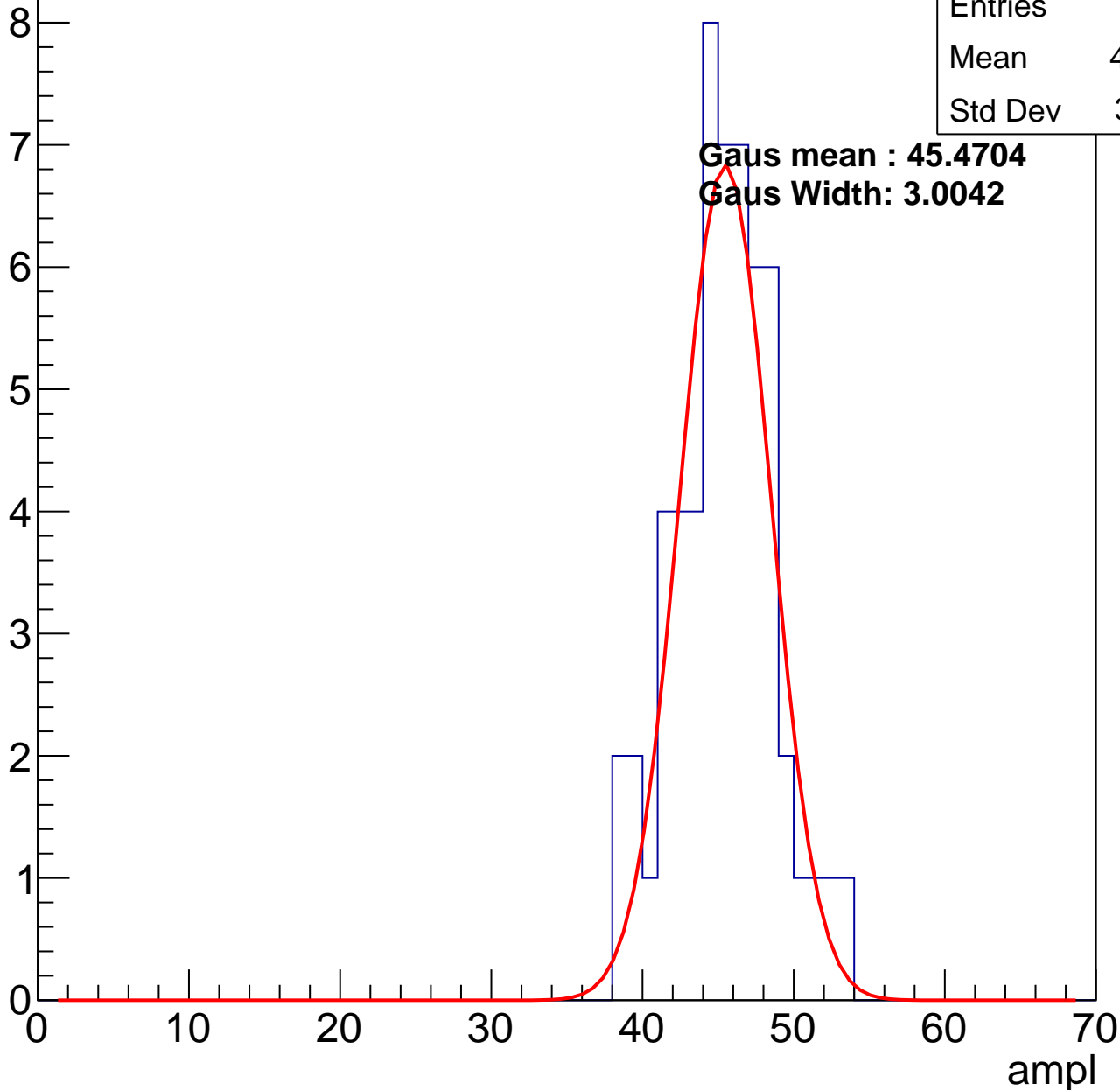
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.93
Std Dev	3.271

Gaus mean : 45.4704

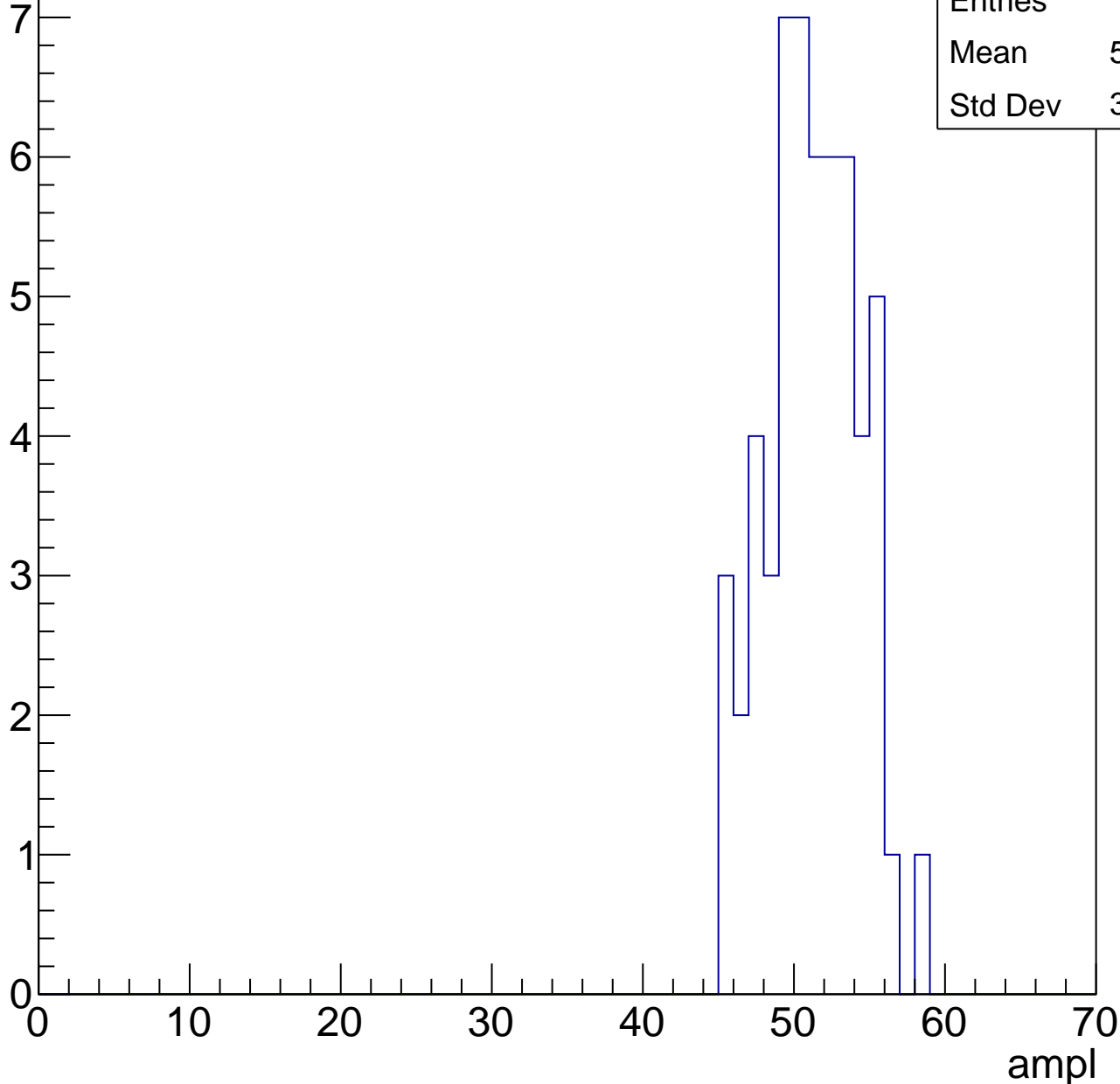
Gaus Width: 3.0042



B1L100S, U25-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

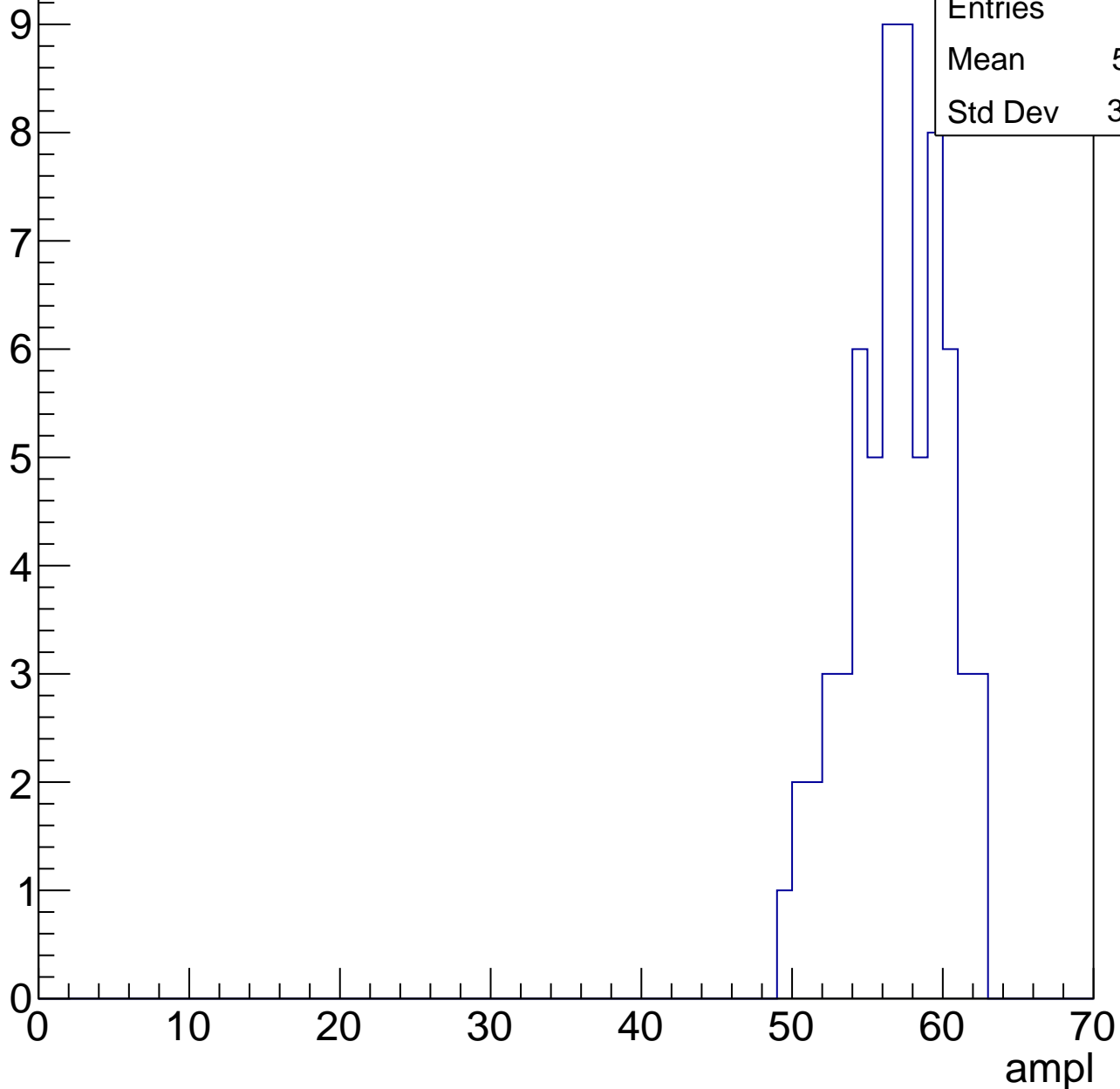


Entries	55
Mean	50.78
Std Dev	3.019

B1L100S, U25-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



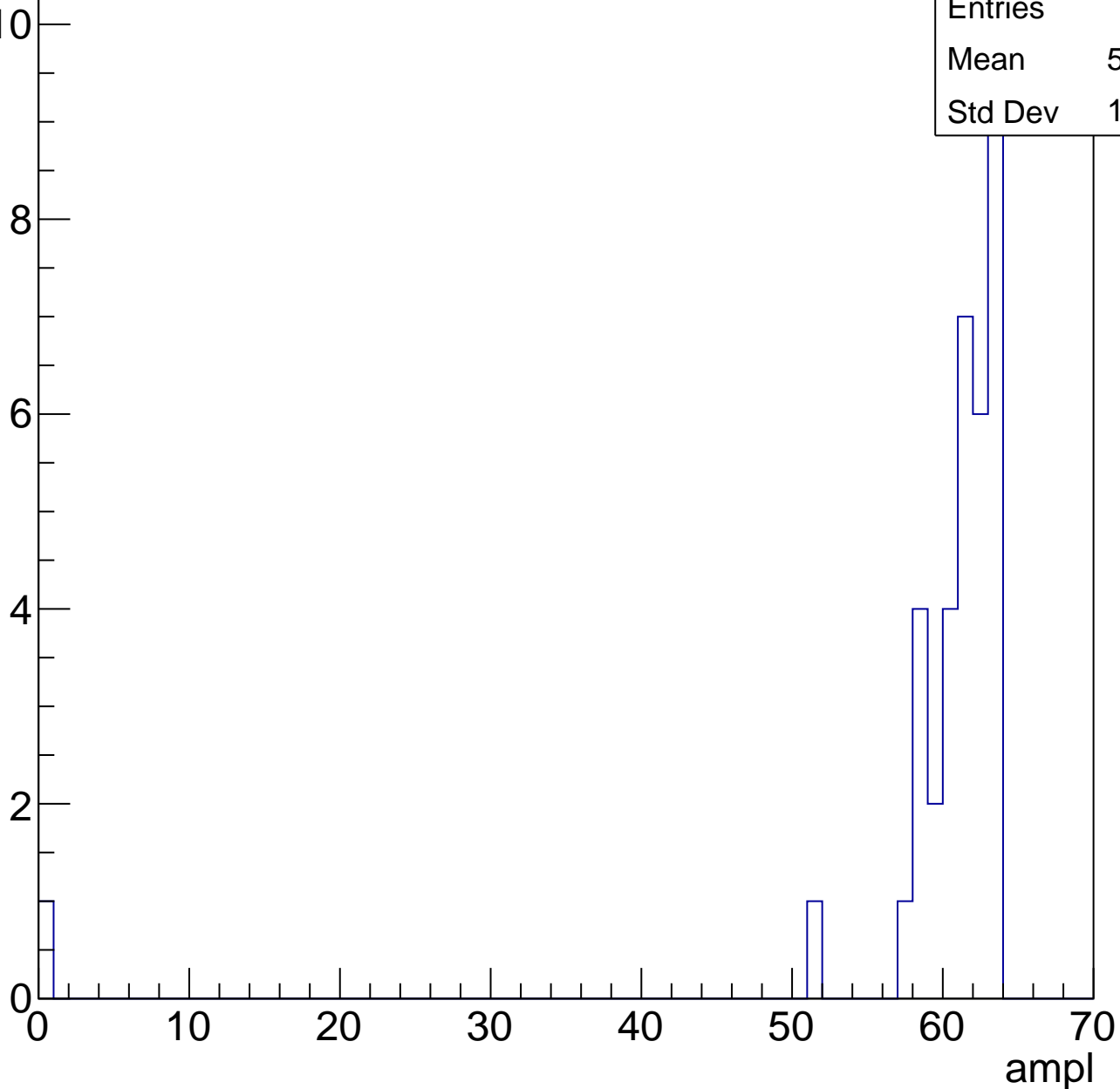
Entries	65
Mean	56.51
Std Dev	3.134

B1L100S, U25-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	59.08
Std Dev	10.27



B1L100S, U25-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U25-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch64, adc0

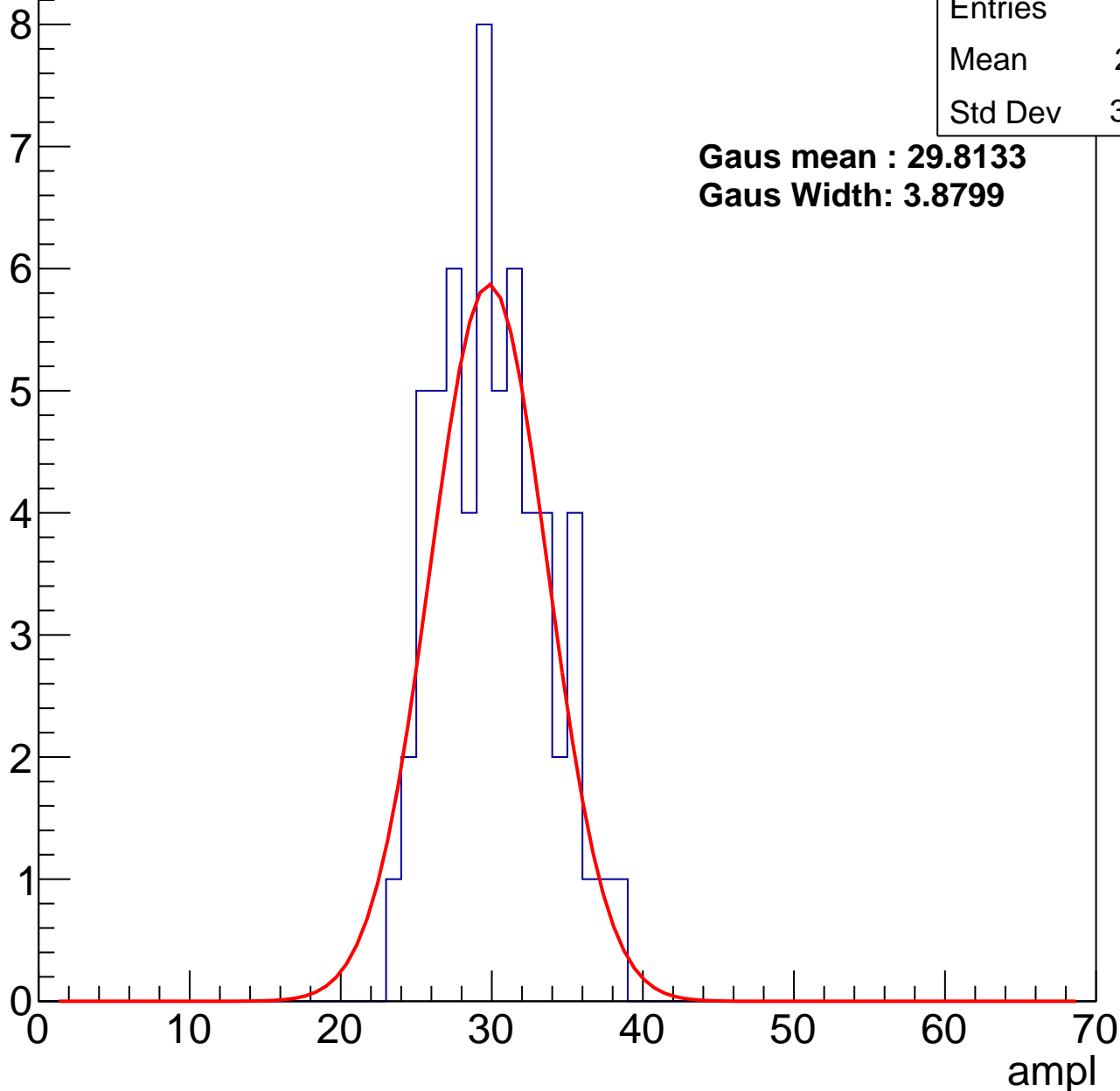
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.61
Std Dev	3.527

Gaus mean : 29.8133

Gaus Width: 3.8799



B1L100S, U25-ch64, adc1

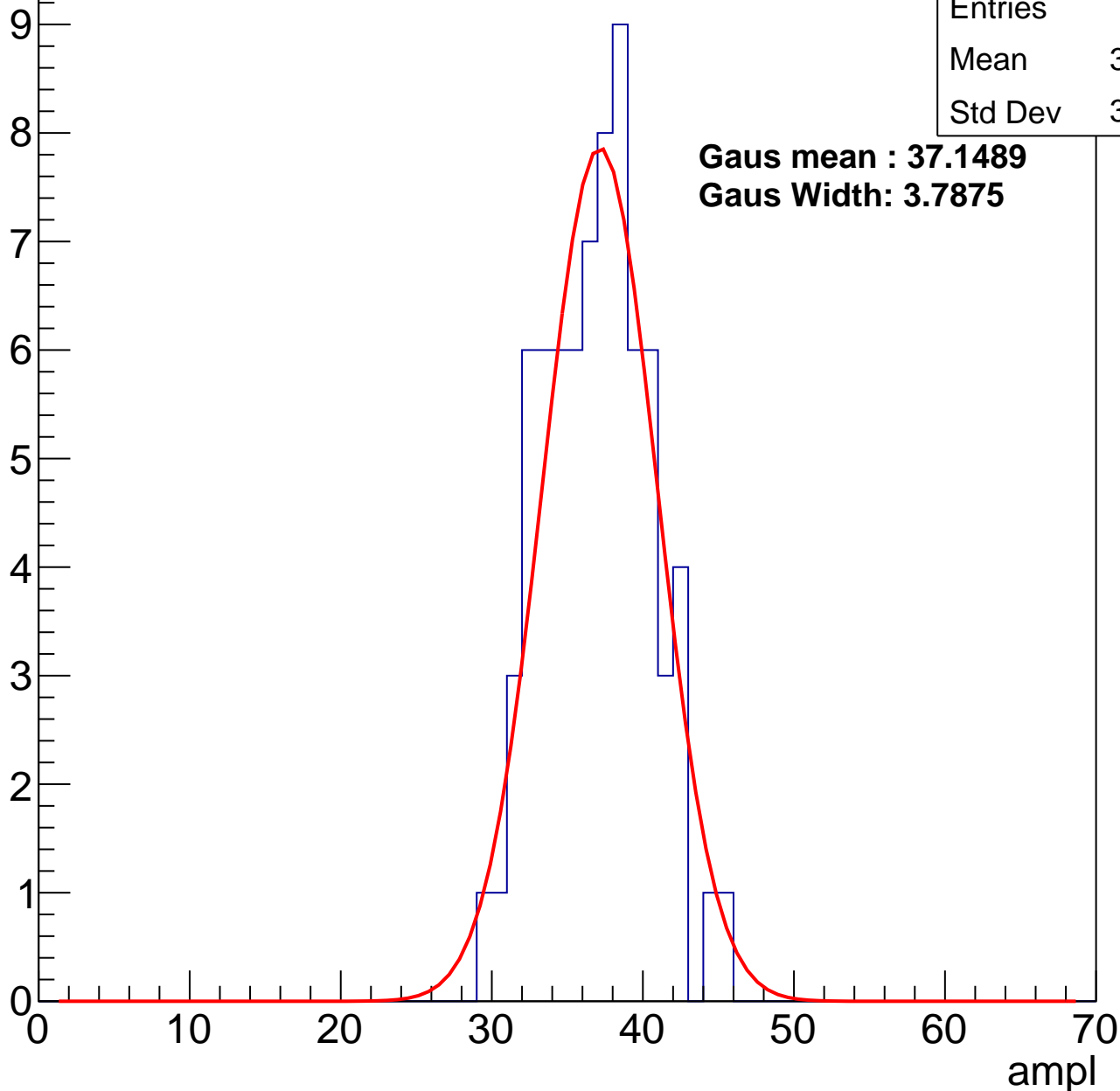
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.49
Std Dev	3.449

Gaus mean : 37.1489

Gaus Width: 3.7875



B1L100S, U25-ch64, adc2

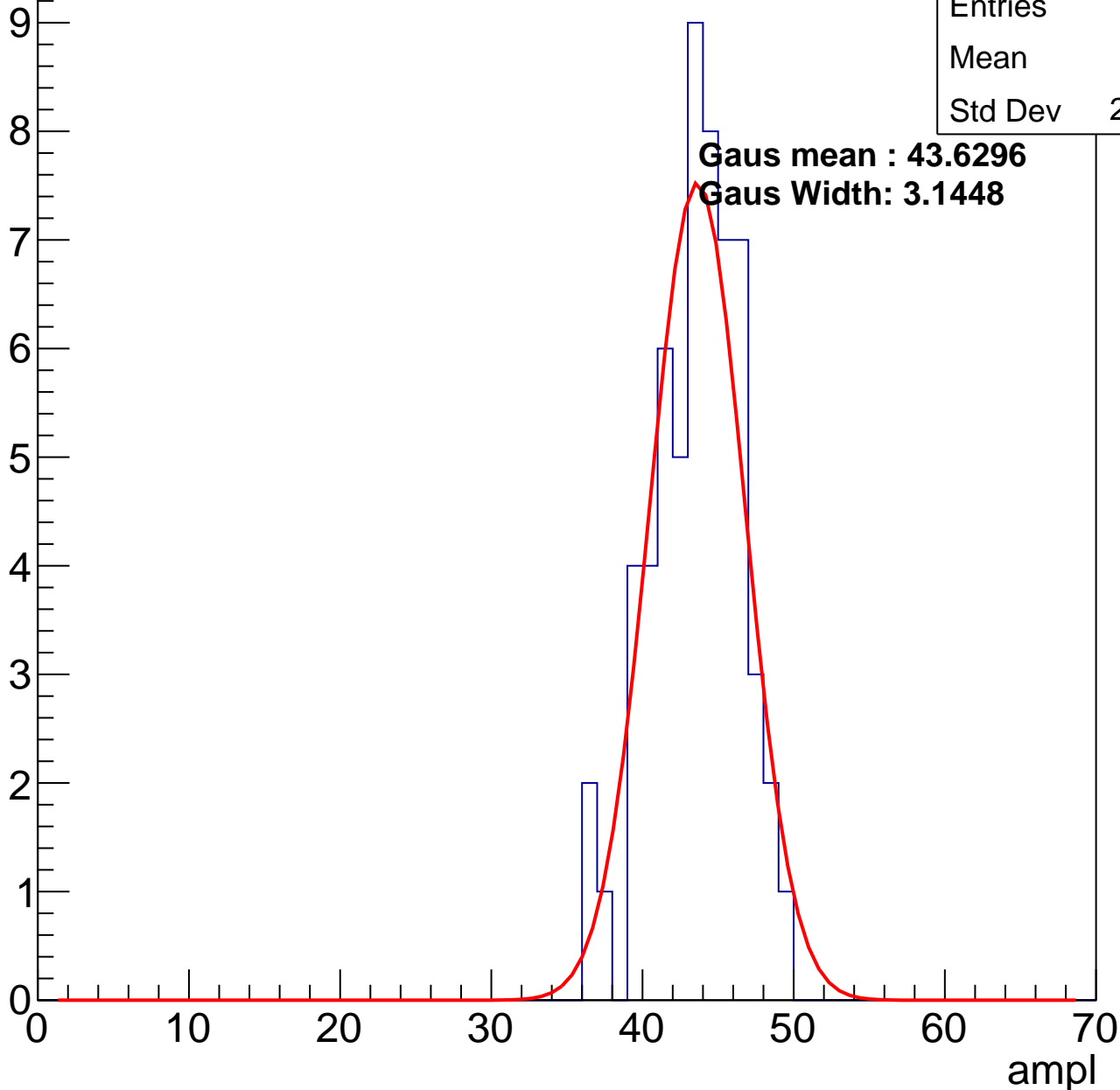
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.1
Std Dev	2.898

Gaus mean : 43.6296

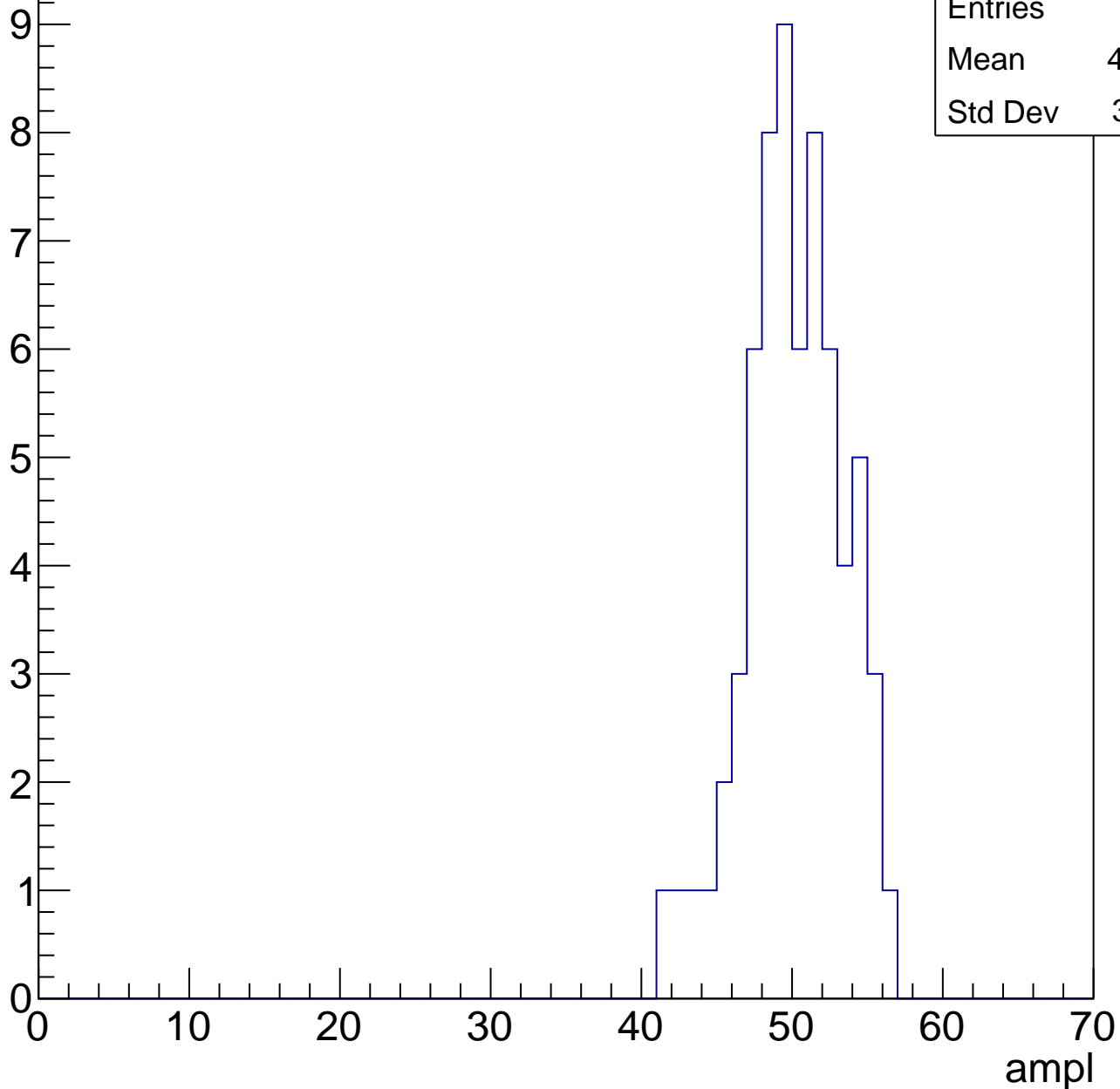
Gaus Width: 3.1448



B1L100S, U25-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

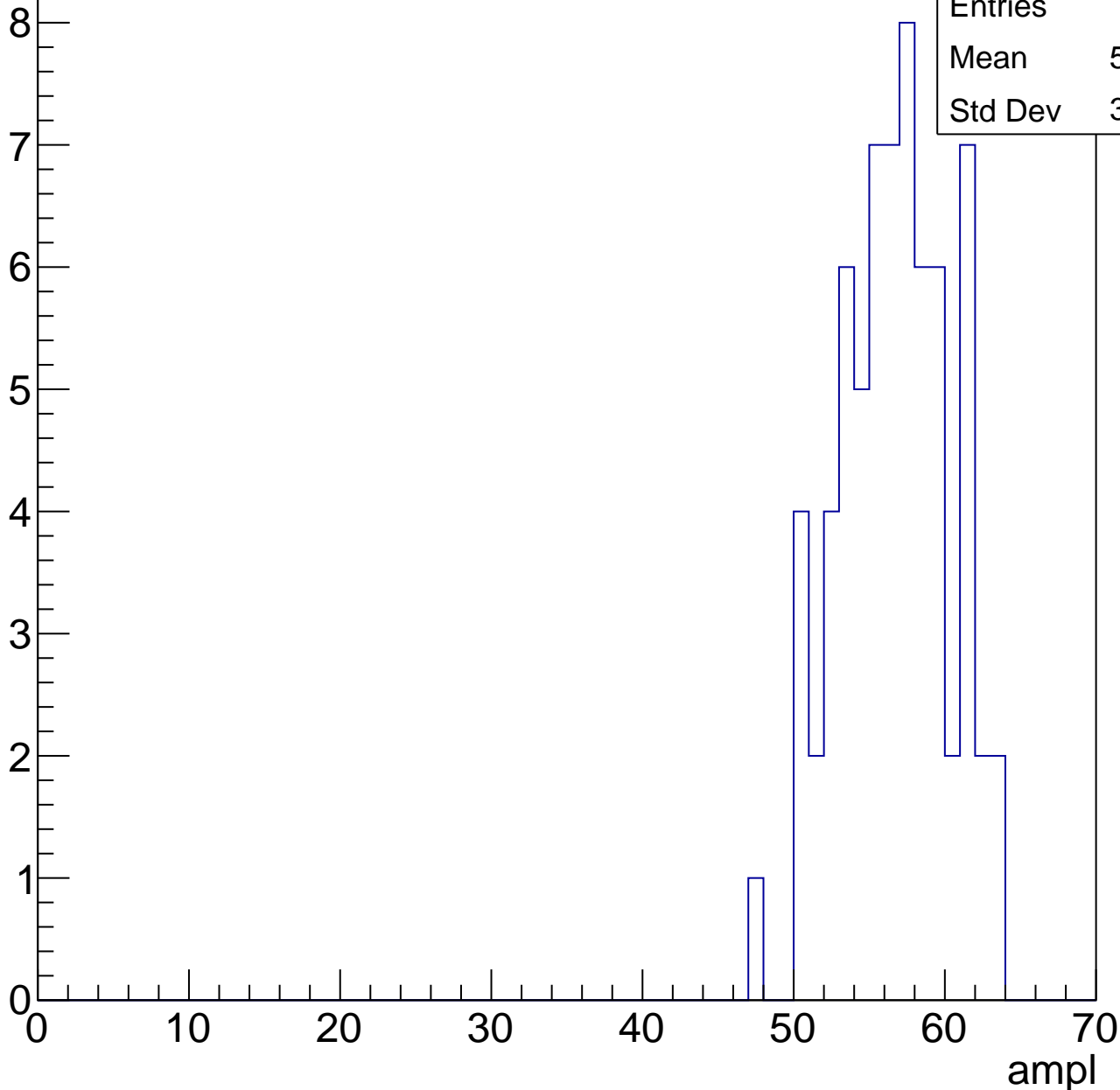


B1L100S, U25-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

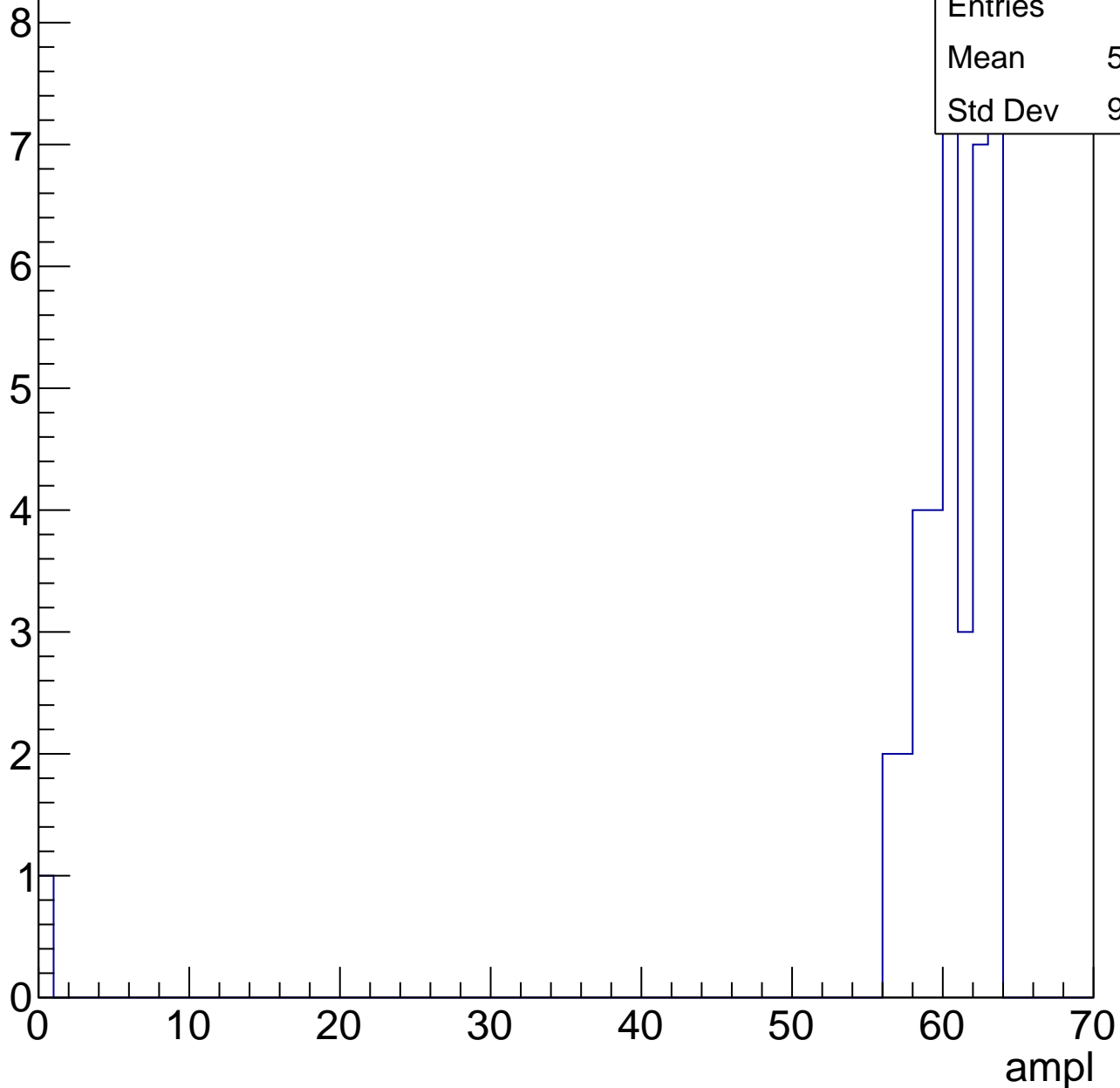
Entries	69
Mean	56.19
Std Dev	3.572



B1L100S, U25-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch65, adc0

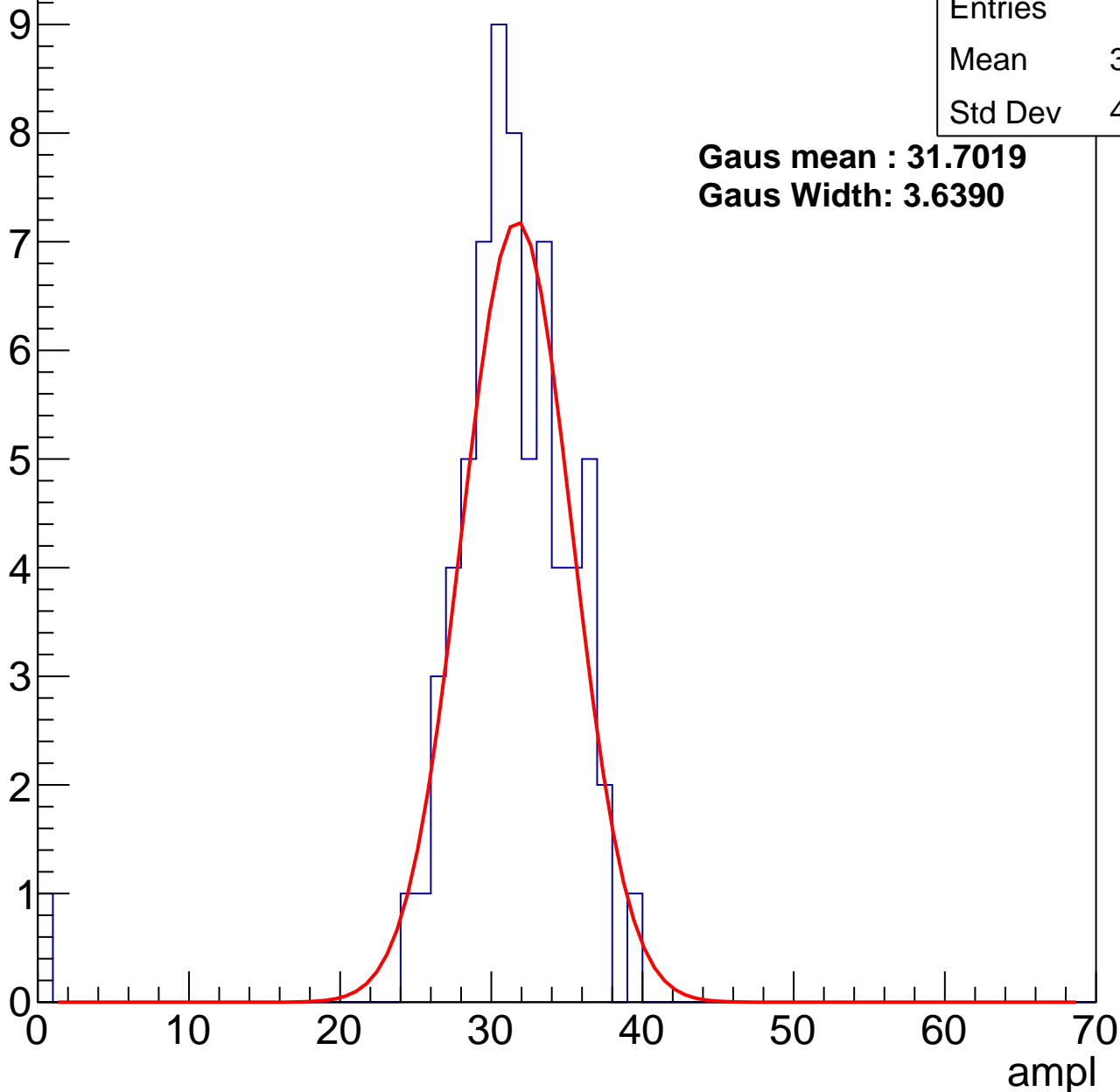
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.69
Std Dev	4.969

Gaus mean : 31.7019

Gaus Width: 3.6390



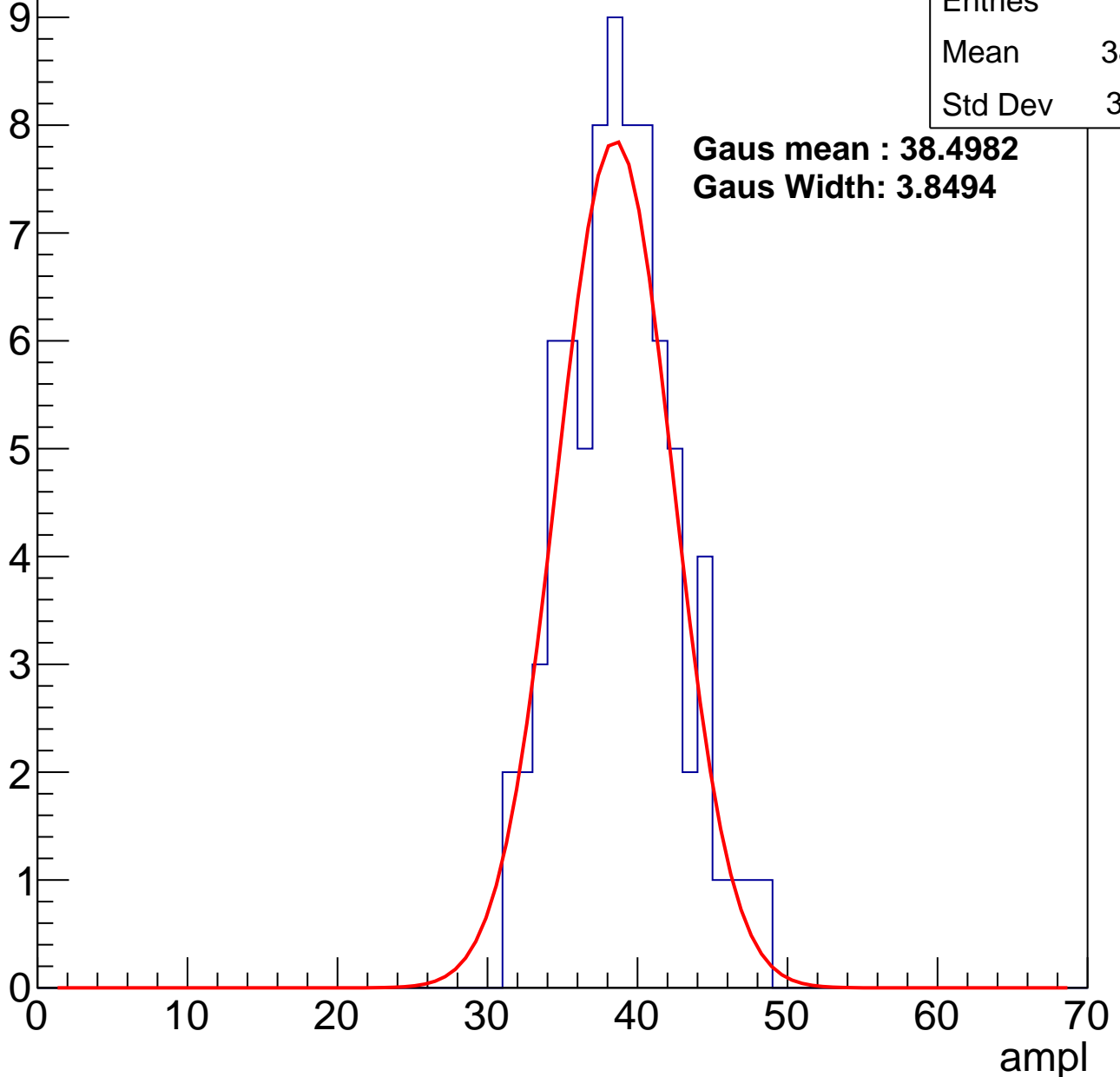
B1L100S, U25-ch65, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	38.37
Std Dev	3.711

Gaus mean : 38.4982
Gaus Width: 3.8494



B1L100S, U25-ch65, adc2

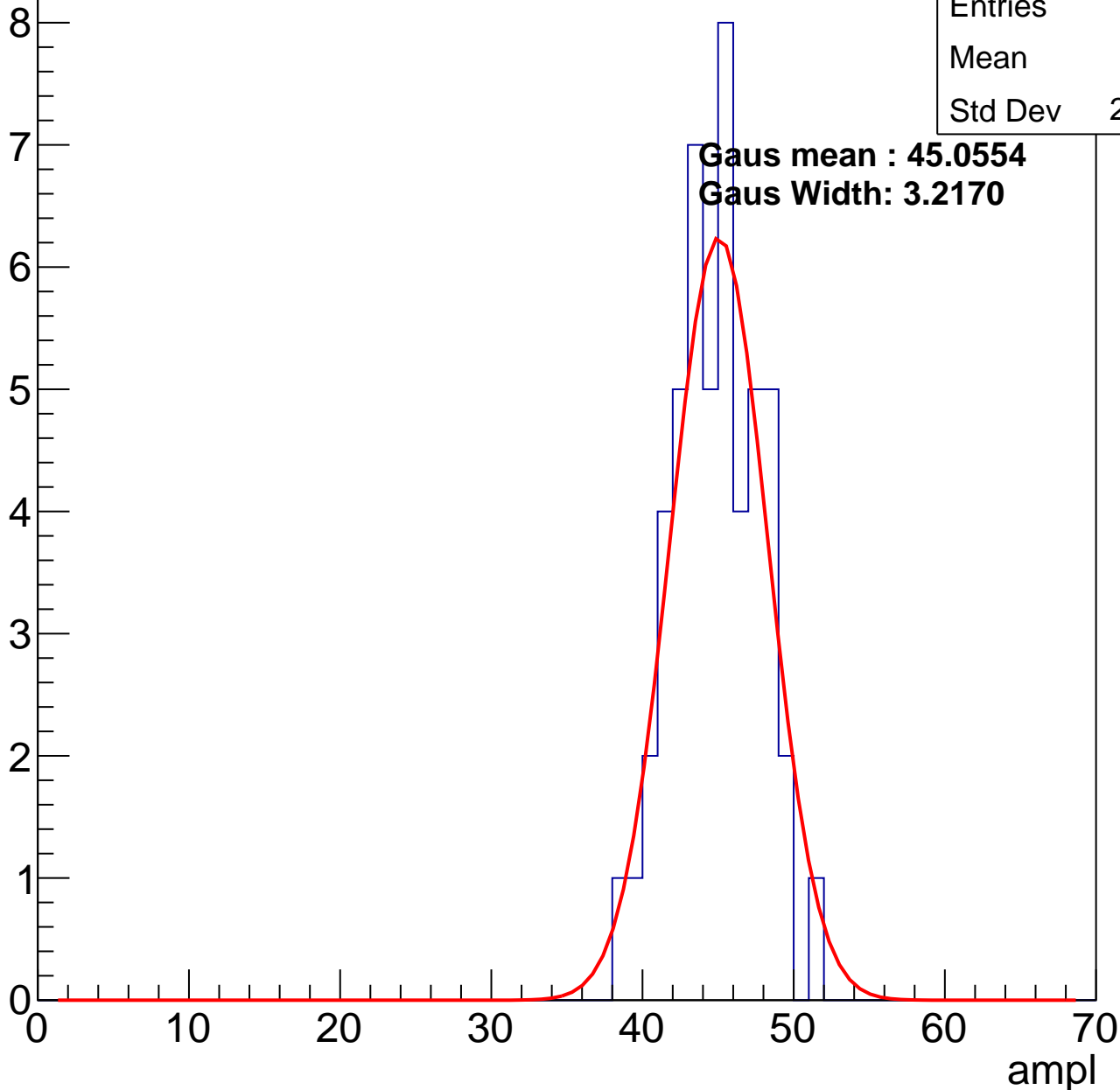
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	44.4
Std Dev	2.814

Gaus mean : 45.0554

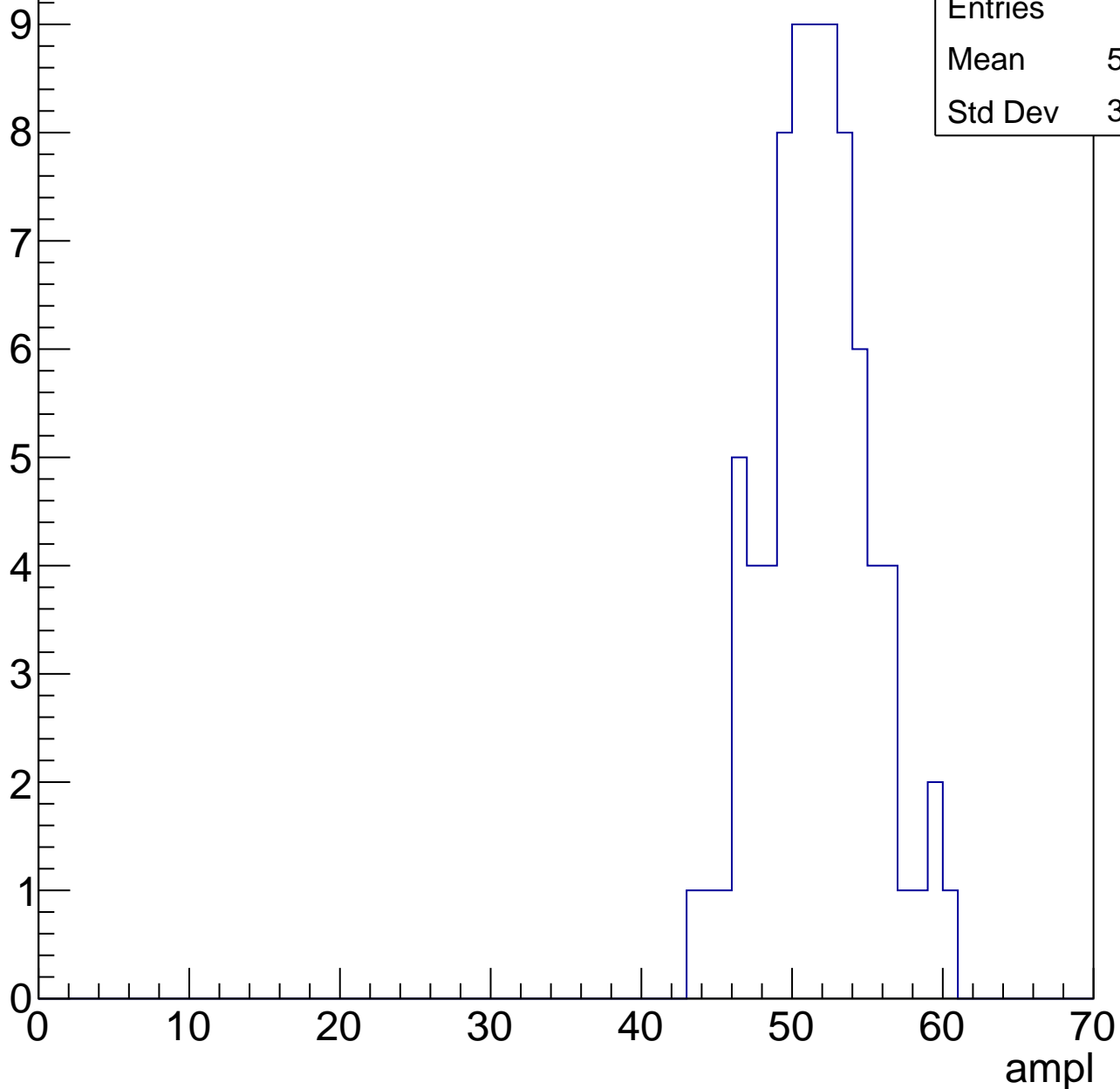
Gaus Width: 3.2170



B1L100S, U25-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

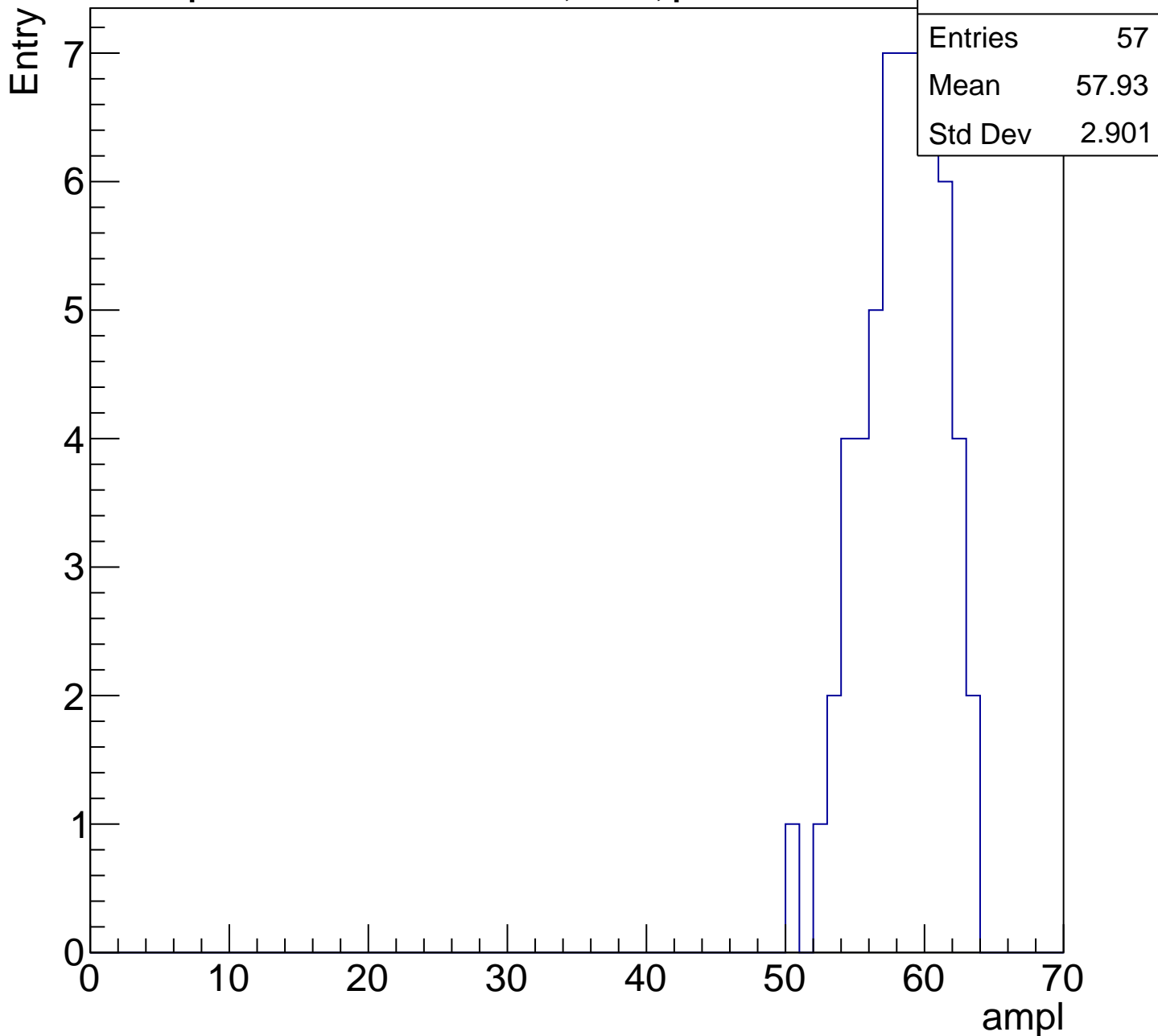
Entry



Entries	78
Mean	51.23
Std Dev	3.515

B1L100S, U25-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

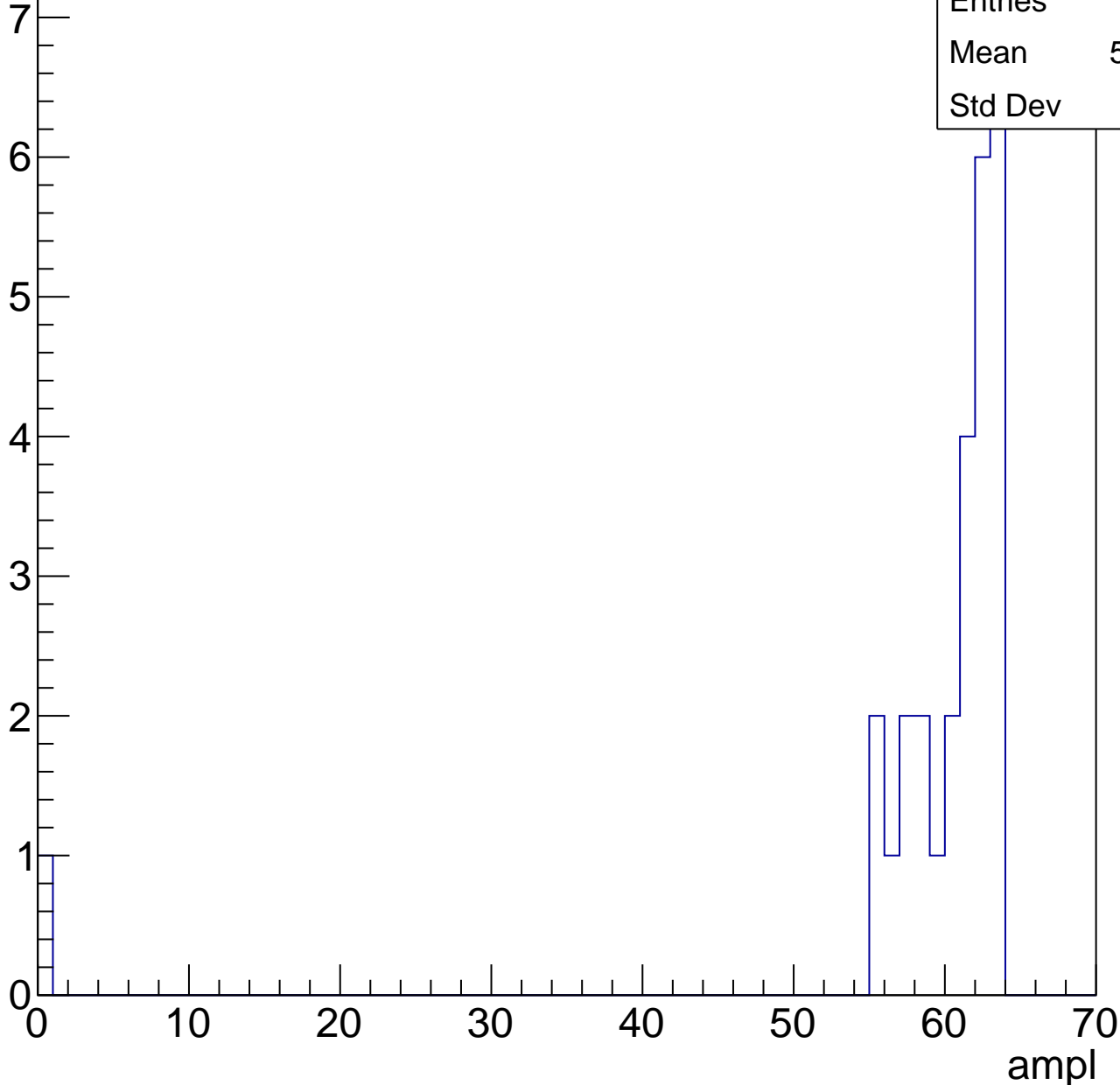


B1L100S, U25-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	58.29
Std Dev	11.5



B1L100S, U25-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U25-ch65, adc7

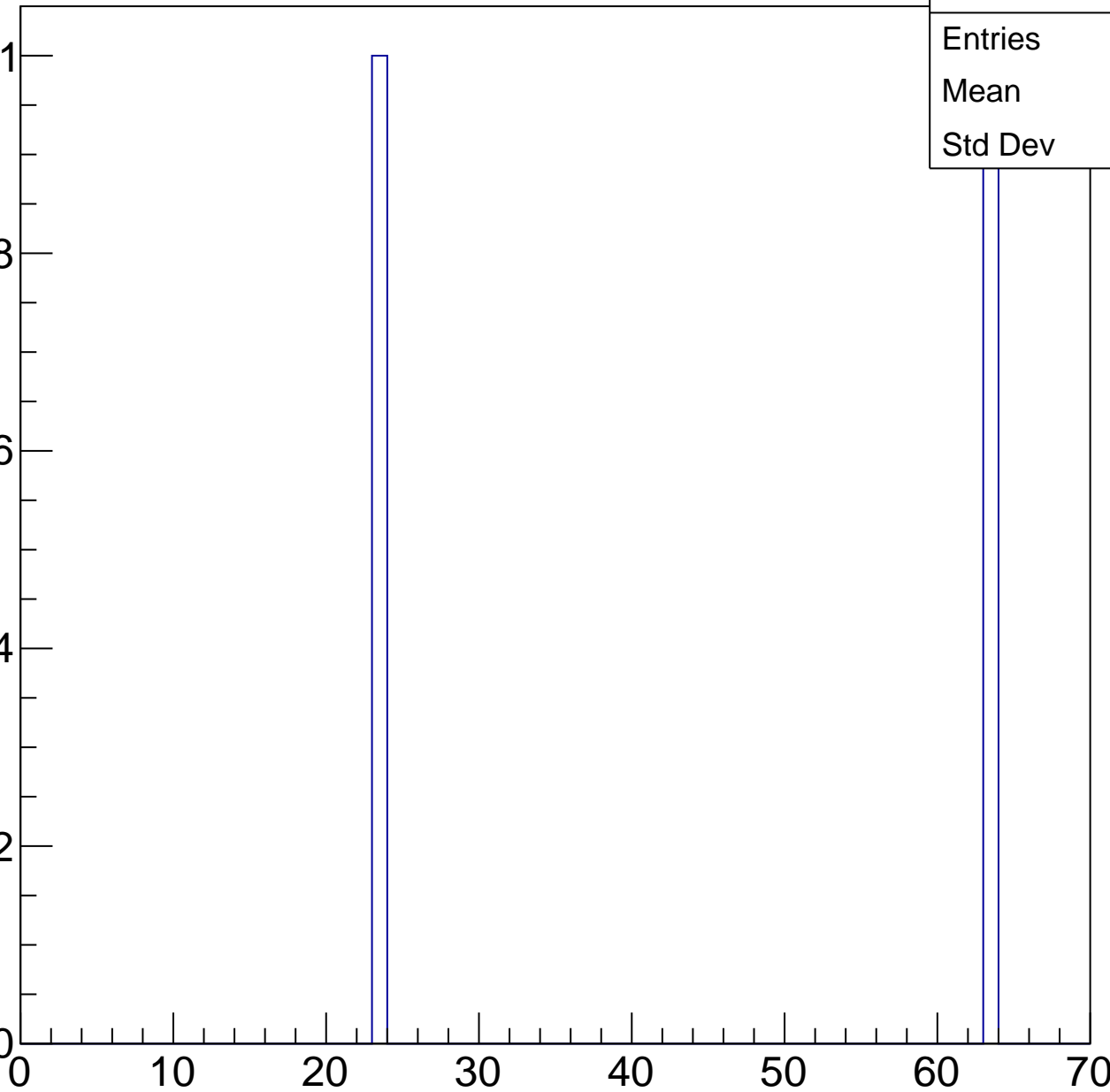
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl



B1L100S, U25-ch66, adc0

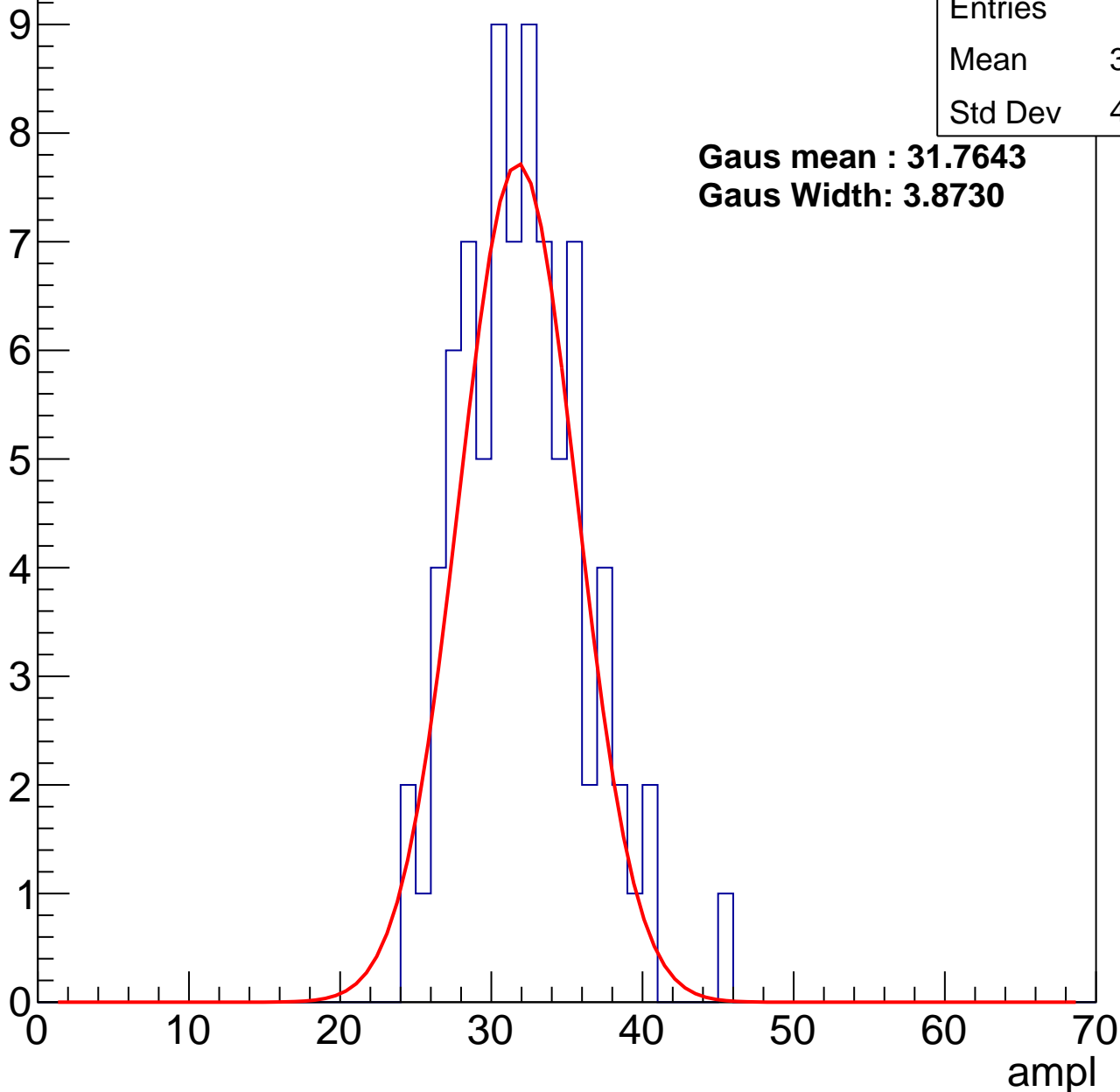
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	31.62
Std Dev	4.005

Gaus mean : 31.7643

Gaus Width: 3.8730



B1L100S, U25-ch66, adc1

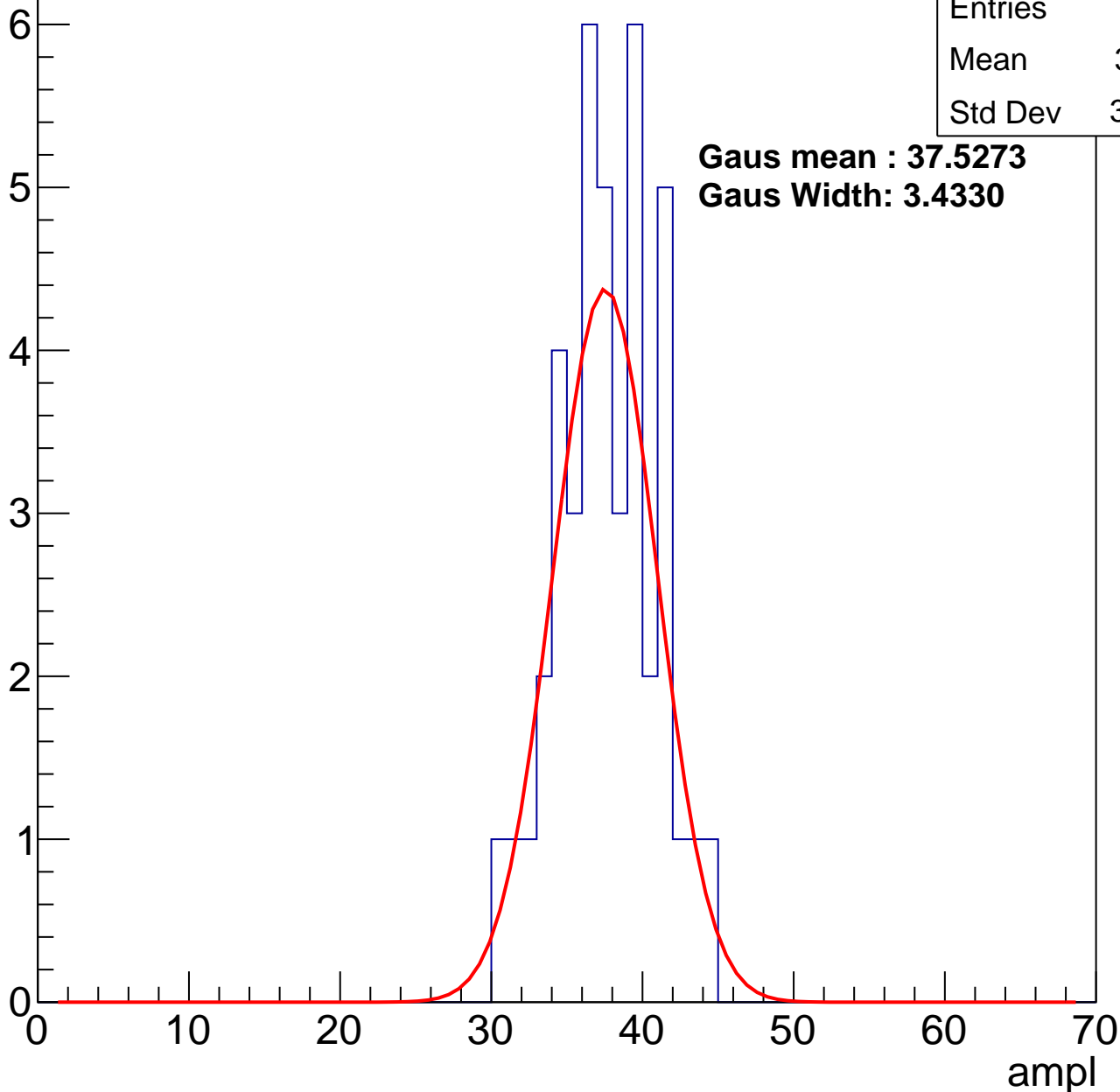
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	37.21
Std Dev	3.196

Gaus mean : 37.5273

Gaus Width: 3.4330



B1L100S, U25-ch66, adc2

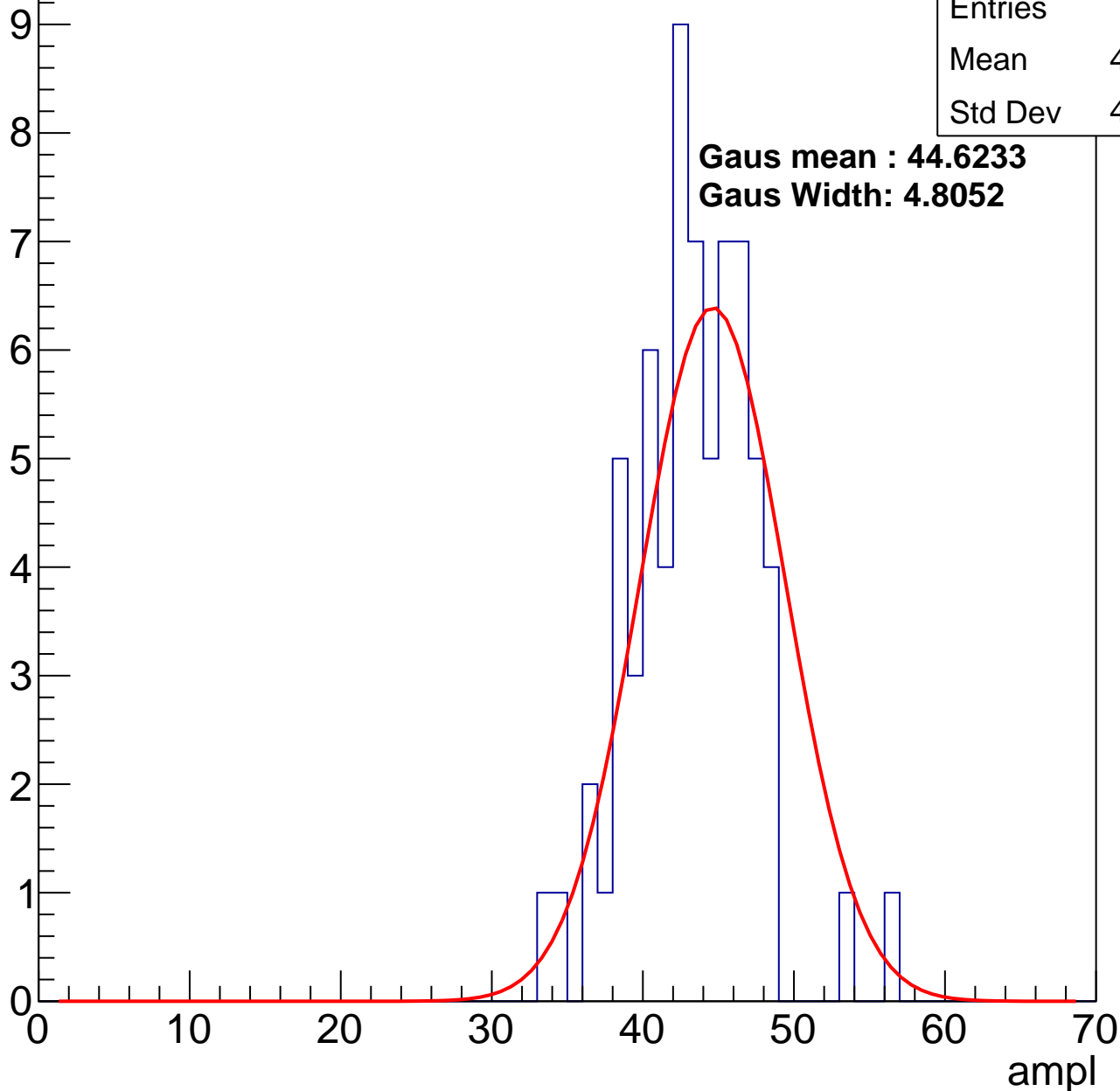
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.88
Std Dev	4.013

Gaus mean : 44.6233

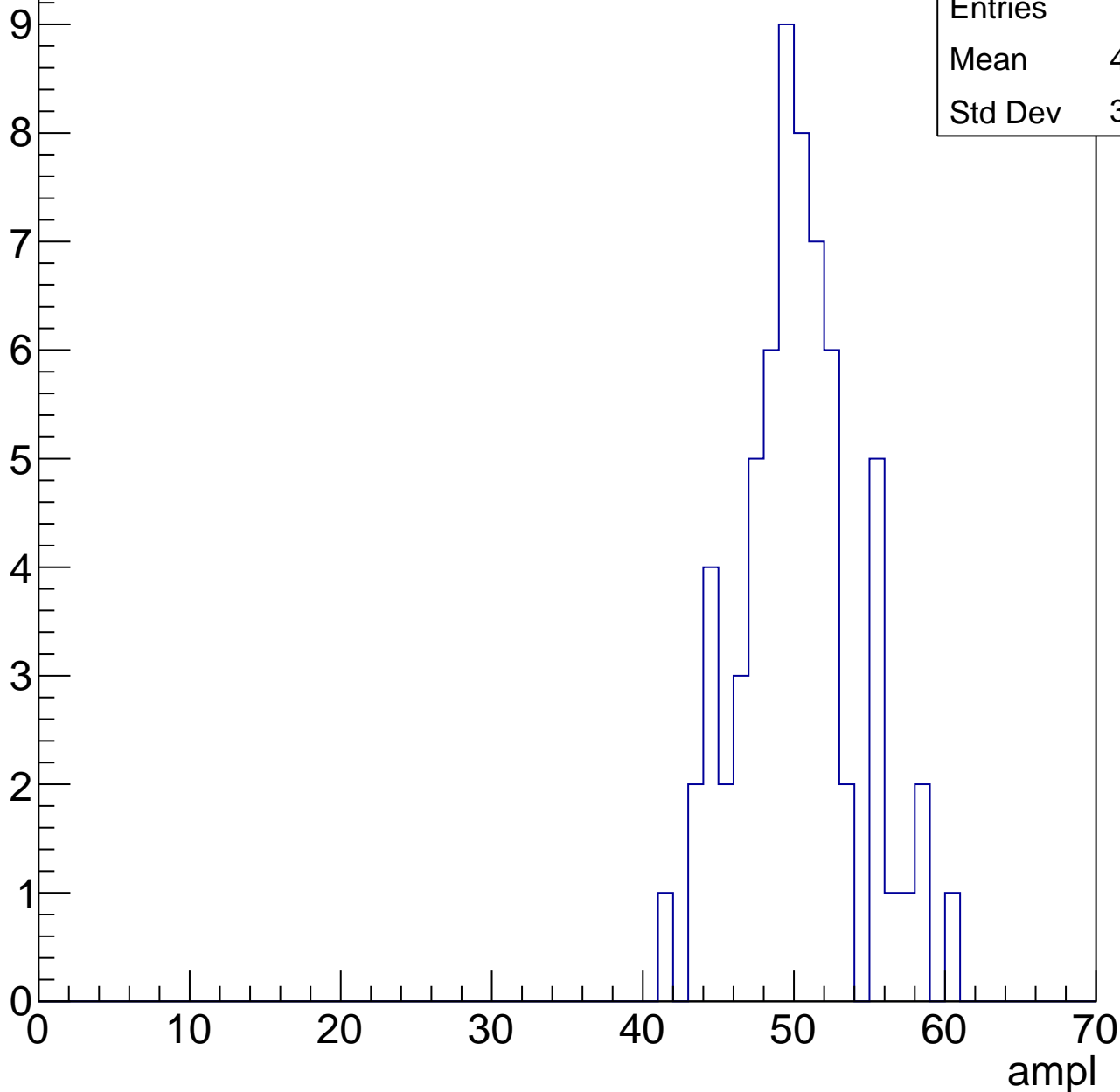
Gaus Width: 4.8052



B1L100S, U25-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

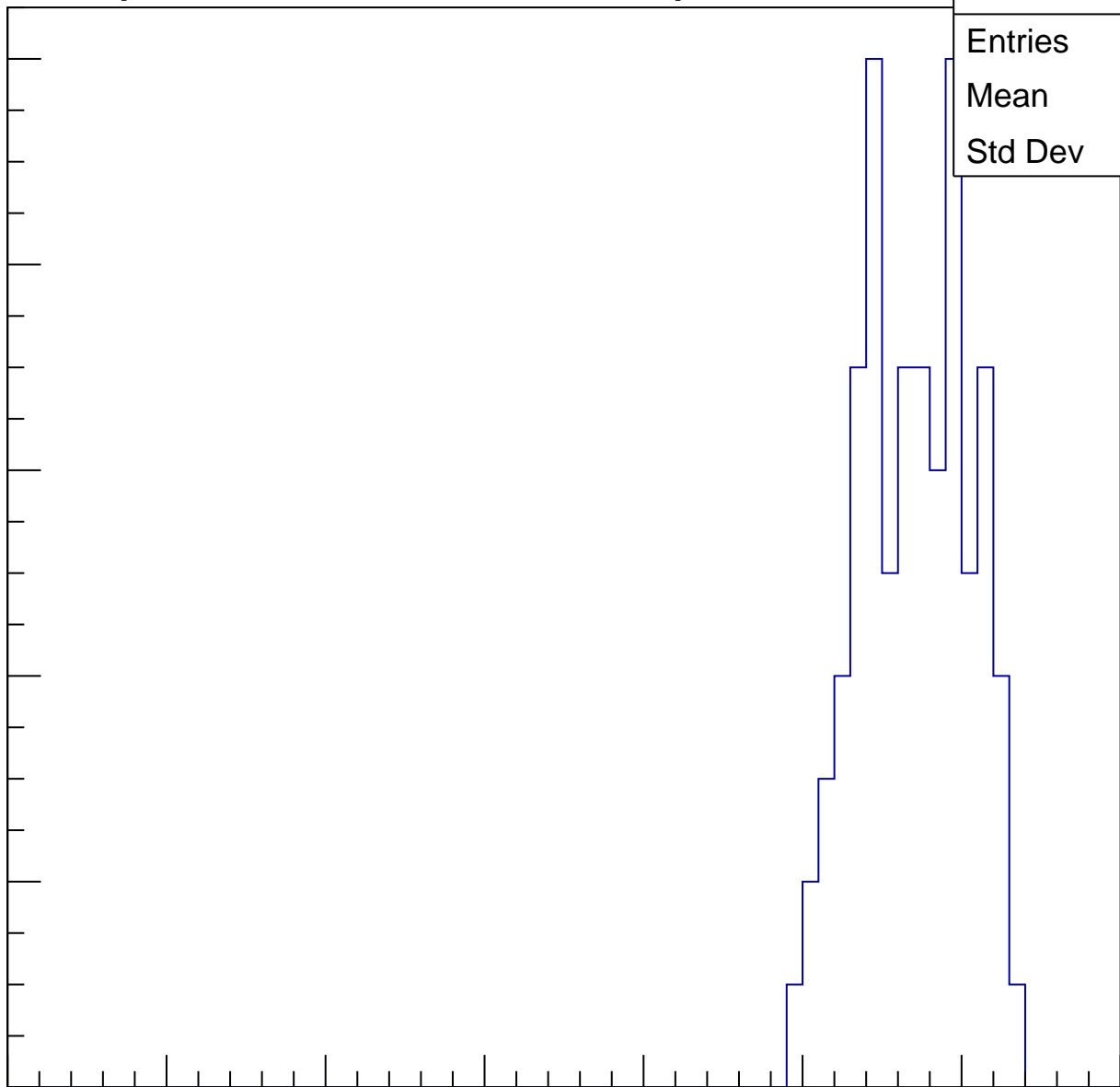
Entries	79
Mean	56.49
Std Dev	3.408

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

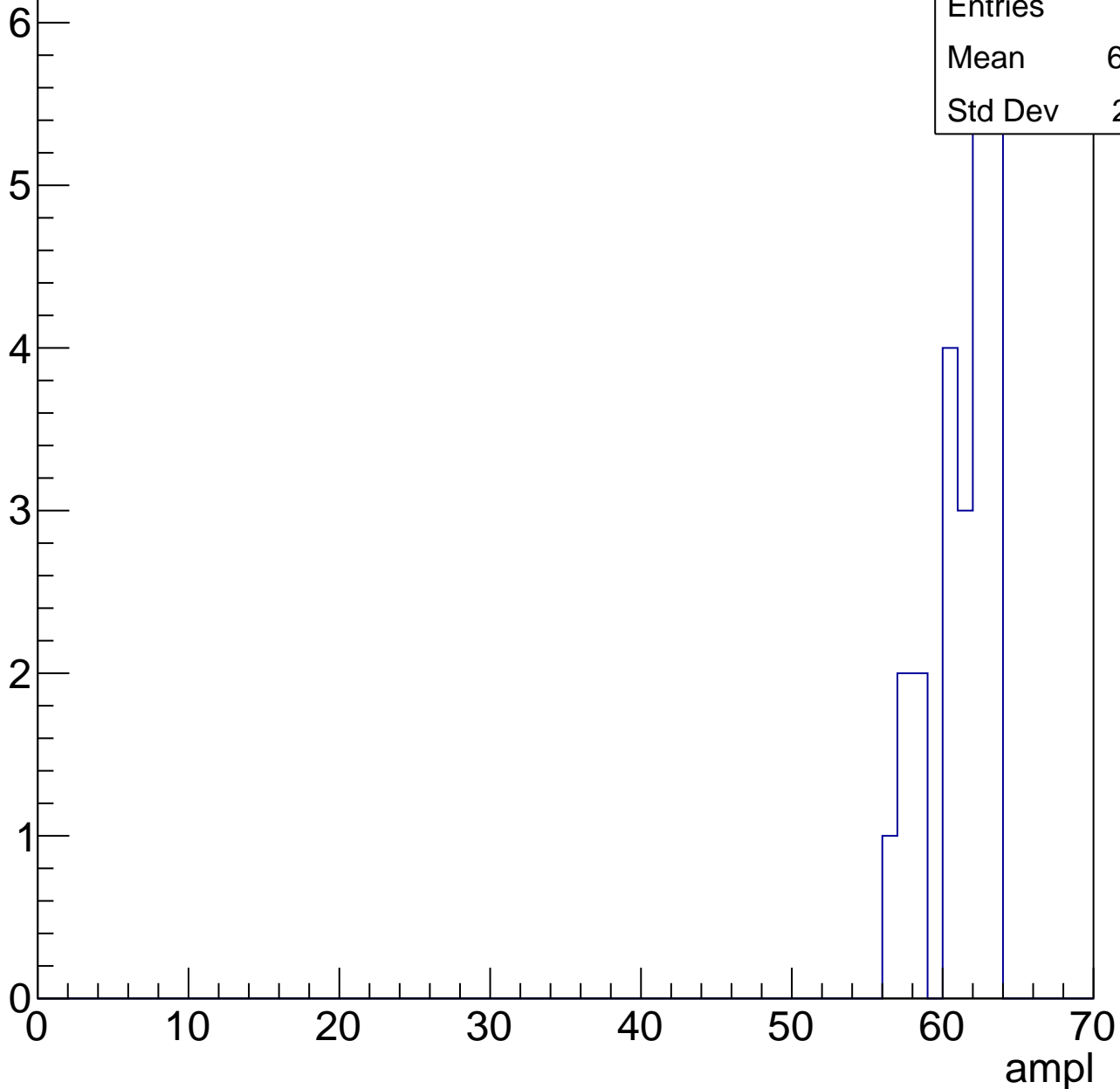


B1L100S, U25-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

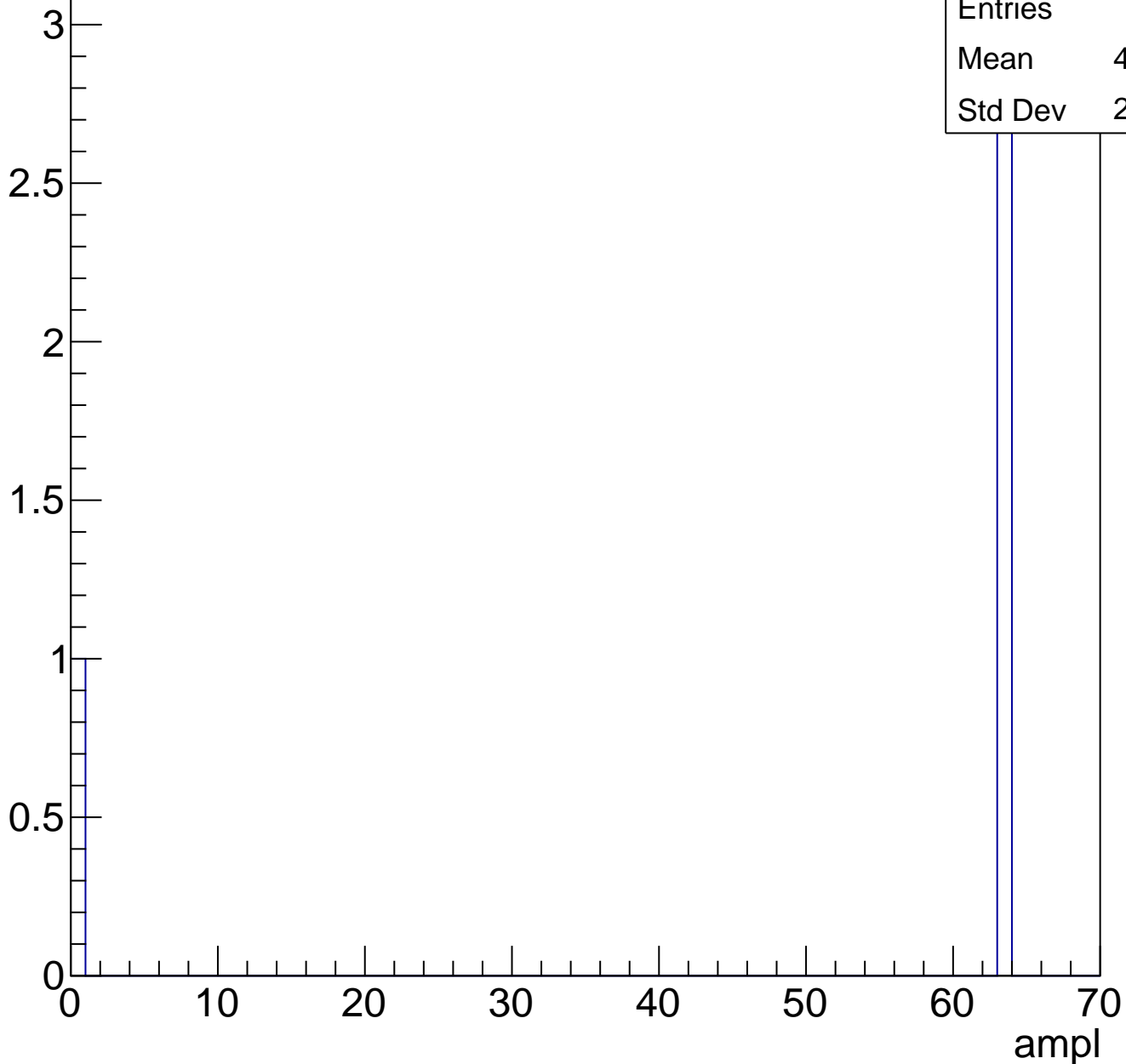
Entries	24
Mean	60.79
Std Dev	2.121



B1L100S, U25-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch67, adc0

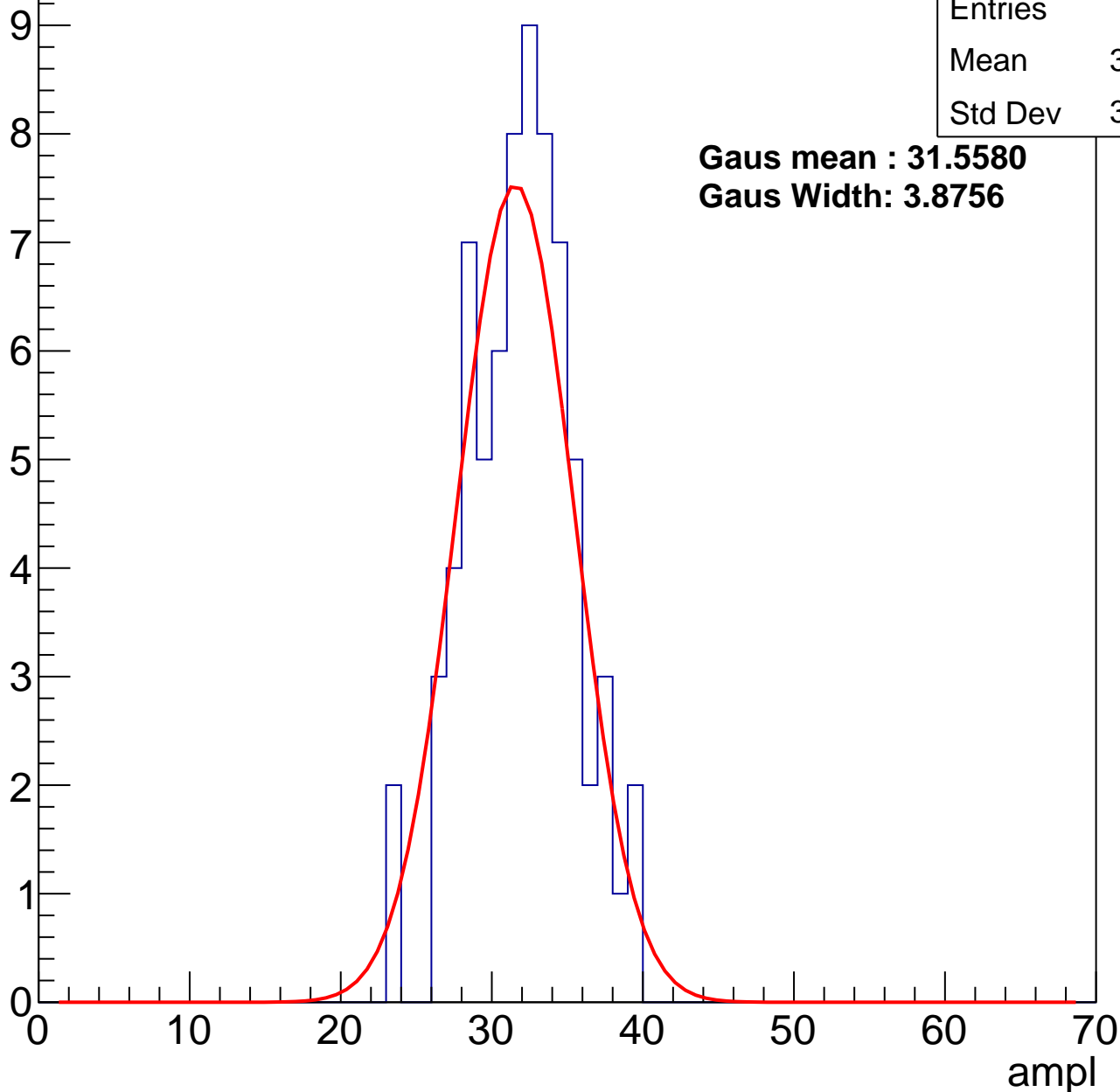
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	31.46
Std Dev	3.456

Gaus mean : 31.5580

Gaus Width: 3.8756



B1L100S, U25-ch67, adc1

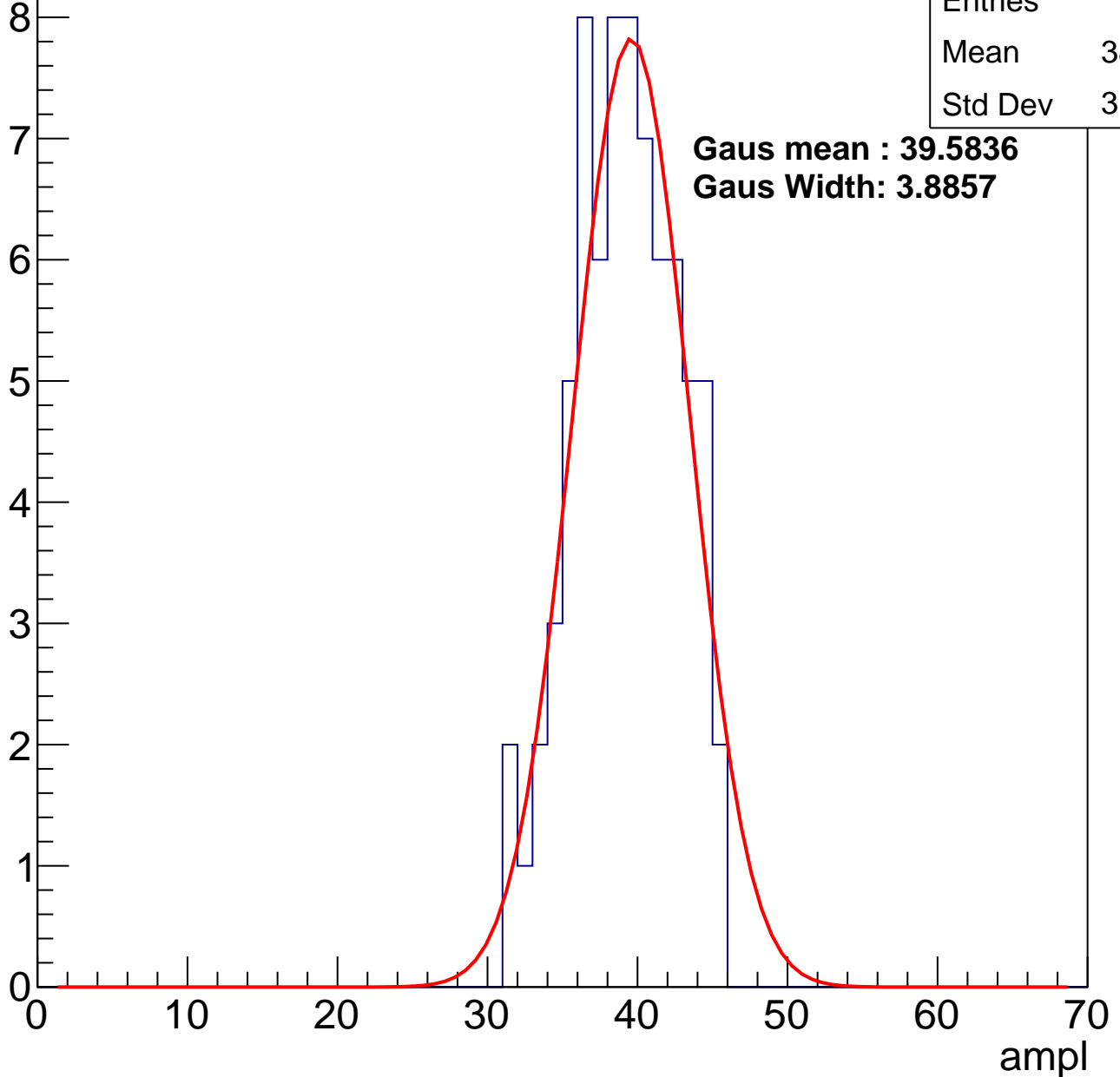
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	38.73
Std Dev	3.418

Gaus mean : 39.5836

Gaus Width: 3.8857



B1L100S, U25-ch67, adc2

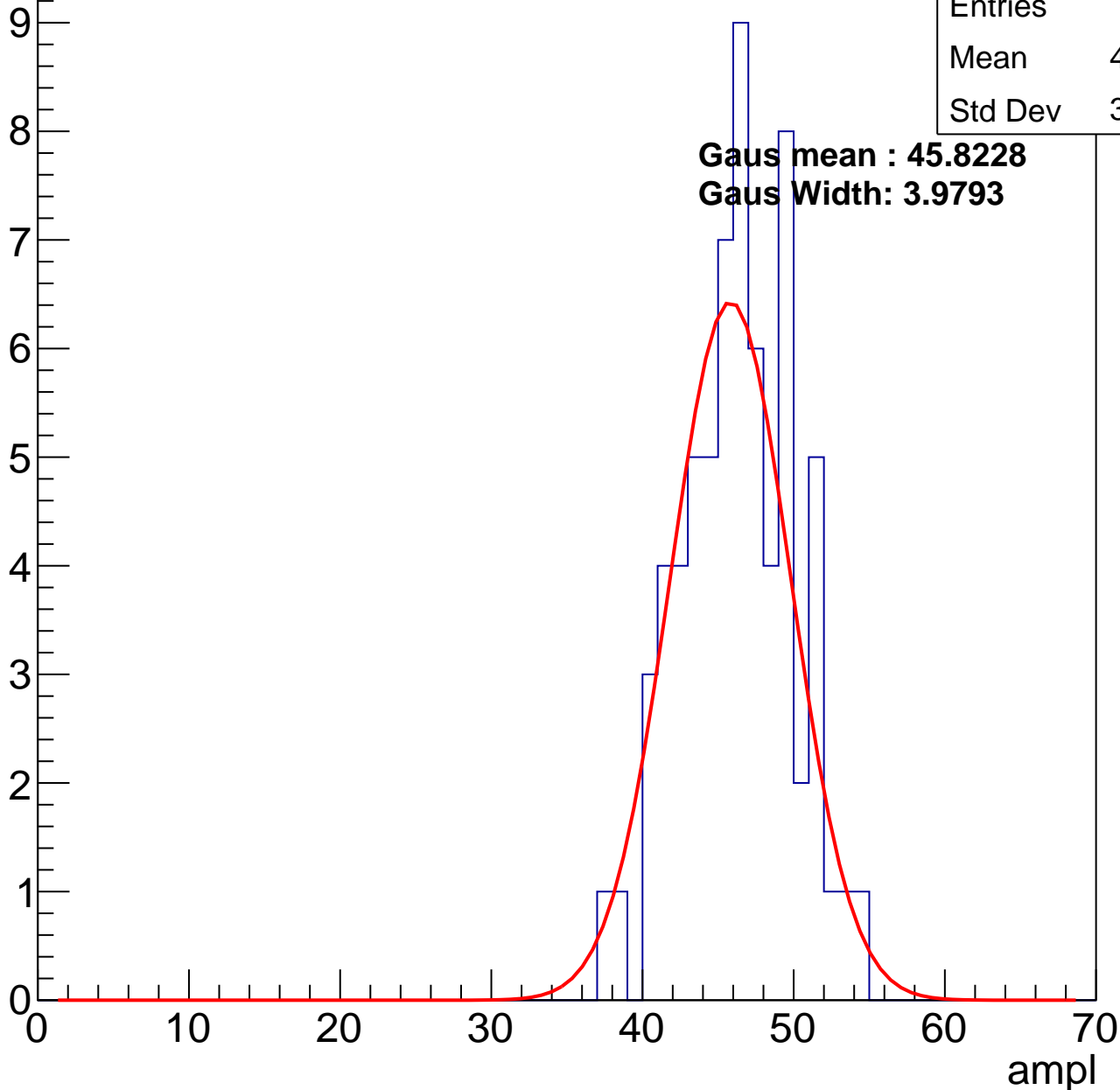
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	45.84
Std Dev	3.639

Gaus mean : 45.8228

Gaus Width: 3.9793

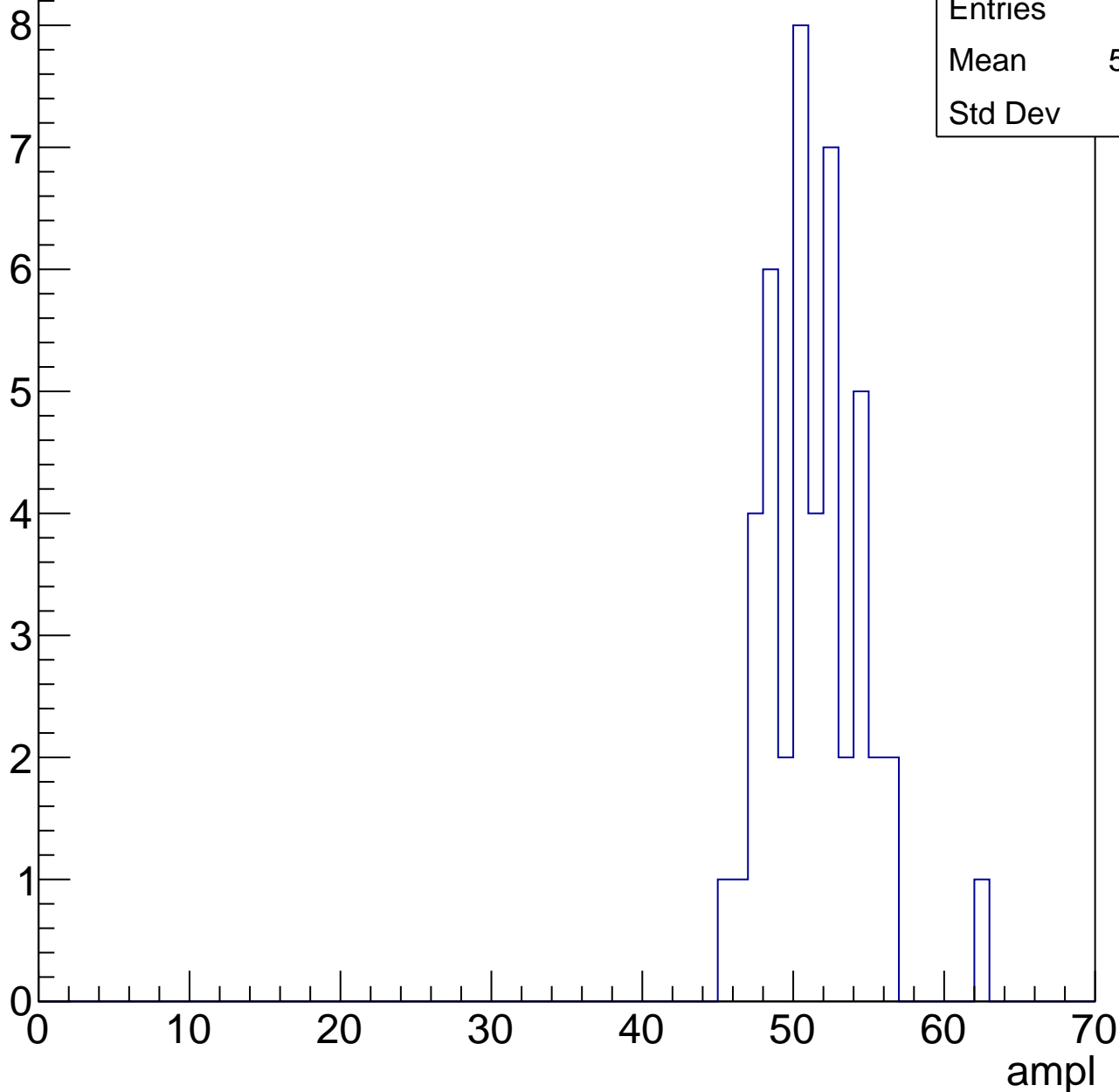


B1L100S, U25-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	50.96
Std Dev	3.19

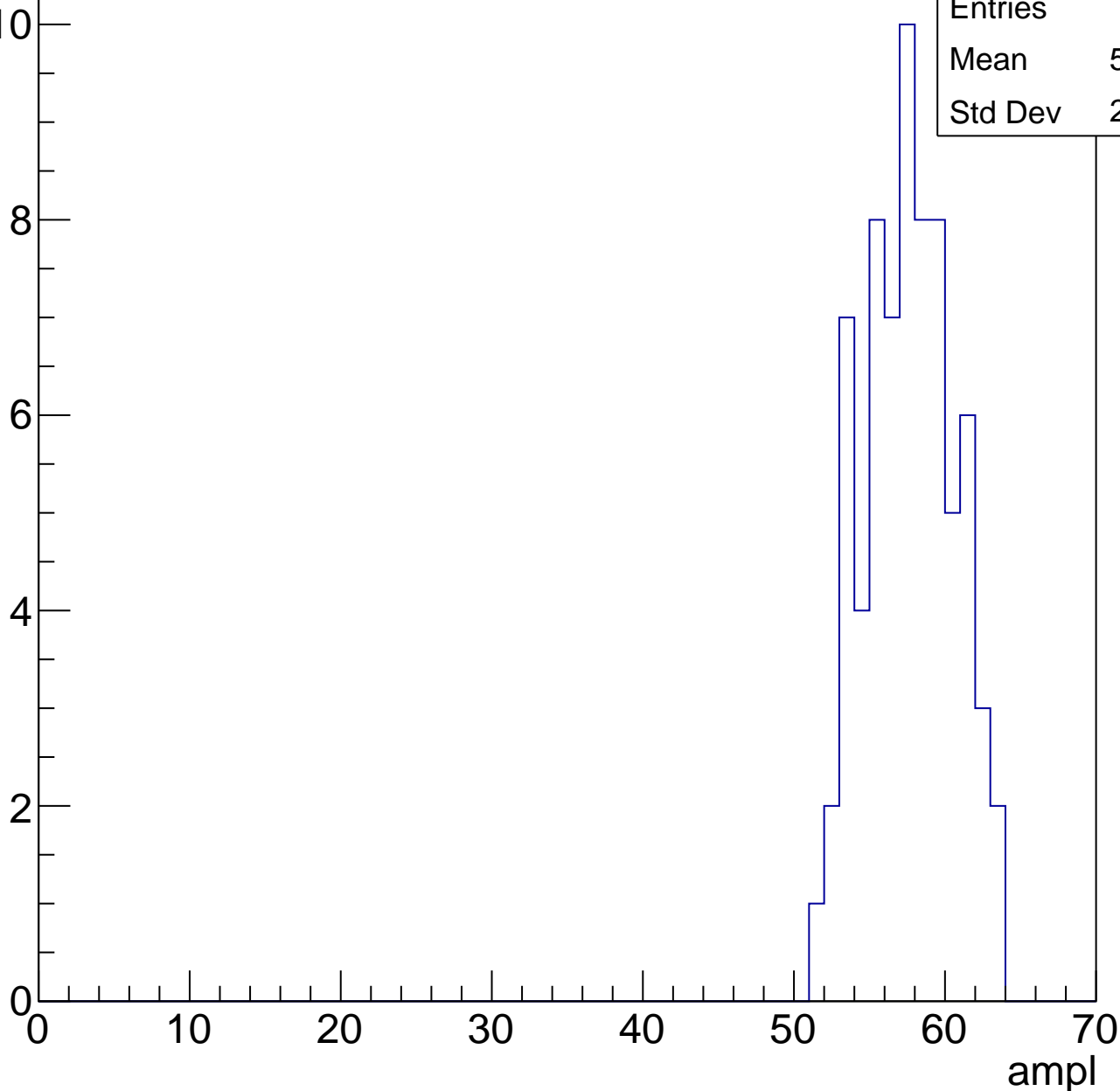


B1L100S, U25-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	57.15
Std Dev	2.905



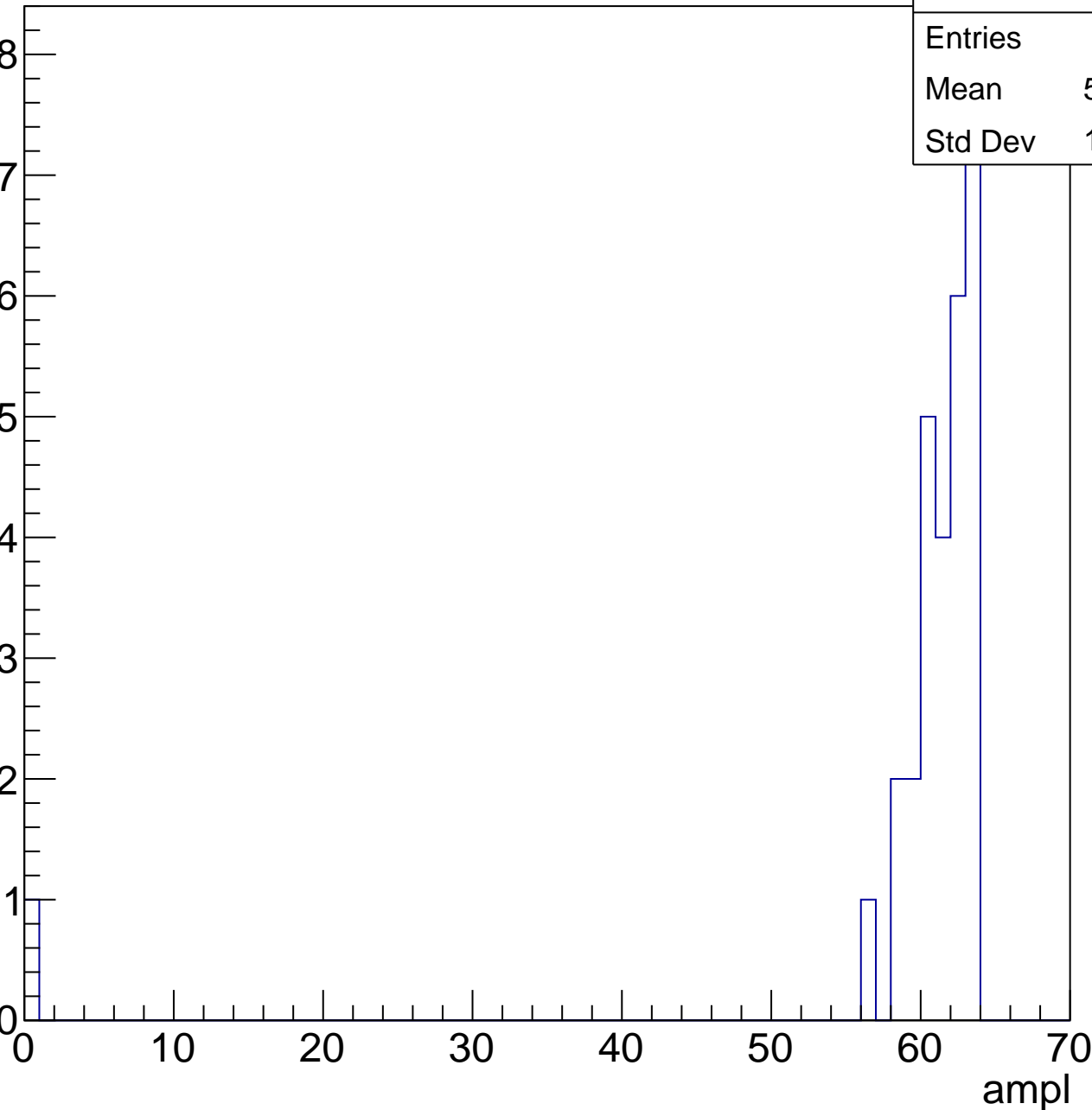
B1L100S, U25-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.97
Std Dev	11.29

8
7
6
5
4
3
2
1
0



B1L100S, U25-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U25-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch68, adc0

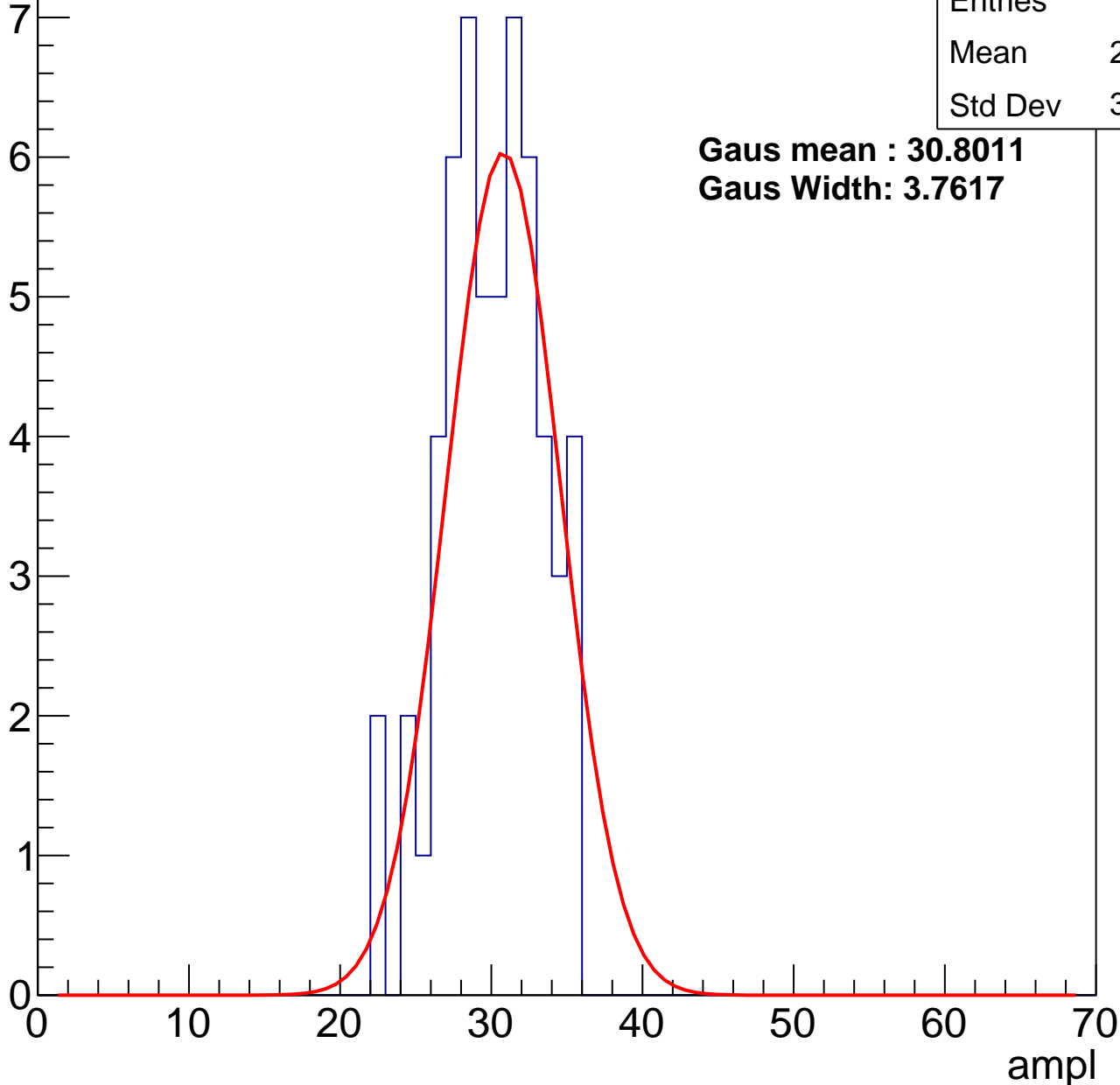
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.59
Std Dev	3.223

Gaus mean : 30.8011

Gaus Width: 3.7617

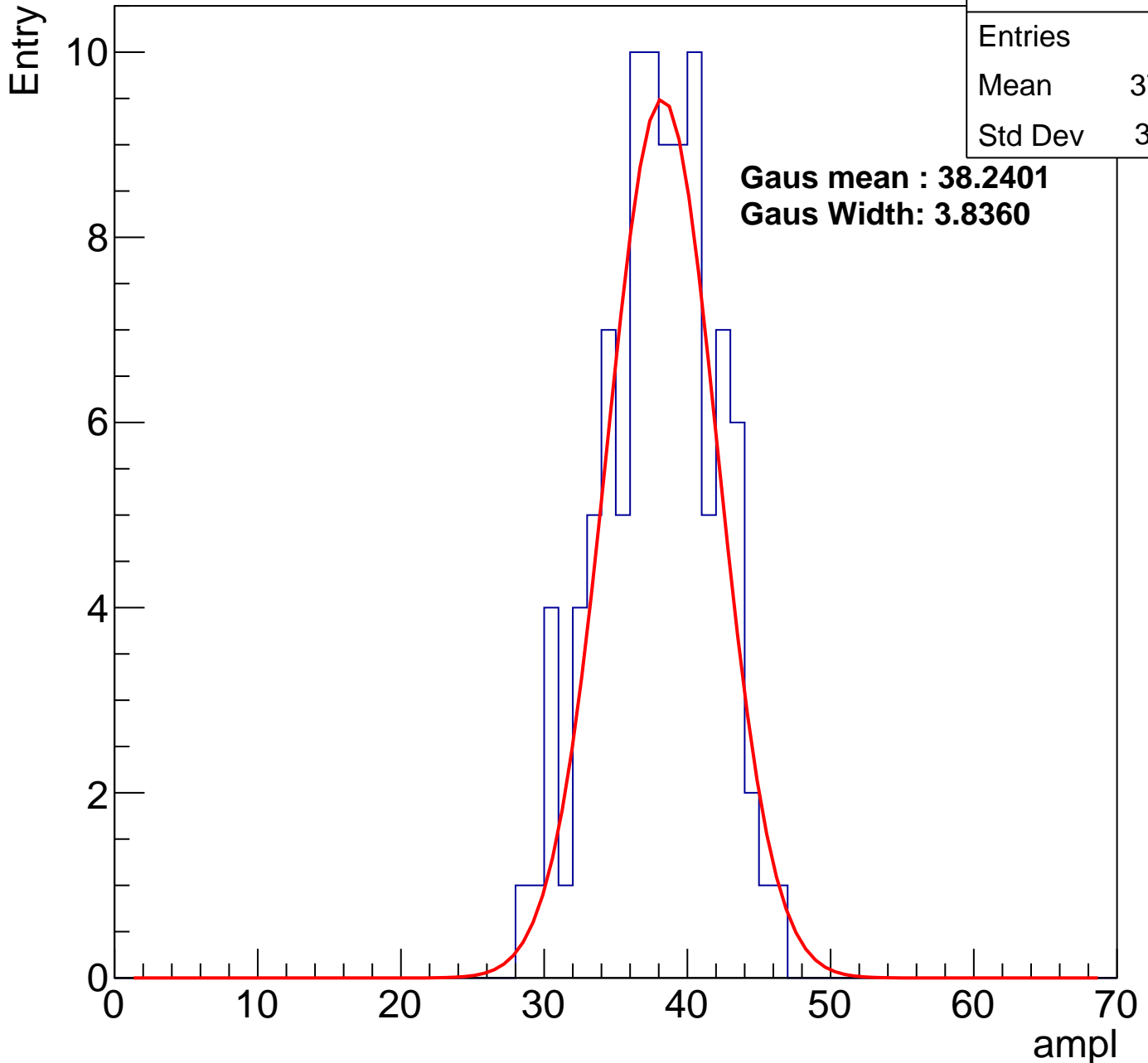


B1L100S, U25-ch68, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	98
Mean	37.48
Std Dev	3.881

Gaus mean : 38.2401
Gaus Width: 3.8360



B1L100S, U25-ch68, adc2

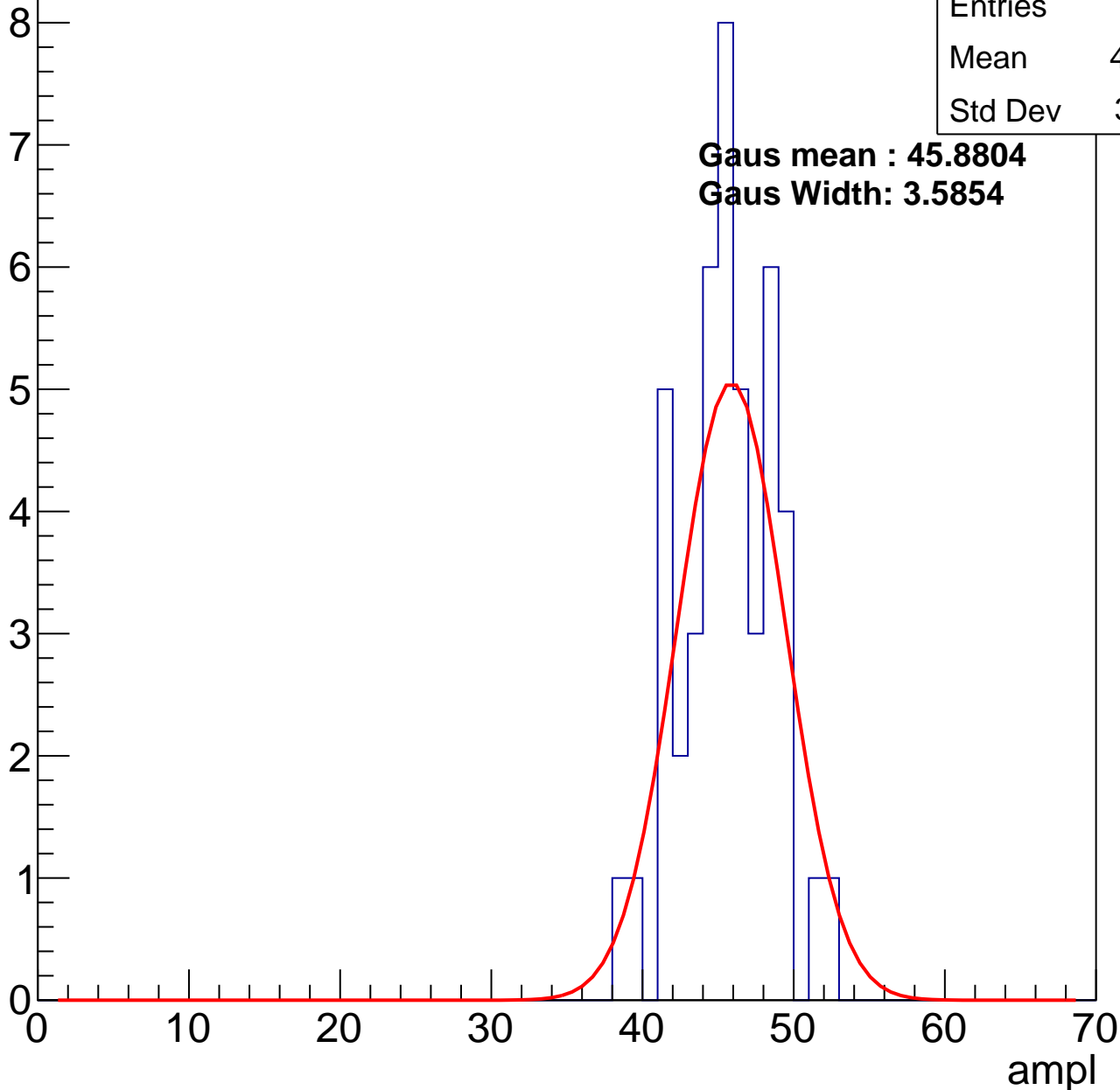
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	45.15
Std Dev	3.021

Gaus mean : 45.8804

Gaus Width: 3.5854

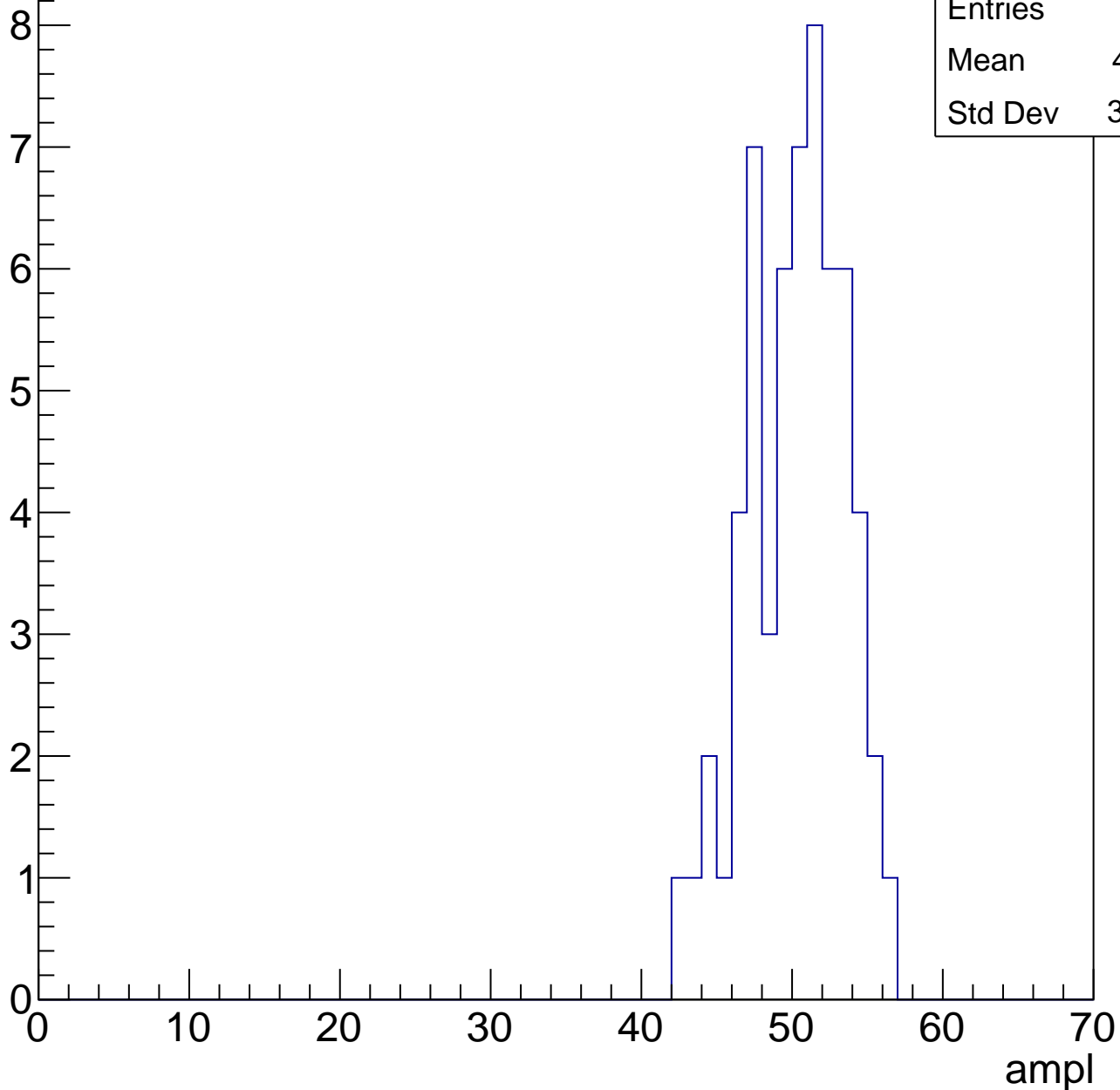


B1L100S, U25-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.81
Std Dev	3.159

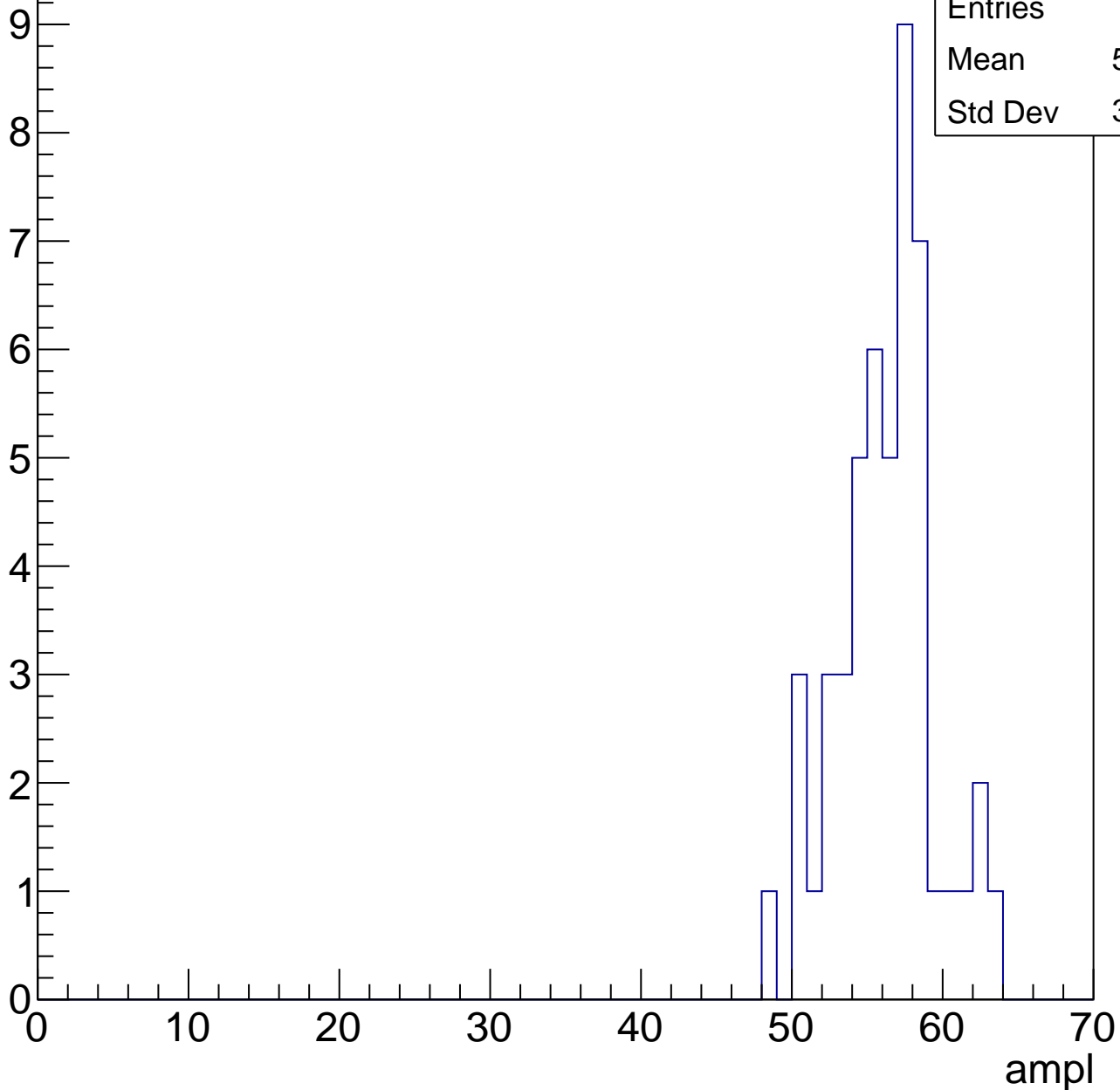


B1L100S, U25-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

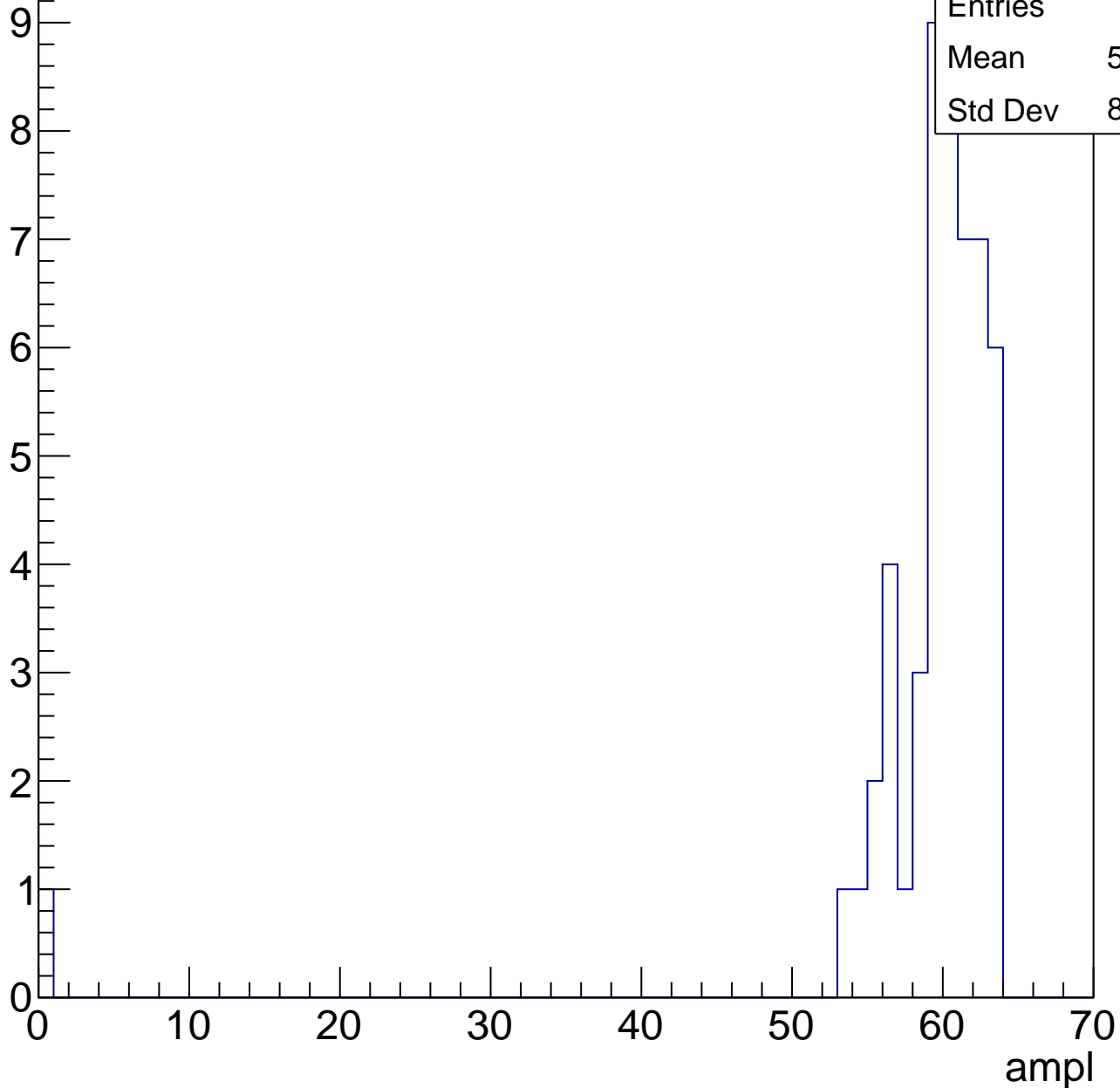
Entries	49
Mean	55.71
Std Dev	3.201



B1L100S, U25-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch69, adc0

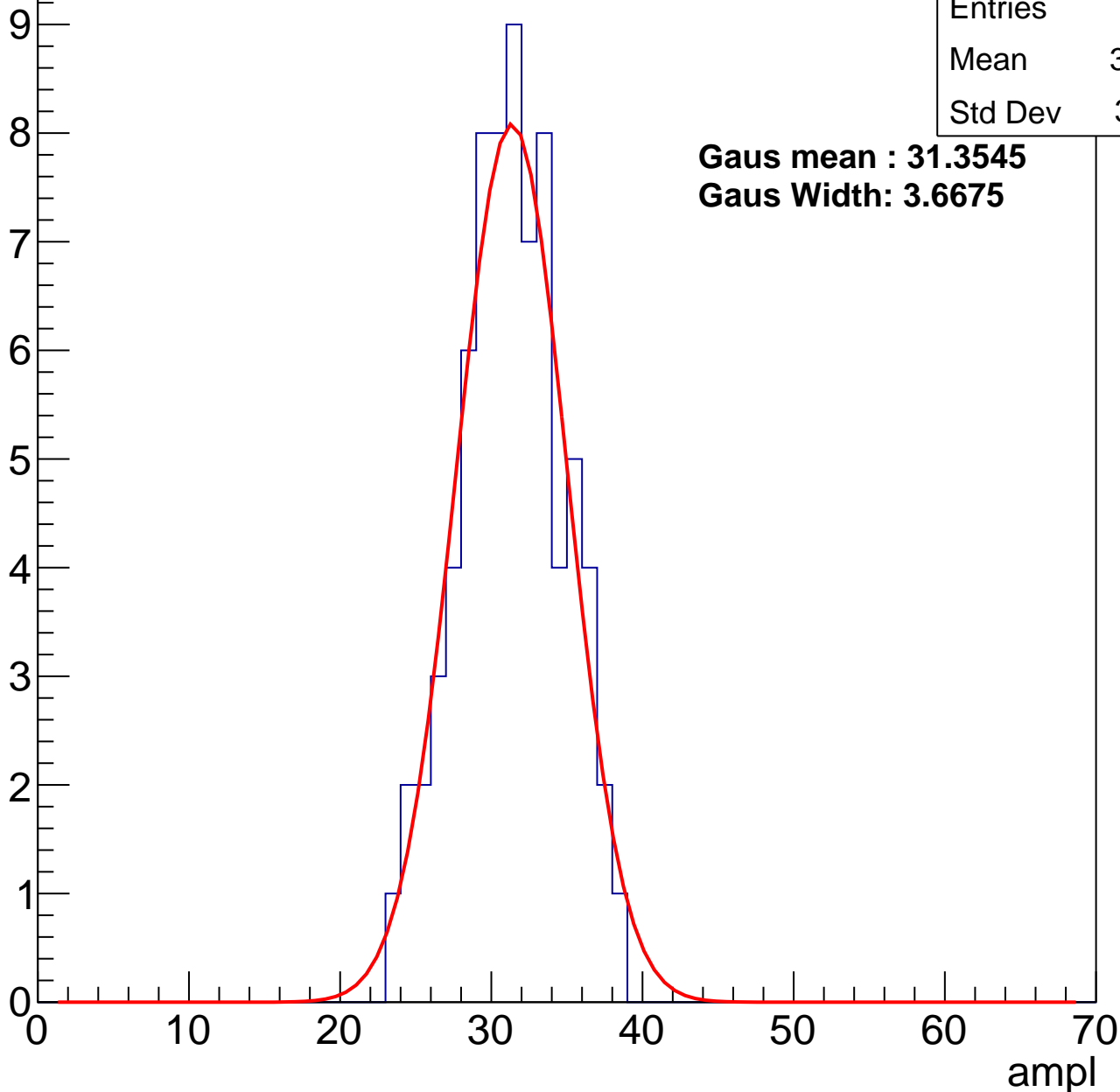
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.82
Std Dev	3.371

Gaus mean : 31.3545

Gaus Width: 3.6675



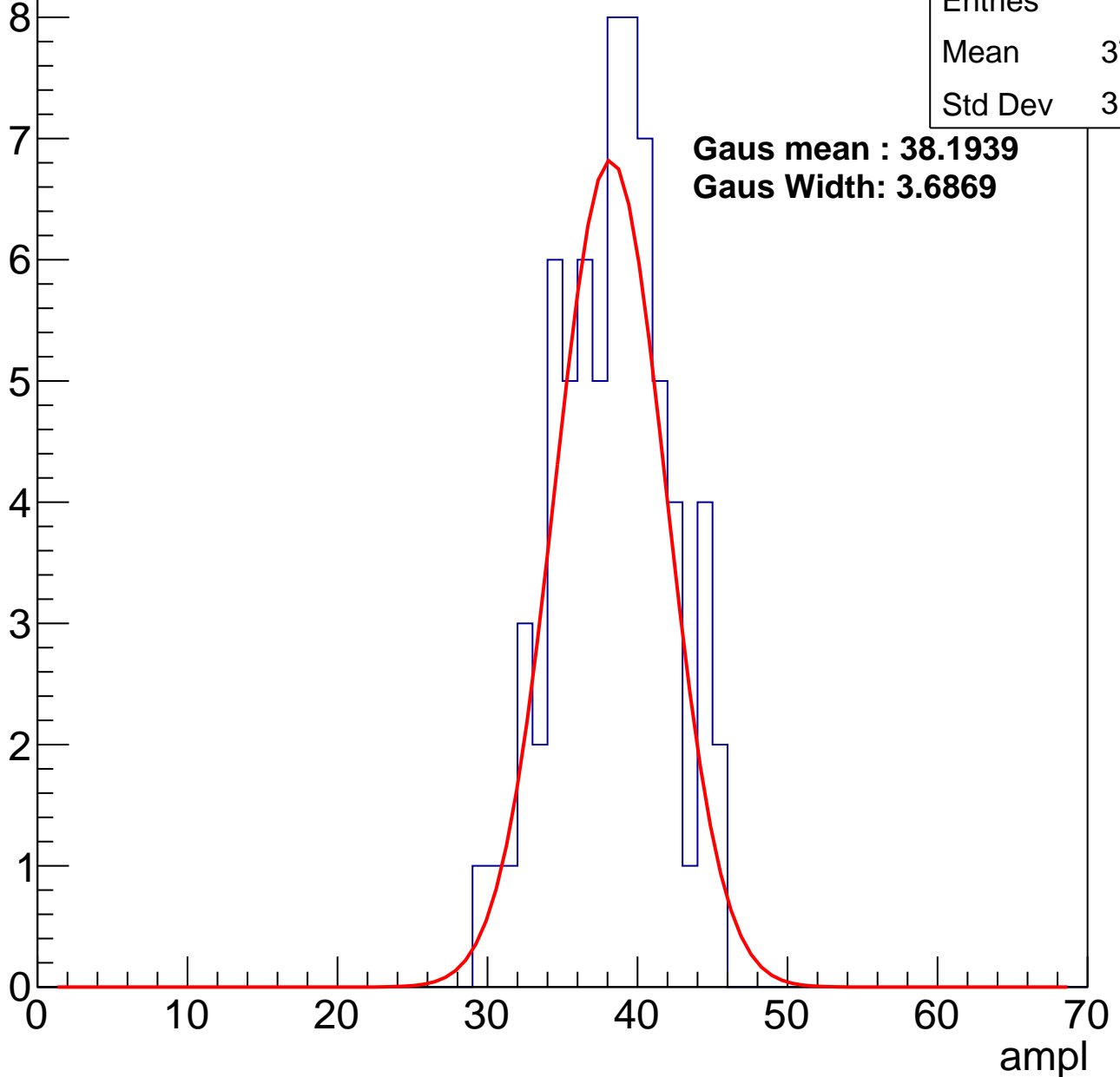
B1L100S, U25-ch69, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.83
Std Dev	3.675

Gaus mean : 38.1939
Gaus Width: 3.6869



B1L100S, U25-ch69, adc2

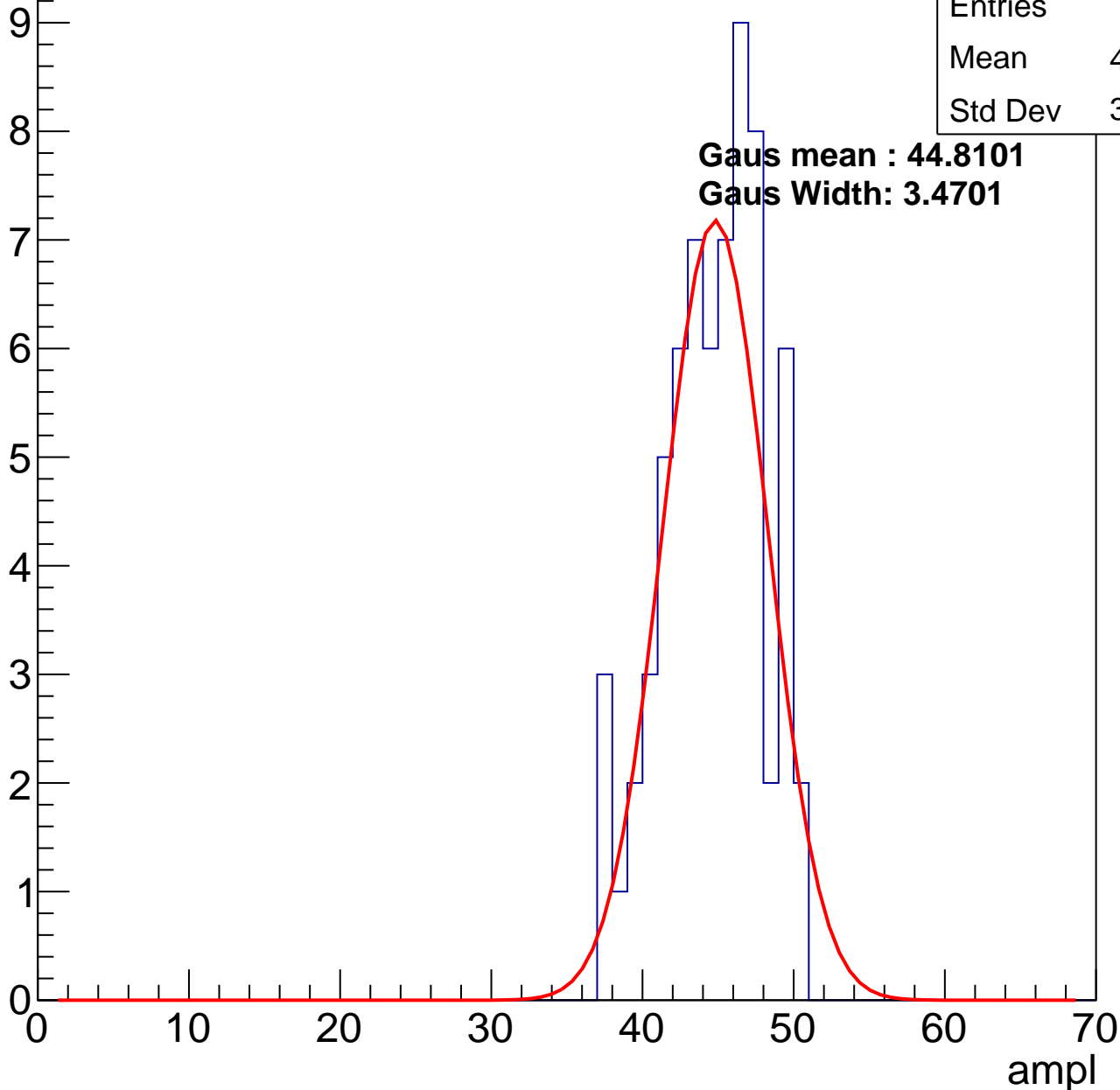
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.24
Std Dev	3.283

Gaus mean : 44.8101

Gaus Width: 3.4701

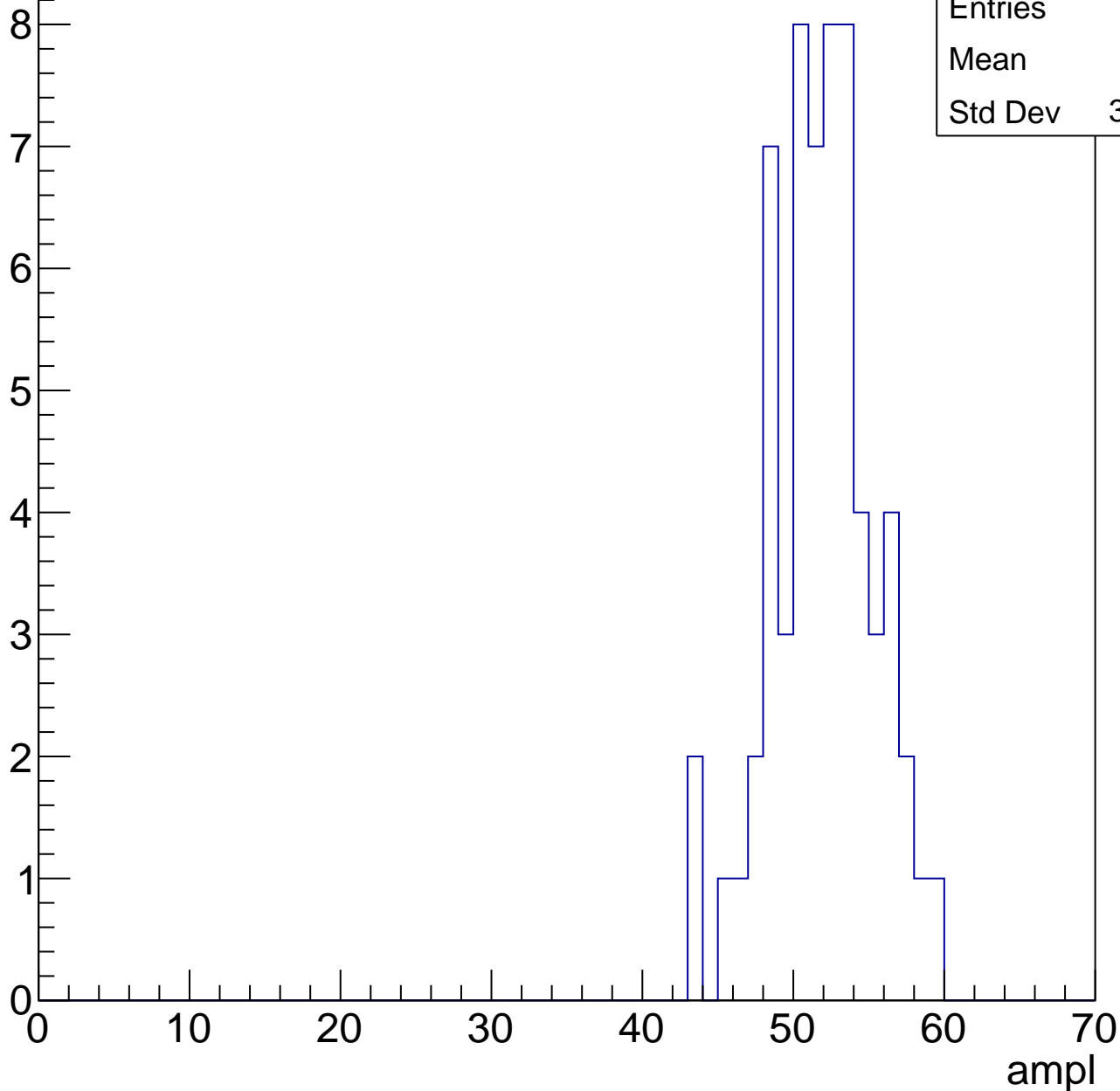


B1L100S, U25-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

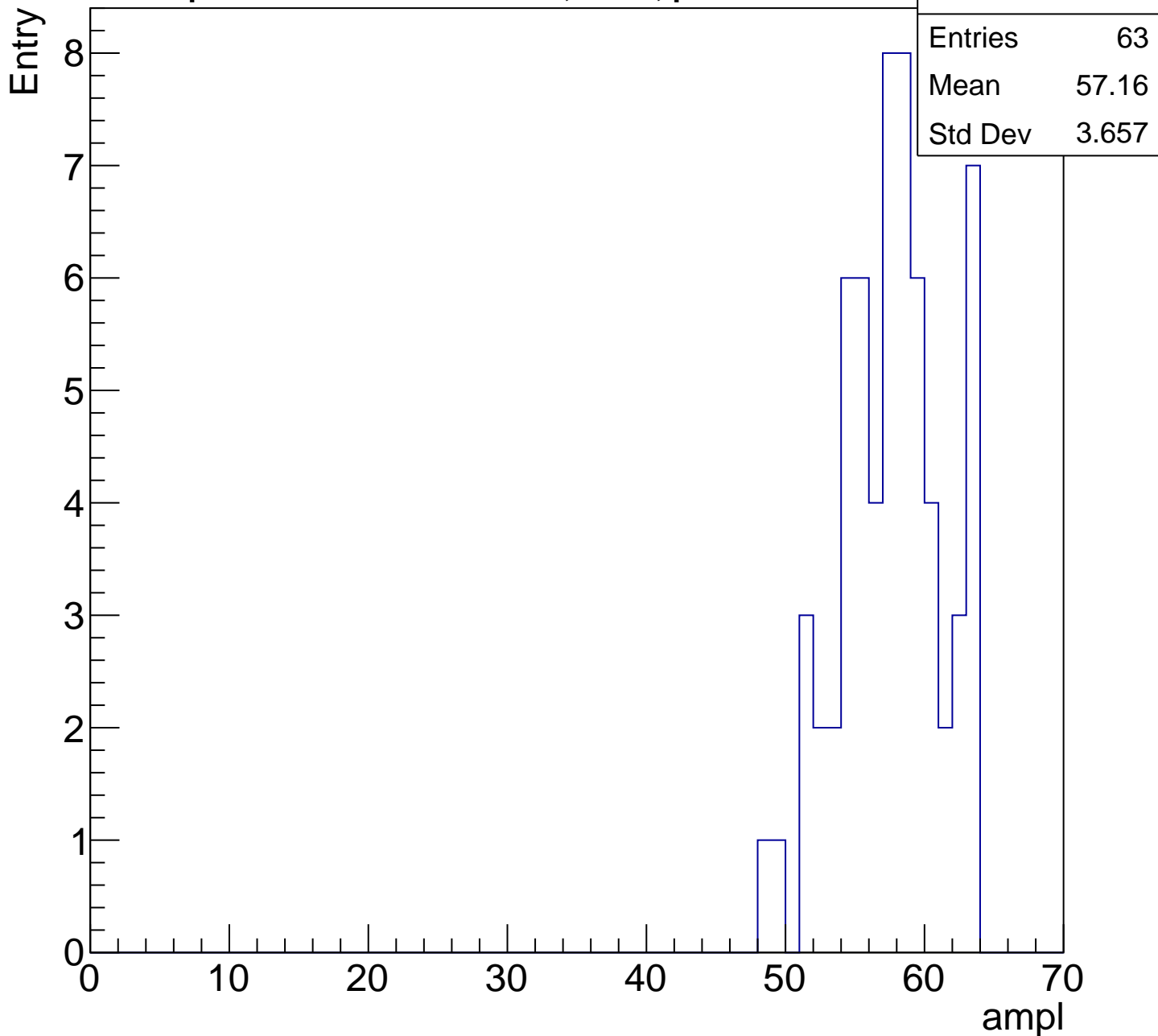
Entry

Entries	62
Mean	51.4
Std Dev	3.367



B1L100S, U25-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	28
Mean	58.14
Std Dev	11.37

ampl

0

10

20

30

40

50

60

70

B1L100S, U25-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

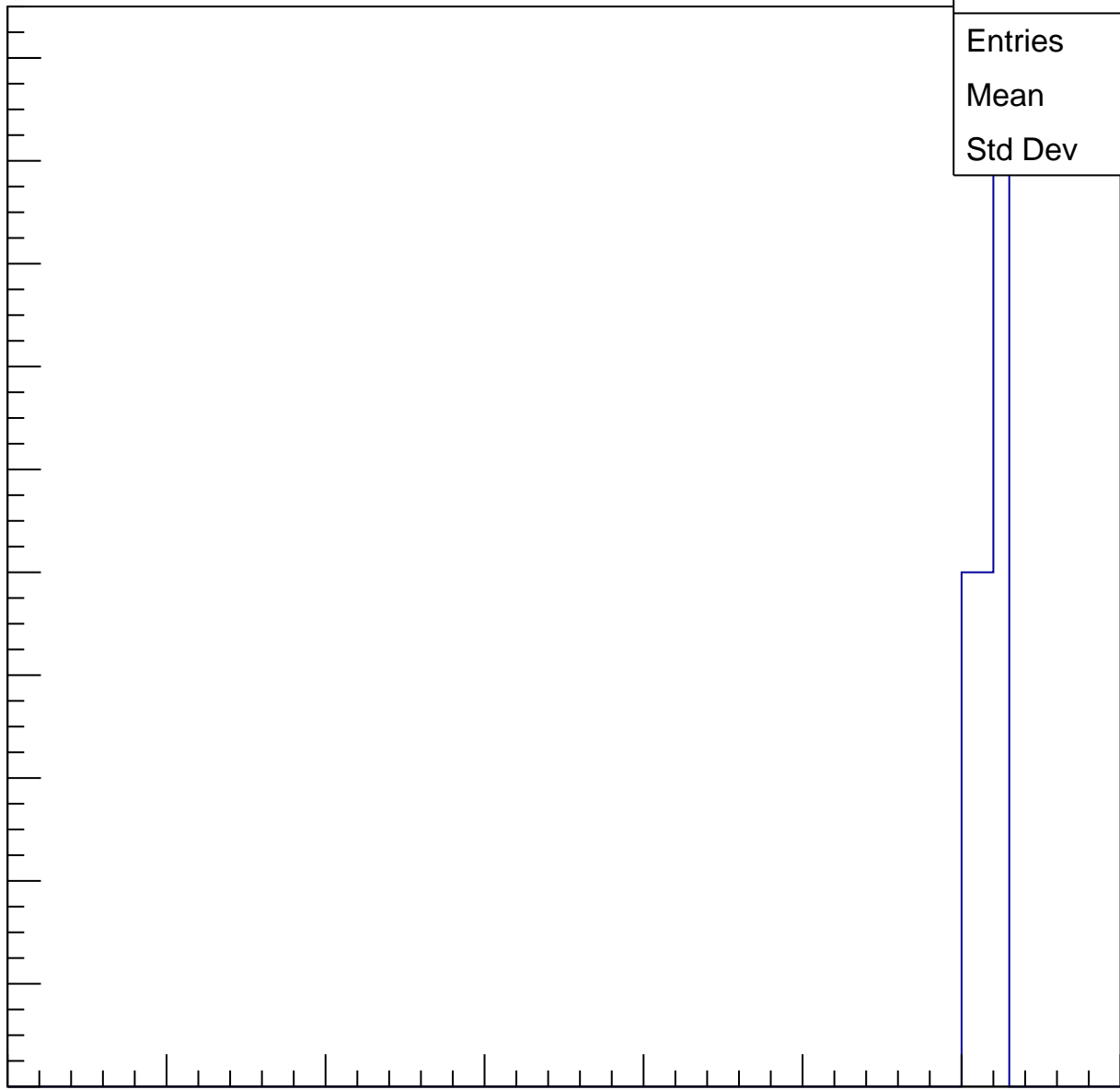
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch70, adc0

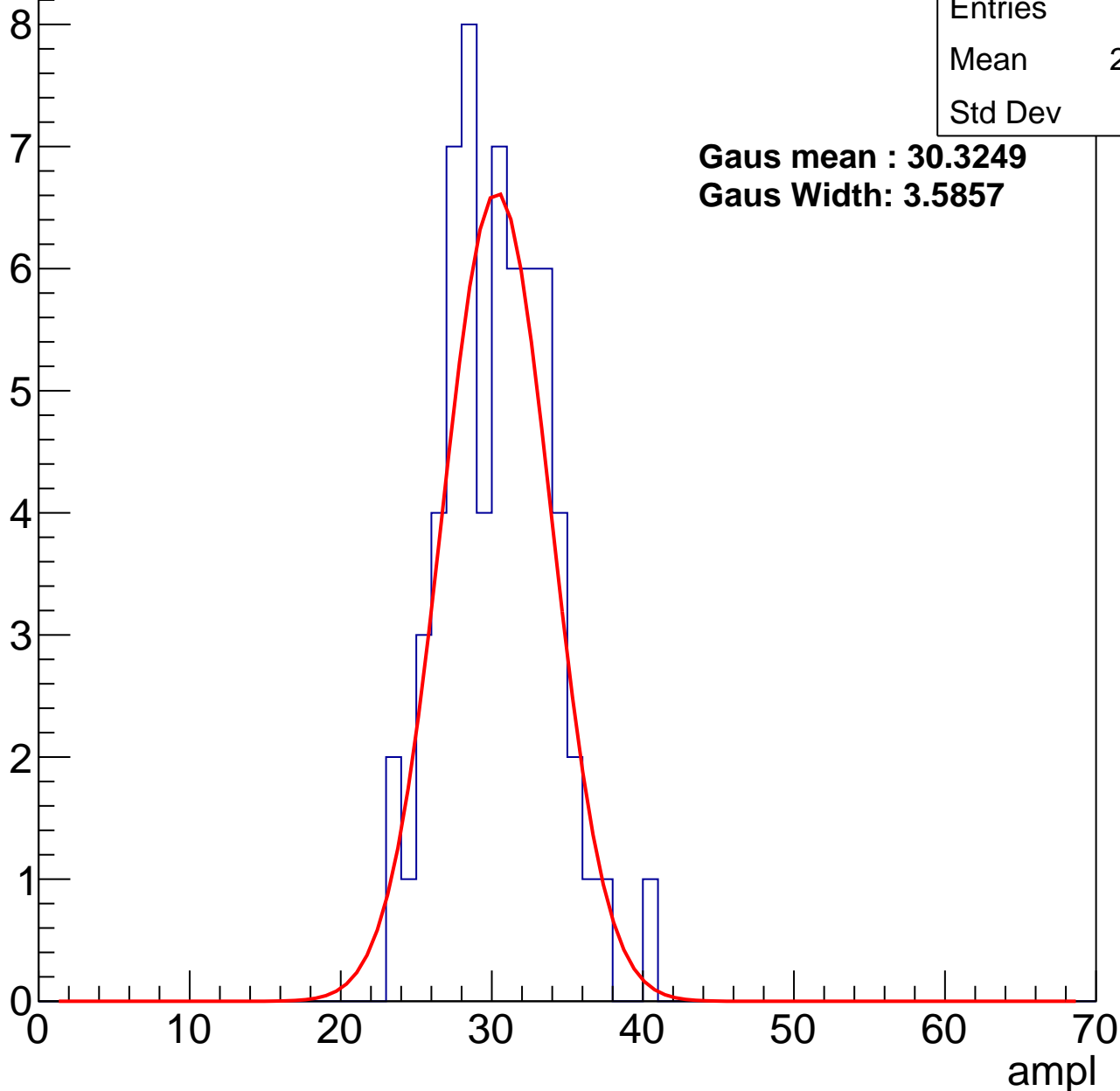
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.89
Std Dev	3.46

Gaus mean : 30.3249

Gaus Width: 3.5857



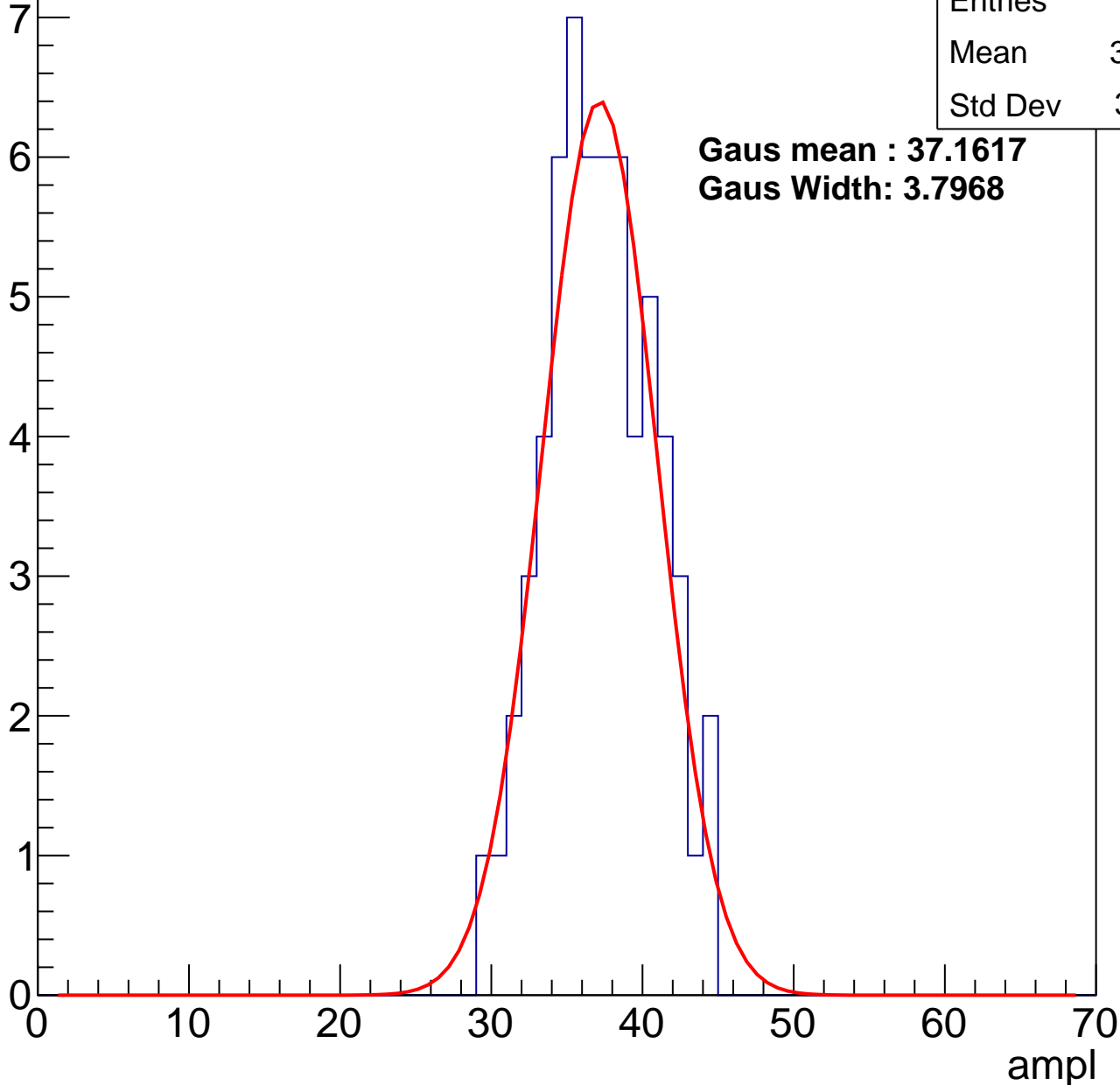
B1L100S, U25-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.74
Std Dev	3.501

Gaus mean : 37.1617
Gaus Width: 3.7968



B1L100S, U25-ch70, adc2

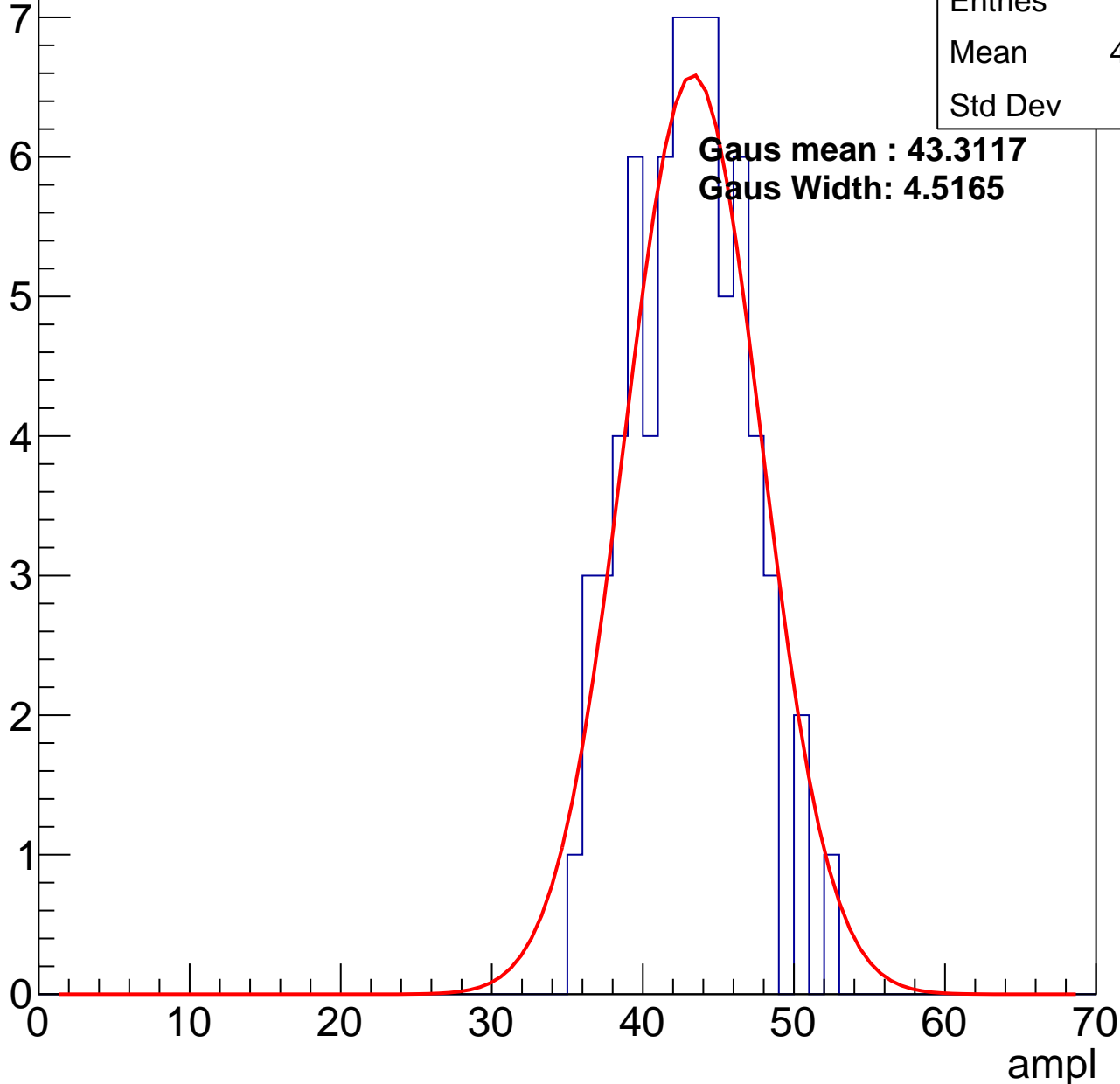
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.52
Std Dev	3.74

Gaus mean : 43.3117

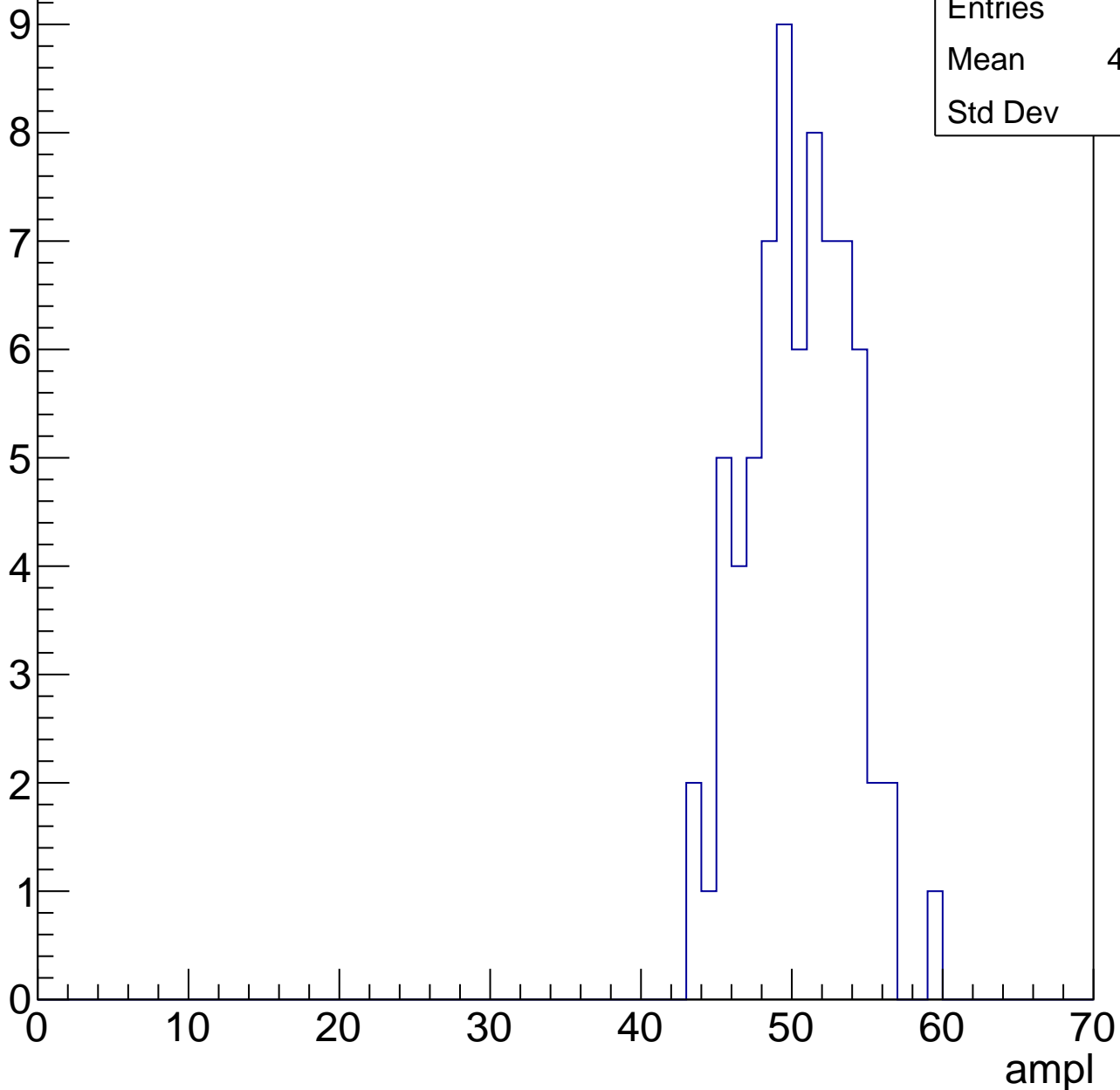
Gaus Width: 4.5165



B1L100S, U25-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

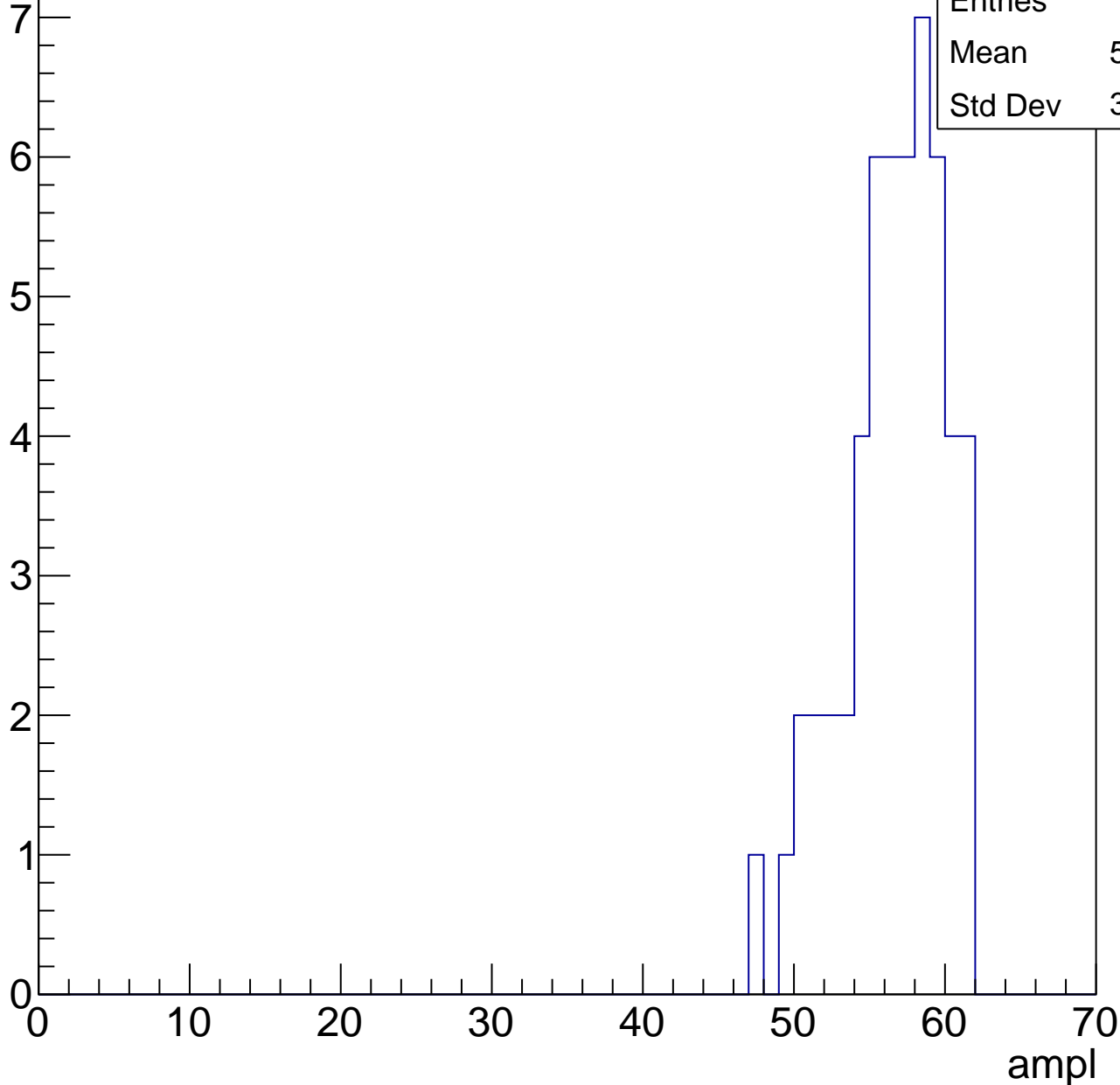
Entry



B1L100S, U25-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



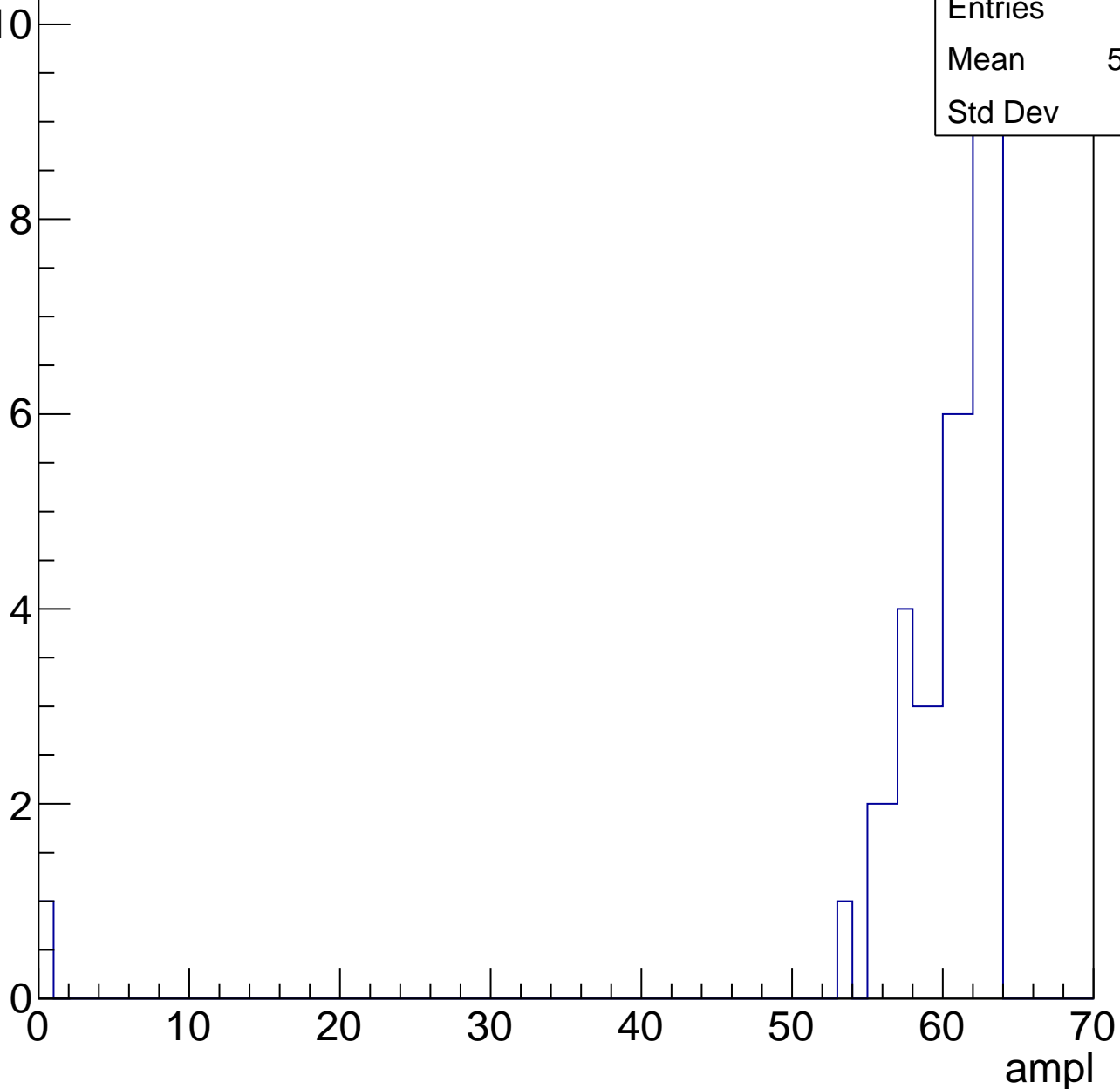
Entries	53
Mean	56.15
Std Dev	3.293

B1L100S, U25-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.87
Std Dev	9.05



B1L100S, U25-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U25-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch71, adc0

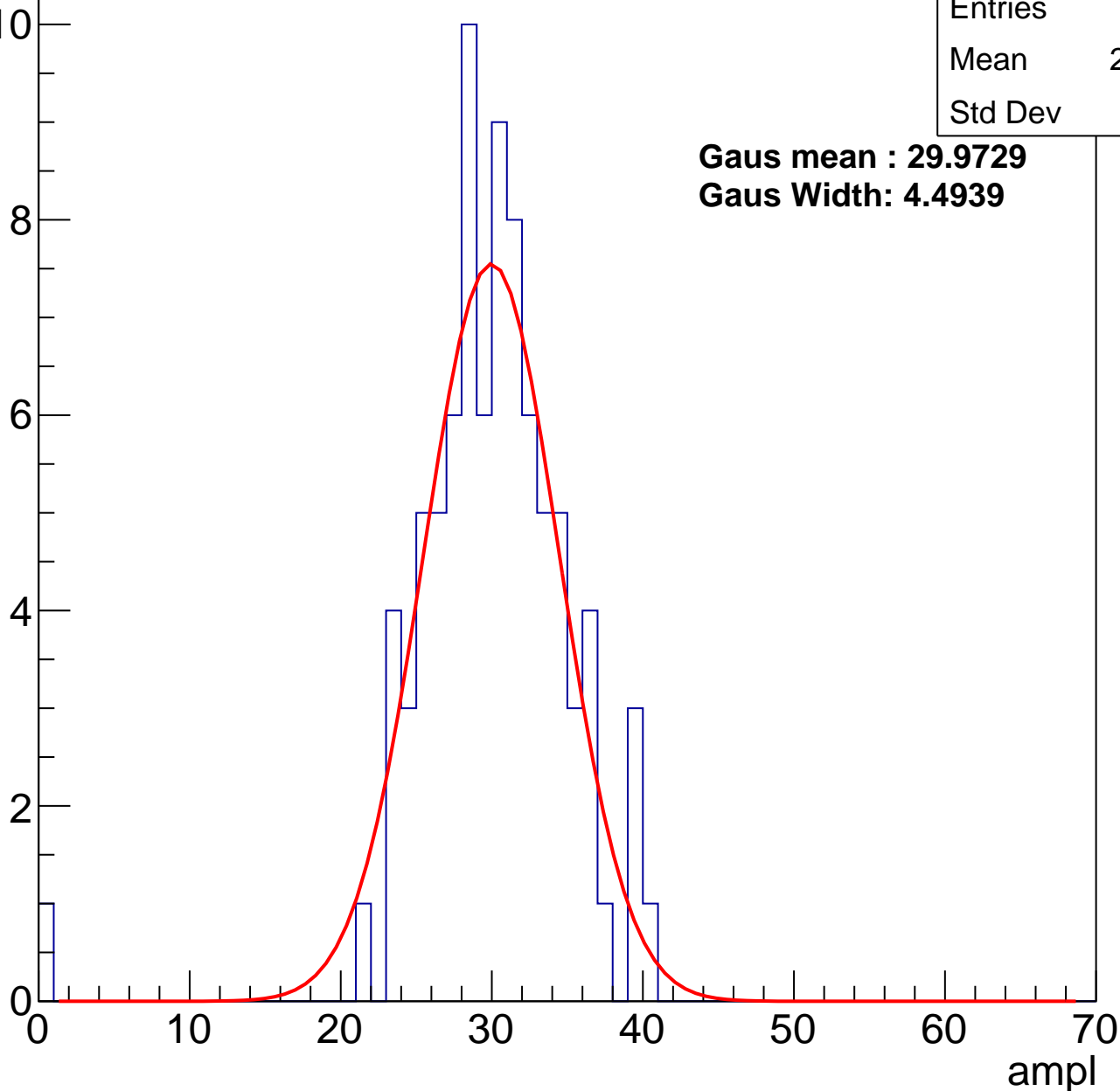
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.58
Std Dev	5.22

Gaus mean : 29.9729

Gaus Width: 4.4939



B1L100S, U25-ch71, adc1

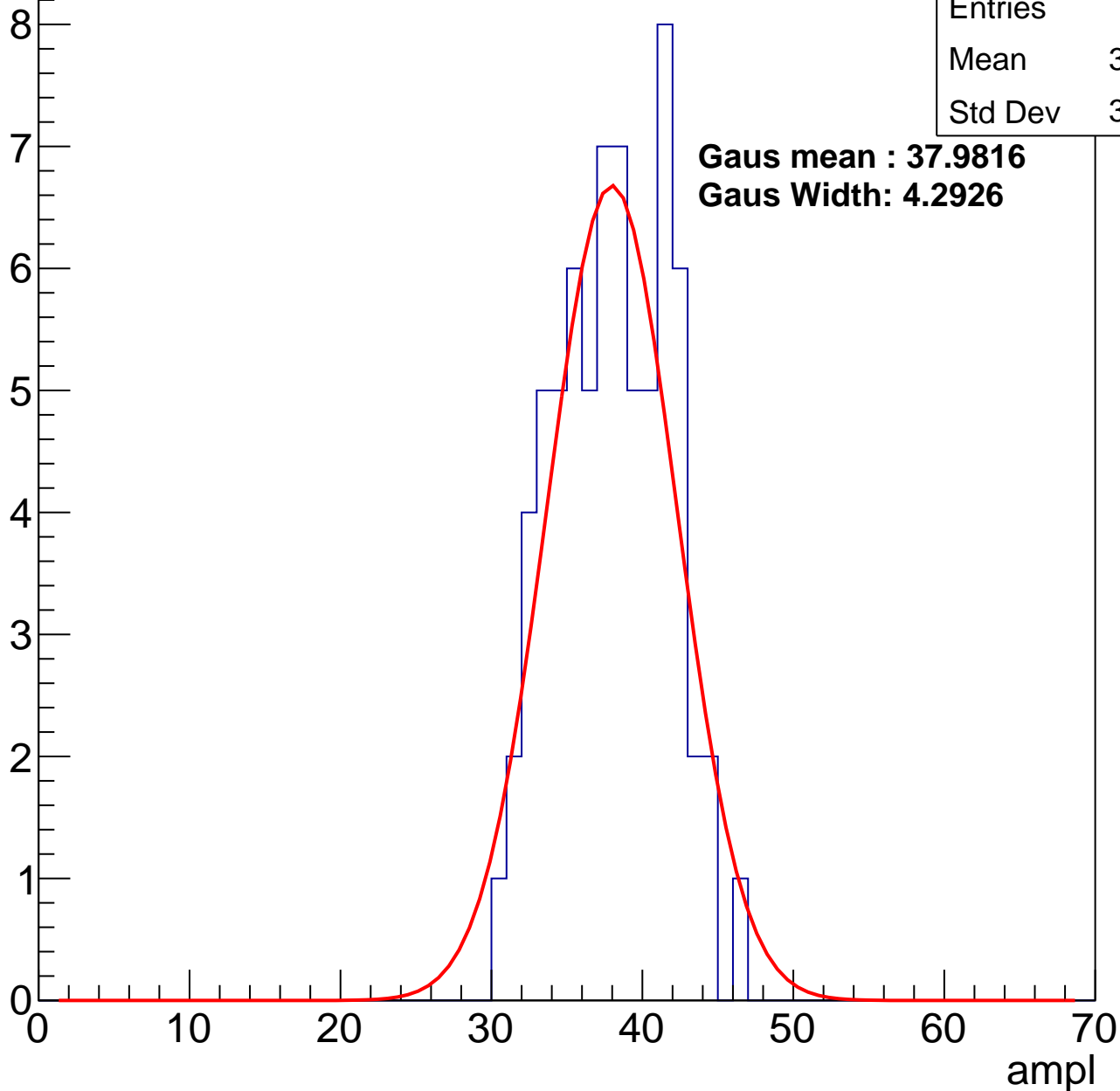
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.54
Std Dev	3.669

Gaus mean : 37.9816

Gaus Width: 4.2926



B1L100S, U25-ch71, adc2

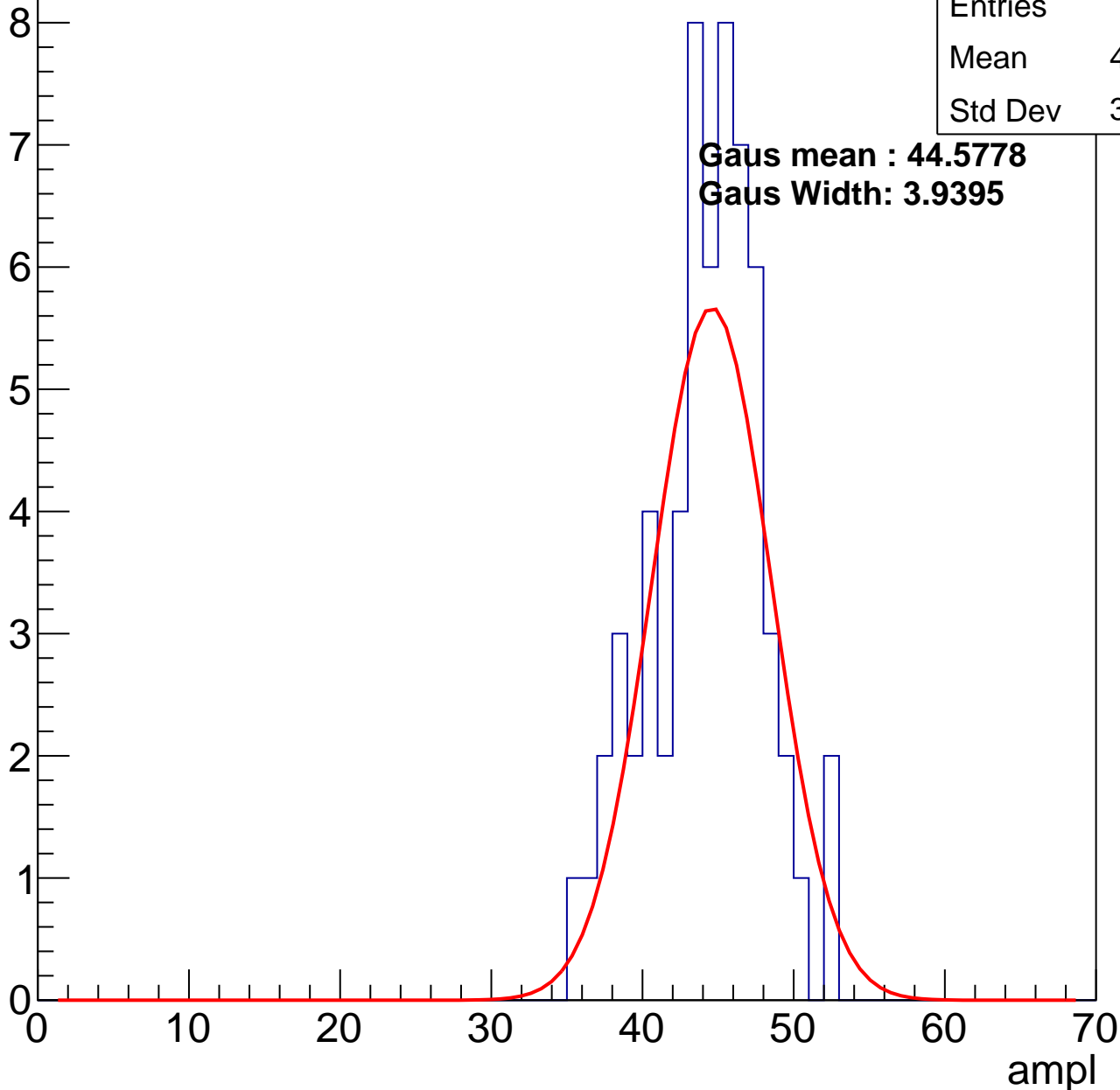
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.79
Std Dev	3.716

Gaus mean : 44.5778

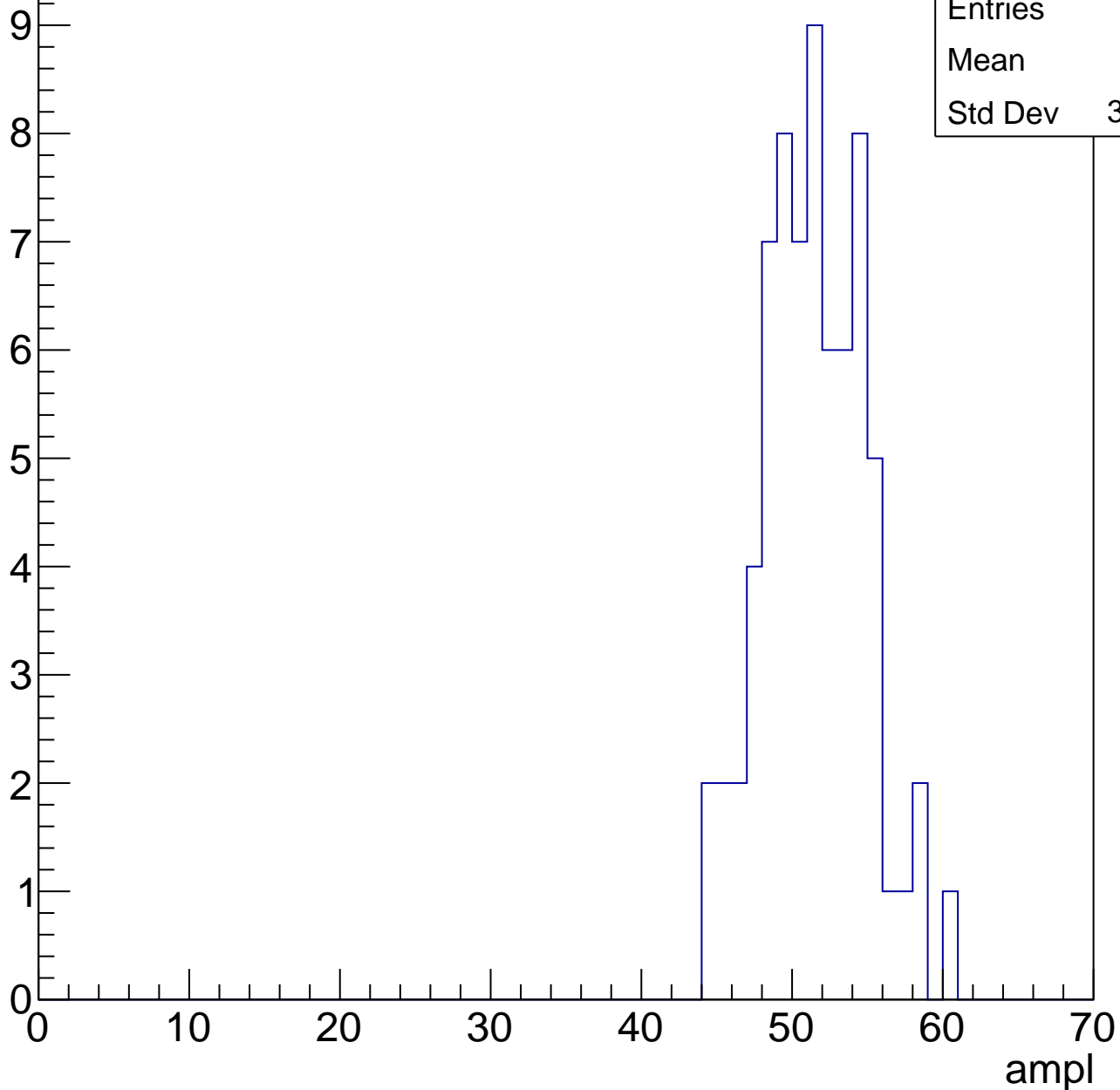
Gaus Width: 3.9395



B1L100S, U25-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

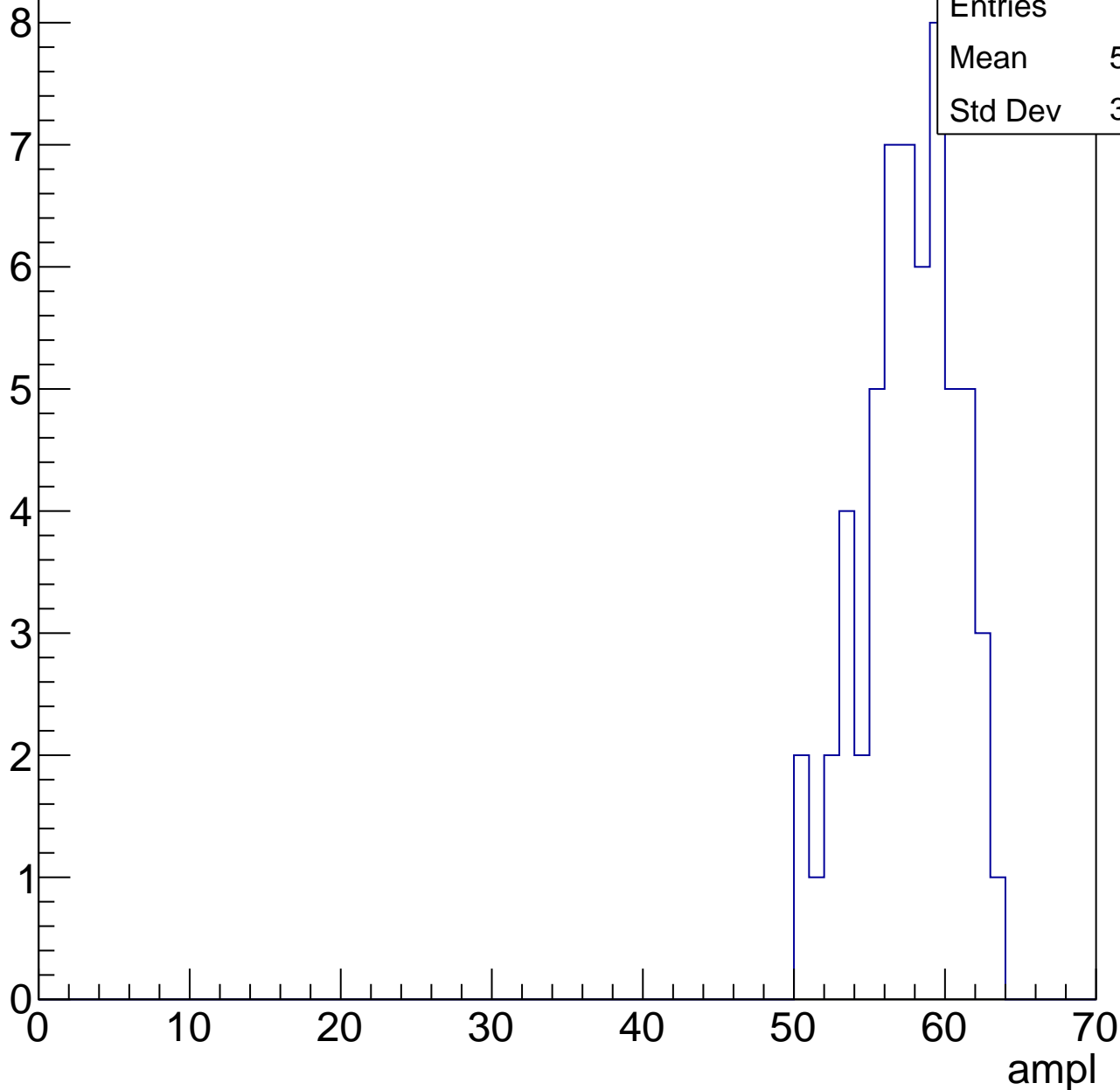


B1L100S, U25-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	57.16
Std Dev	3.123

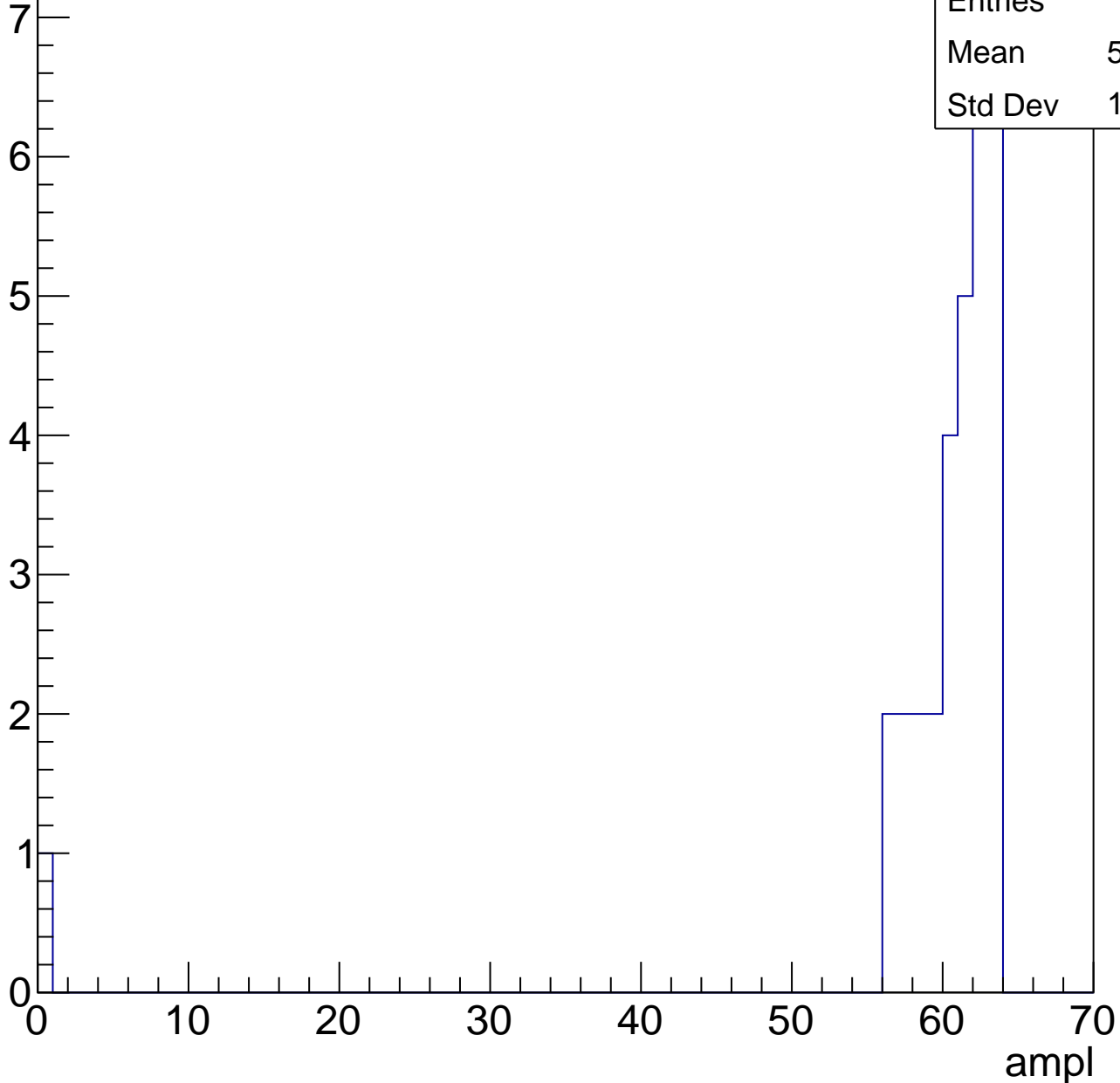


B1L100S, U25-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.75
Std Dev	10.76



B1L100S, U25-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U25-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch72, adc0

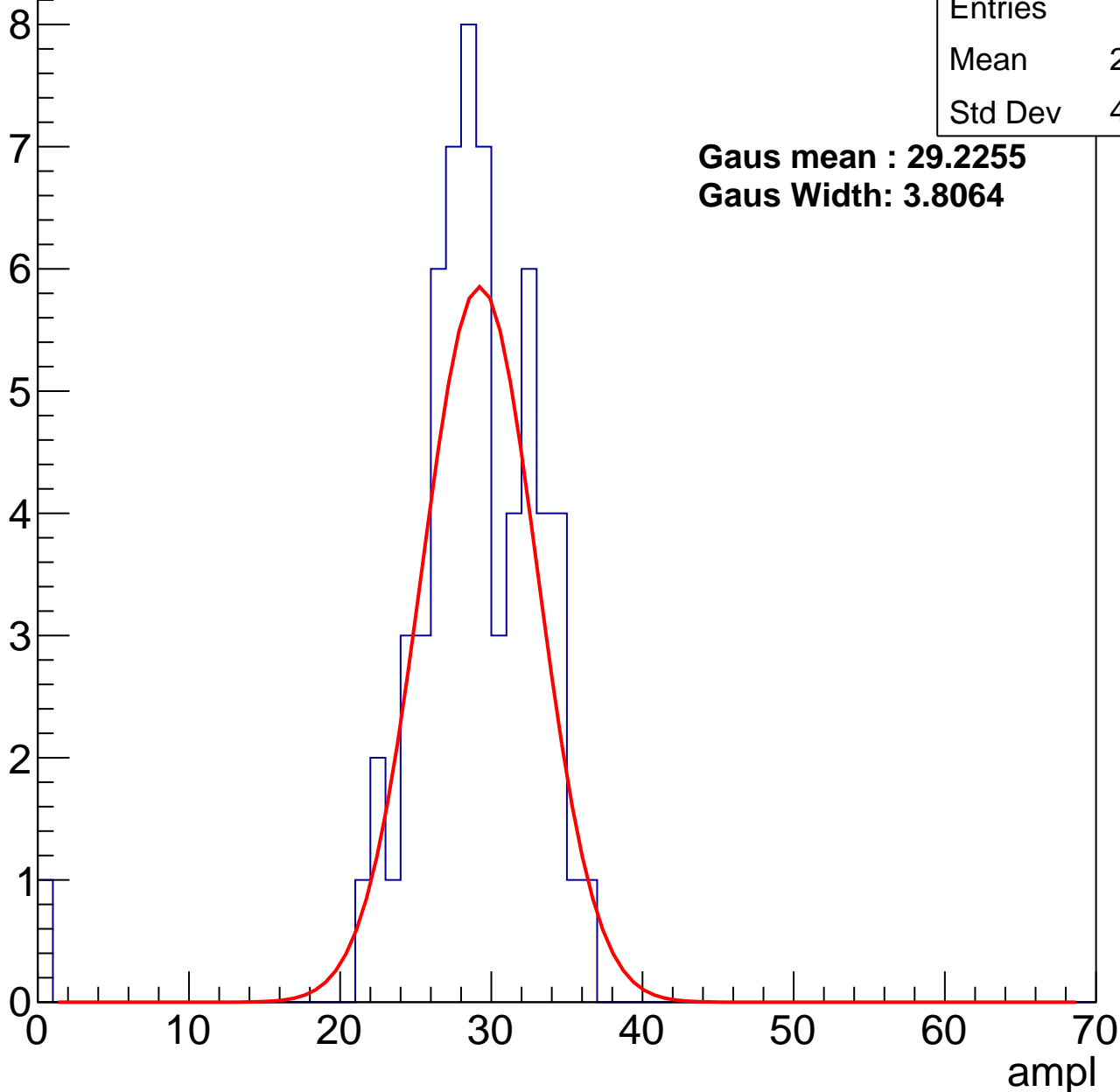
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.26
Std Dev	4.984

Gaus mean : 29.2255

Gaus Width: 3.8064



B1L100S, U25-ch72, adc1

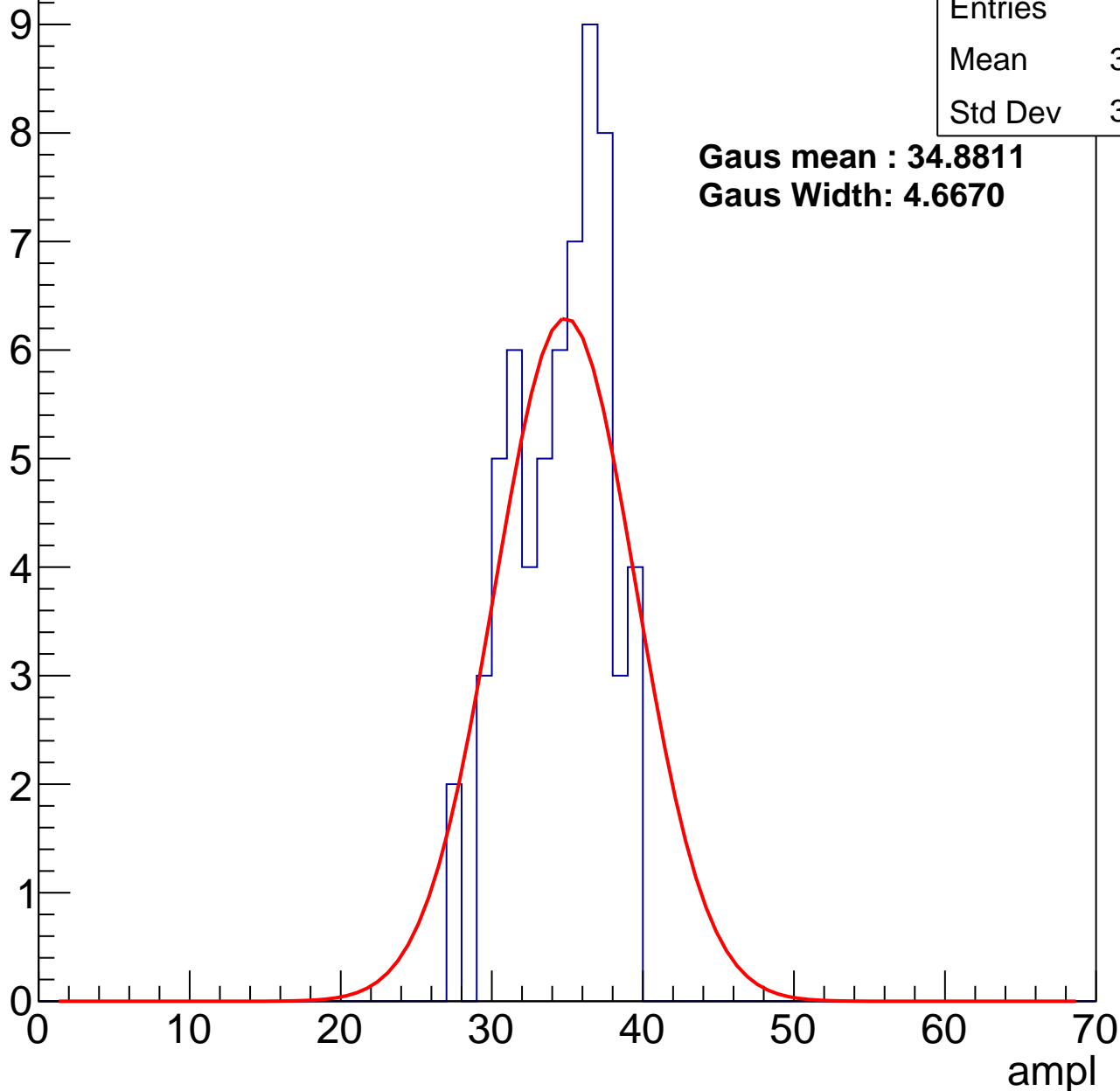
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	34.02
Std Dev	3.087

Gaus mean : 34.8811

Gaus Width: 4.6670



B1L100S, U25-ch72, adc2

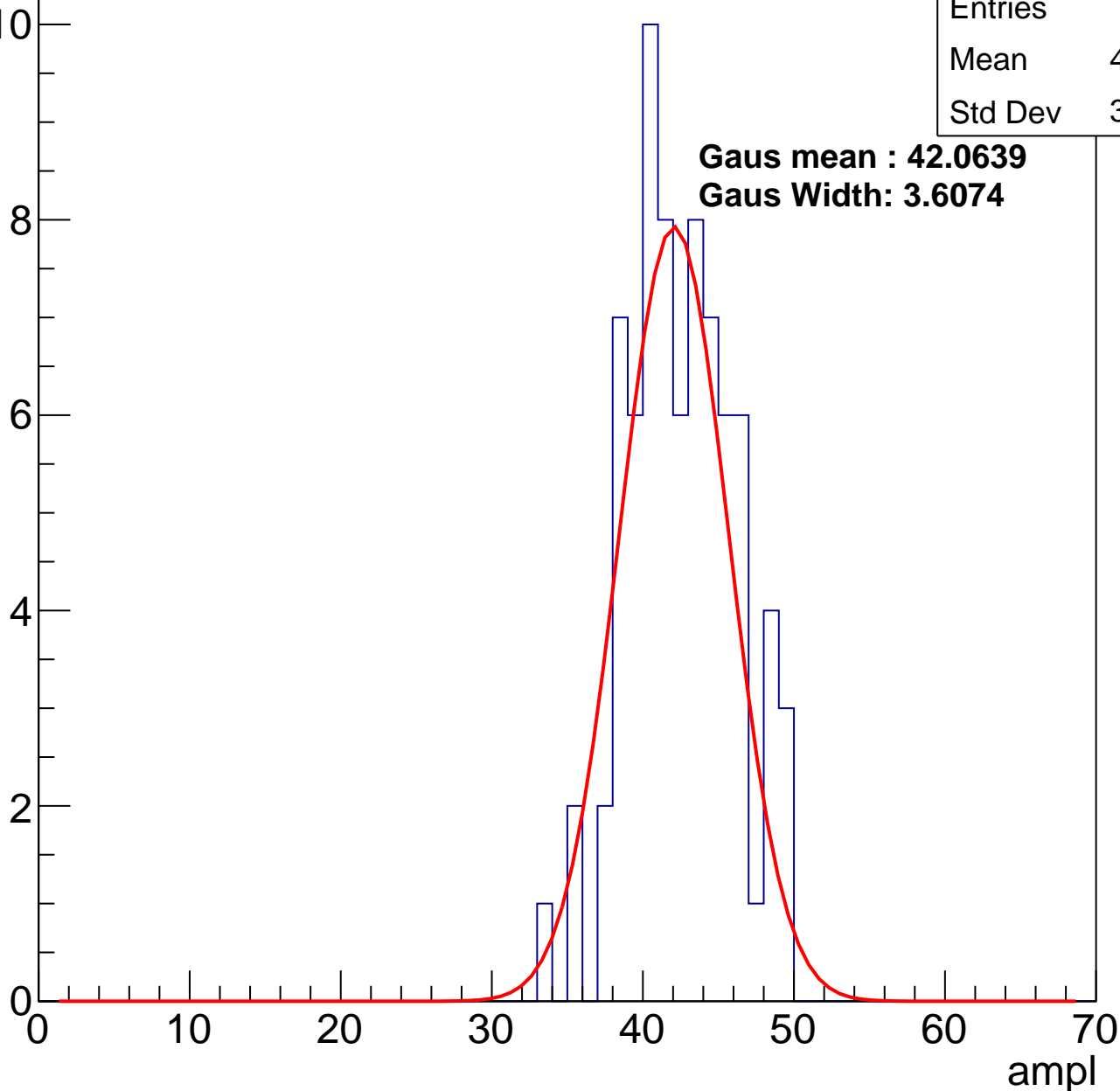
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.09
Std Dev	3.502

Gaus mean : 42.0639

Gaus Width: 3.6074

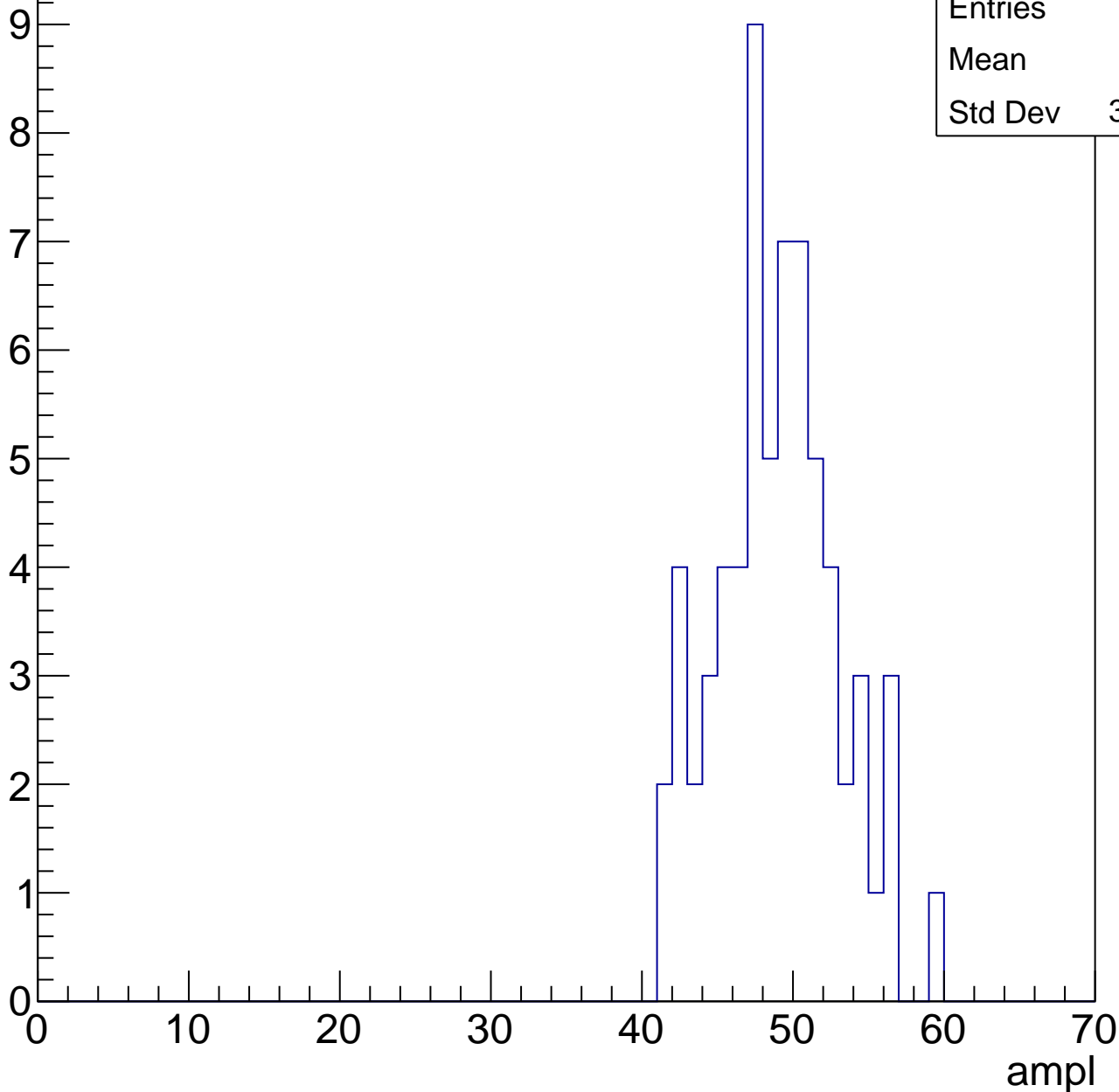


B1L100S, U25-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	48.5
Std Dev	3.986

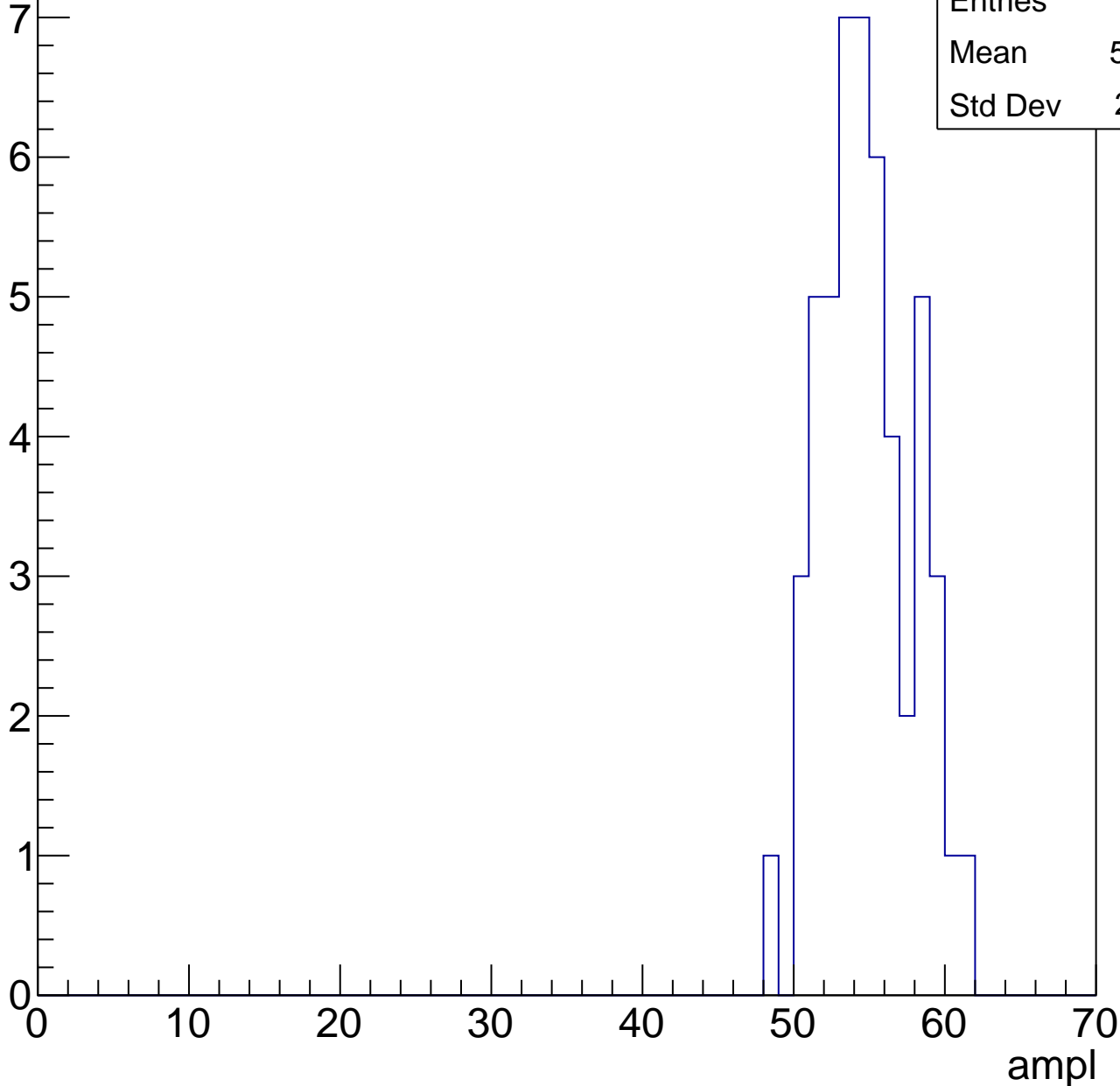


B1L100S, U25-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	54.36
Std Dev	2.931

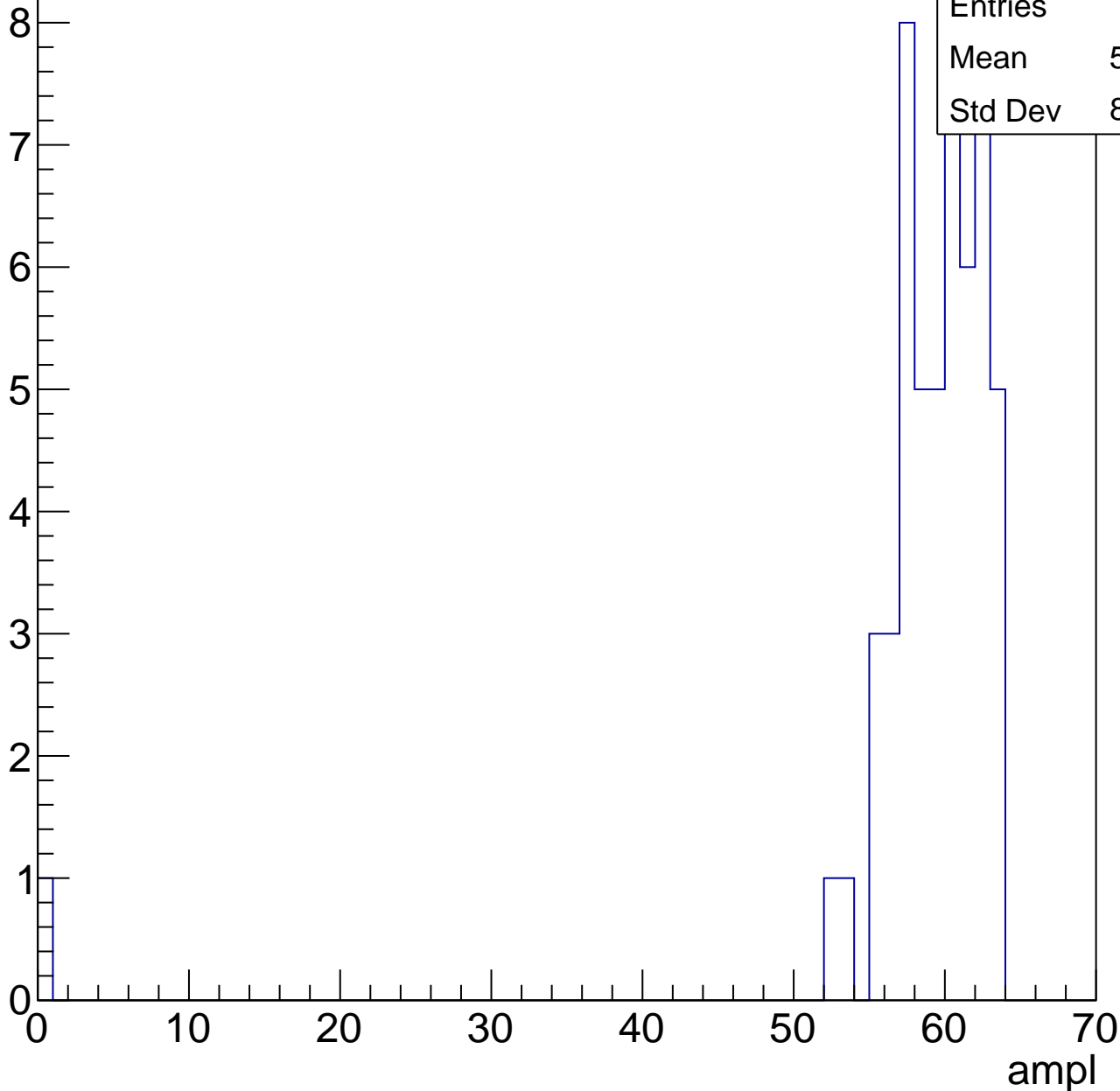


B1L100S, U25-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

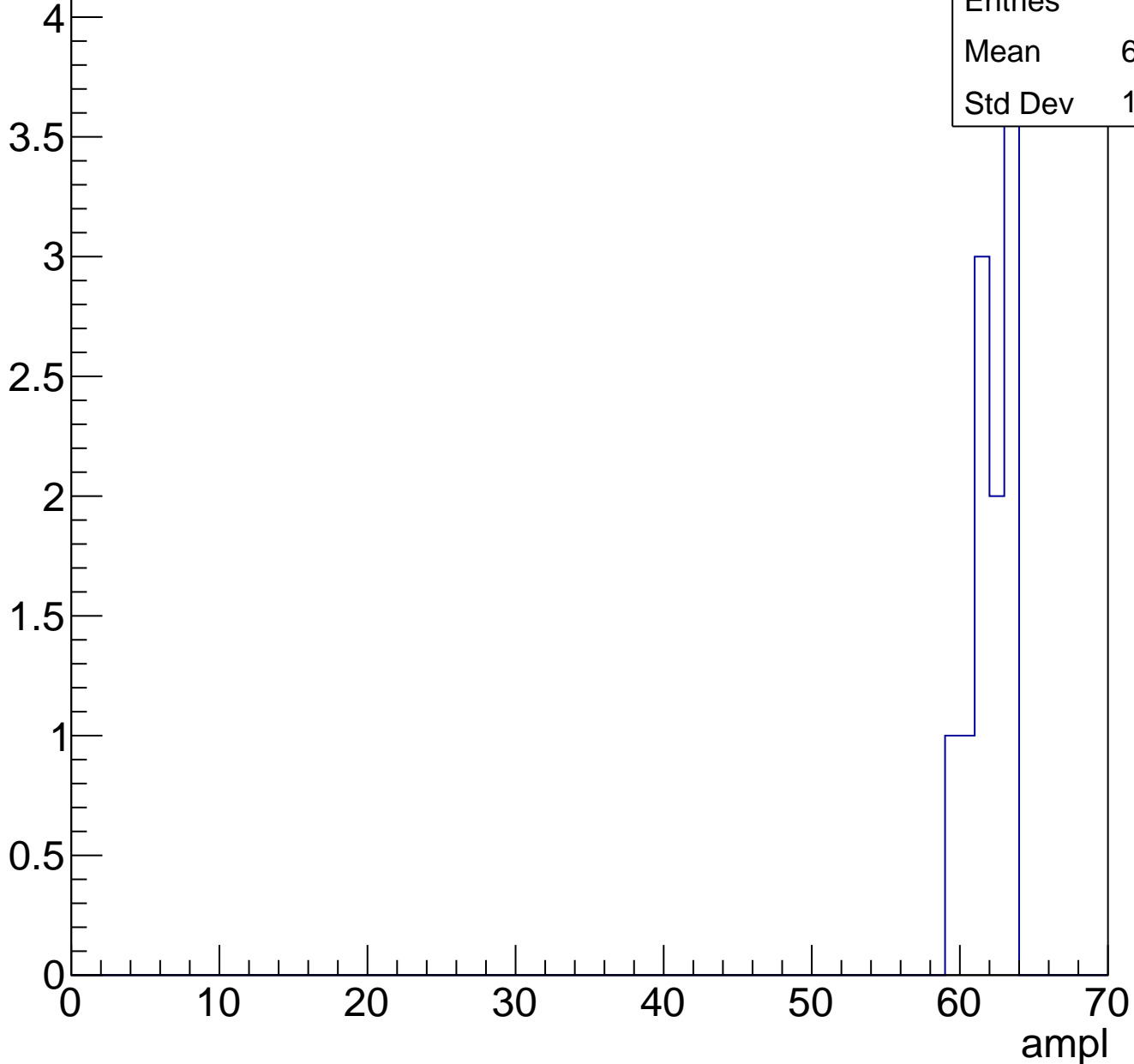
Entries	54
Mean	58.07
Std Dev	8.406



B1L100S, U25-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

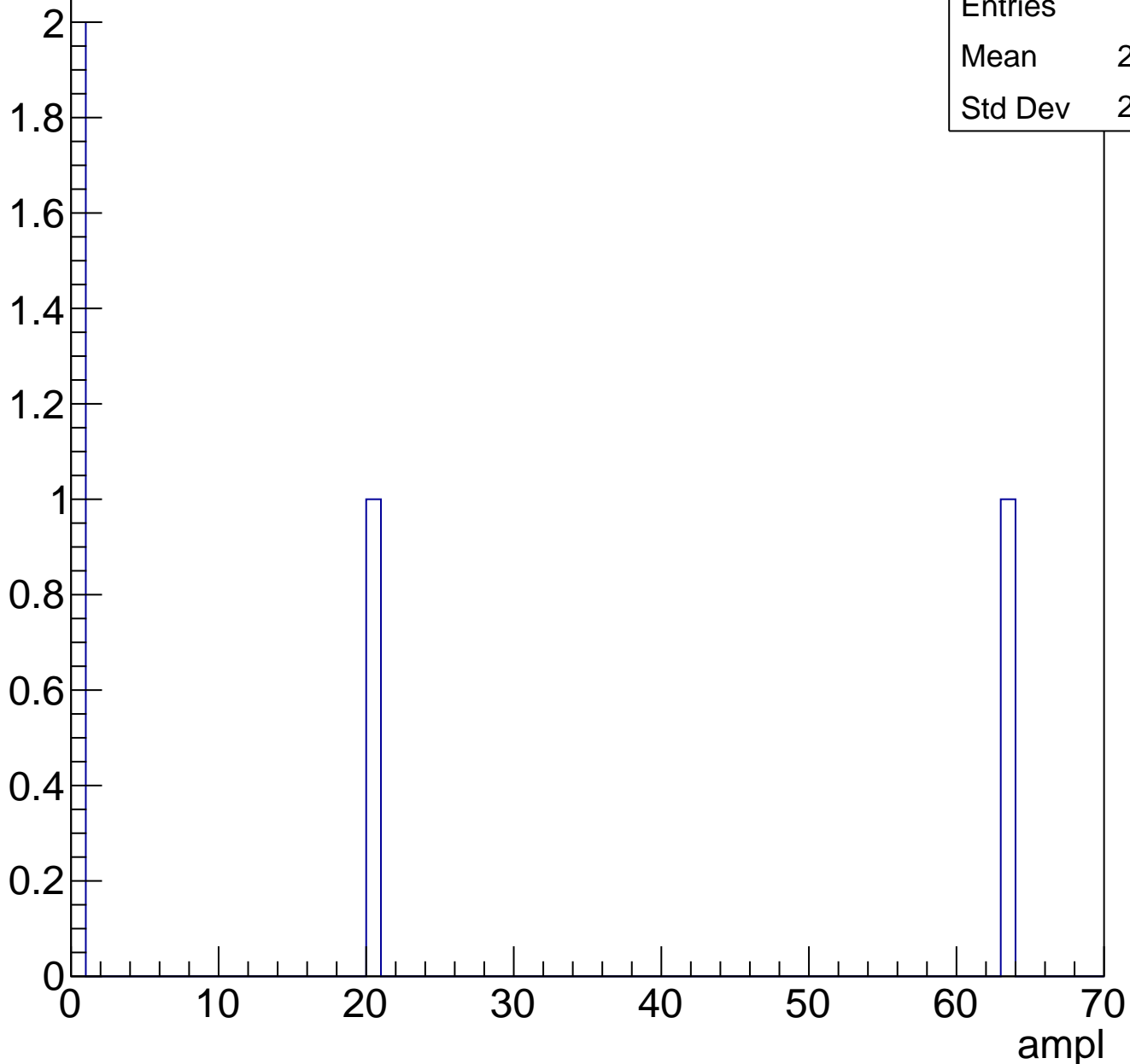
Entry



B1L100S, U25-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	20.75
Std Dev	25.72

B1L100S, U25-ch73, adc0

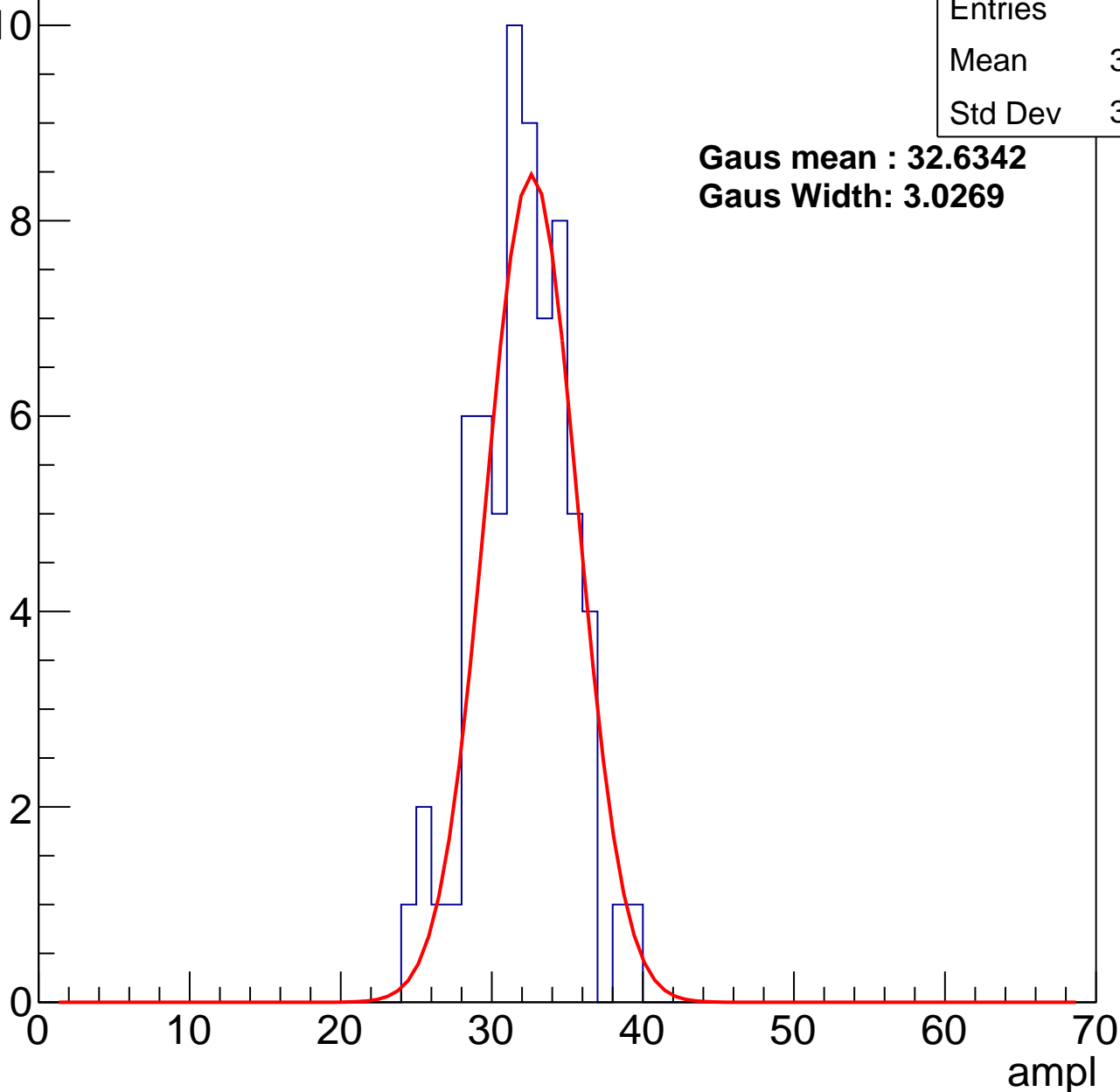
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	31.58
Std Dev	3.053

Gaus mean : 32.6342

Gaus Width: 3.0269



B1L100S, U25-ch73, adc1

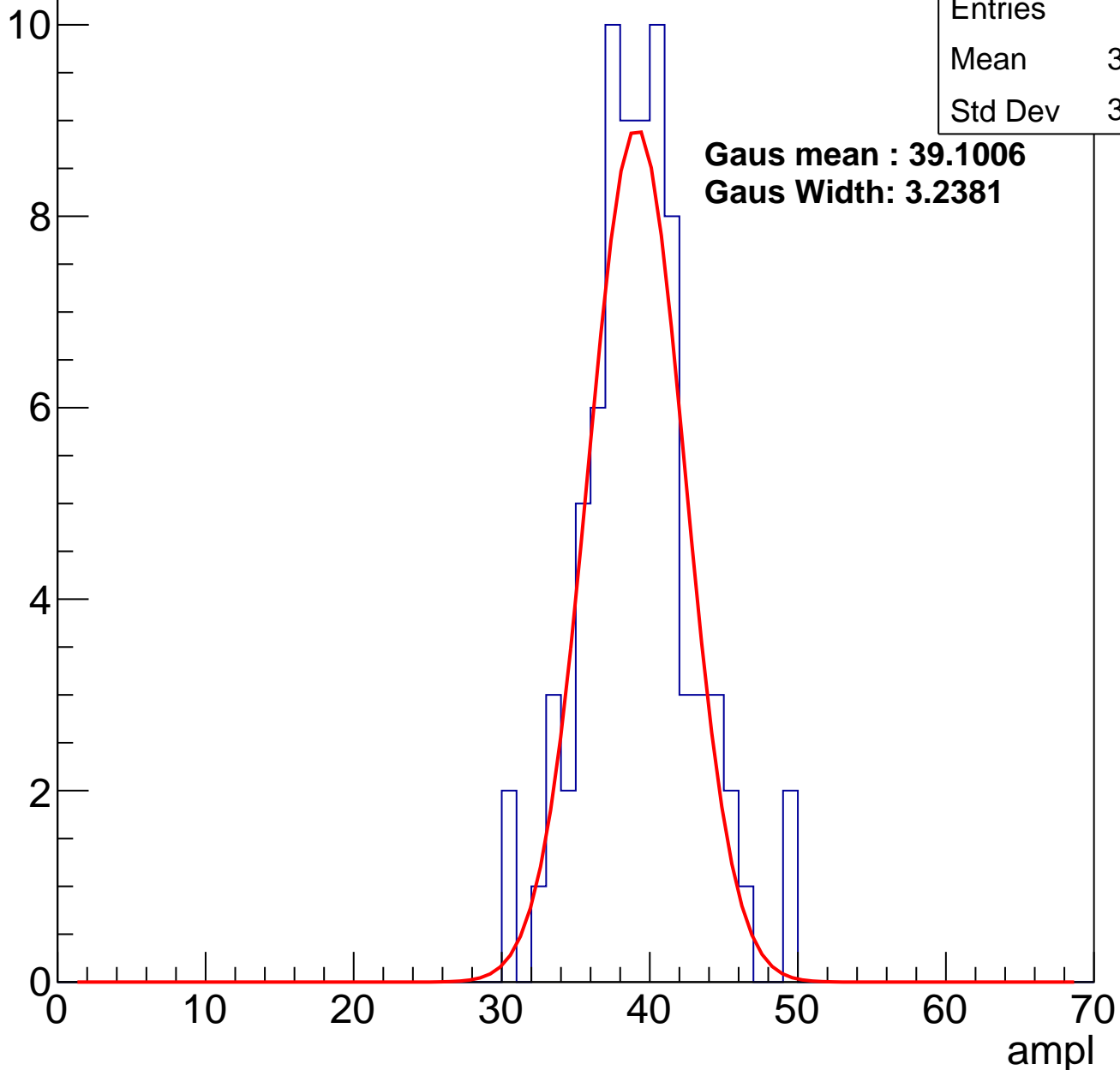
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	38.76
Std Dev	3.664

Gaus mean : 39.1006

Gaus Width: 3.2381

Entry



B1L100S, U25-ch73, adc2

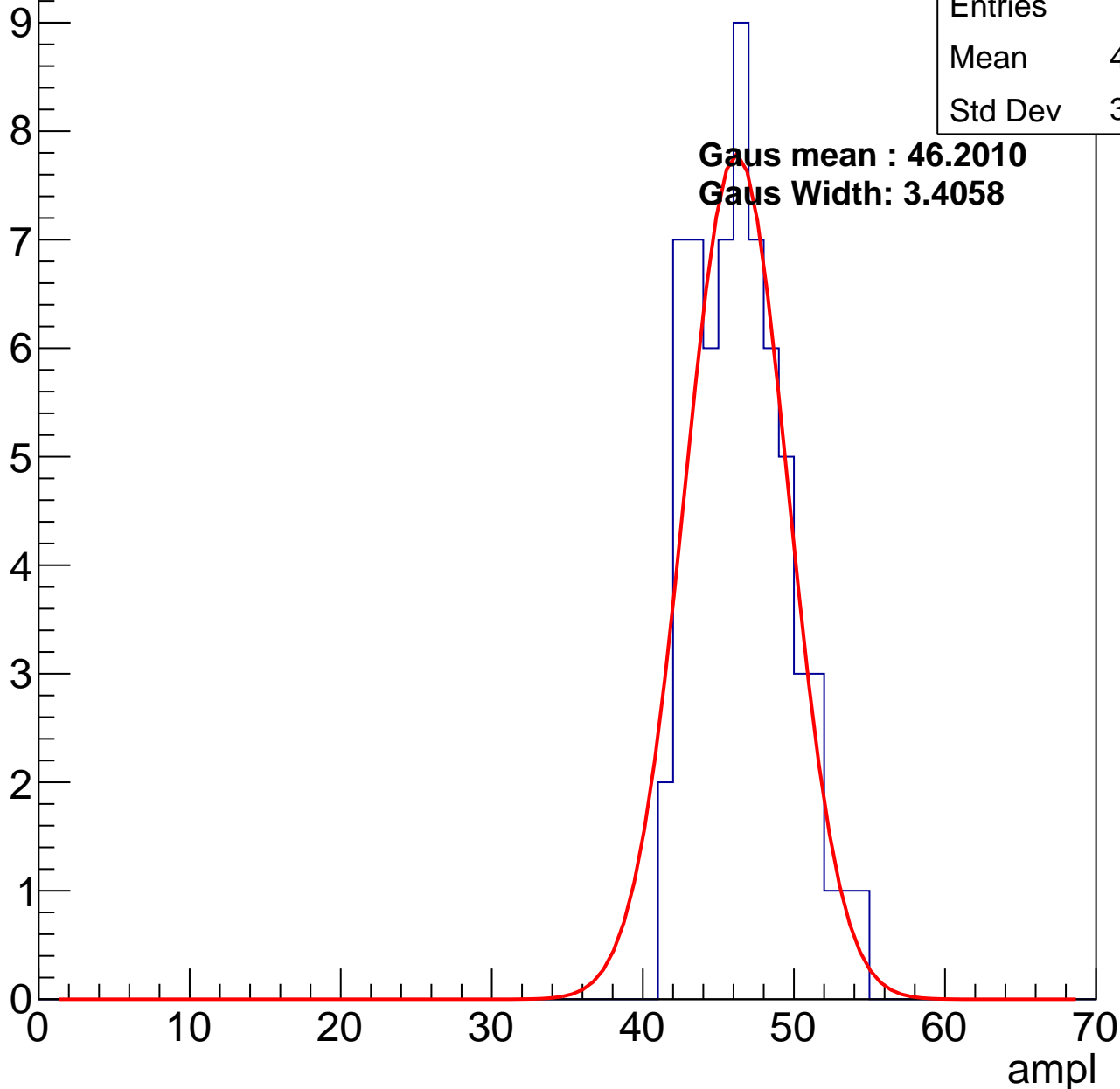
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	46.06
Std Dev	3.048

Gaus mean : 46.2010

Gaus Width: 3.4058

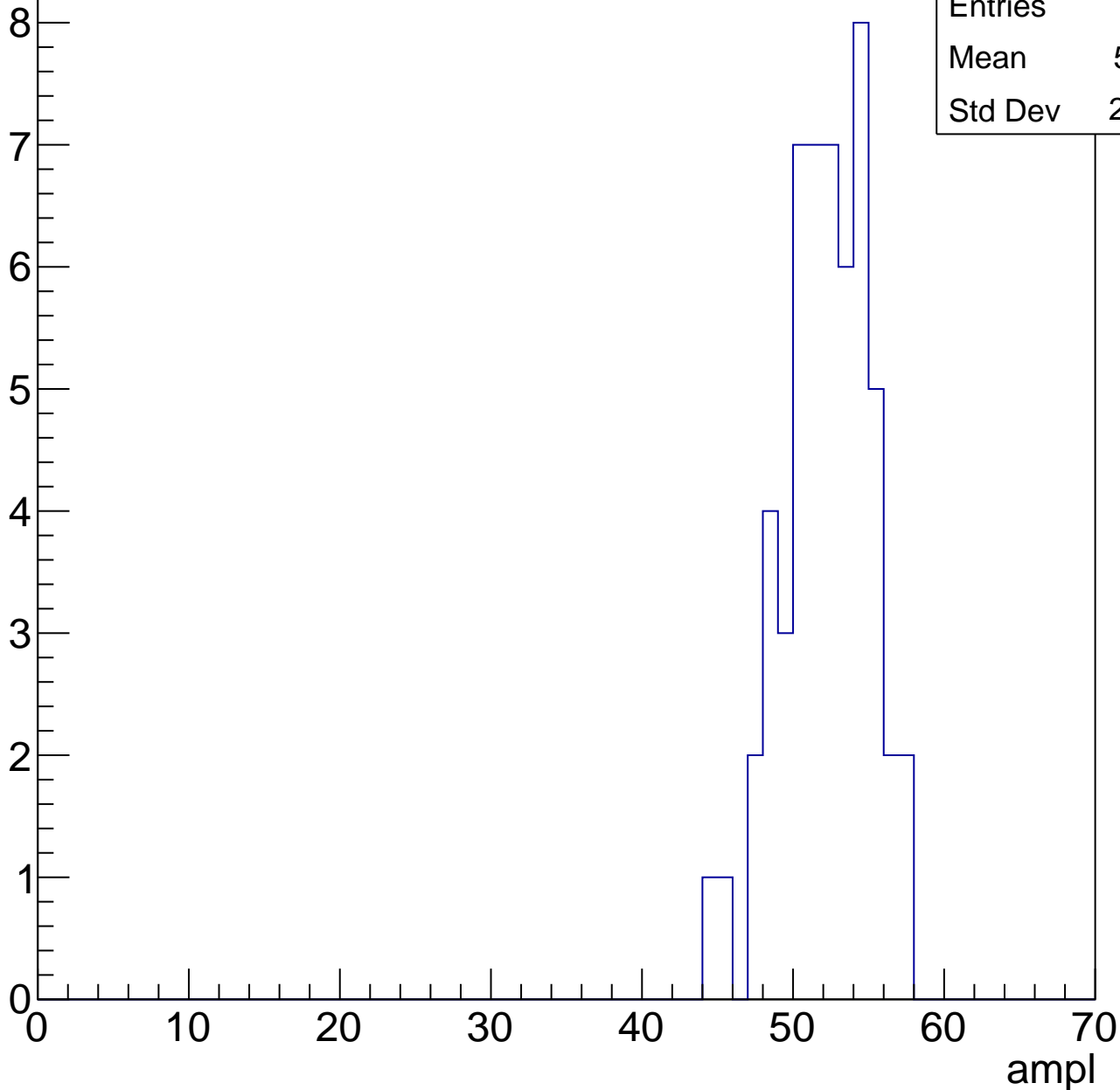


B1L100S, U25-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	51.71
Std Dev	2.858

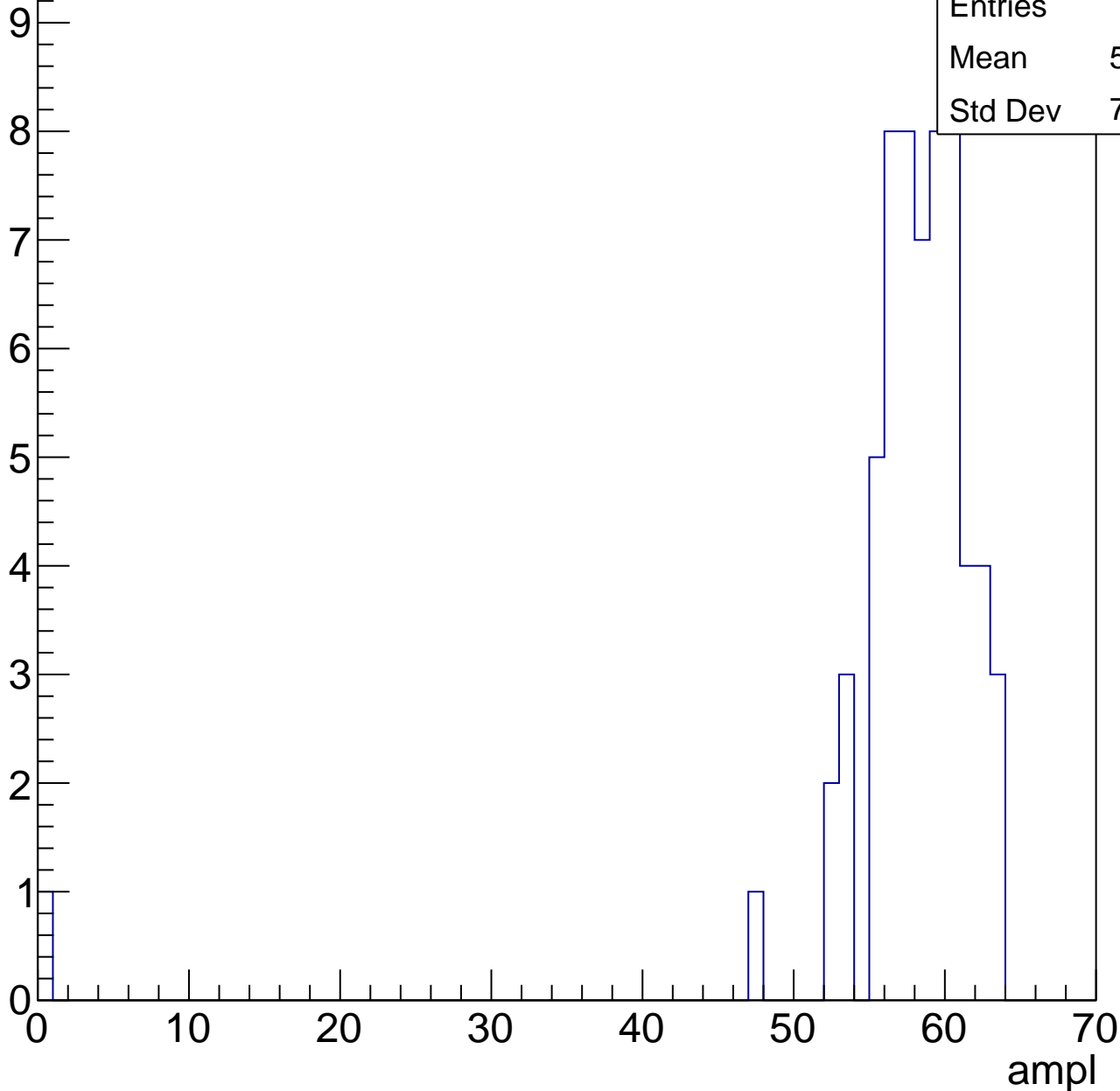


B1L100S, U25-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	56.95
Std Dev	7.834

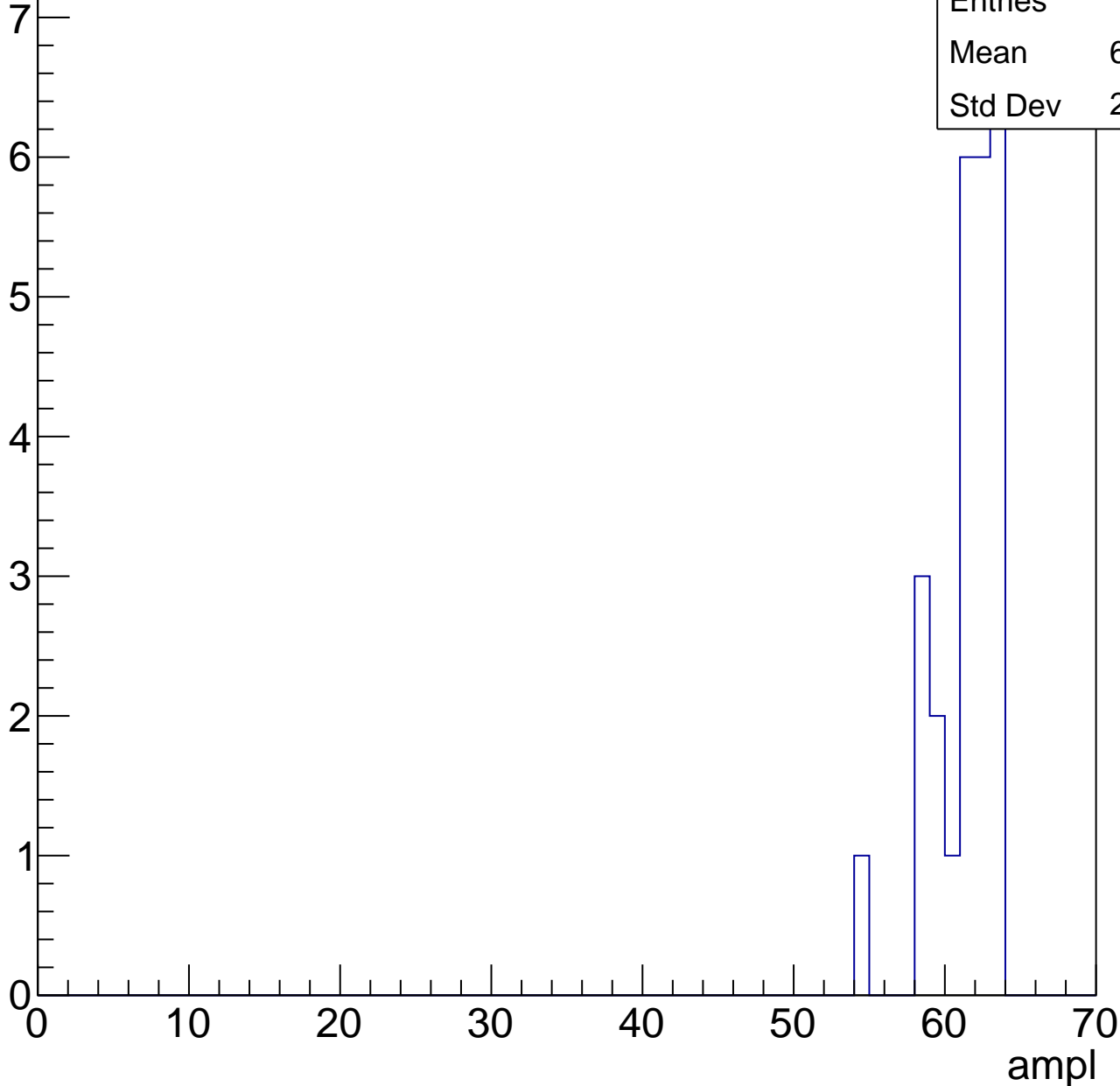


B1L100S, U25-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	60.96
Std Dev	2.139



B1L100S, U25-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch74, adc0

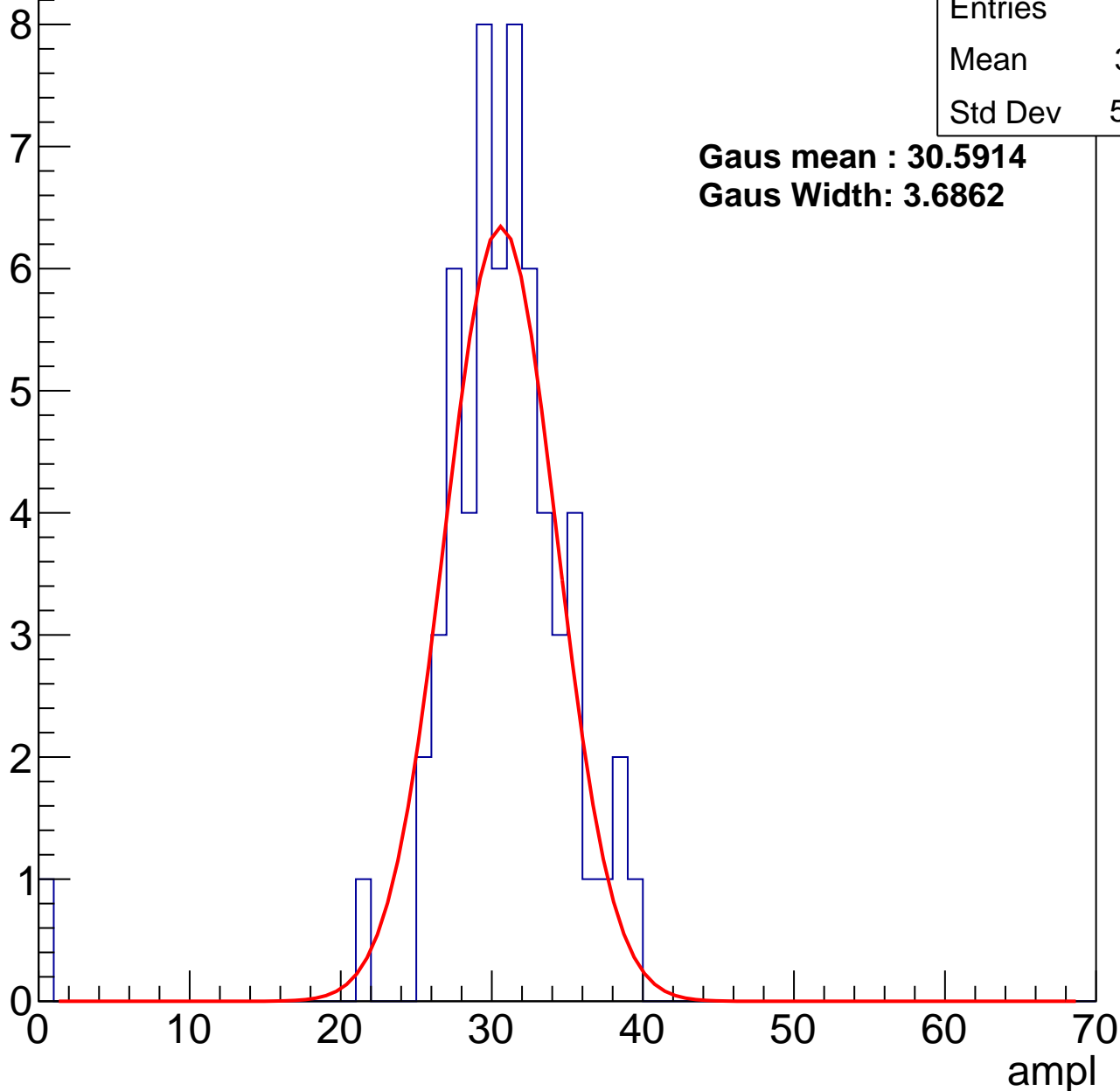
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.11
Std Dev	5.226

Gaus mean : 30.5914

Gaus Width: 3.6862



B1L100S, U25-ch74, adc1

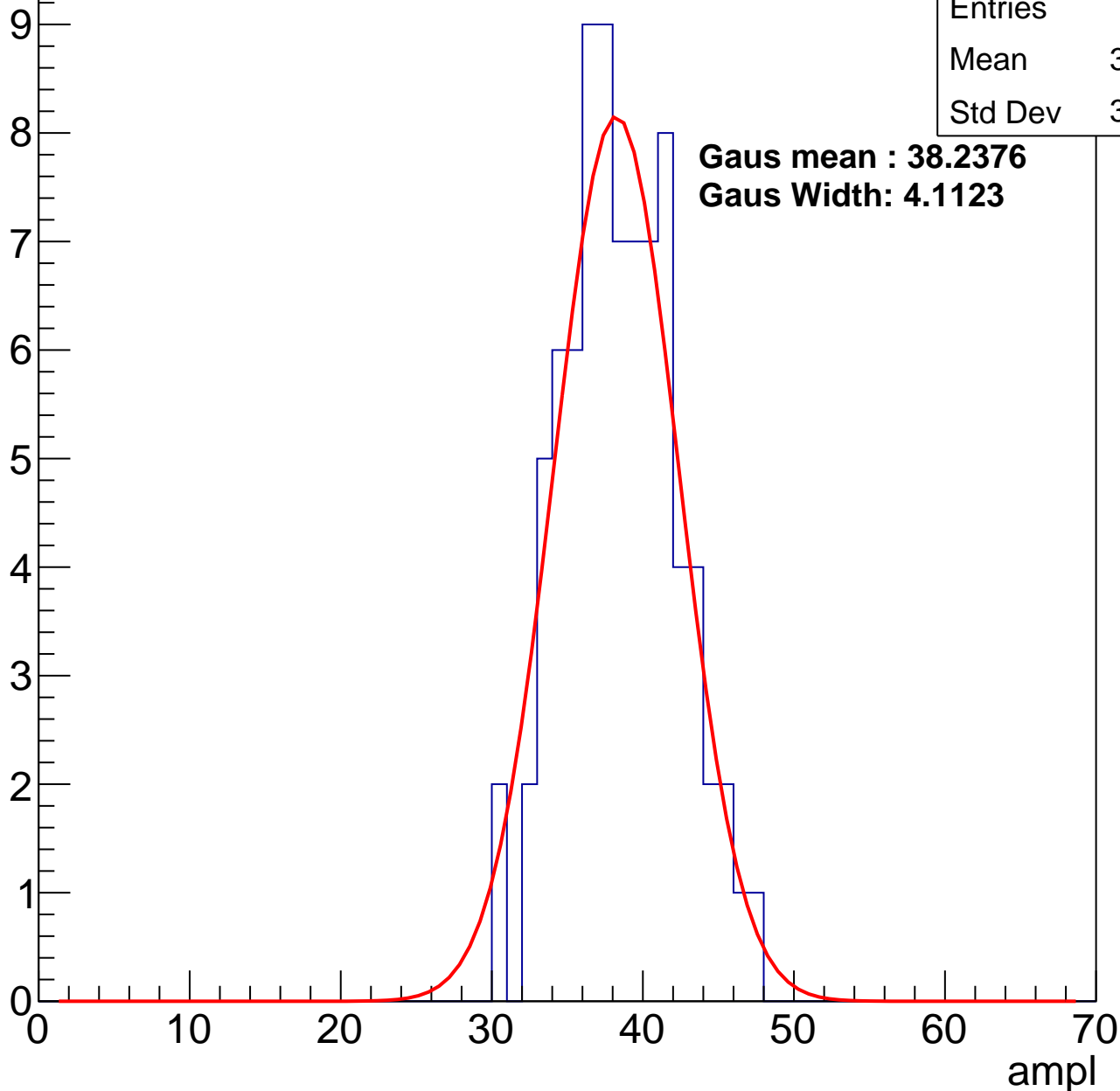
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	38.02
Std Dev	3.672

Gaus mean : 38.2376

Gaus Width: 4.1123



B1L100S, U25-ch74, adc2

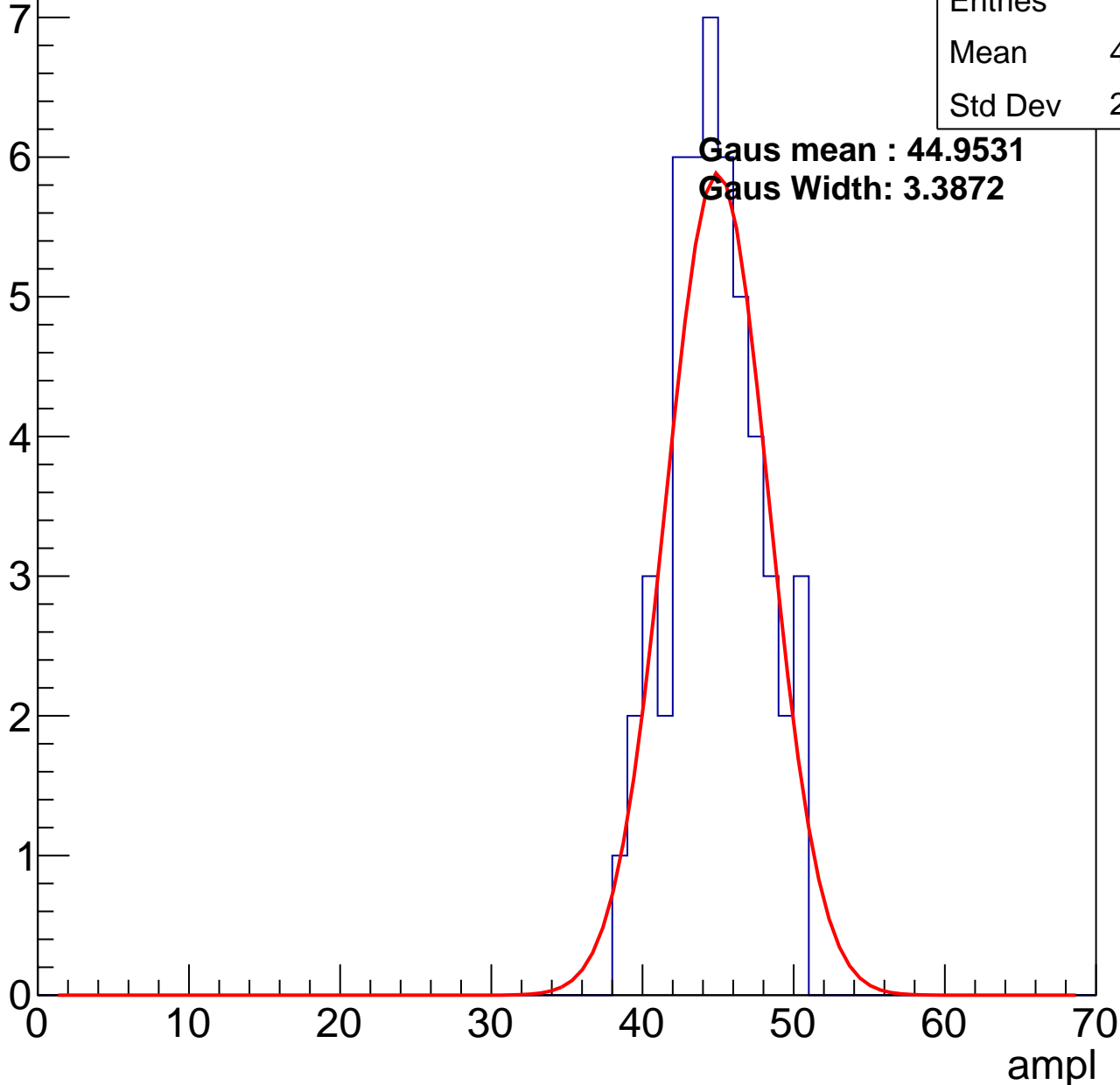
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	44.32
Std Dev	2.983

Gaus mean : 44.9531

Gaus Width: 3.3872

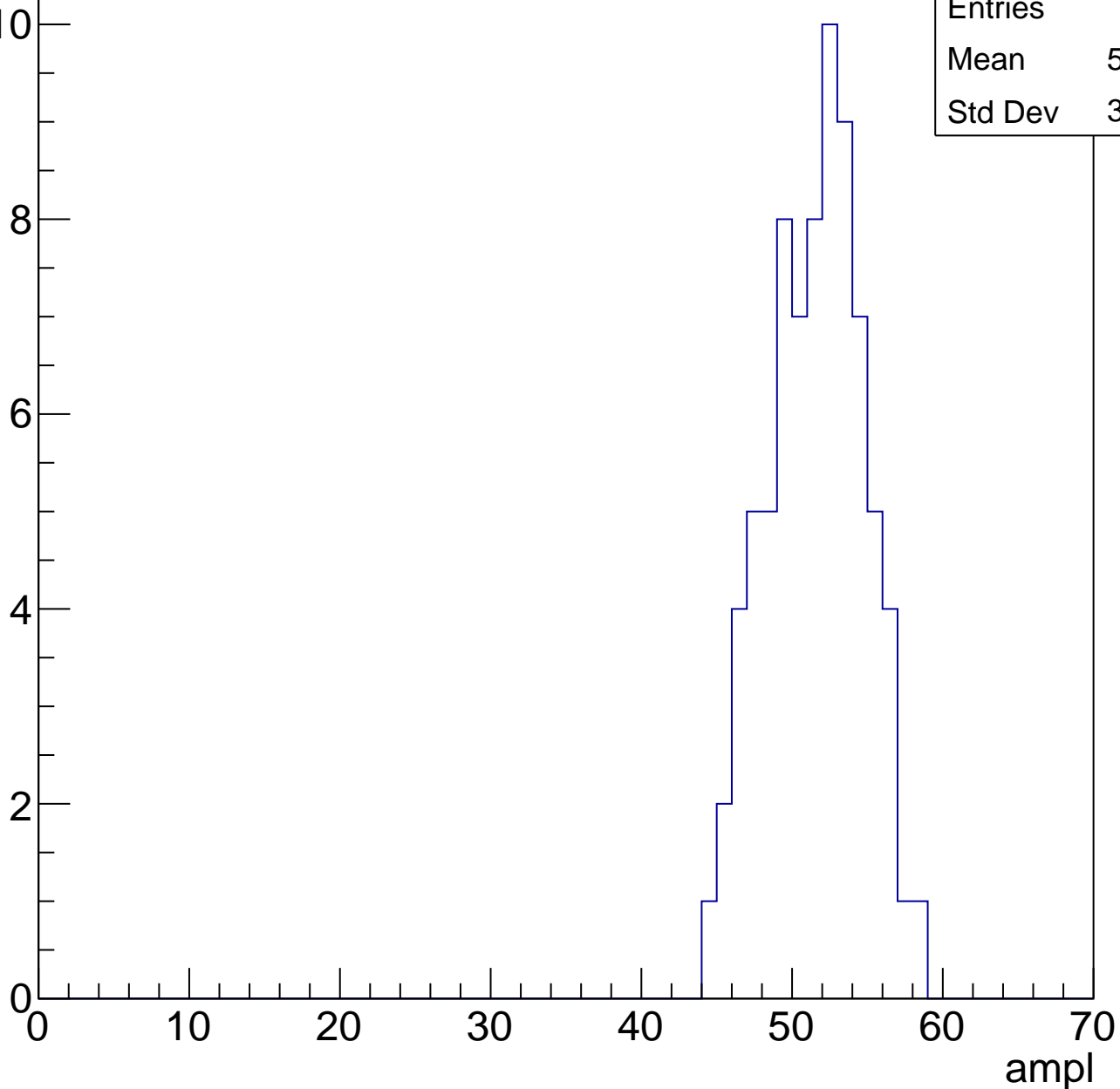


B1L100S, U25-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	51.06
Std Dev	3.139

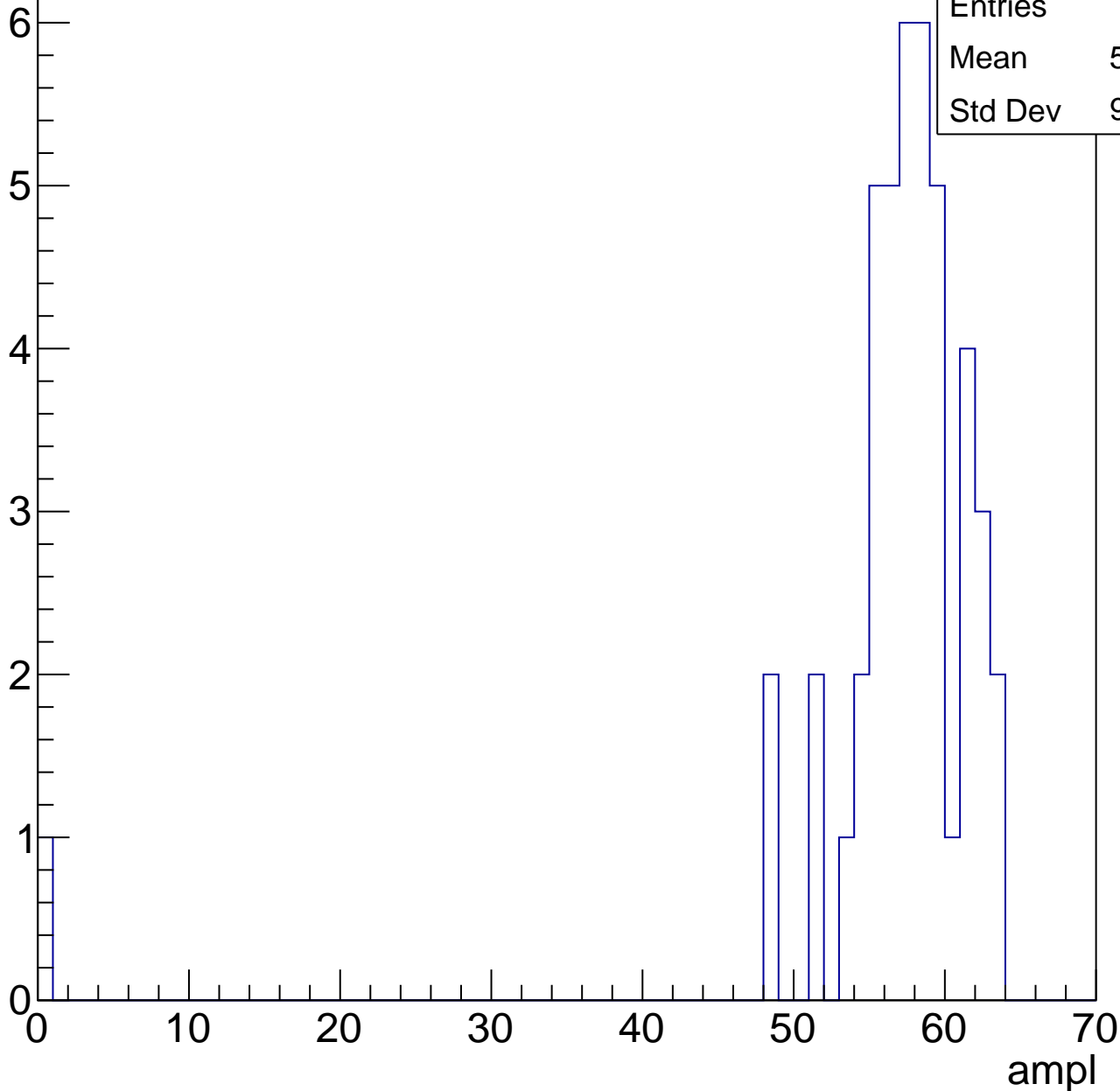


B1L100S, U25-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	55.89
Std Dev	9.107



B1L100S, U25-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.13
Std Dev	2.166

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L100S, U25-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

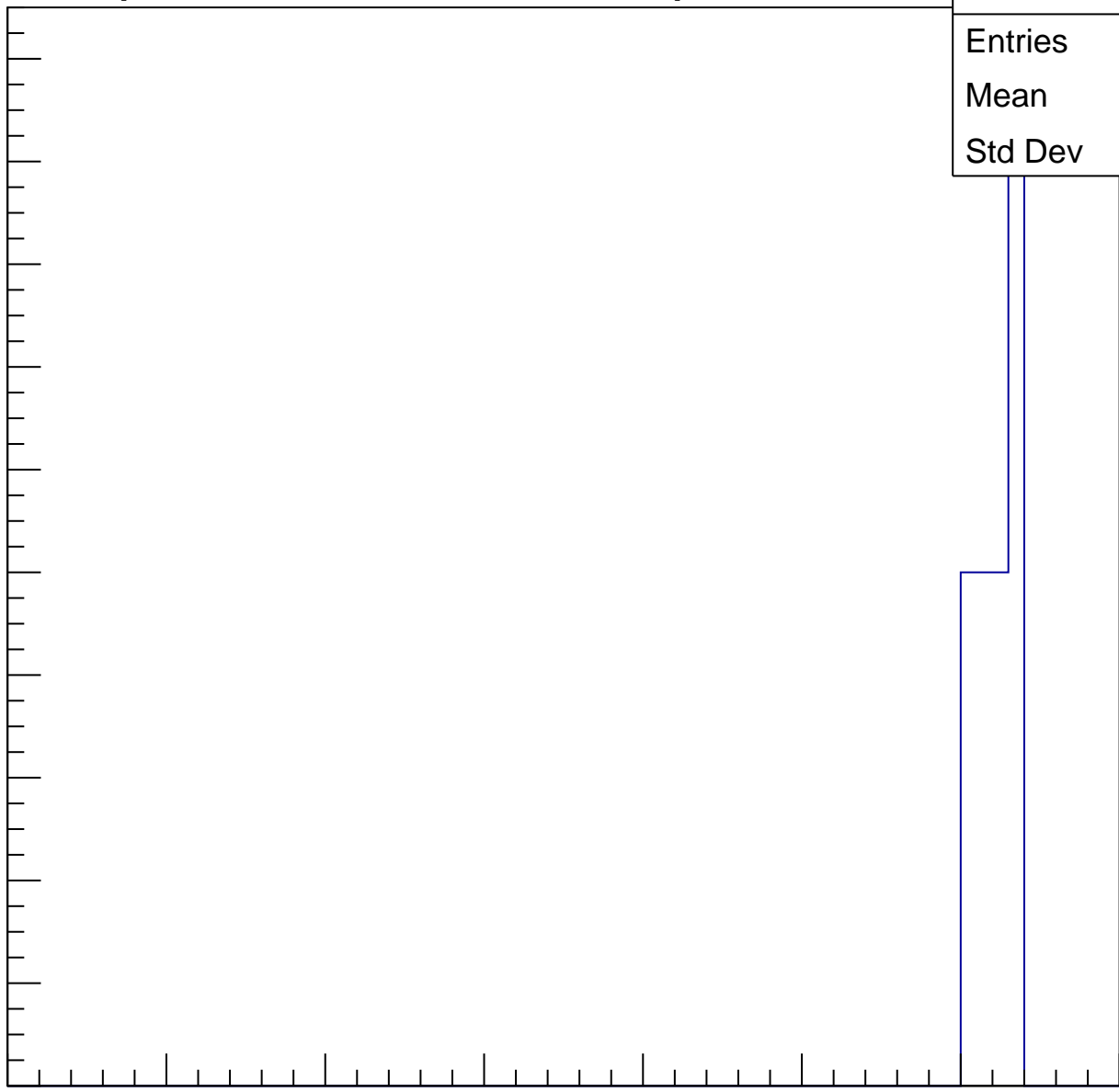
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch75, adc0

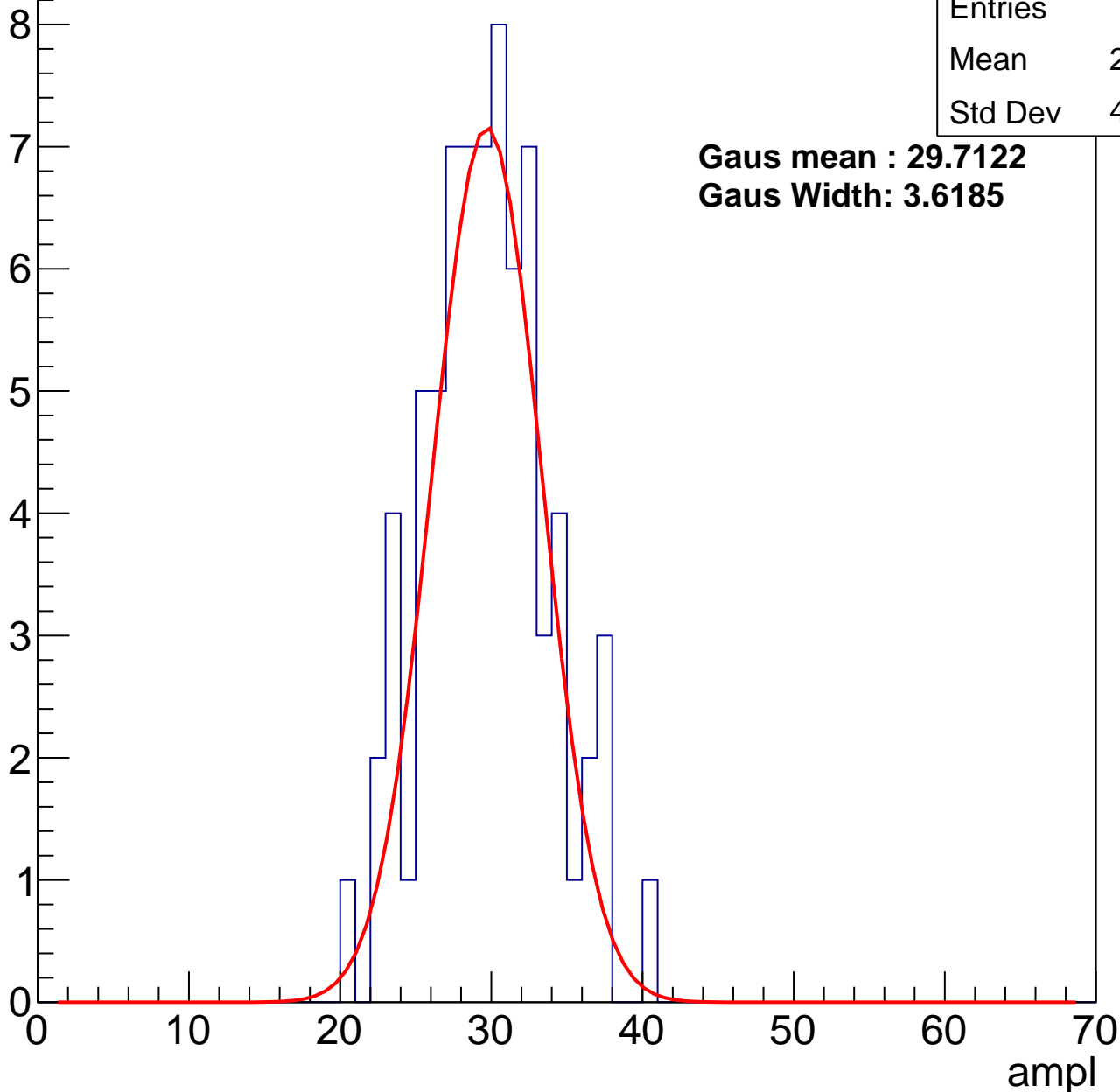
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.27
Std Dev	4.014

Gaus mean : 29.7122

Gaus Width: 3.6185



B1L100S, U25-ch75, adc1

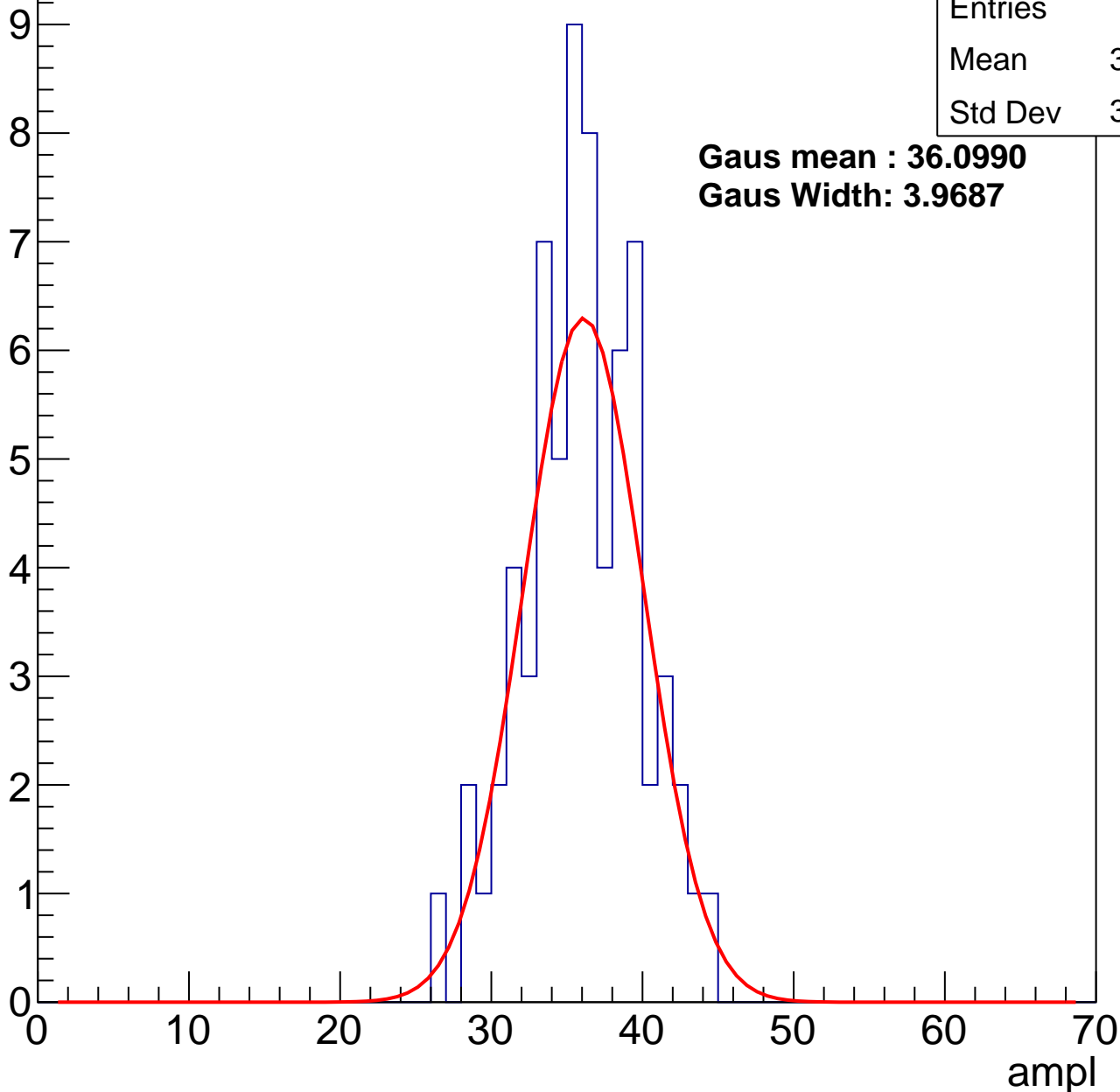
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.56
Std Dev	3.755

Gaus mean : 36.0990

Gaus Width: 3.9687



B1L100S, U25-ch75, adc2

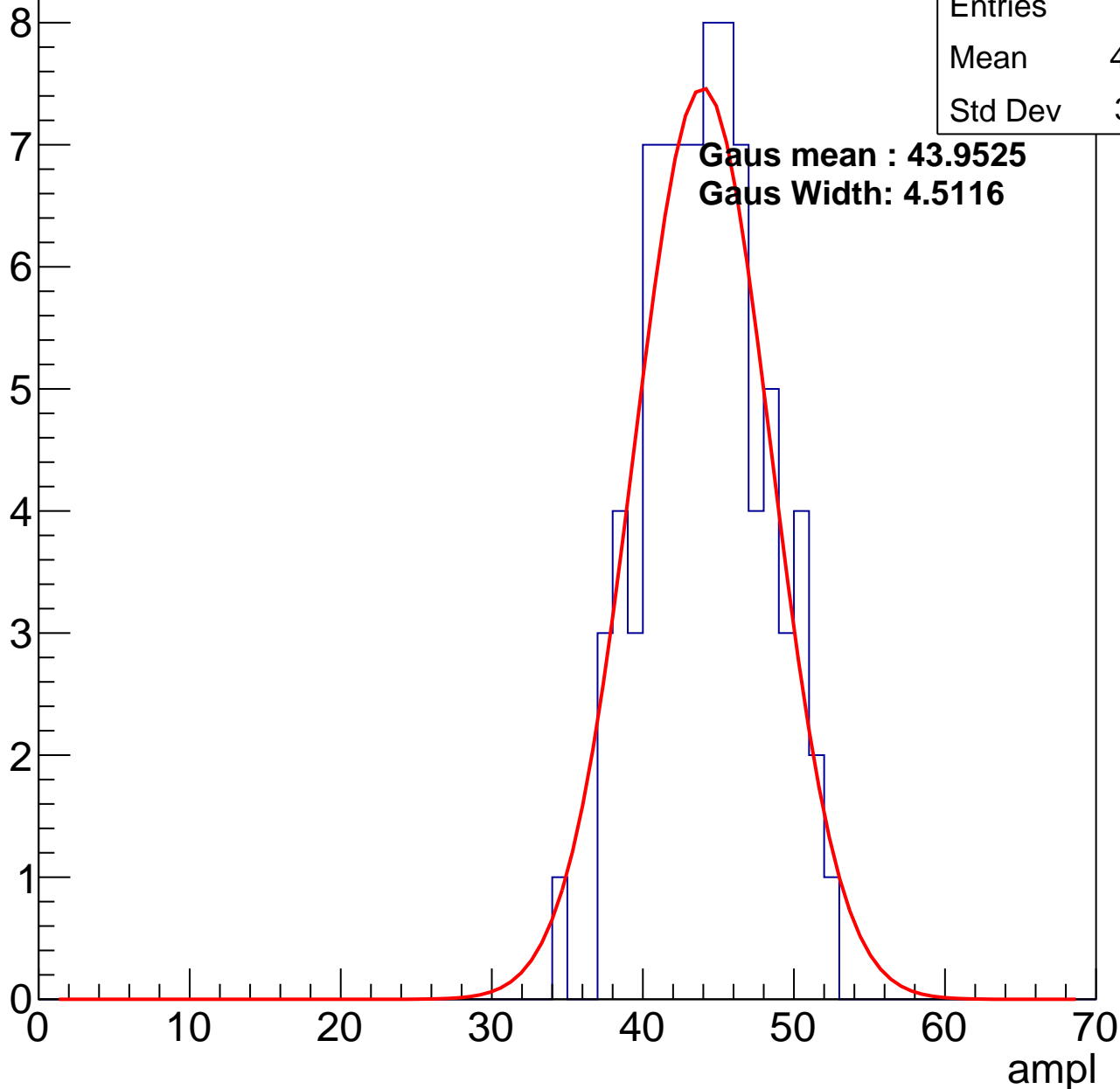
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	43.69
Std Dev	3.851

Gaus mean : 43.9525

Gaus Width: 4.5116

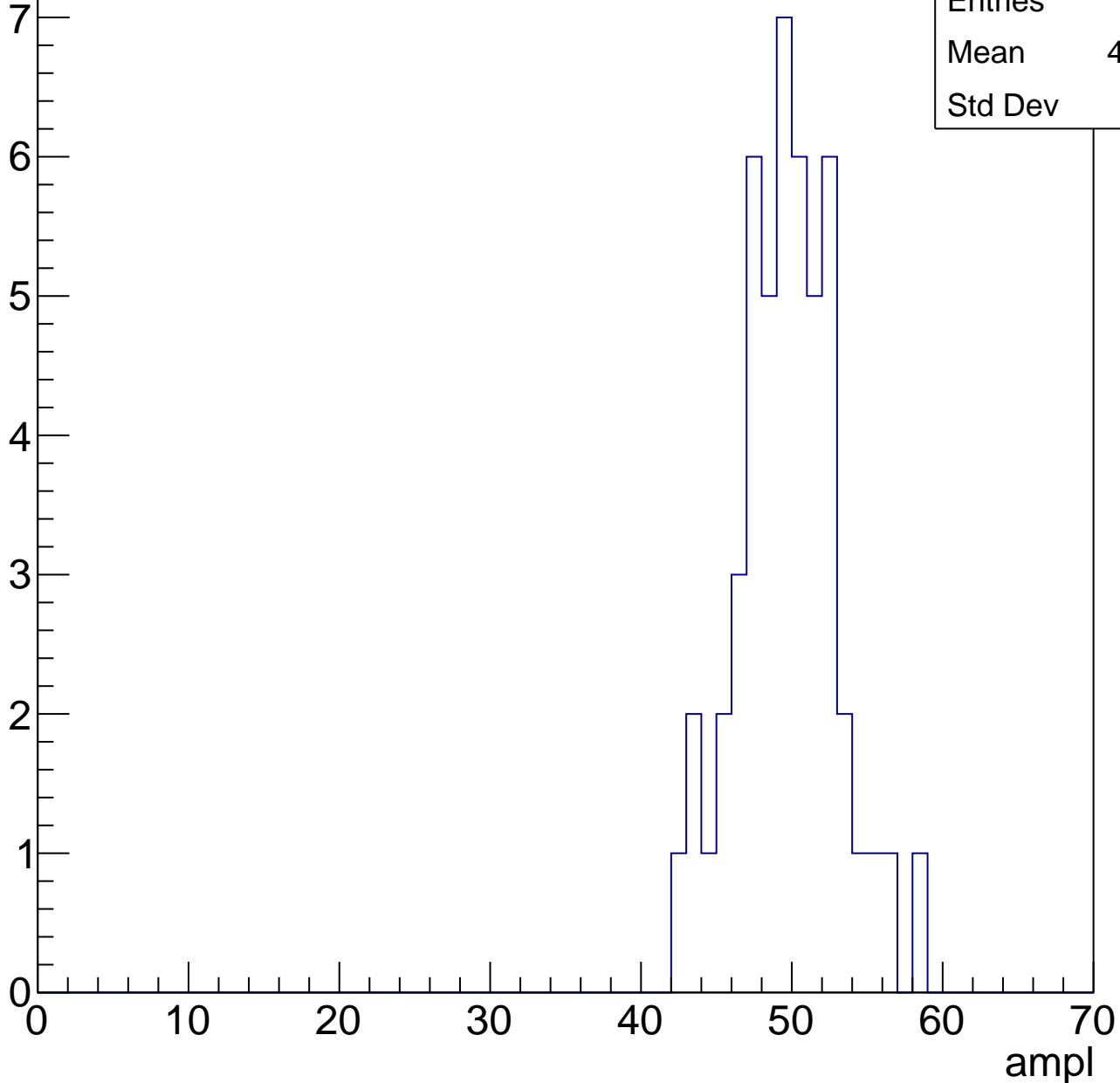


B1L100S, U25-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

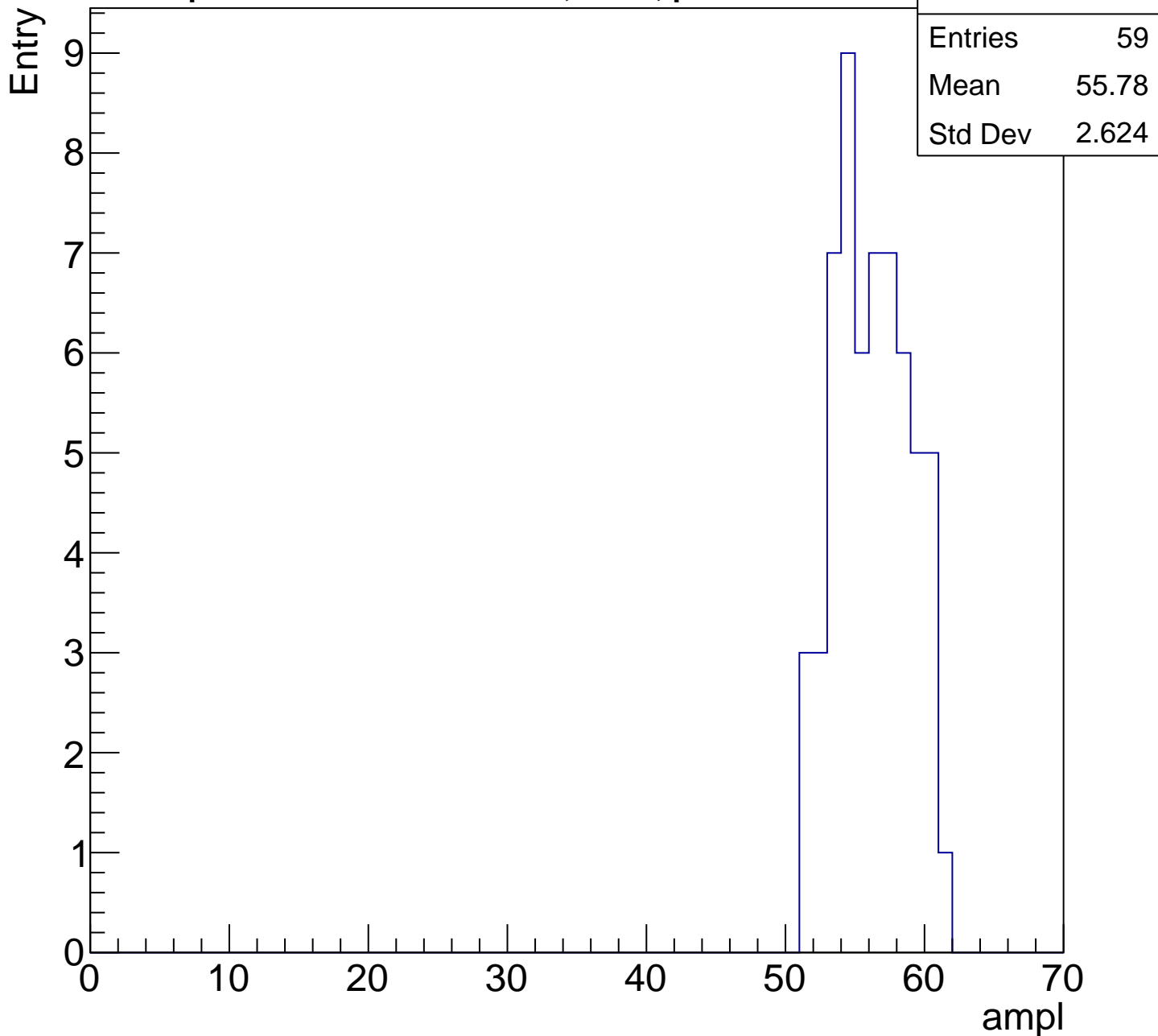
Entry

Entries	50
Mean	49.22
Std Dev	3.27



B1L100S, U25-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

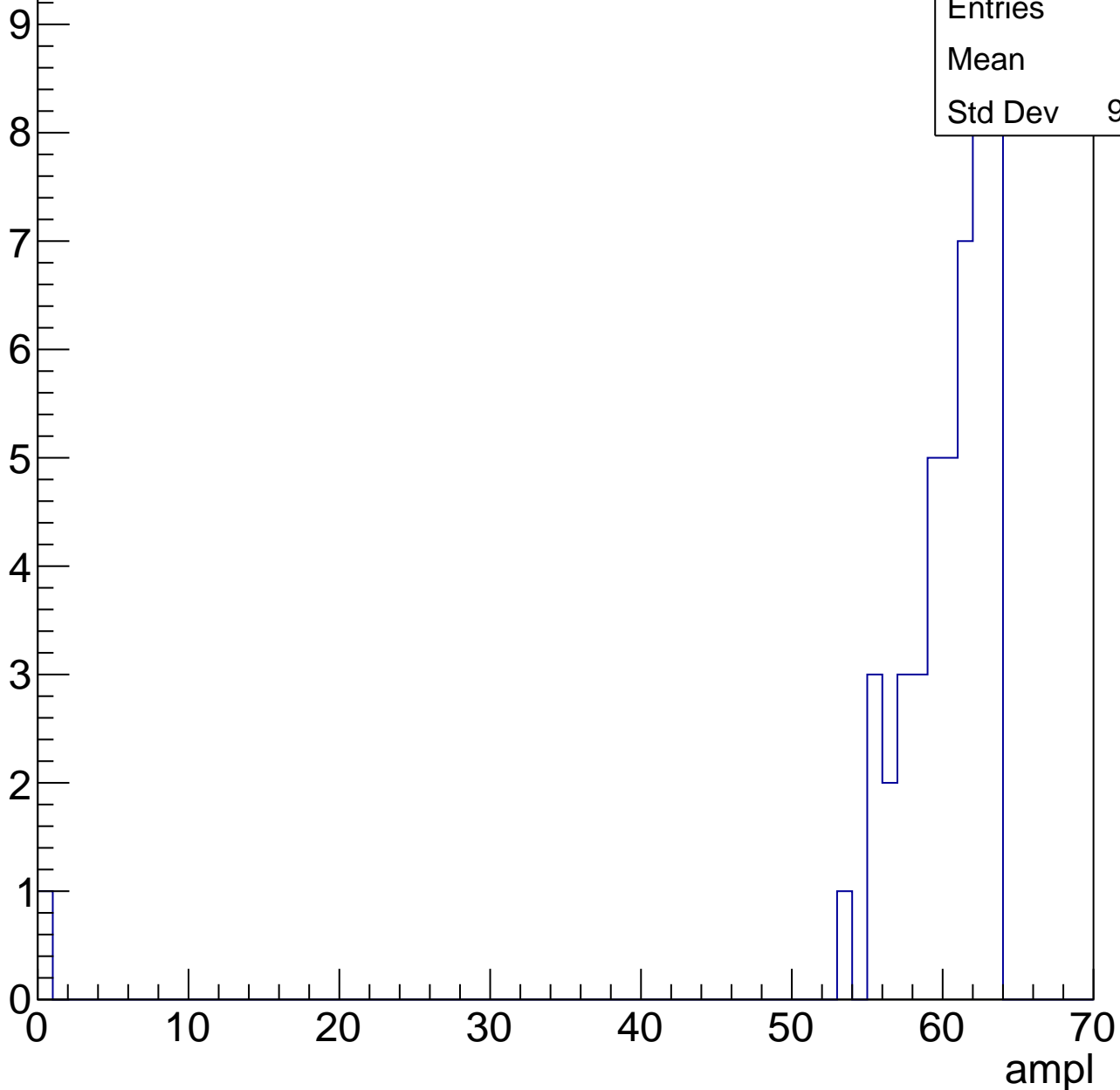


B1L100S, U25-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.7
Std Dev	9.034



B1L100S, U25-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

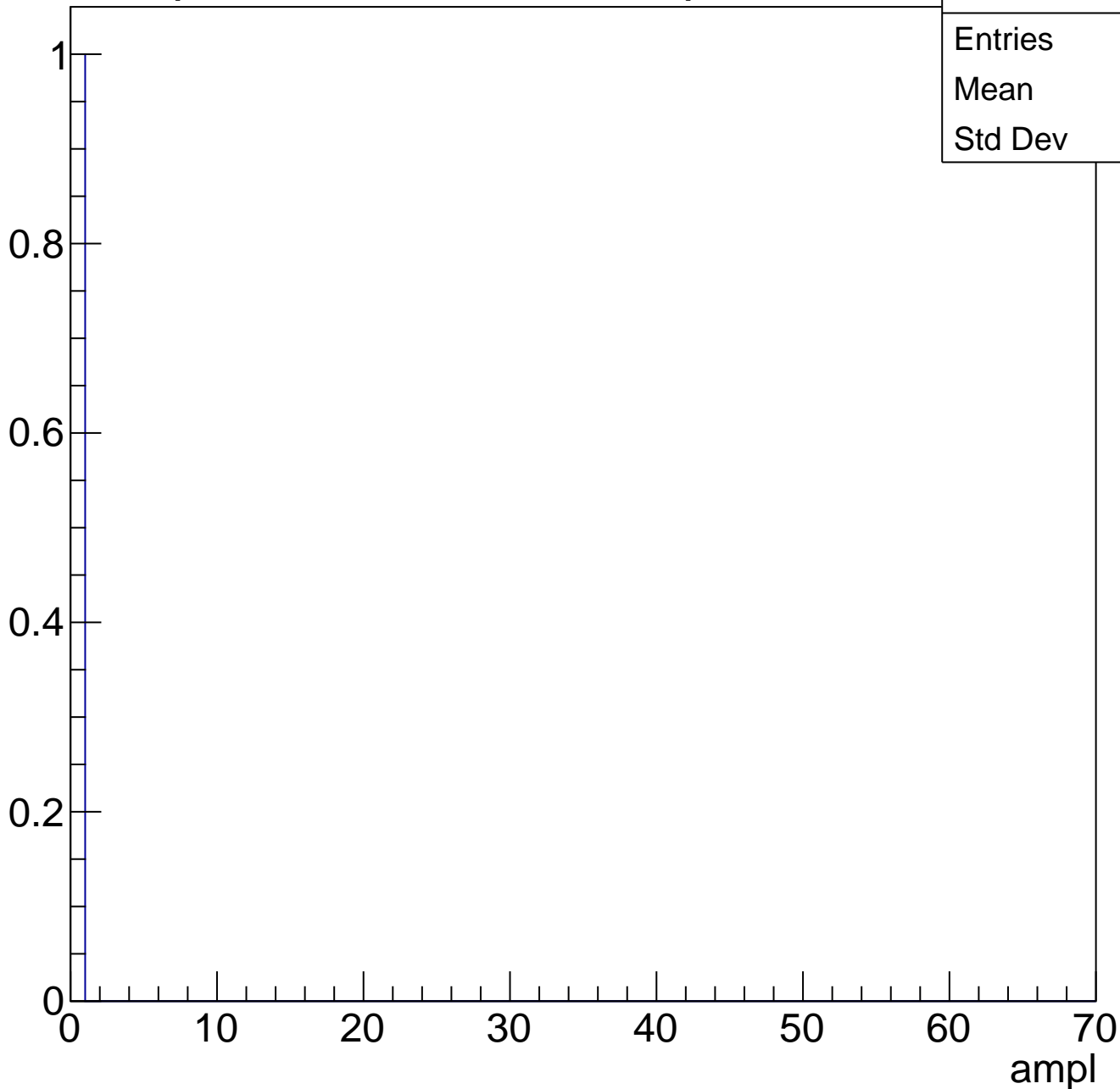
Entries	5
Mean	62
Std Dev	0.8944

ampl

B1L100S, U25-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch76, adc0

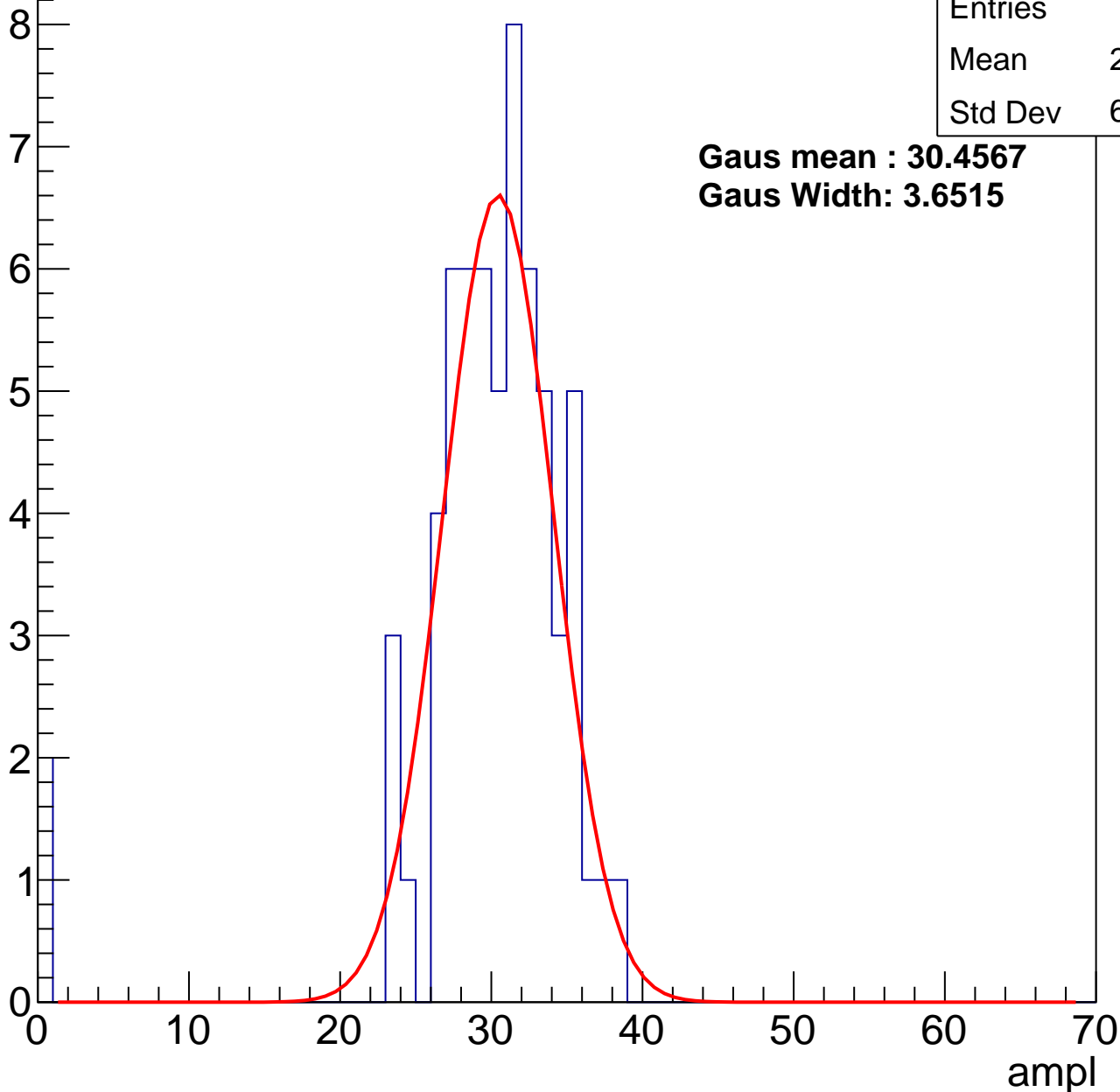
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.27
Std Dev	6.295

Gaus mean : 30.4567

Gaus Width: 3.6515



B1L100S, U25-ch76, adc1

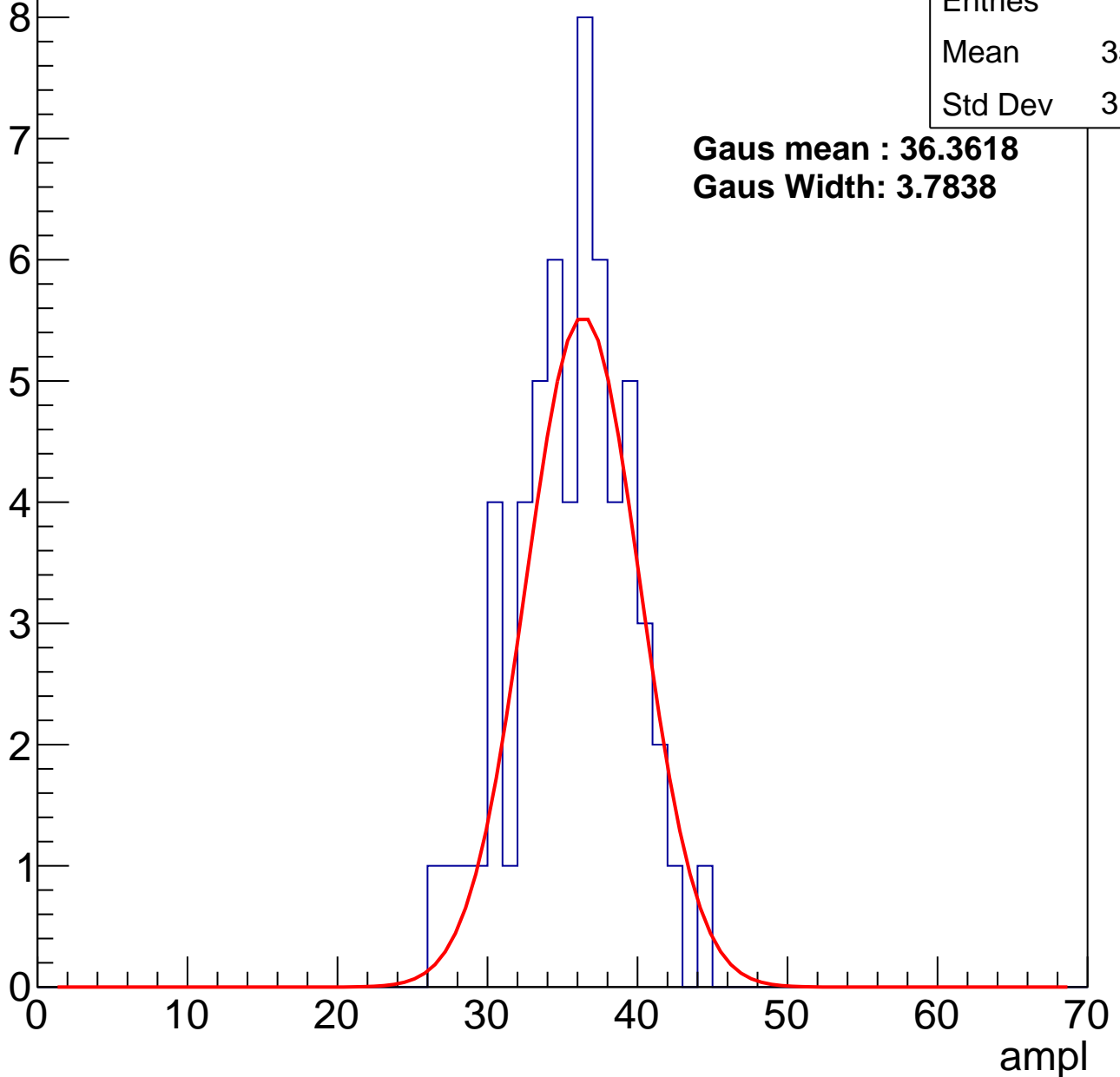
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.22
Std Dev	3.783

Gaus mean : 36.3618

Gaus Width: 3.7838



B1L100S, U25-ch76, adc2

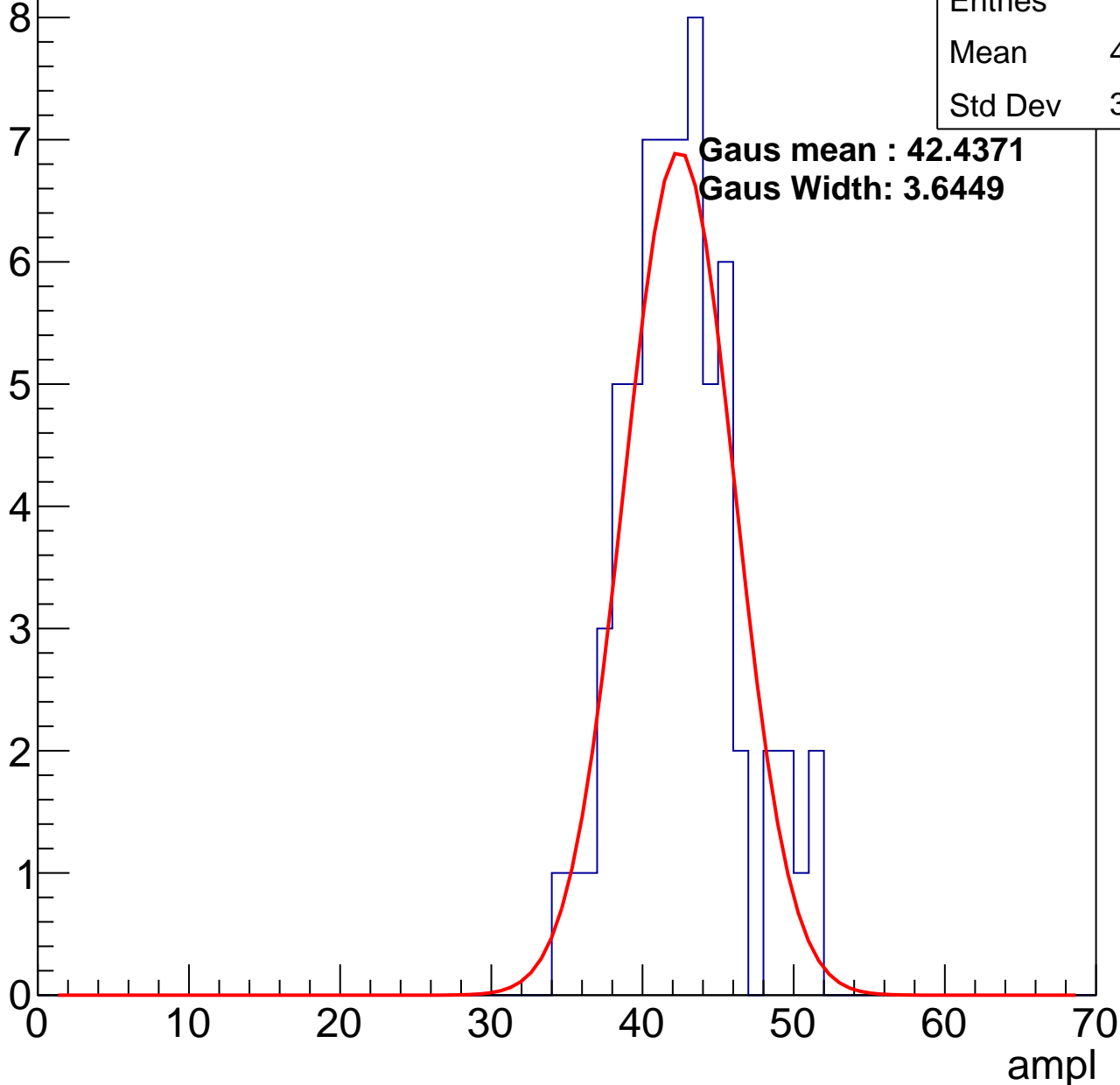
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.06
Std Dev	3.708

Gaus mean : 42.4371

Gaus Width: 3.6449



B1L100S, U25-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	48.99
Std Dev	3.697

Entry

10

8

6

4

2

0

0

10

20

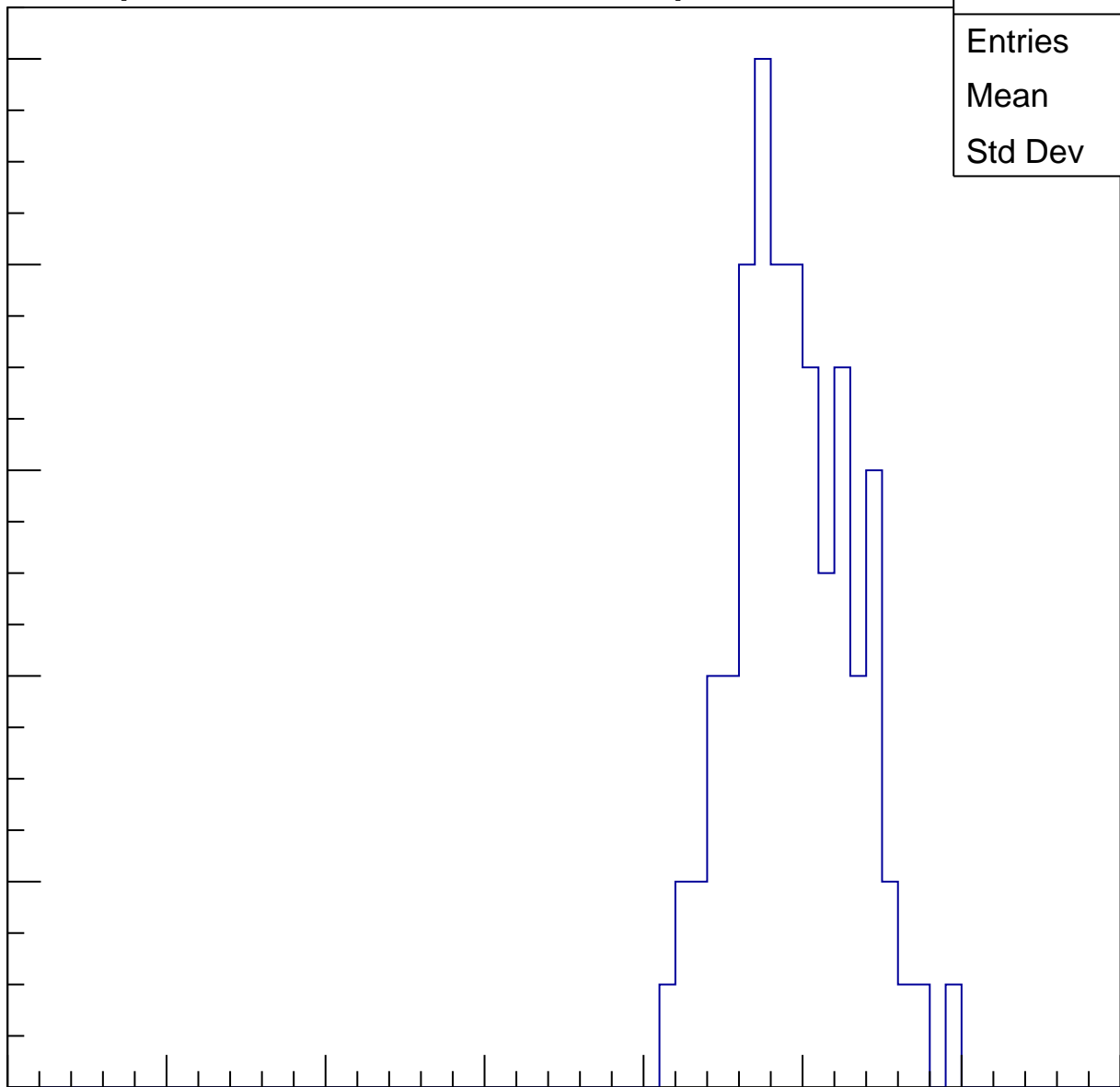
30

40

50

60

ampl

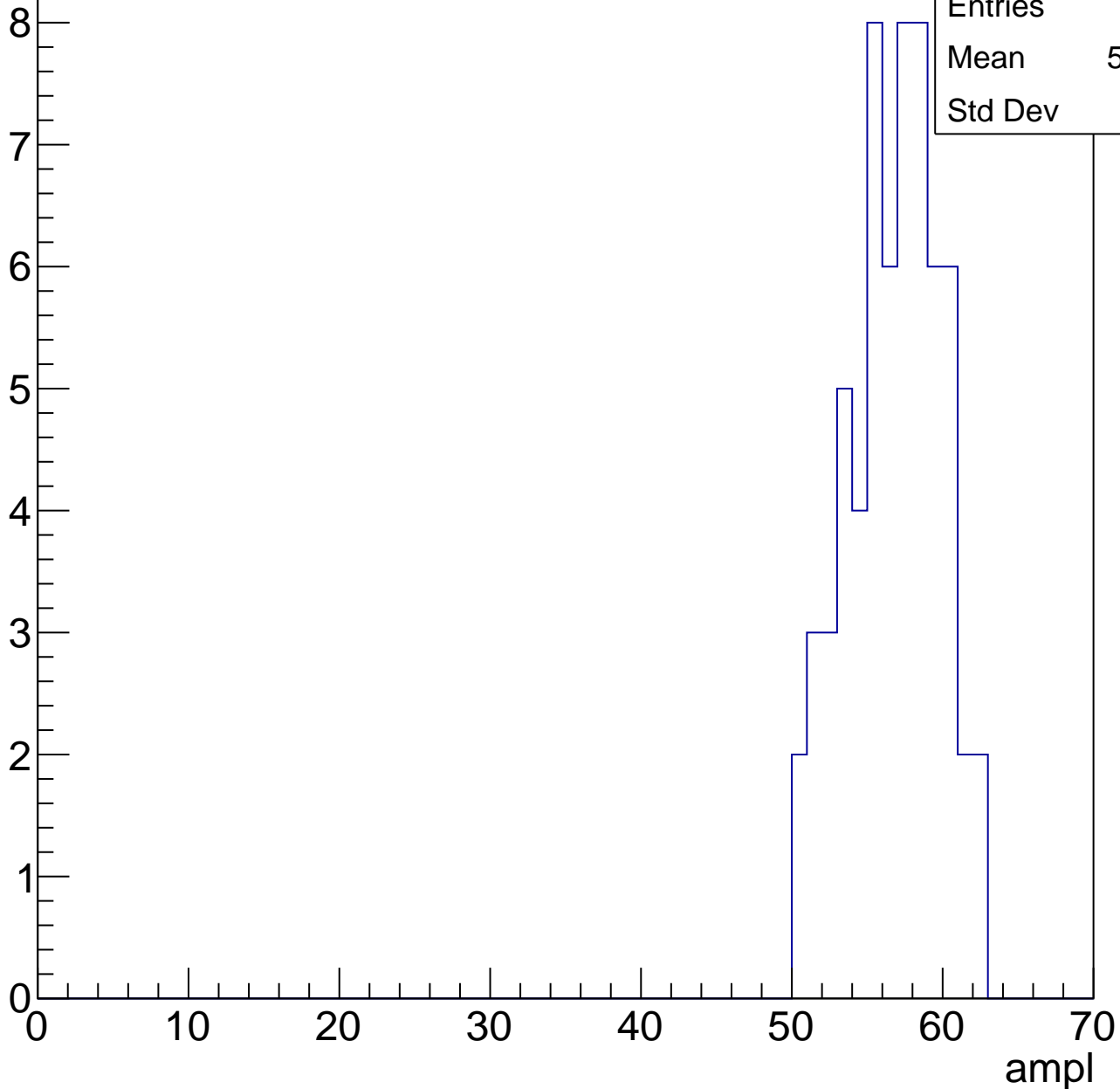


B1L100S, U25-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	56.29
Std Dev	3.01

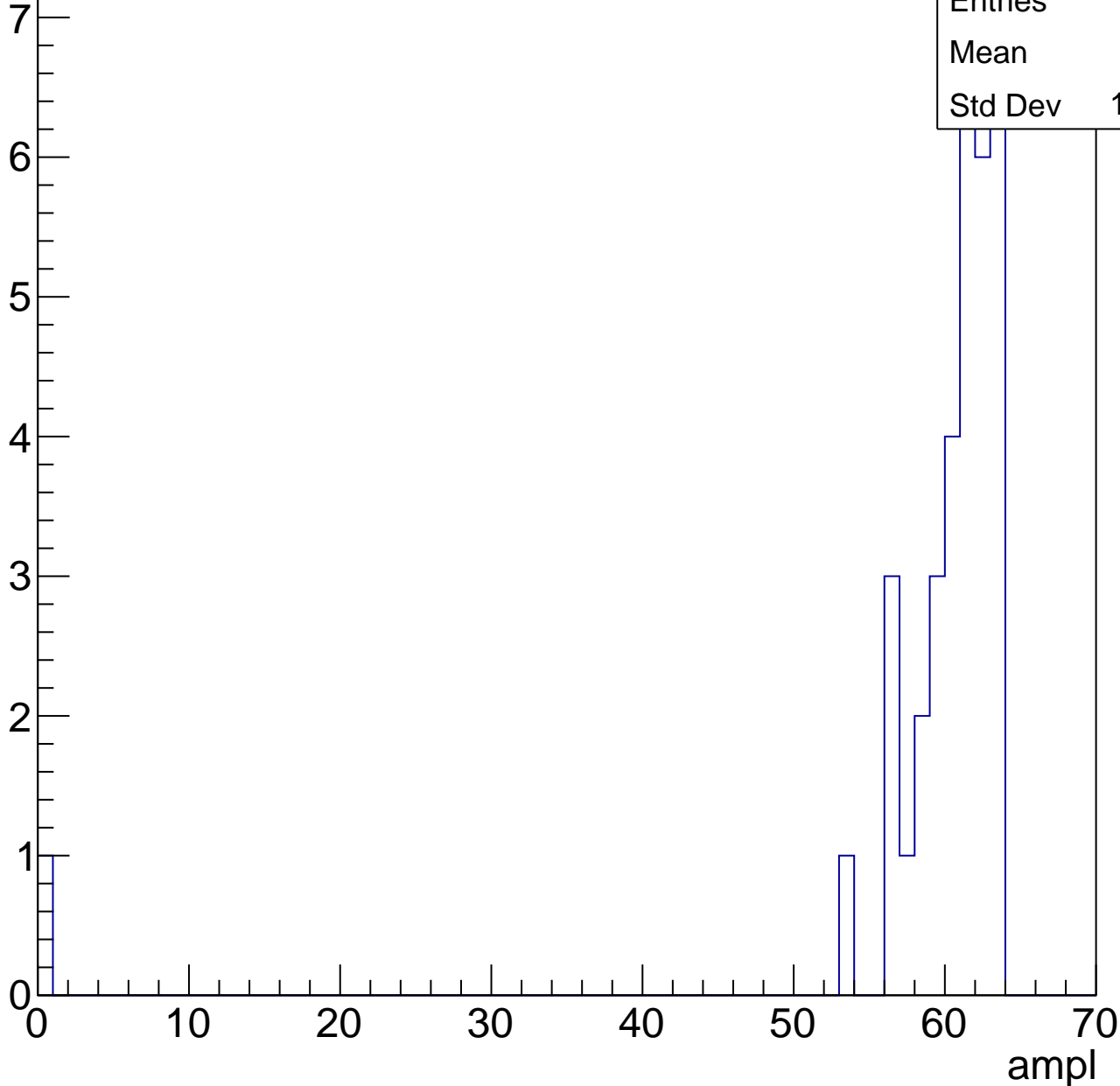


B1L100S, U25-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	58.6
Std Dev	10.34



B1L100S, U25-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch76, adc7

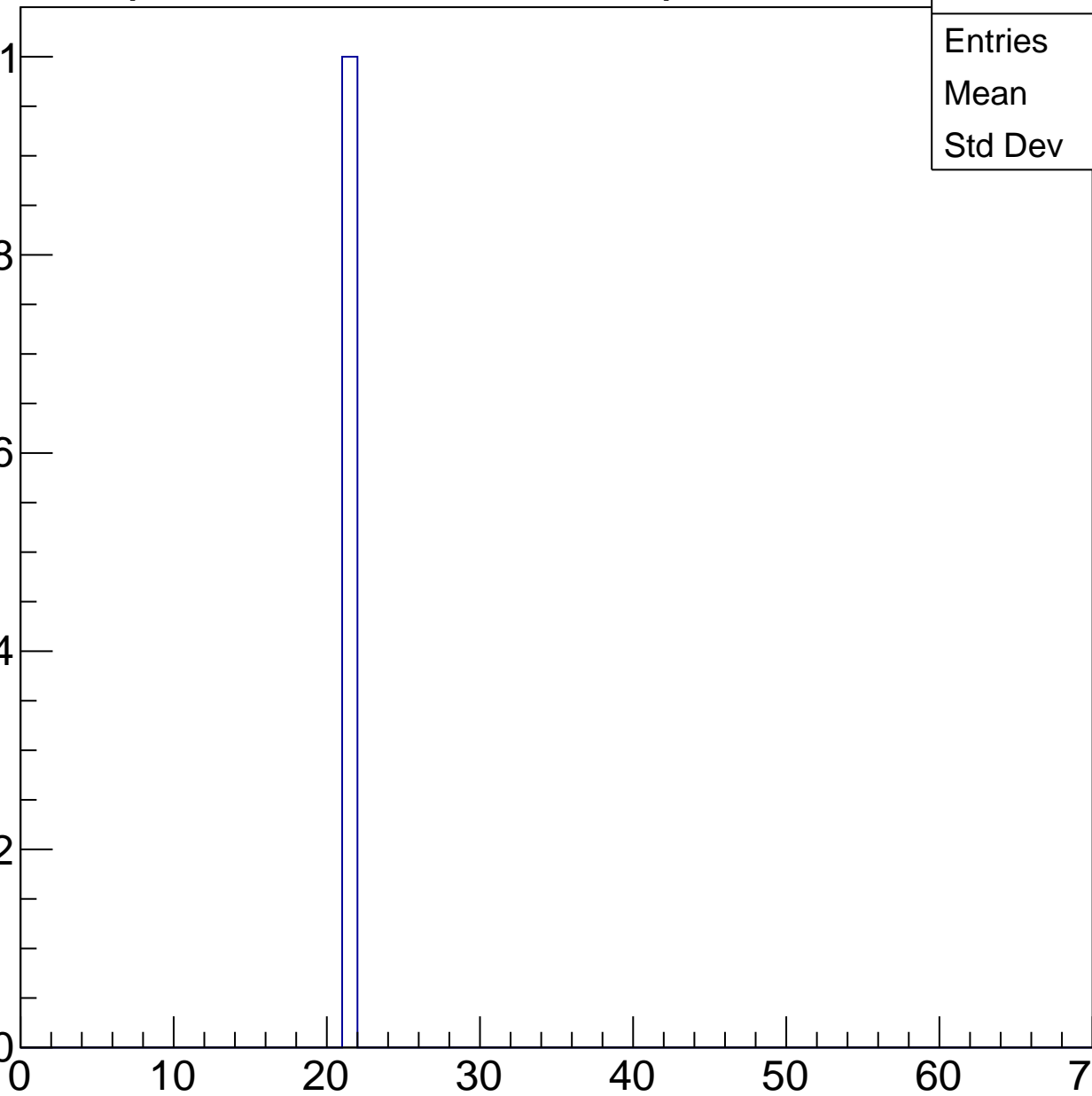
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl



B1L100S, U25-ch77, adc0

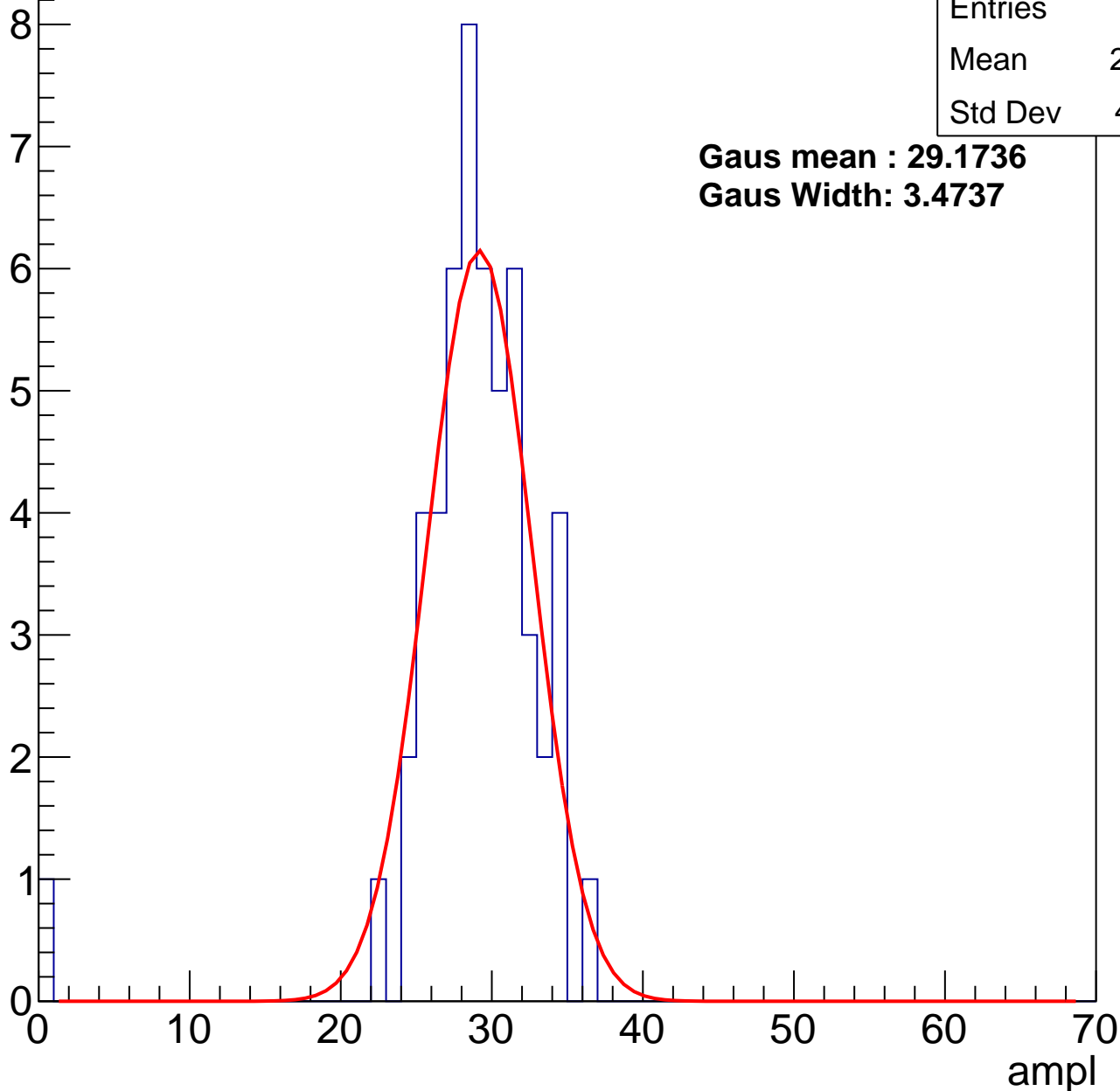
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	28.38
Std Dev	4.931

Gaus mean : 29.1736

Gaus Width: 3.4737



B1L100S, U25-ch77, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	35.39
Std Dev	3.706

Gaus mean : 35.8538

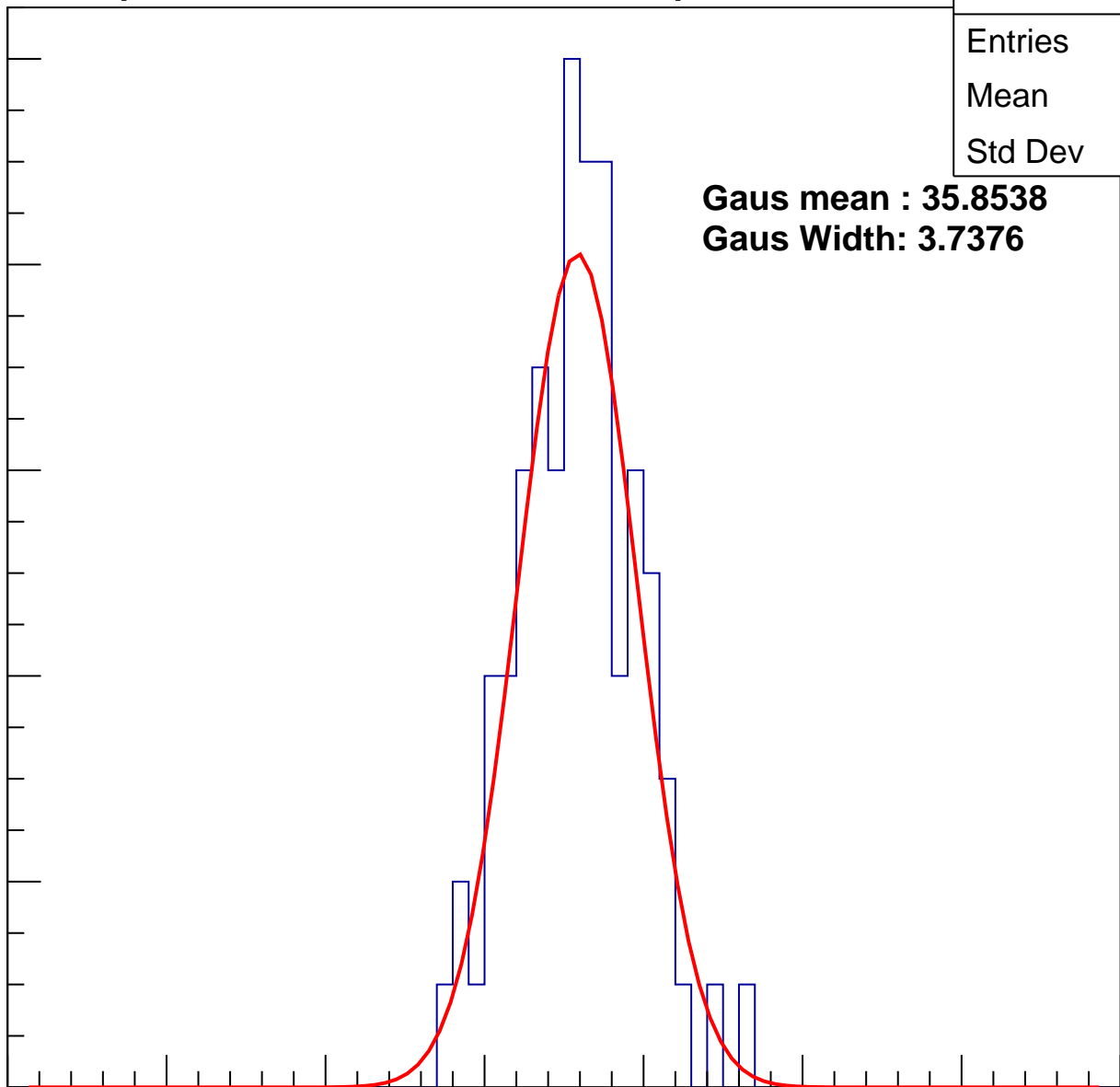
Gaus Width: 3.7376

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



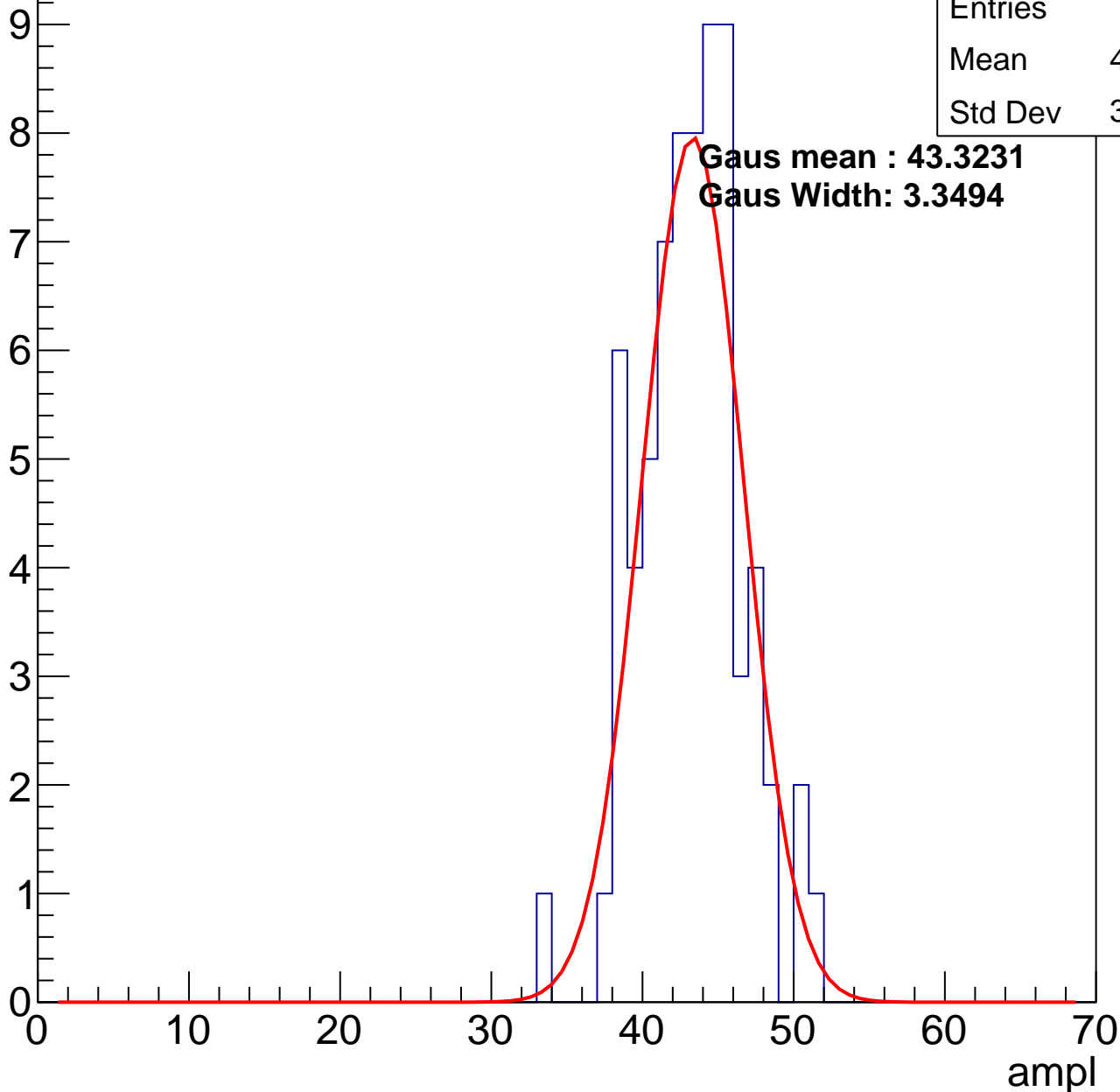
B1L100S, U25-ch77, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.79
Std Dev	3.329

Gaus mean : 43.3231
Gaus Width: 3.3494

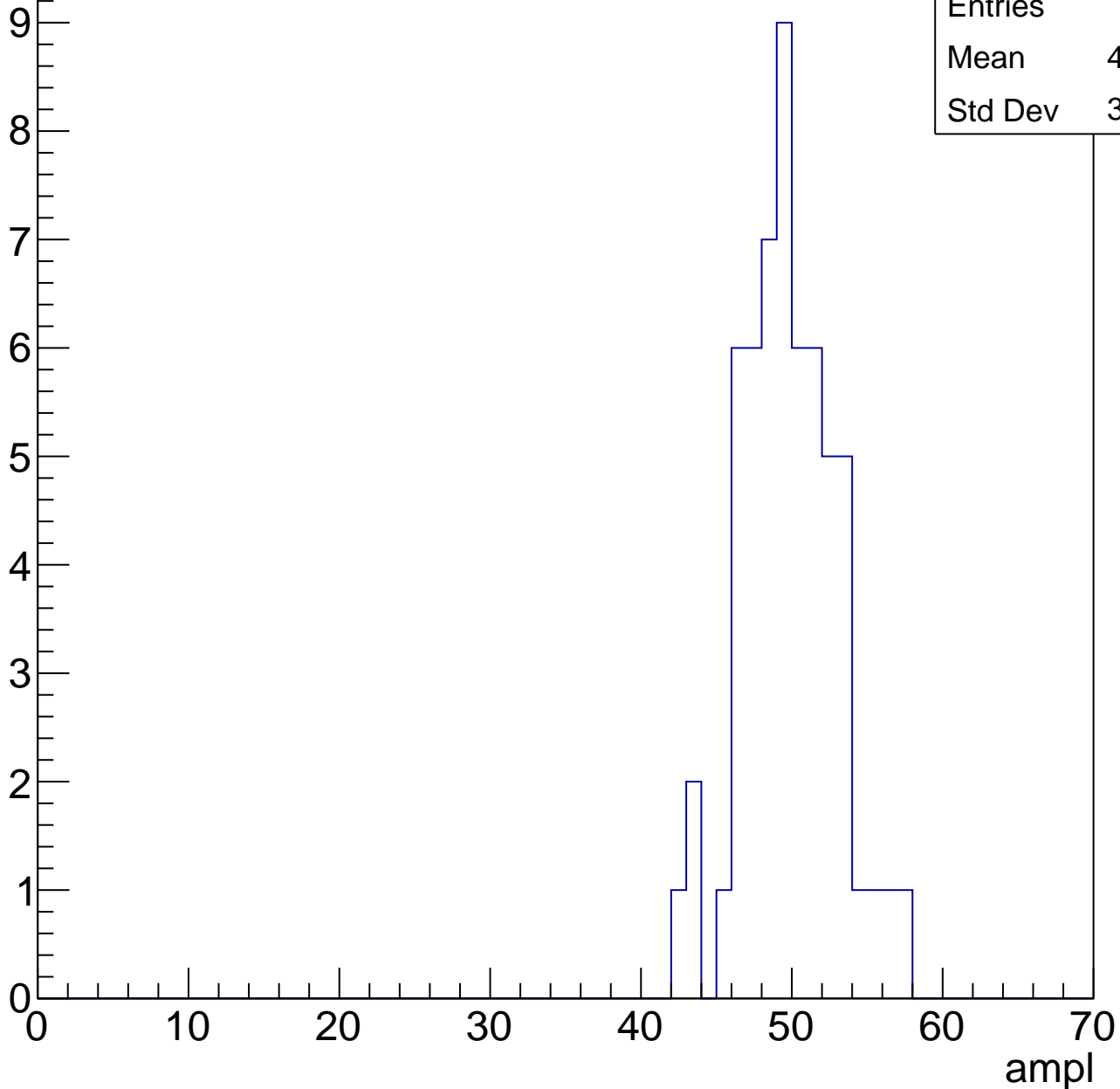


B1L100S, U25-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.33
Std Dev	3.065

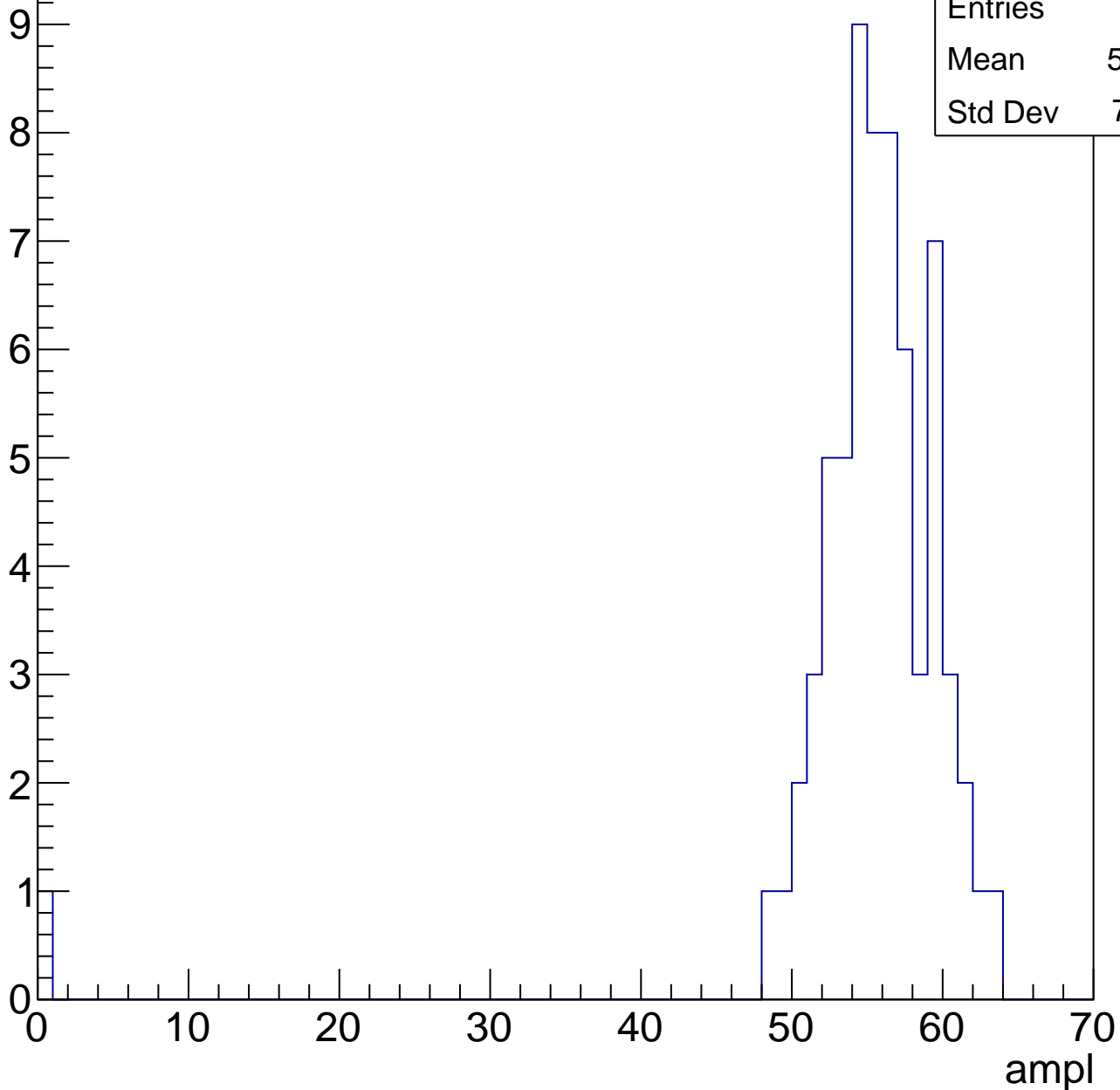


B1L100S, U25-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	54.62
Std Dev	7.491



B1L100S, U25-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	43
Mean	60.16
Std Dev	2.134

B1L100S, U25-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch78, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	29.49
Std Dev	3.895

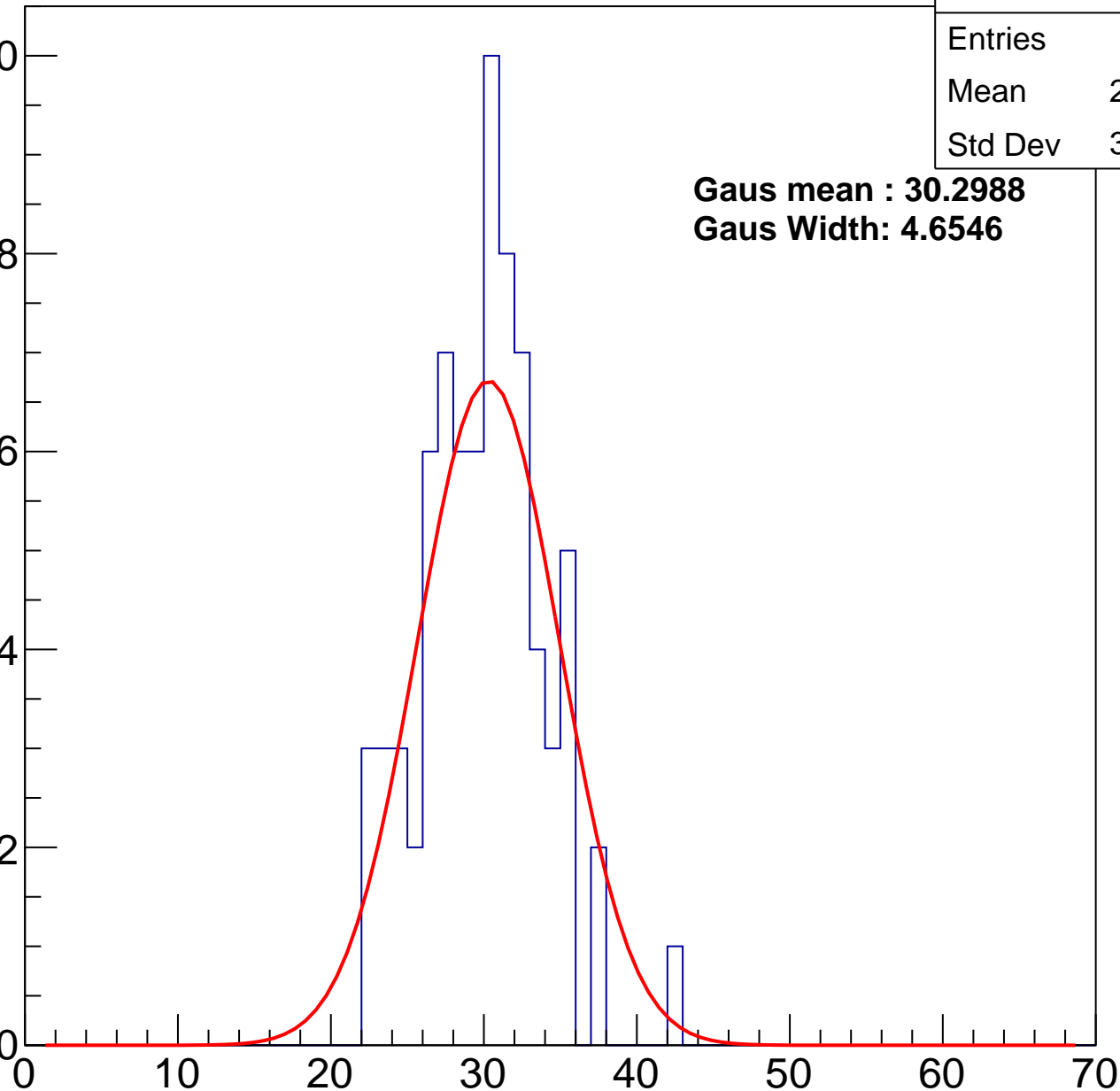
Gaus mean : 30.2988

Gaus Width: 4.6546

Entry

10
8
6
4
2
0

ampl

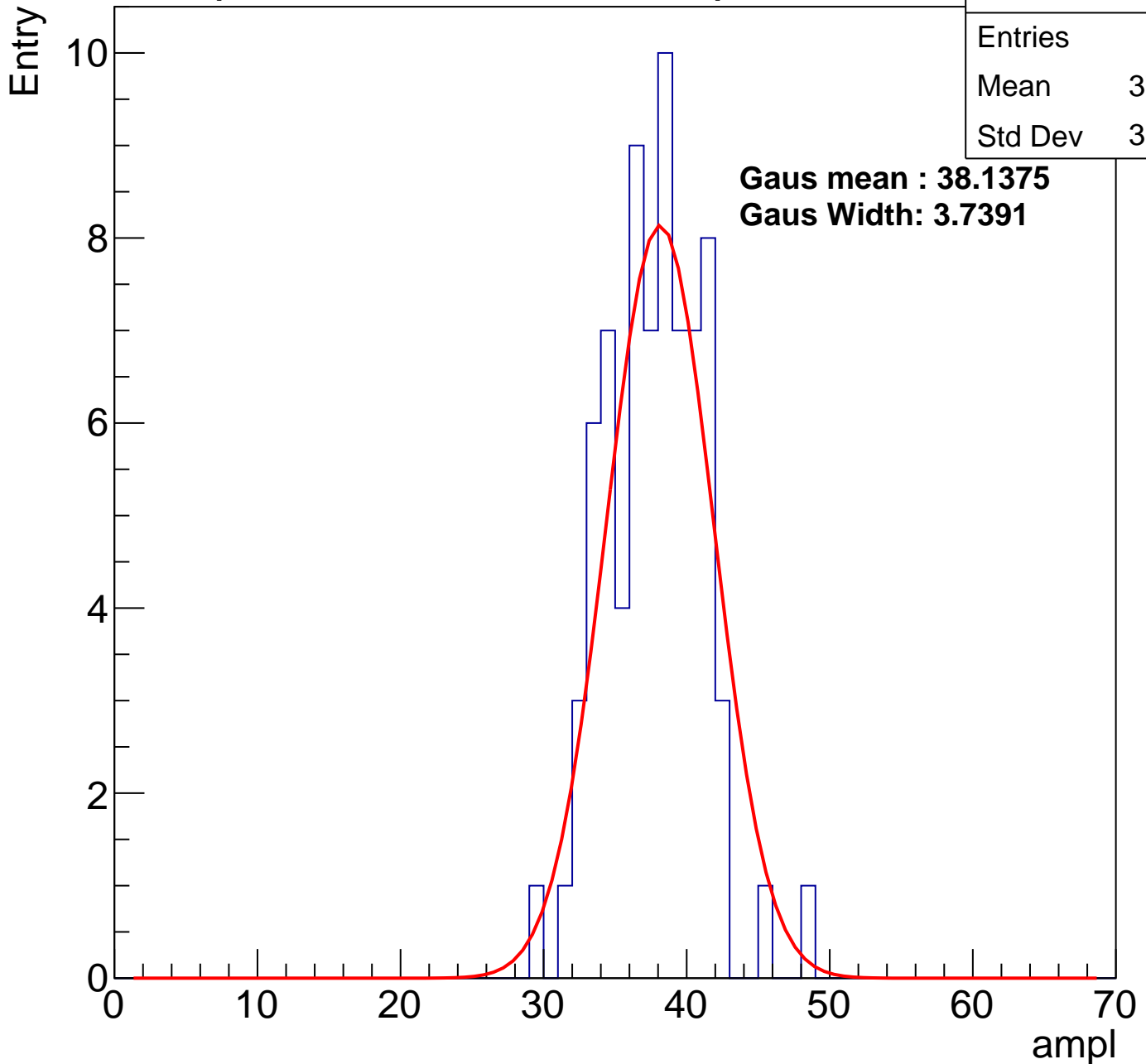


B1L100S, U25-ch78, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	37.27
Std Dev	3.352

Gaus mean : 38.1375
Gaus Width: 3.7391



B1L100S, U25-ch78, adc2

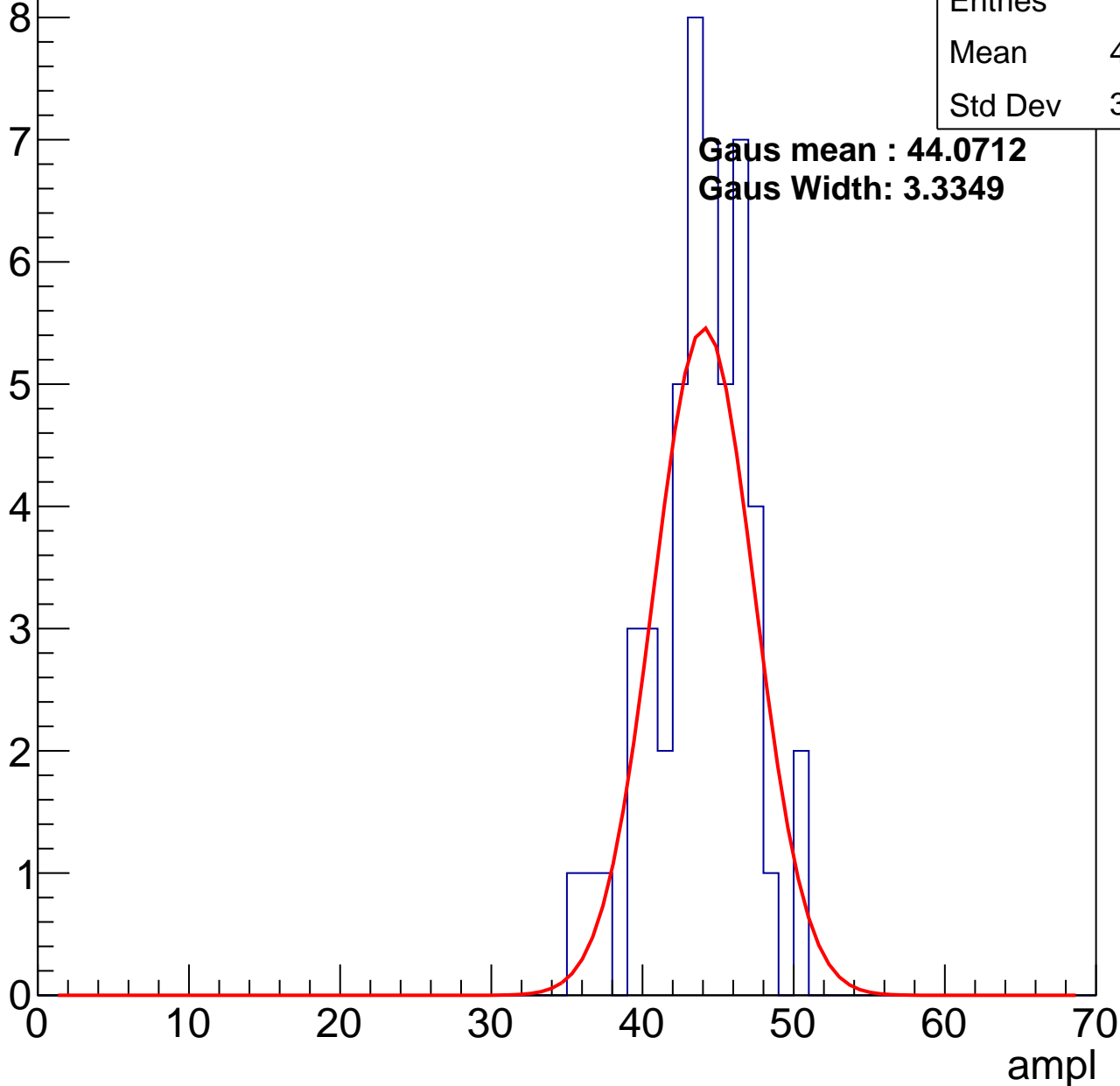
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	43.44
Std Dev	3.176

Gaus mean : 44.0712

Gaus Width: 3.3349

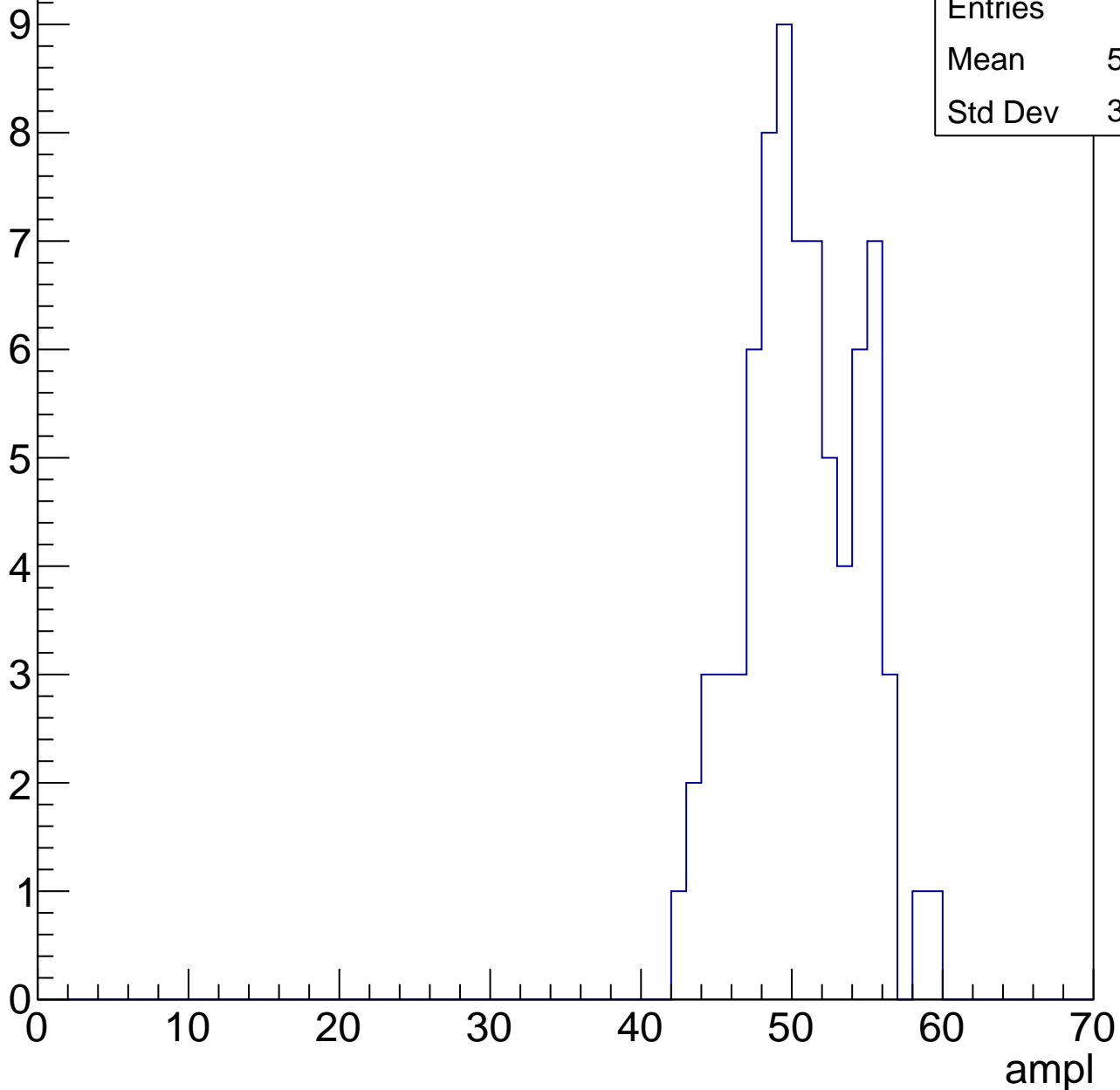


B1L100S, U25-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

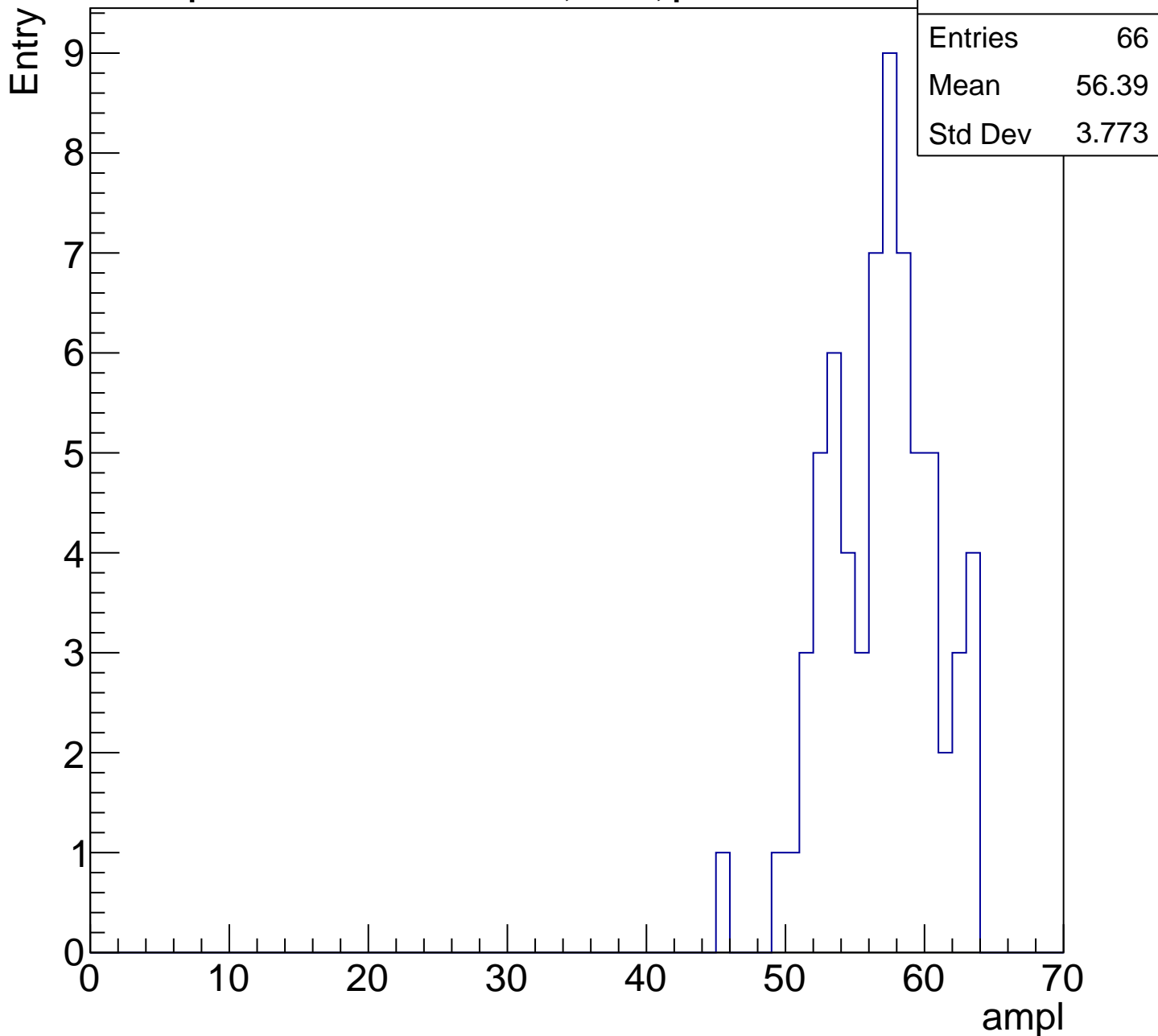
Entry

Entries	76
Mean	50.17
Std Dev	3.757



B1L100S, U25-ch78, adc4

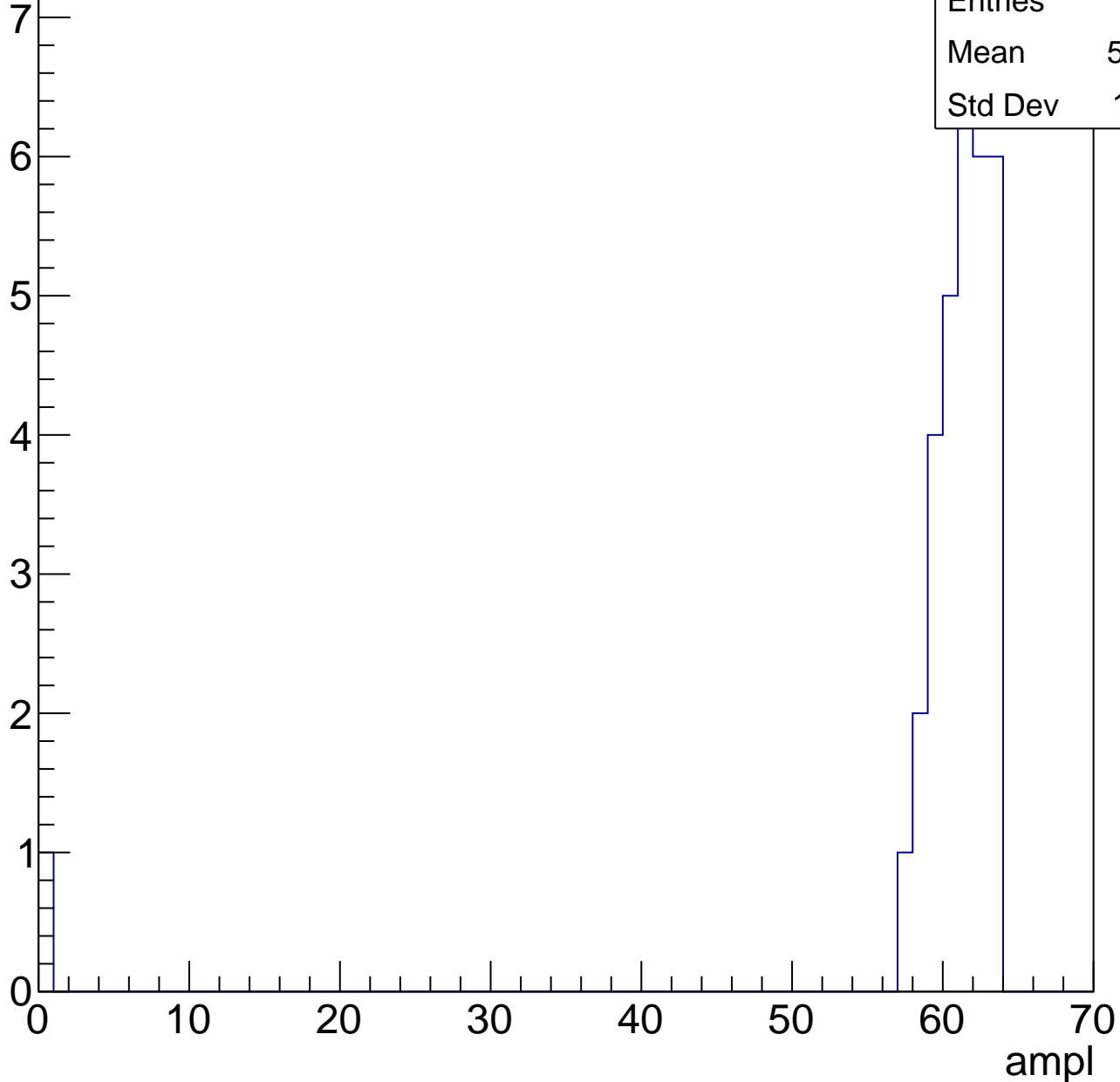
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

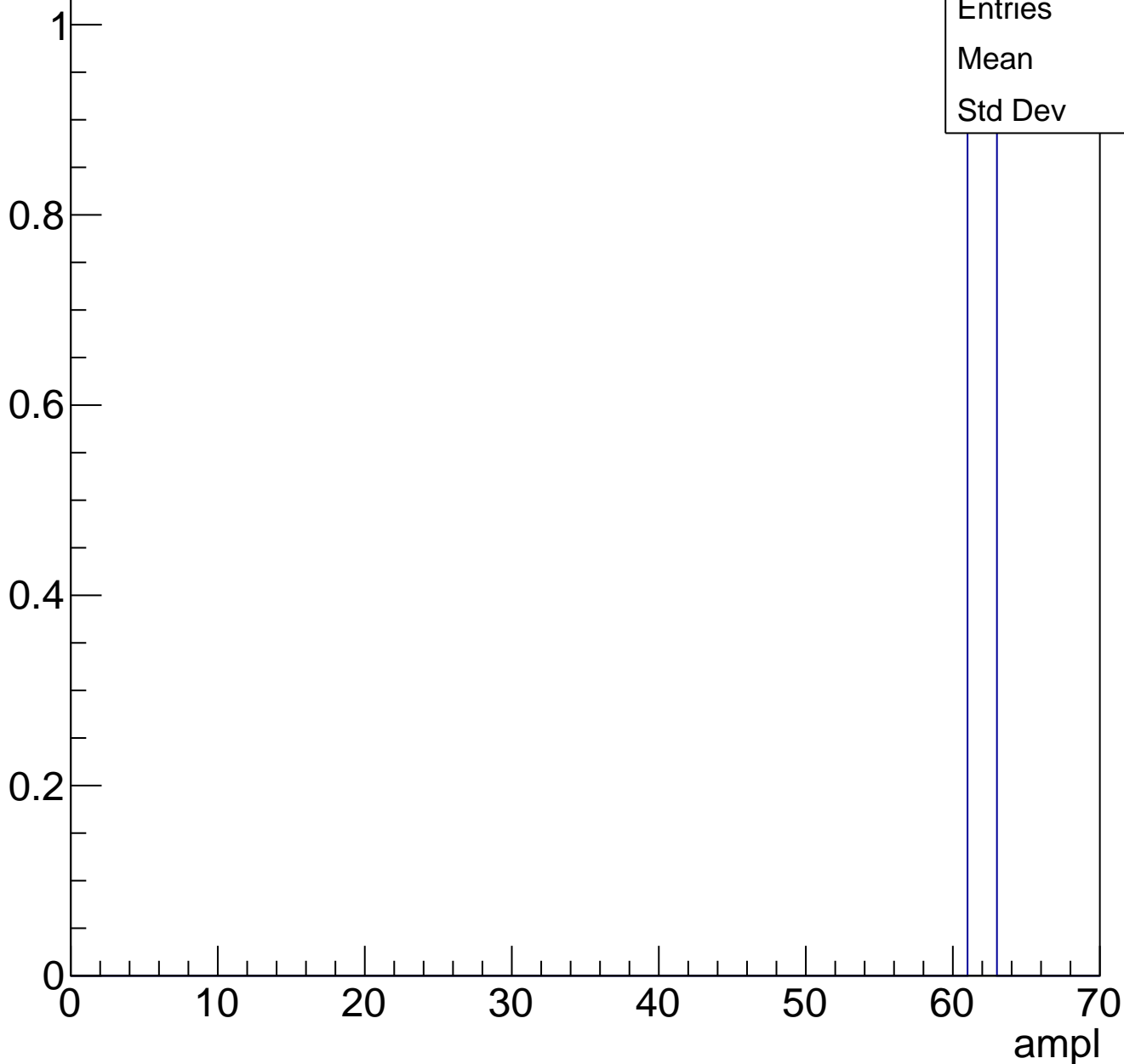


Entries	32
Mean	58.94
Std Dev	10.71

B1L100S, U25-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch79, adc0

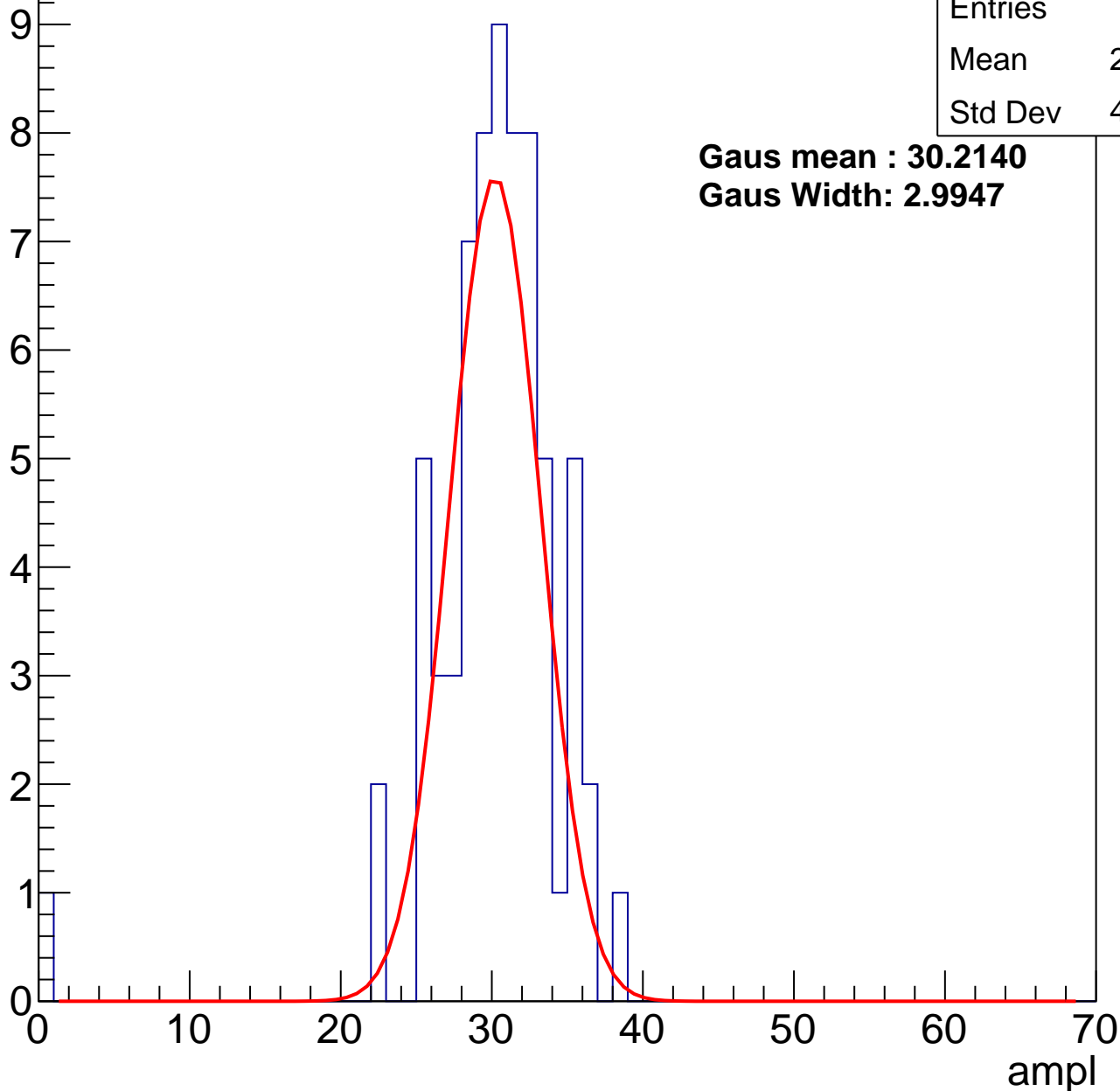
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.62
Std Dev	4.875

Gaus mean : 30.2140

Gaus Width: 2.9947



B1L100S, U25-ch79, adc1

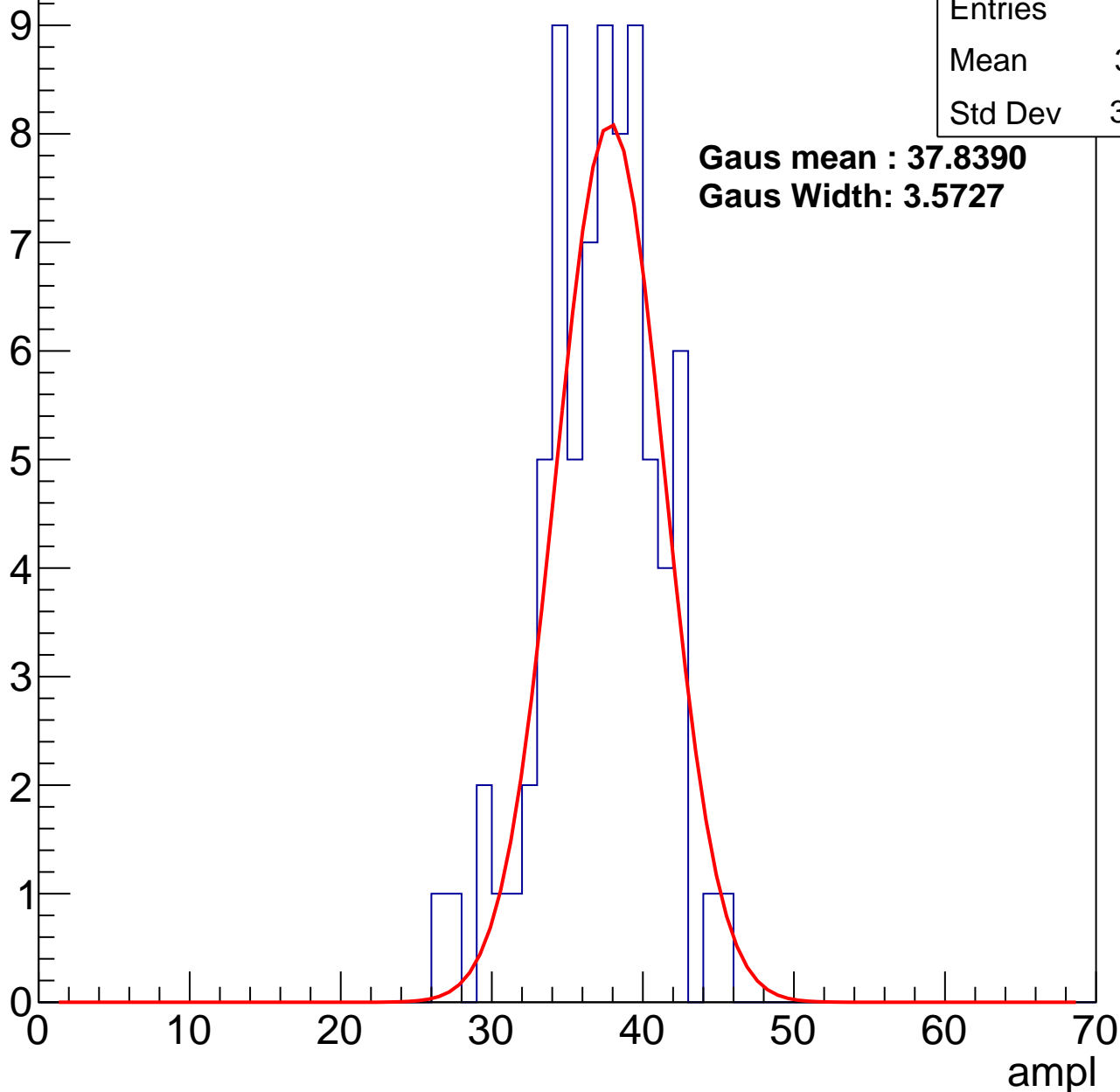
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	36.71
Std Dev	3.748

Gaus mean : 37.8390

Gaus Width: 3.5727



B1L100S, U25-ch79, adc2

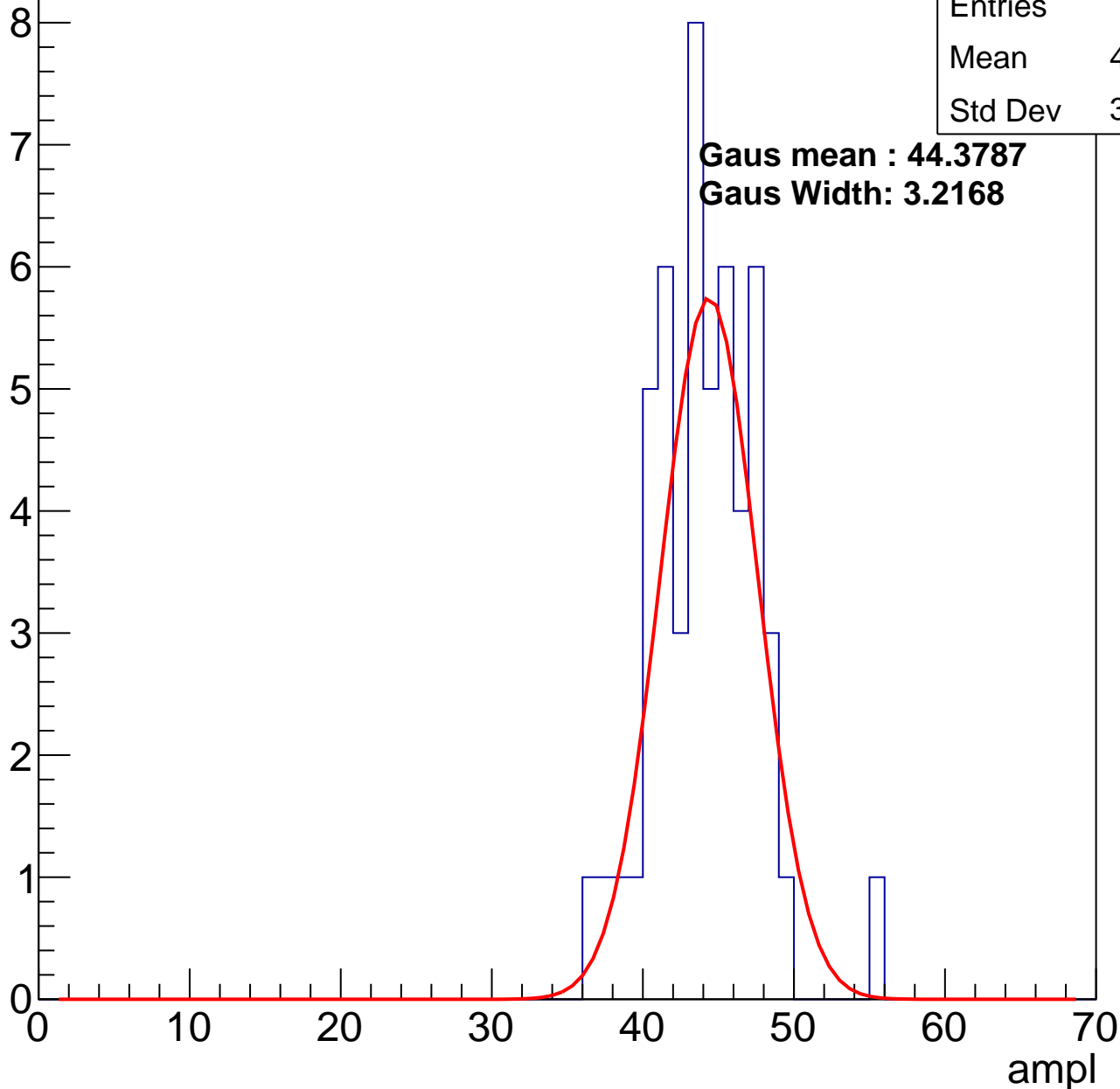
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.65
Std Dev	3.373

Gaus mean : 44.3787

Gaus Width: 3.2168



B1L100S, U25-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	50.08
Std Dev	3.691

Entry

10

8

6

4

2

0

0

10

20

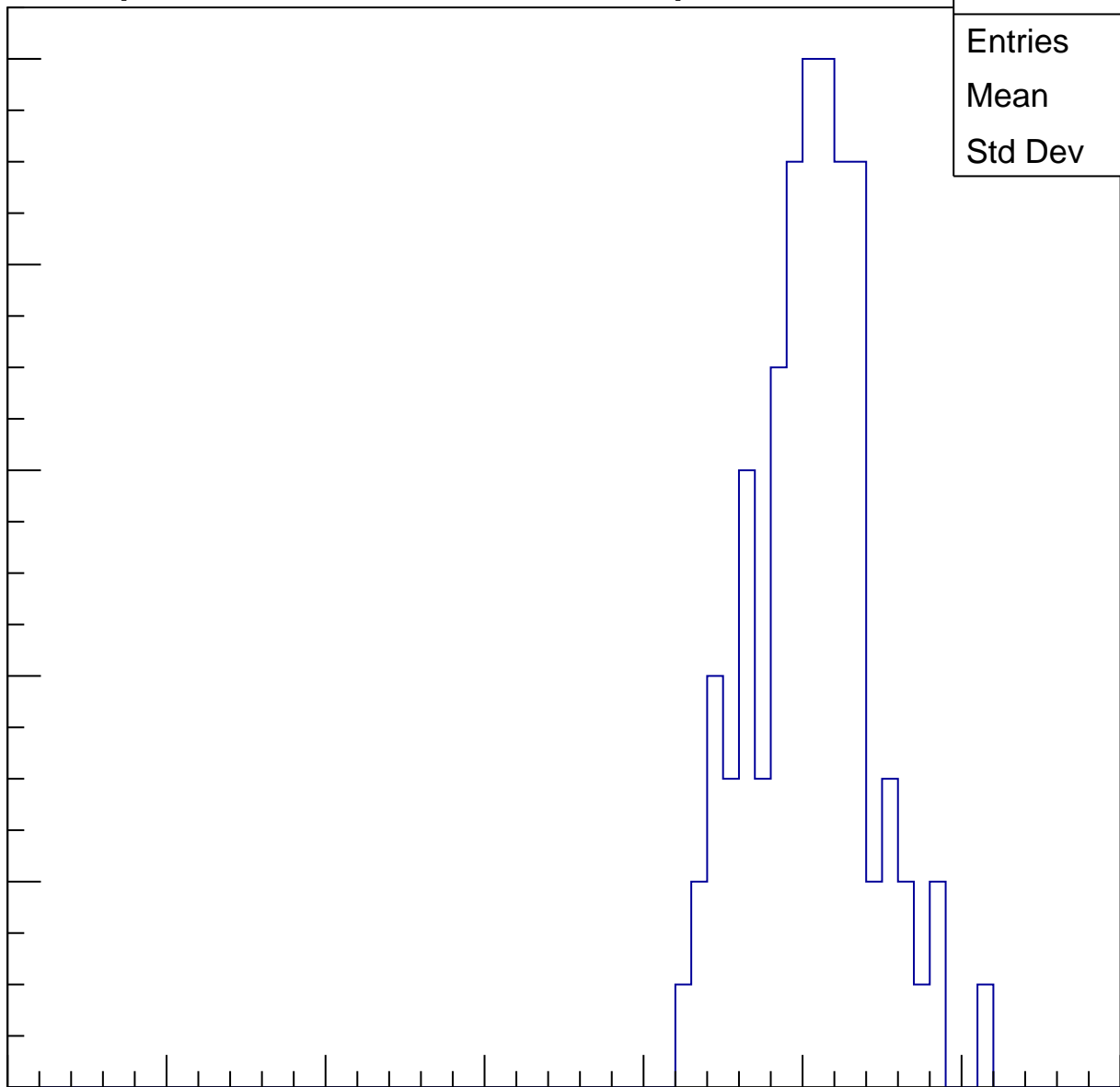
30

40

50

60

ampl

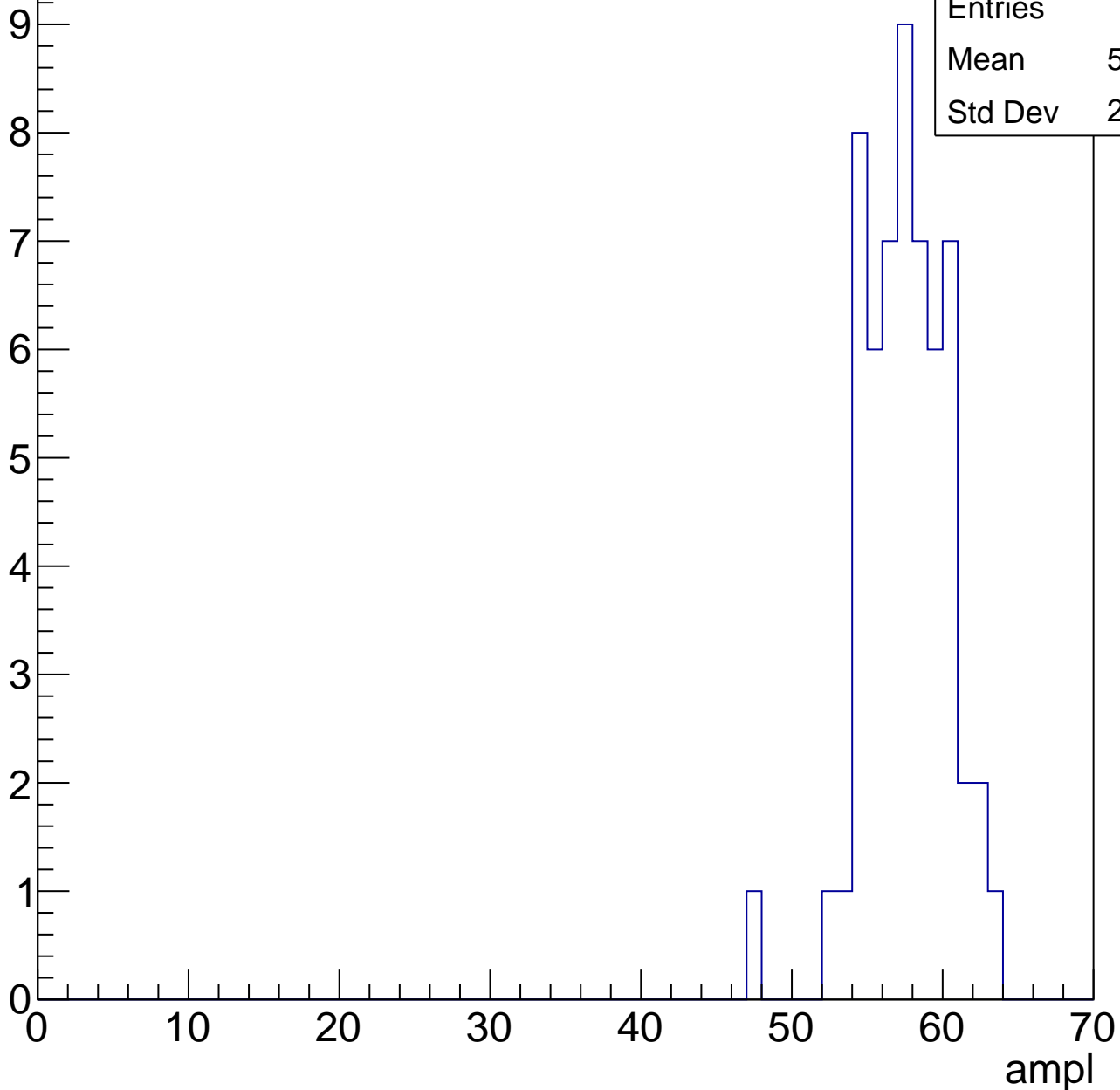


B1L100S, U25-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	57.03
Std Dev	2.804

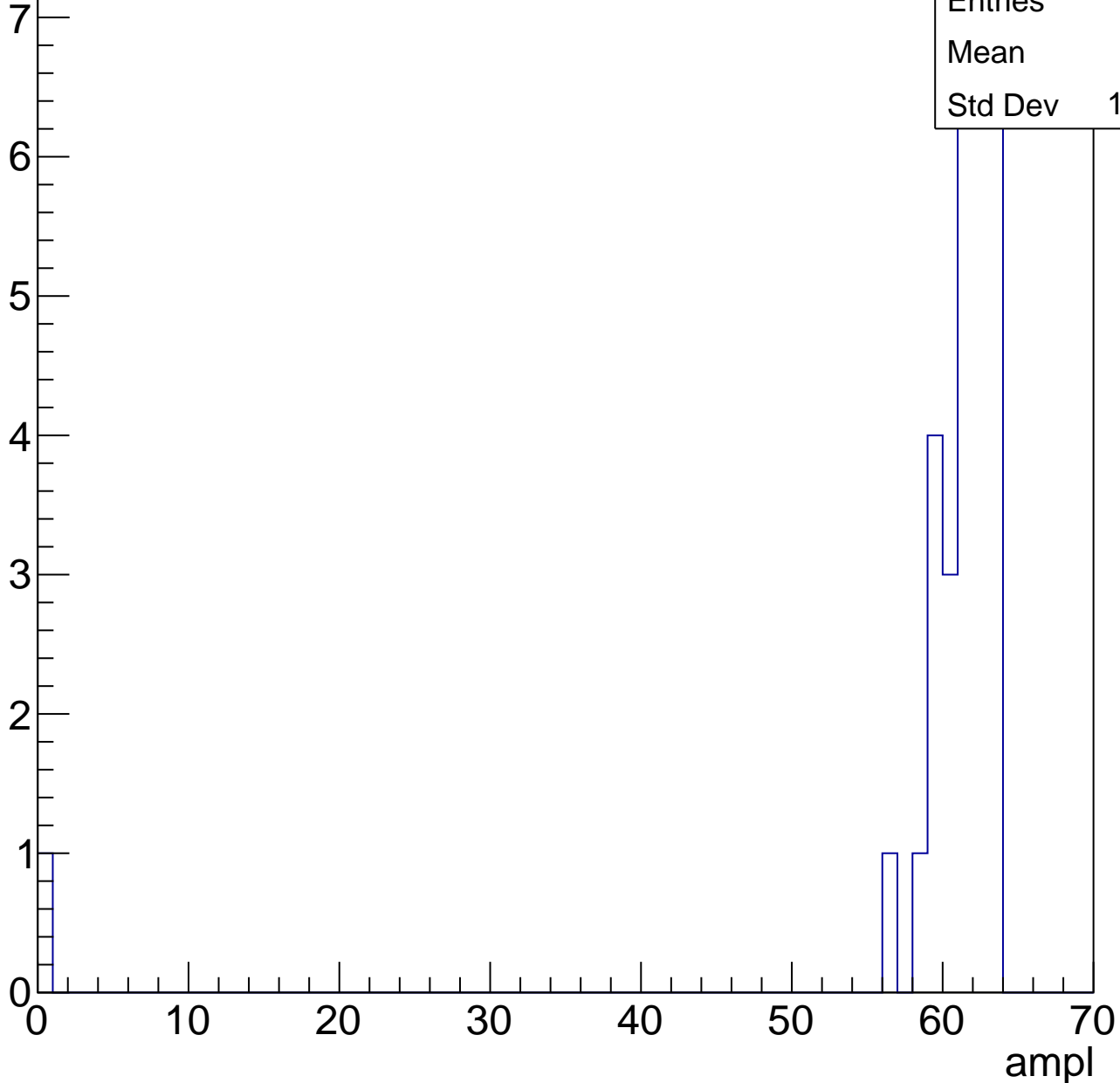


B1L100S, U25-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	59.1
Std Dev	10.92



B1L100S, U25-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch80, adc0

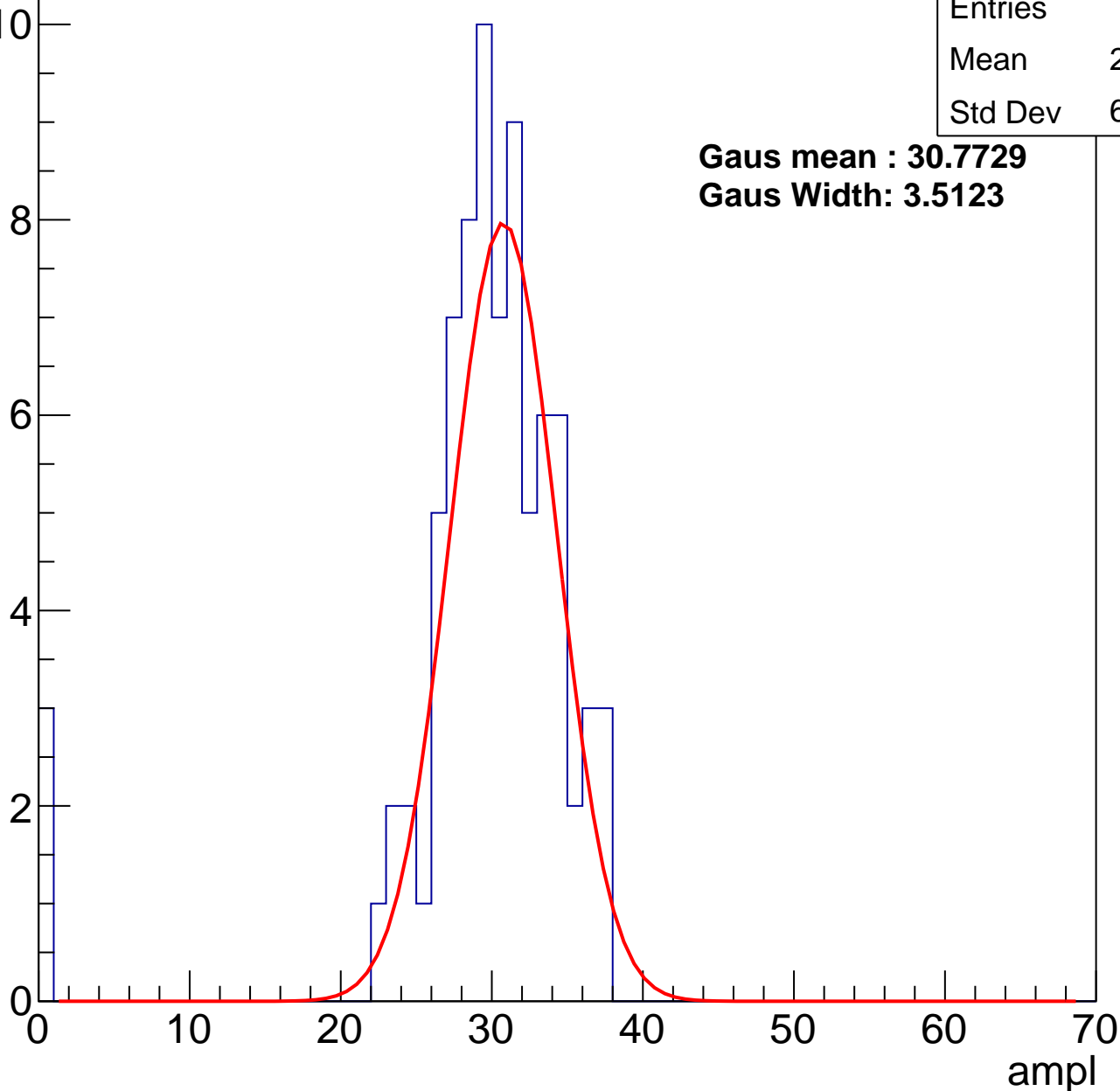
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	28.93
Std Dev	6.648

Gaus mean : 30.7729

Gaus Width: 3.5123



B1L100S, U25-ch80, adc1

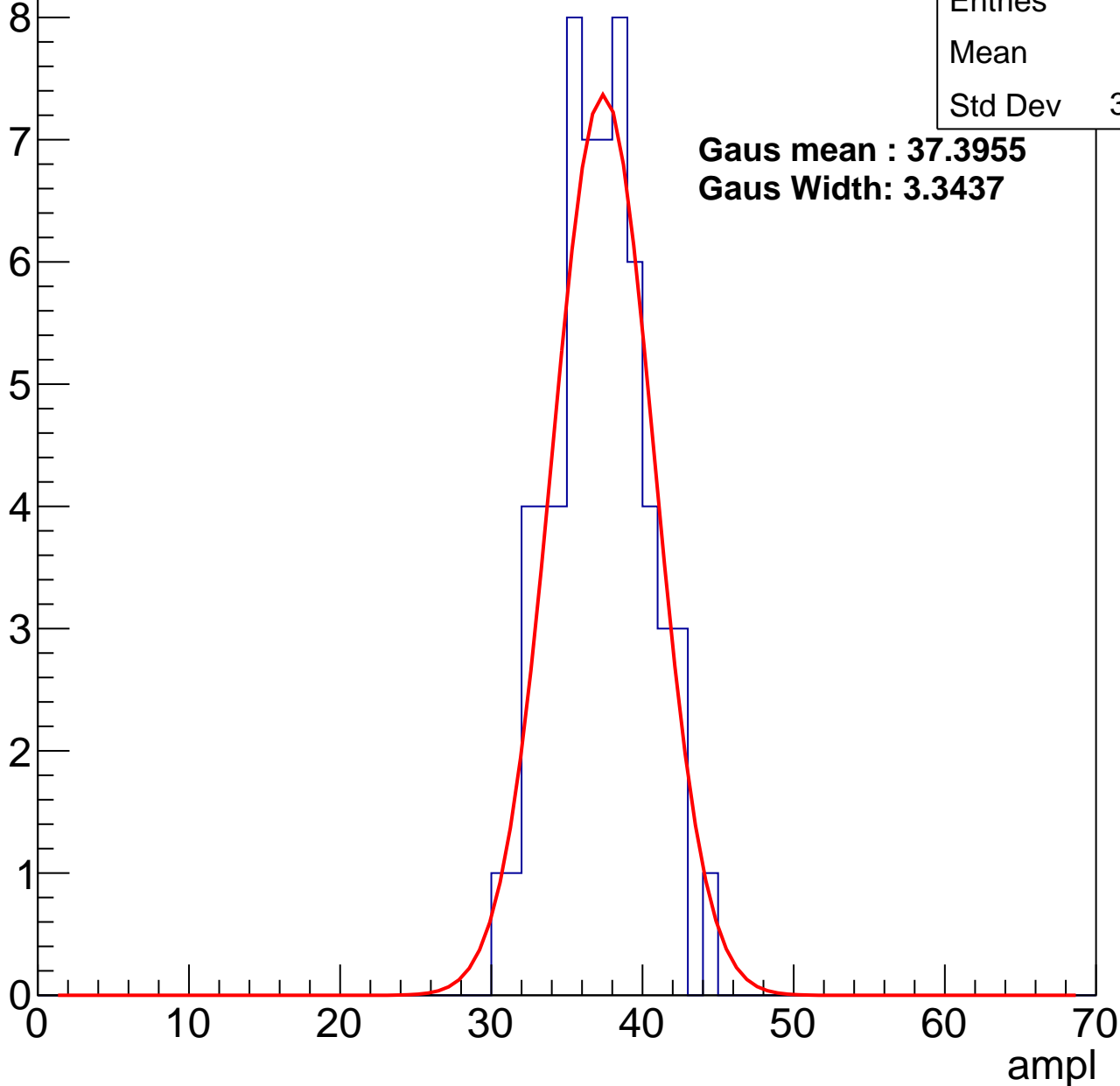
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.7
Std Dev	3.026

Gaus mean : 37.3955

Gaus Width: 3.3437



B1L100S, U25-ch80, adc2

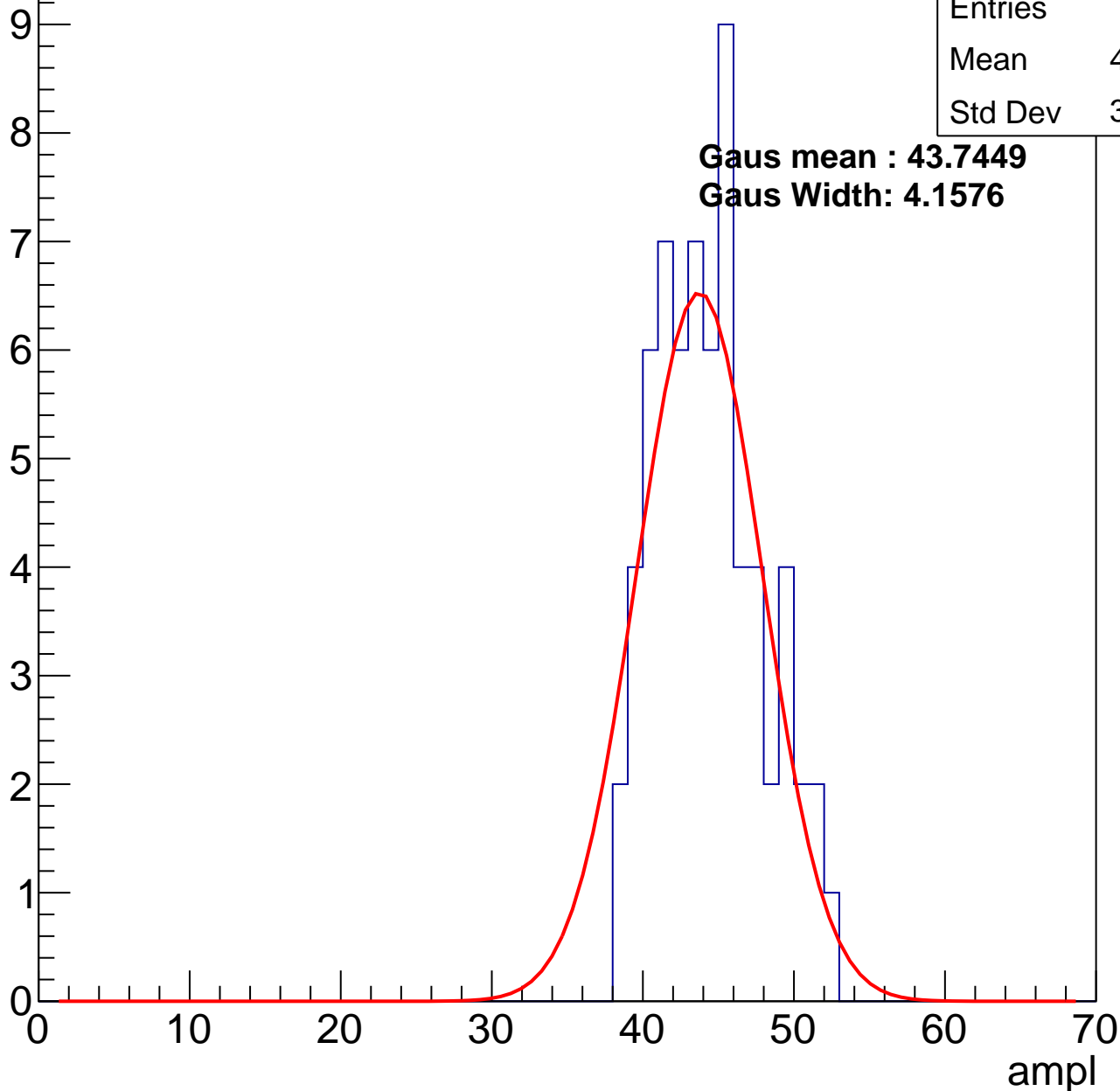
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.92
Std Dev	3.457

Gaus mean : 43.7449

Gaus Width: 4.1576

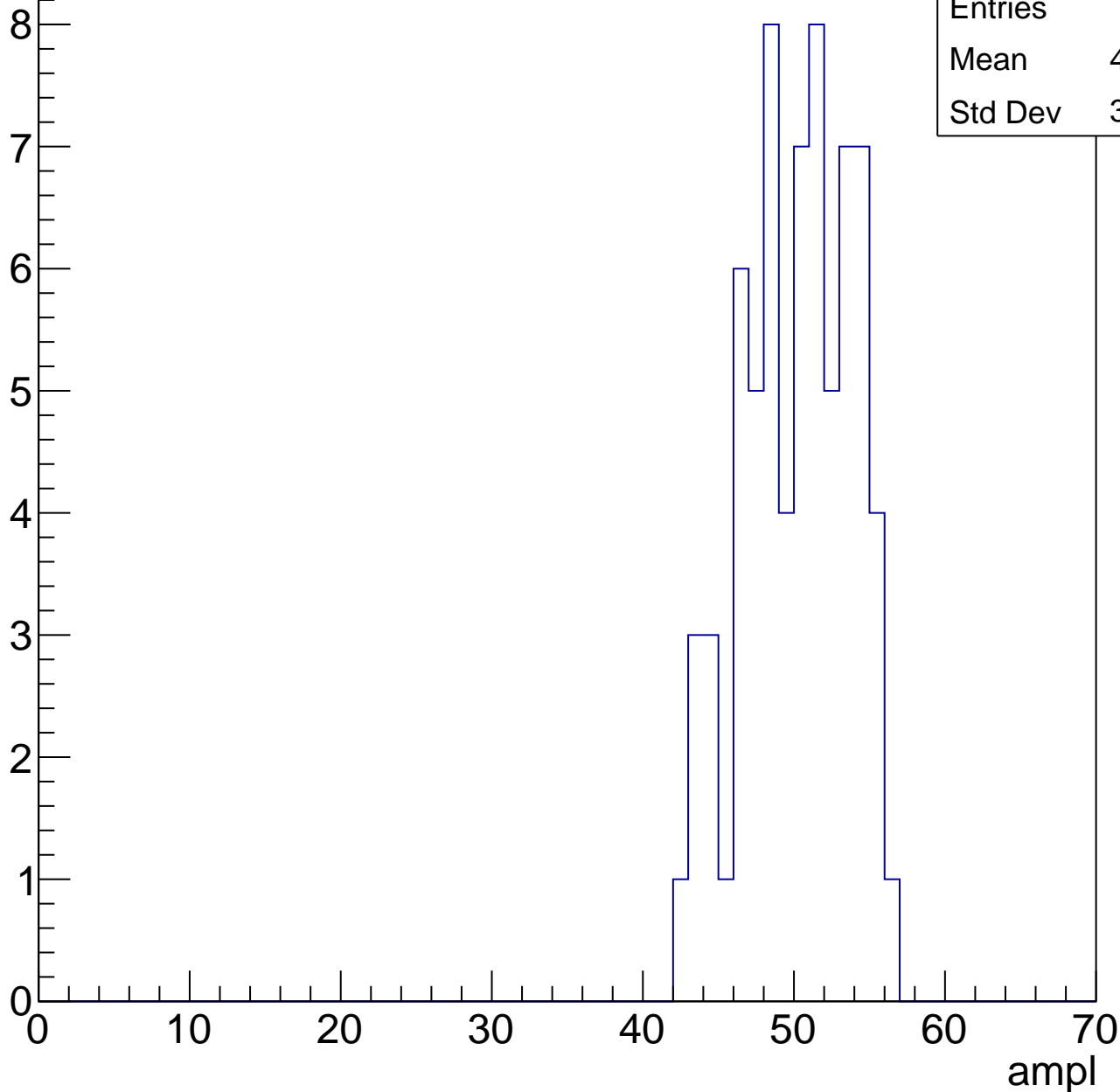


B1L100S, U25-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	49.74
Std Dev	3.496

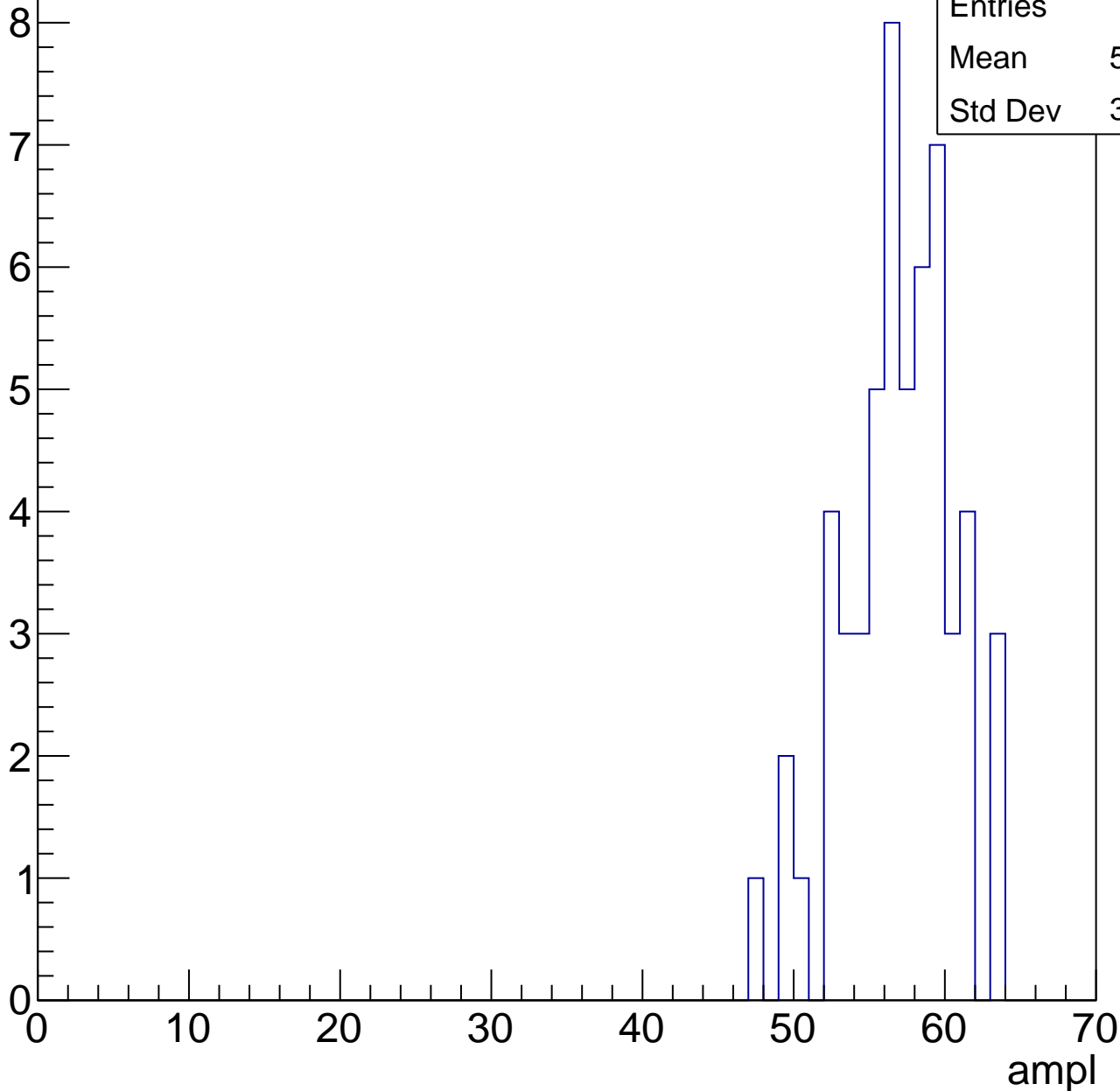


B1L100S, U25-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	56.47
Std Dev	3.562

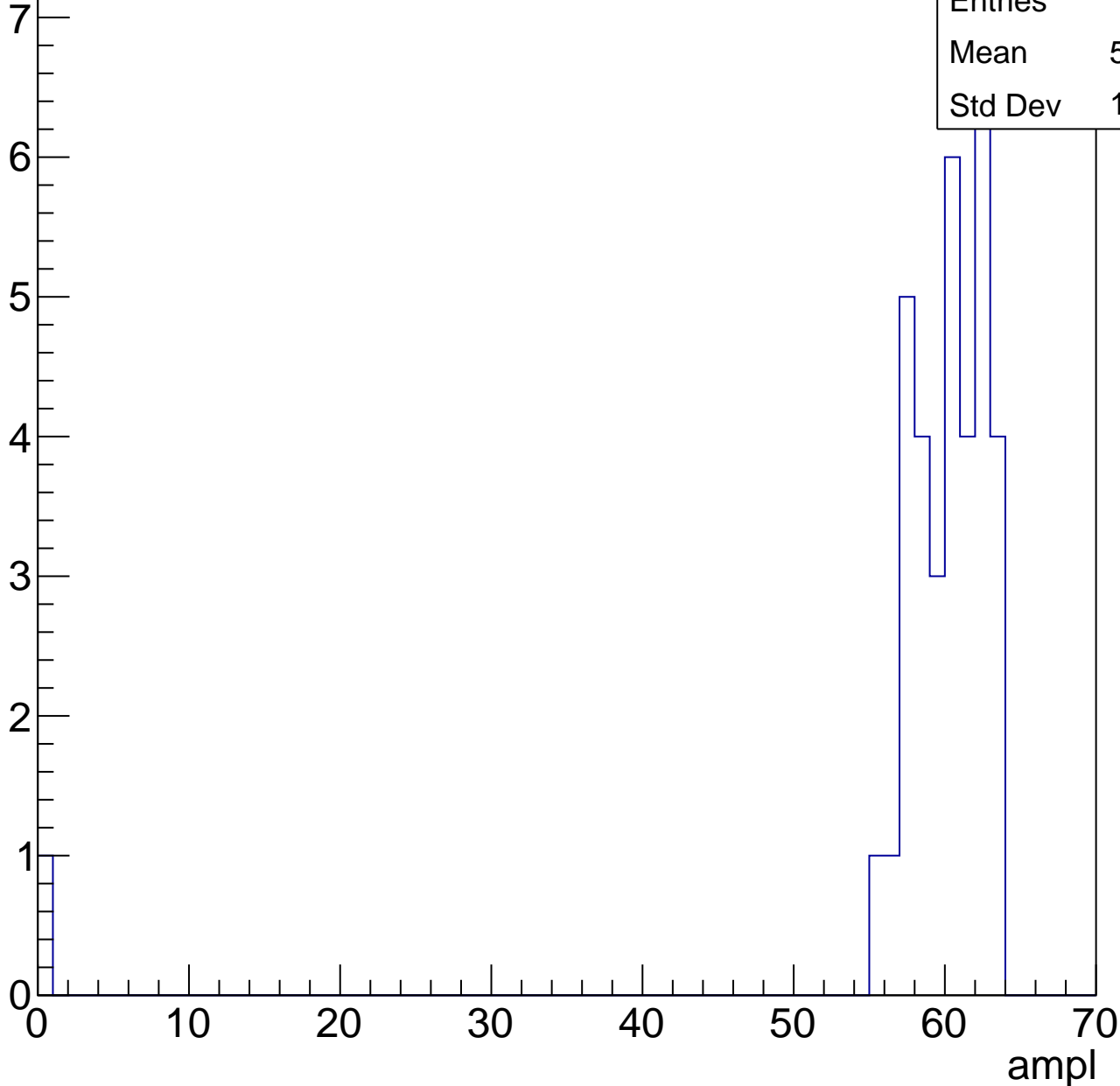


B1L100S, U25-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.19
Std Dev	10.08



B1L100S, U25-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.89

Std Dev

0.9938

B1L100S, U25-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch81, adc0

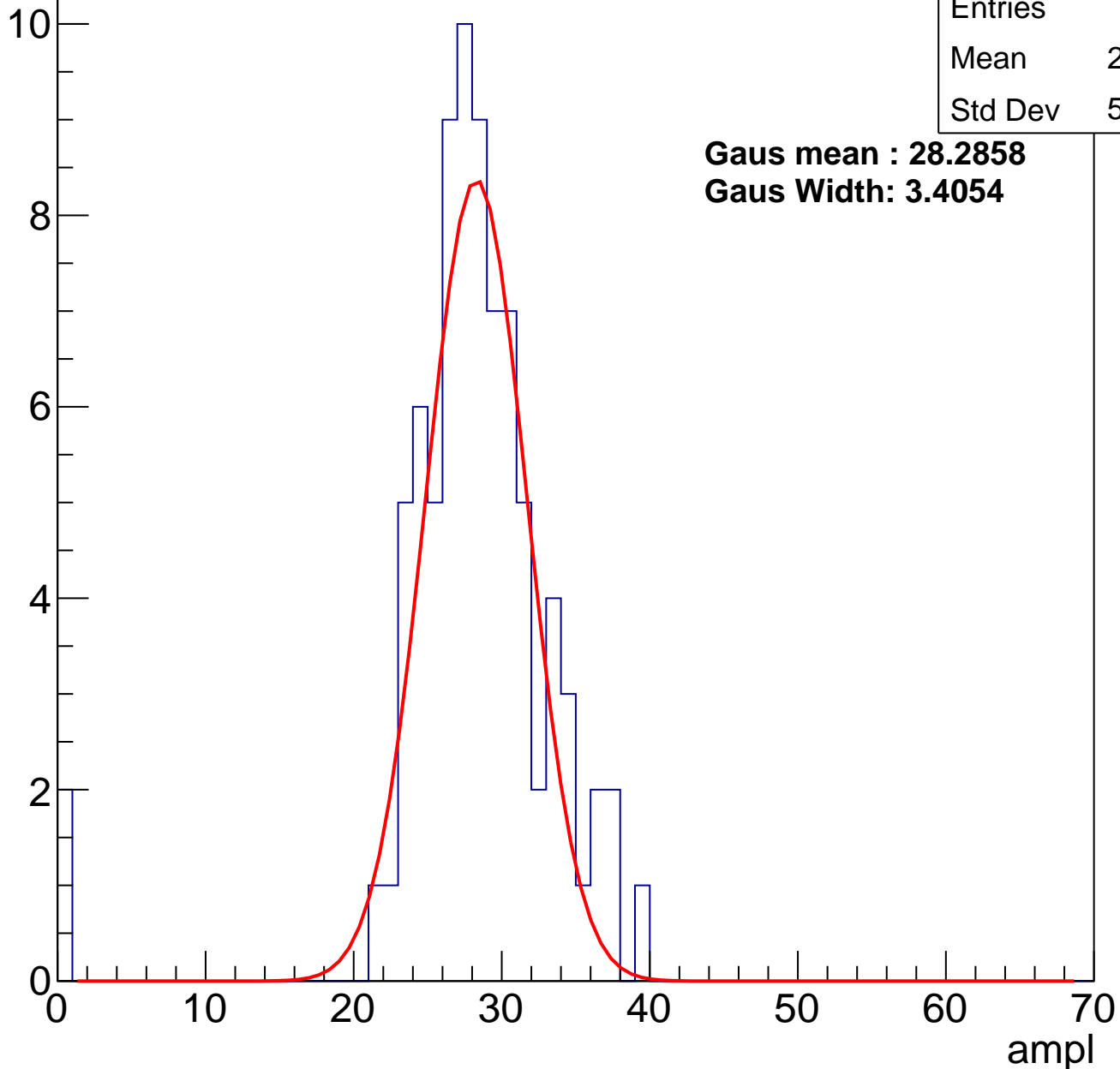
calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	27.67
Std Dev	5.776

Gaus mean : 28.2858

Gaus Width: 3.4054

Entry



B1L100S, U25-ch81, adc1

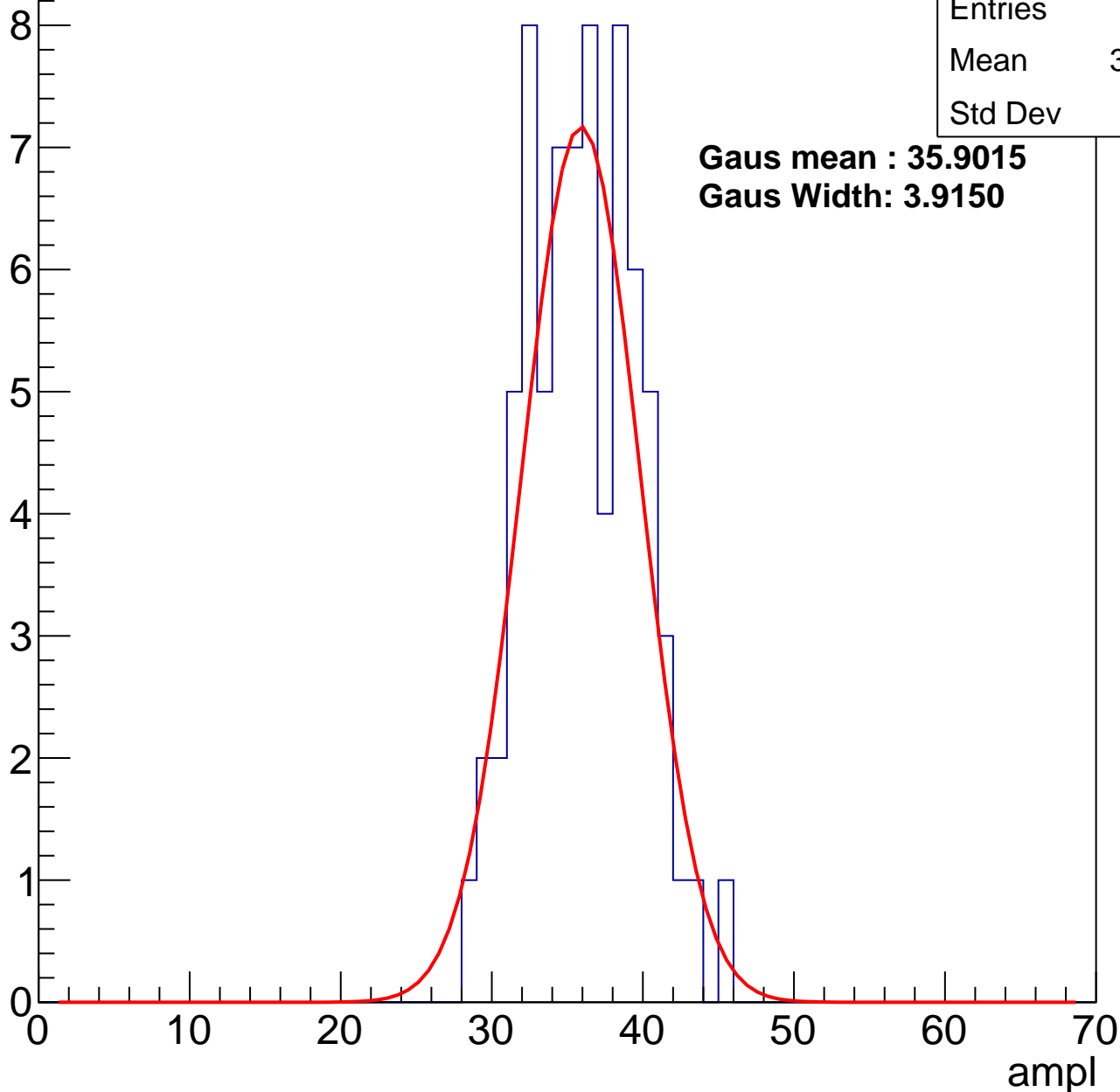
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.57
Std Dev	3.61

Gaus mean : 35.9015

Gaus Width: 3.9150



B1L100S, U25-ch81, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

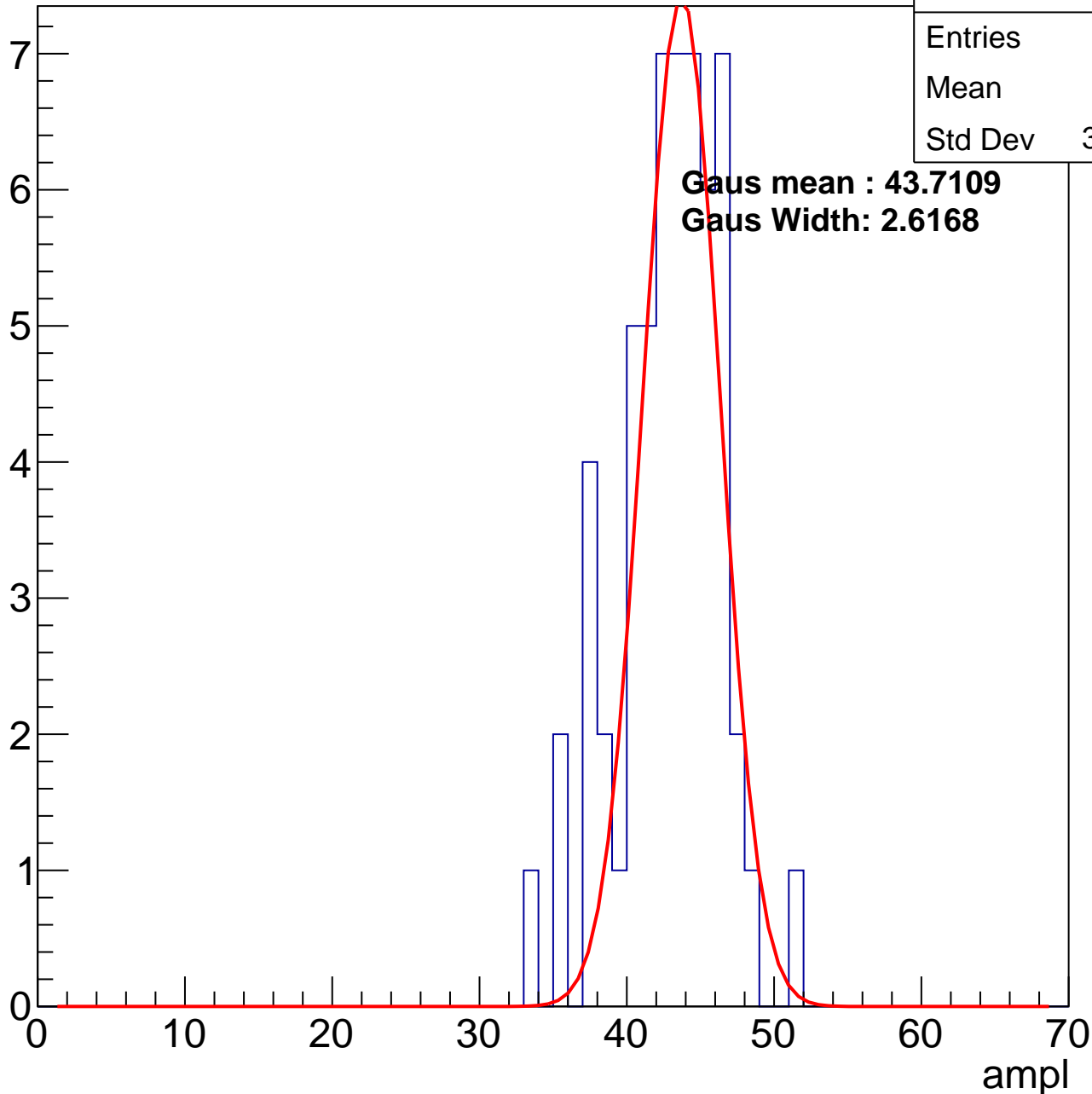
70

ampl

Entries	58
Mean	42.4
Std Dev	3.484

Gaus mean : 43.7109

Gaus Width: 2.6168



B1L100S, U25-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	49.11
Std Dev	3.911

Entry

10

8

6

4

2

0

0

10

20

30

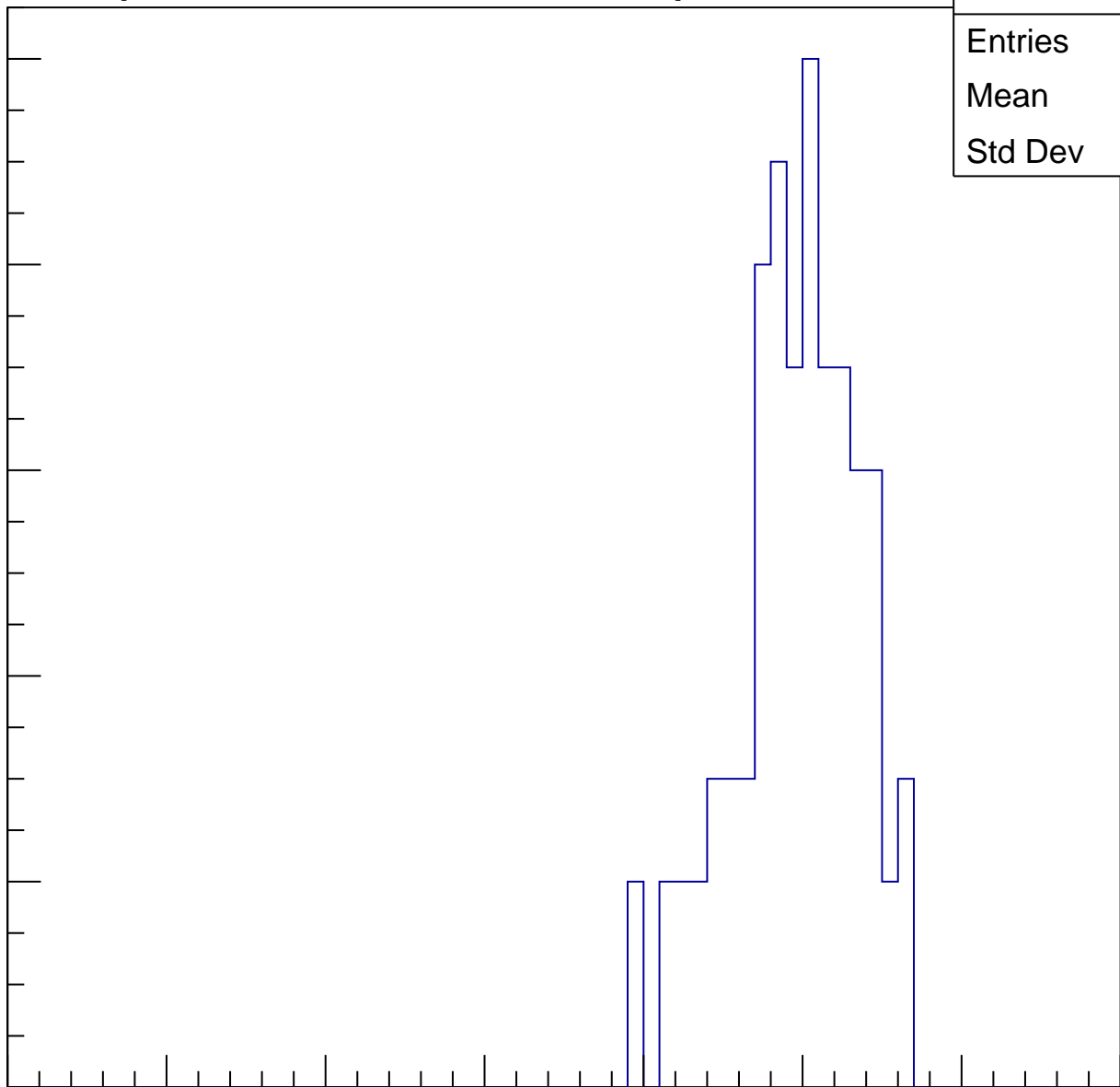
40

50

60

70

ampl



B1L100S, U25-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 56

Mean 56.05

Std Dev 3.148

3

2

1

0

0

10

20

30

40

50

60

70

ampl

3

2

1

0

0

10

20

30

40

50

60

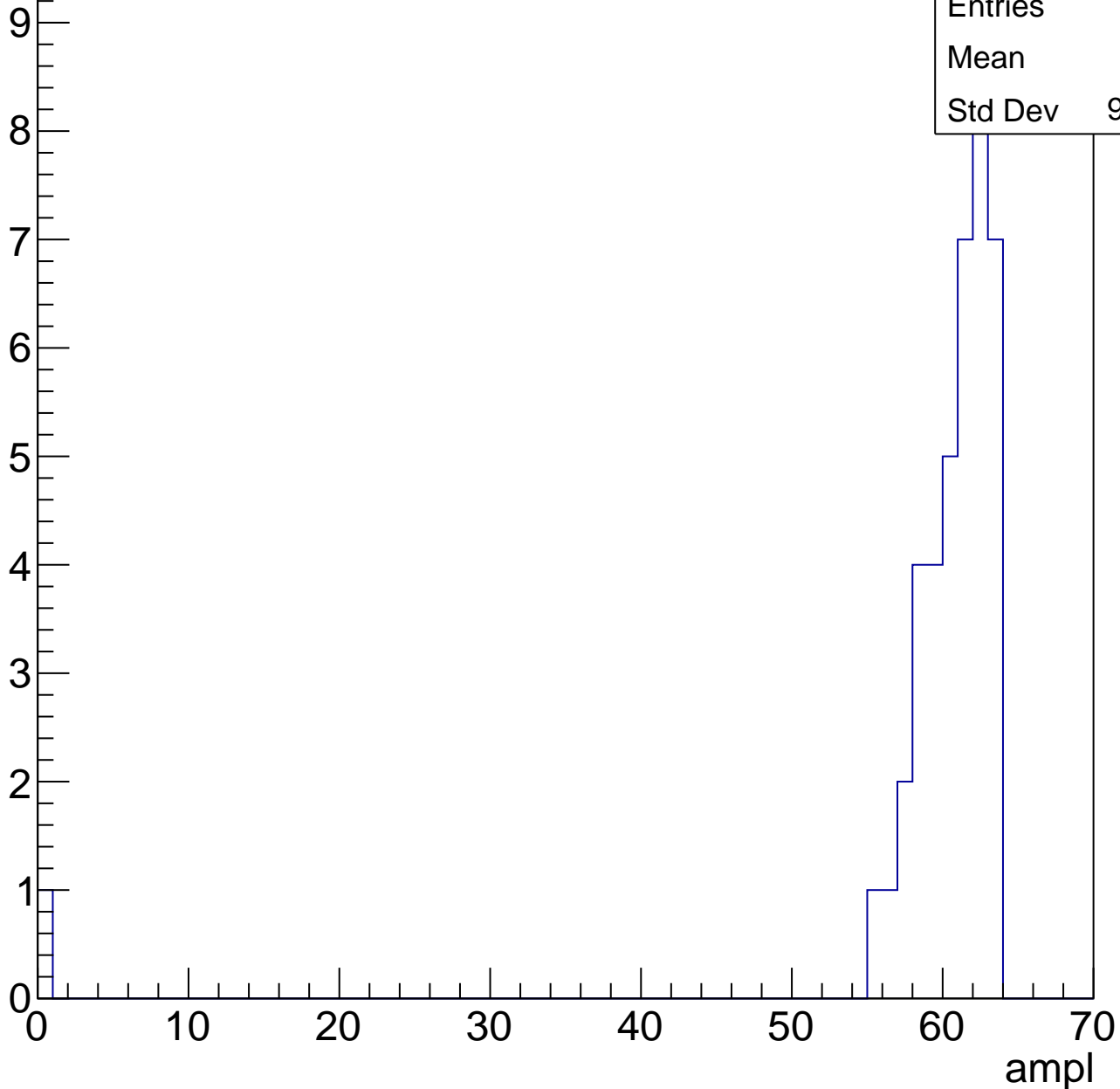
70

ampl

B1L100S, U25-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

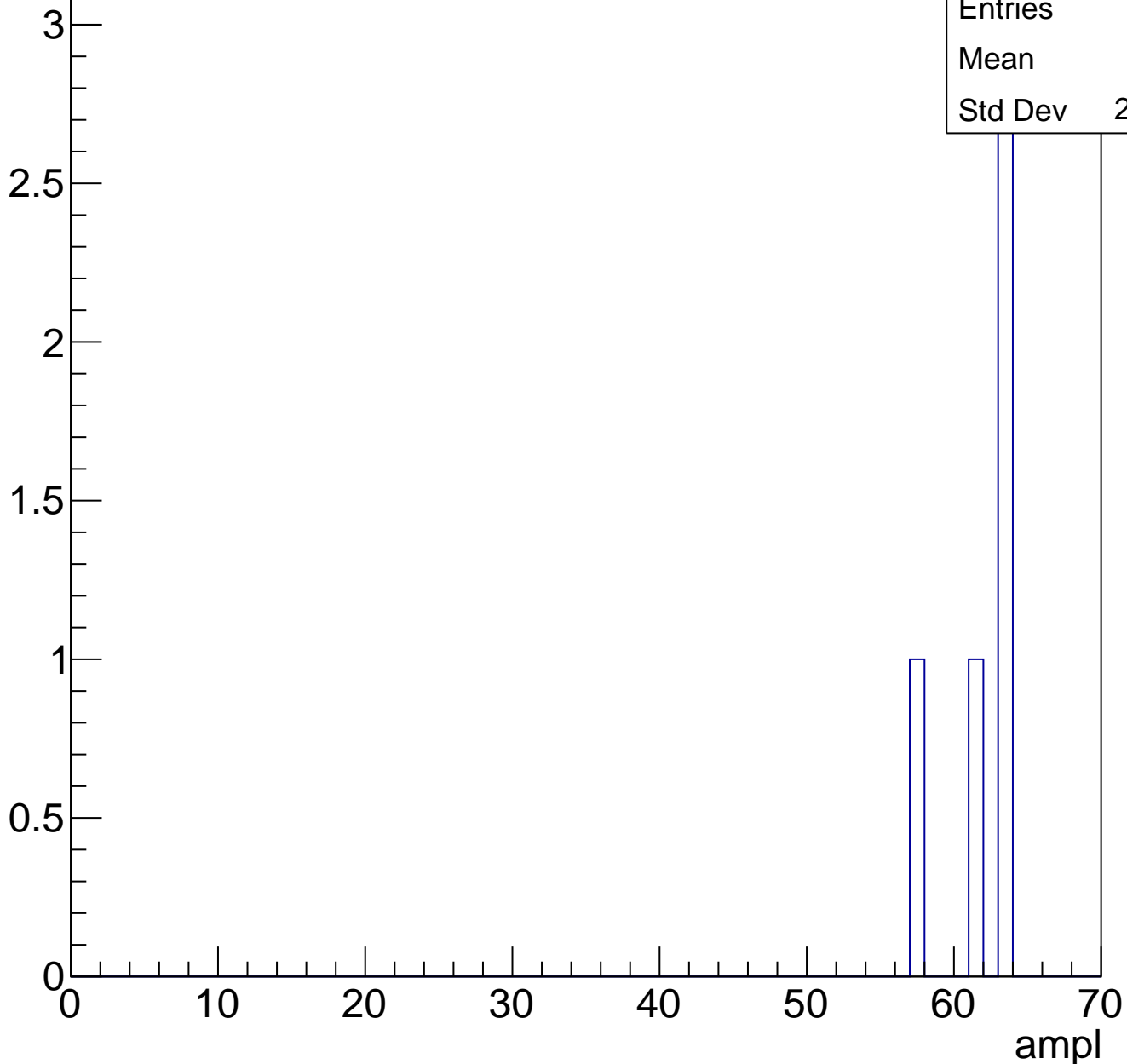
Entry



B1L100S, U25-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	61.4
Std Dev	2.332

B1L100S, U25-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch82, adc0

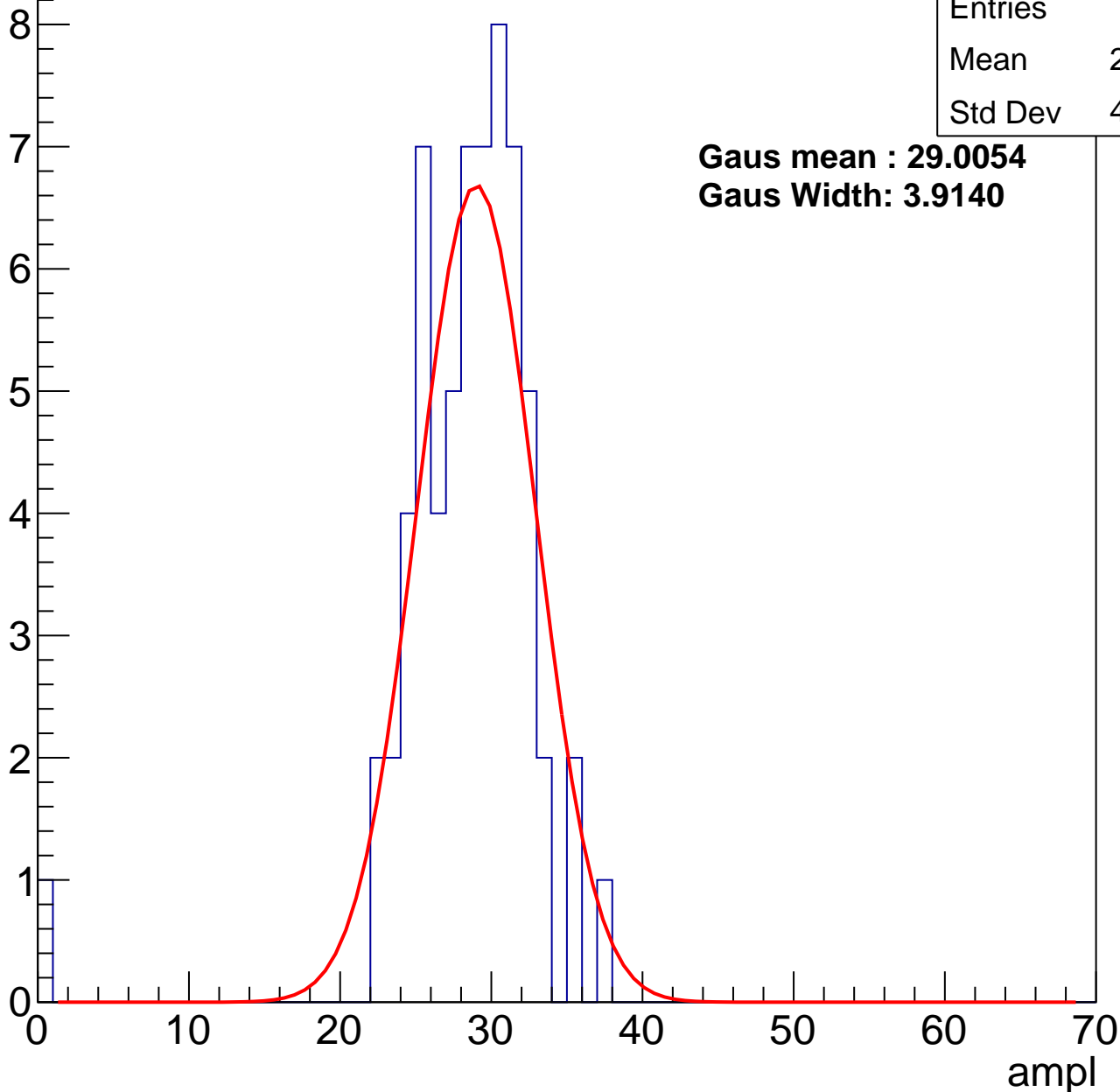
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	27.95
Std Dev	4.784

Gaus mean : 29.0054

Gaus Width: 3.9140



B1L100S, U25-ch82, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	36.01
Std Dev	3.508

Gaus mean : 36.5334

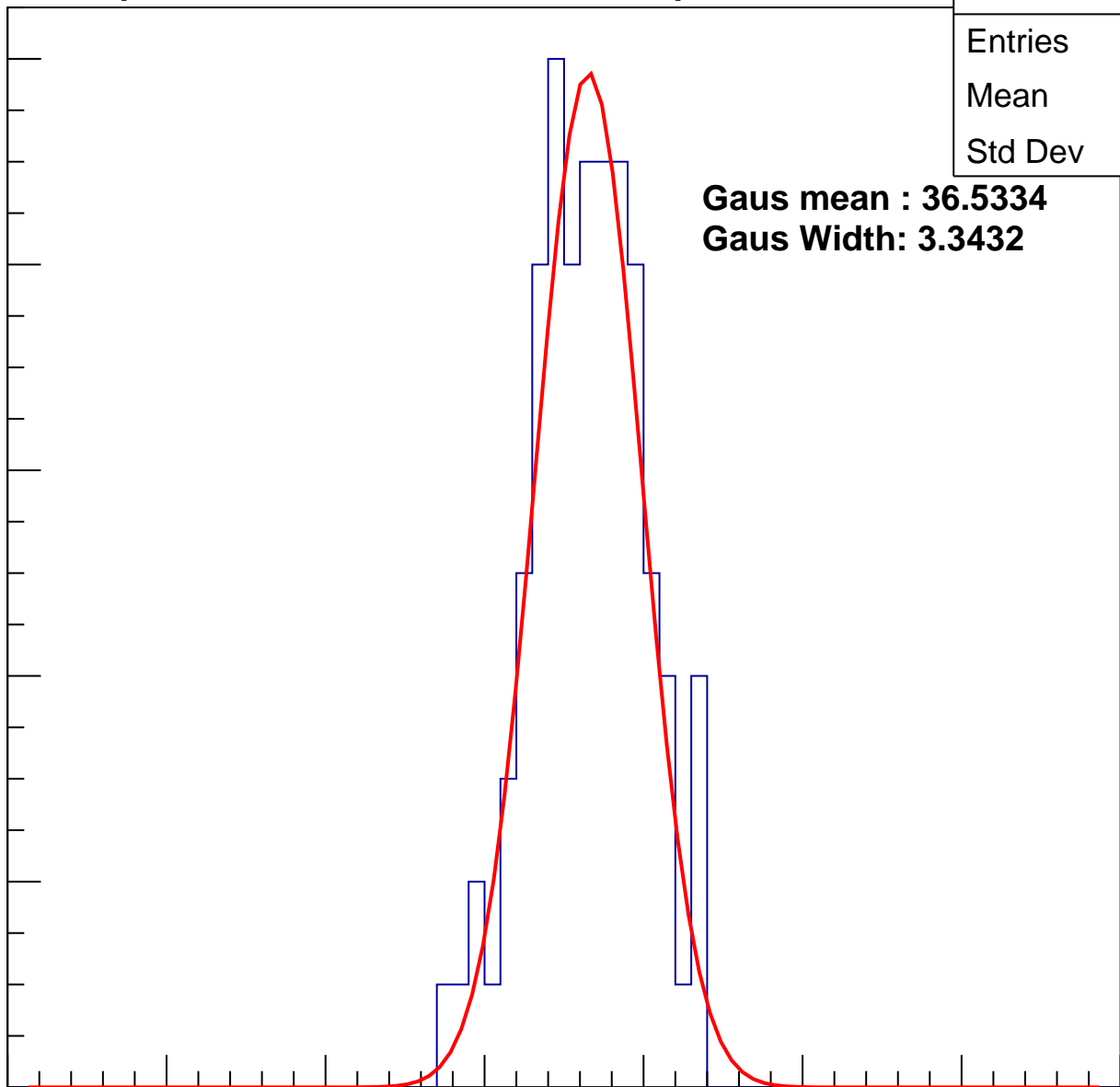
Gaus Width: 3.3432

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch82, adc2

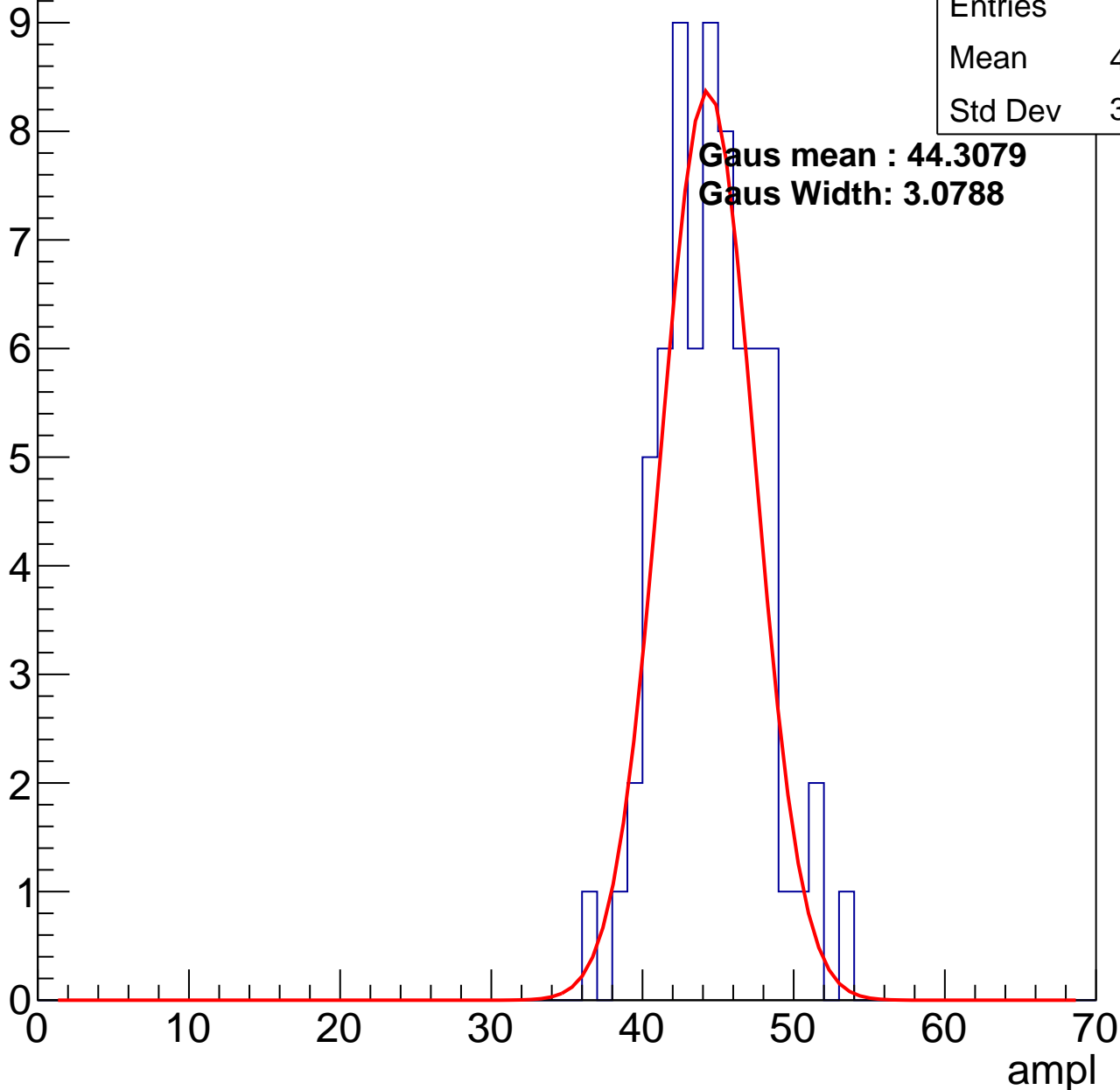
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.14
Std Dev	3.266

Gaus mean : 44.3079

Gaus Width: 3.0788

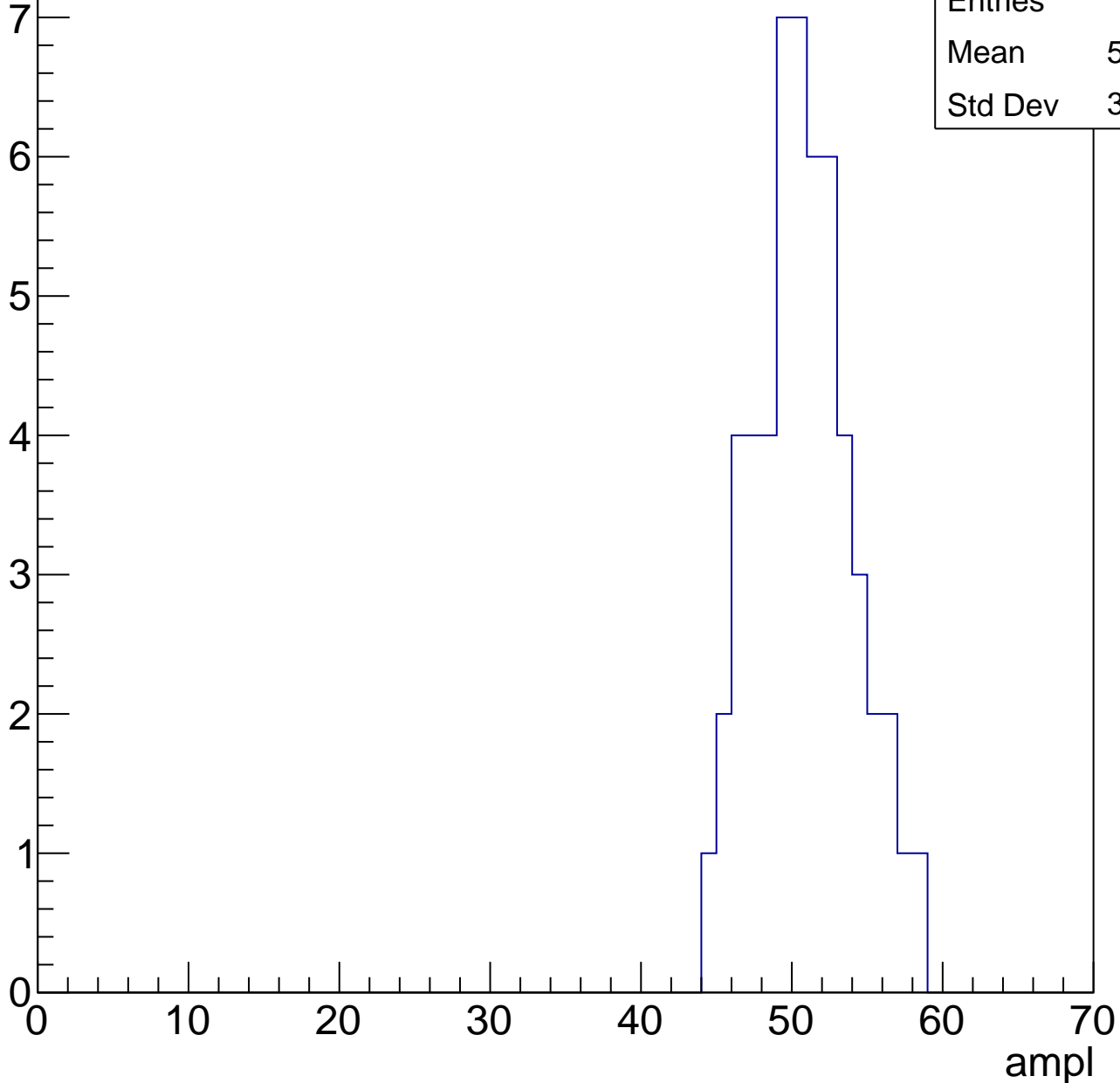


B1L100S, U25-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

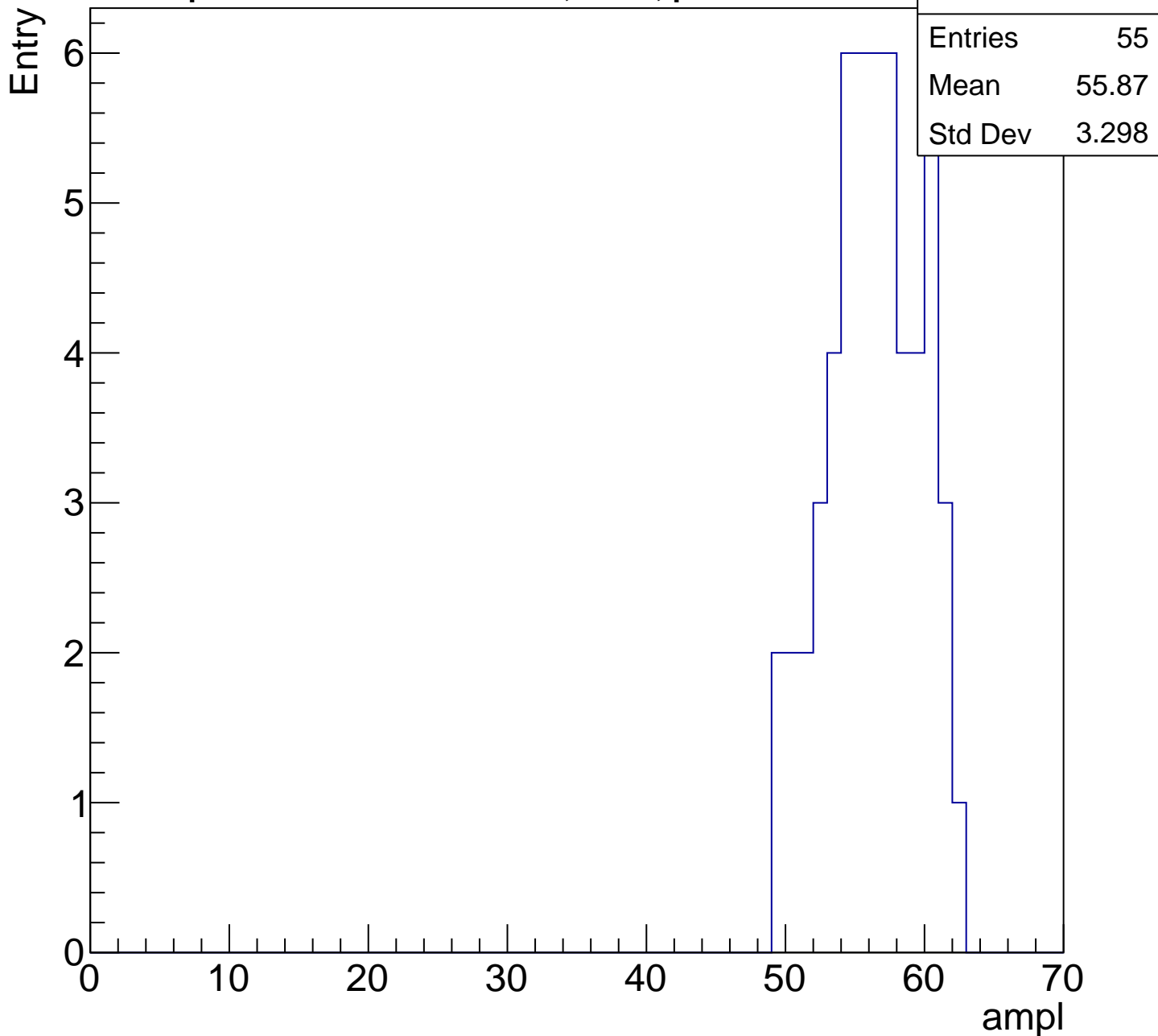
Entry

Entries	54
Mean	50.37
Std Dev	3.193



B1L100S, U25-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

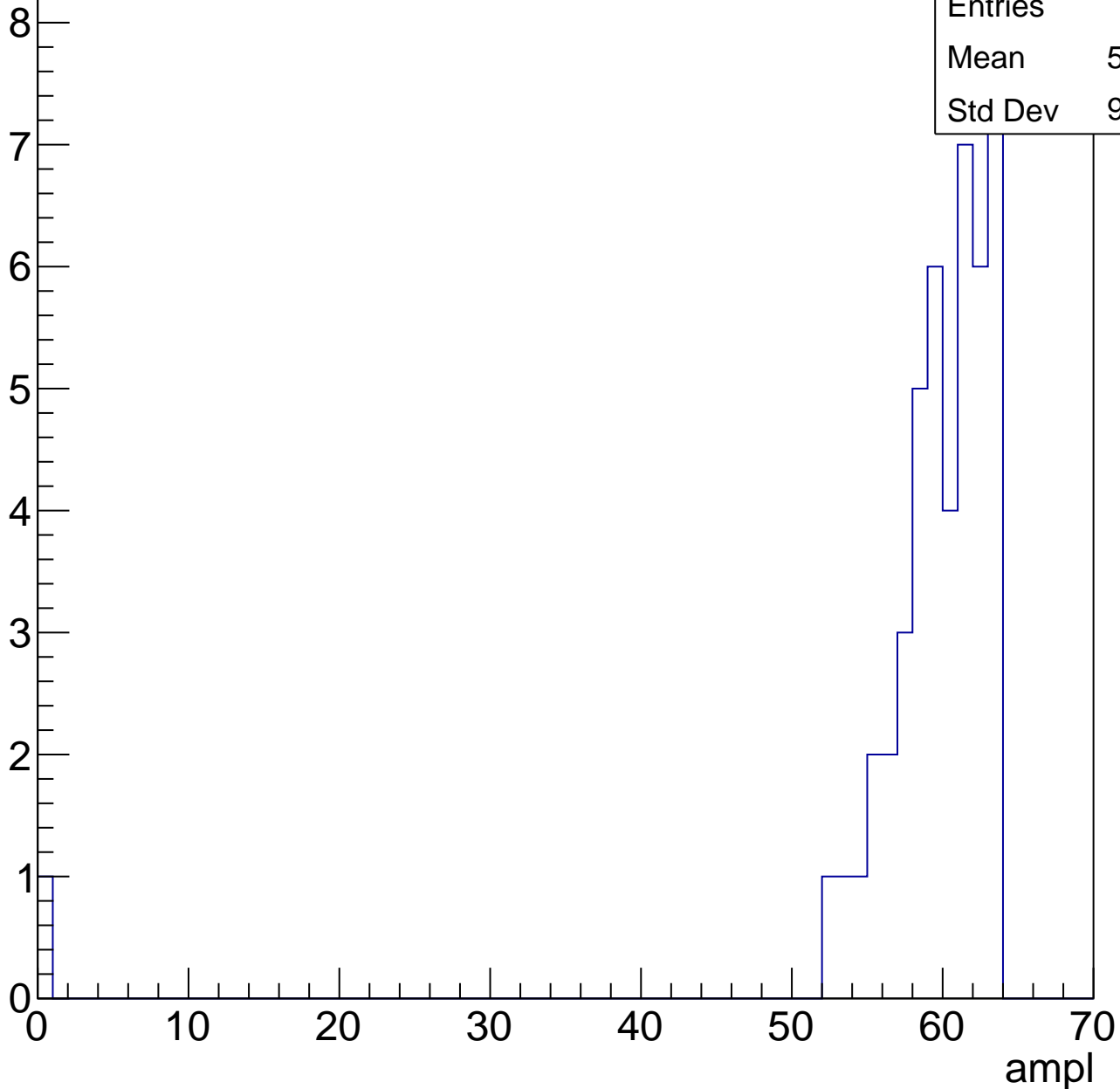


B1L100S, U25-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.28
Std Dev	9.048



B1L100S, U25-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch83, adc0

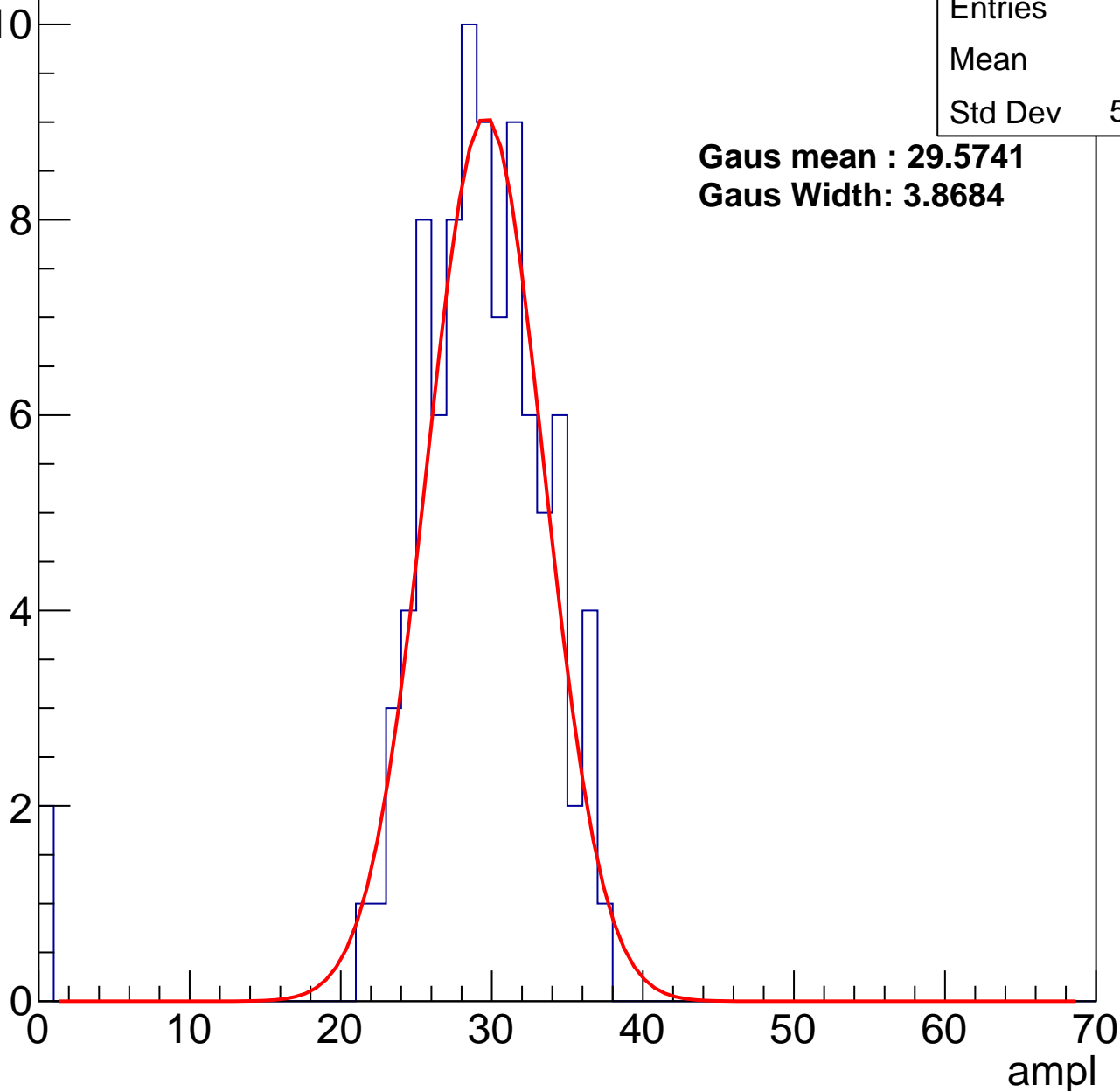
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	92
Mean	28.5
Std Dev	5.578

Gaus mean : 29.5741

Gaus Width: 3.8684



B1L100S, U25-ch83, adc1

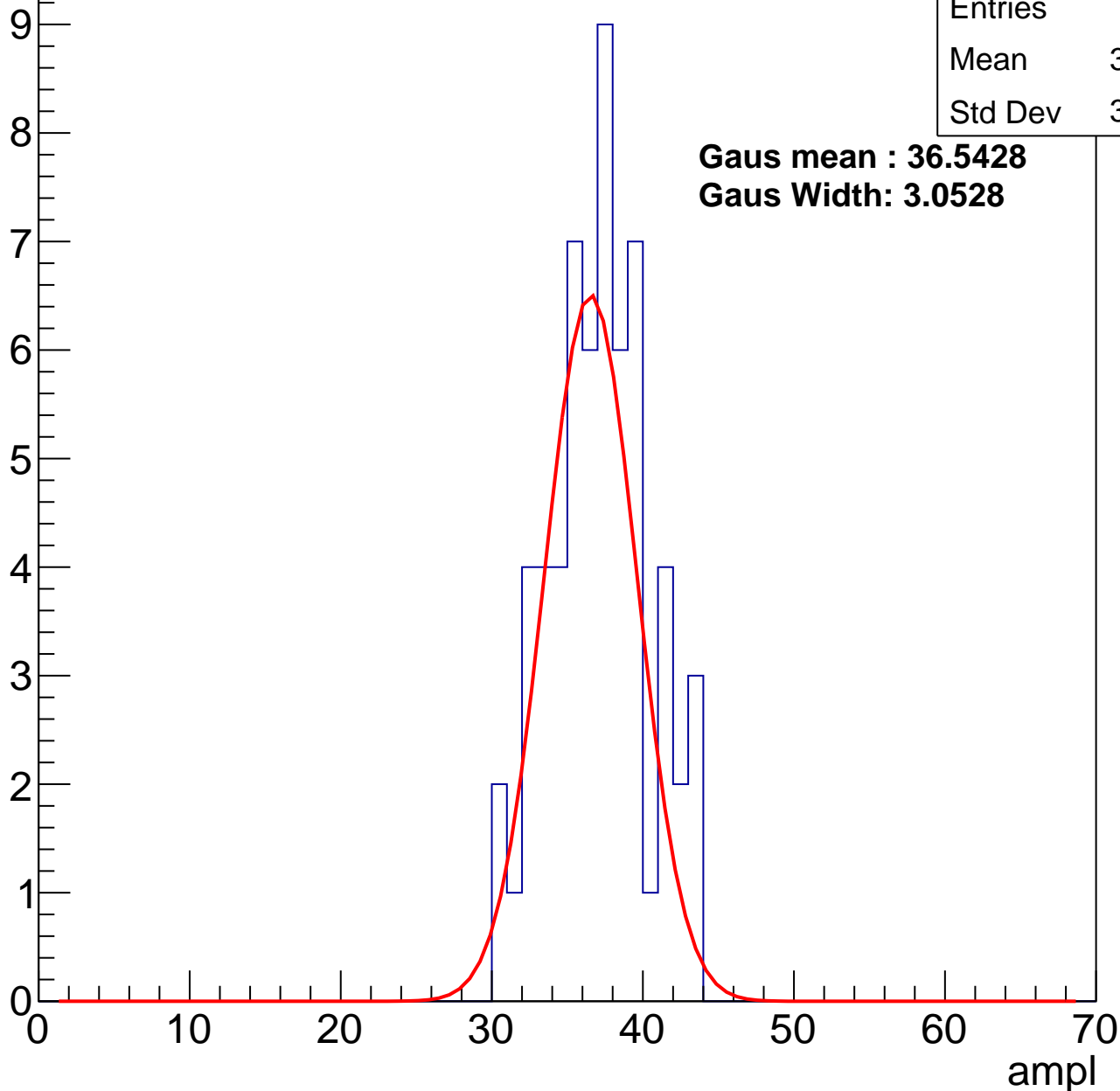
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	36.65
Std Dev	3.229

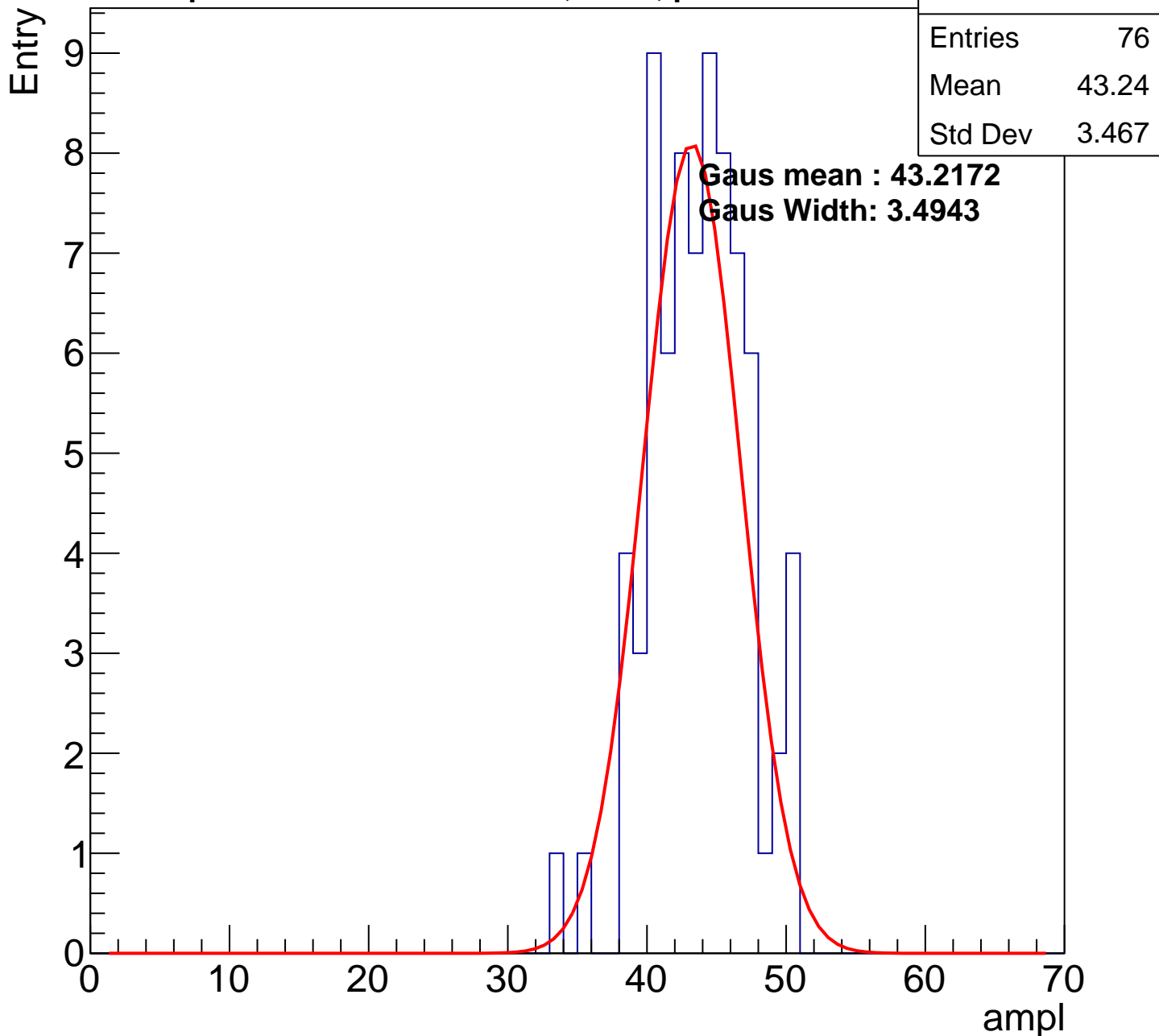
Gaus mean : 36.5428

Gaus Width: 3.0528



B1L100S, U25-ch83, adc2

calib_packv5_042523_0143.root, FC#4, port A2

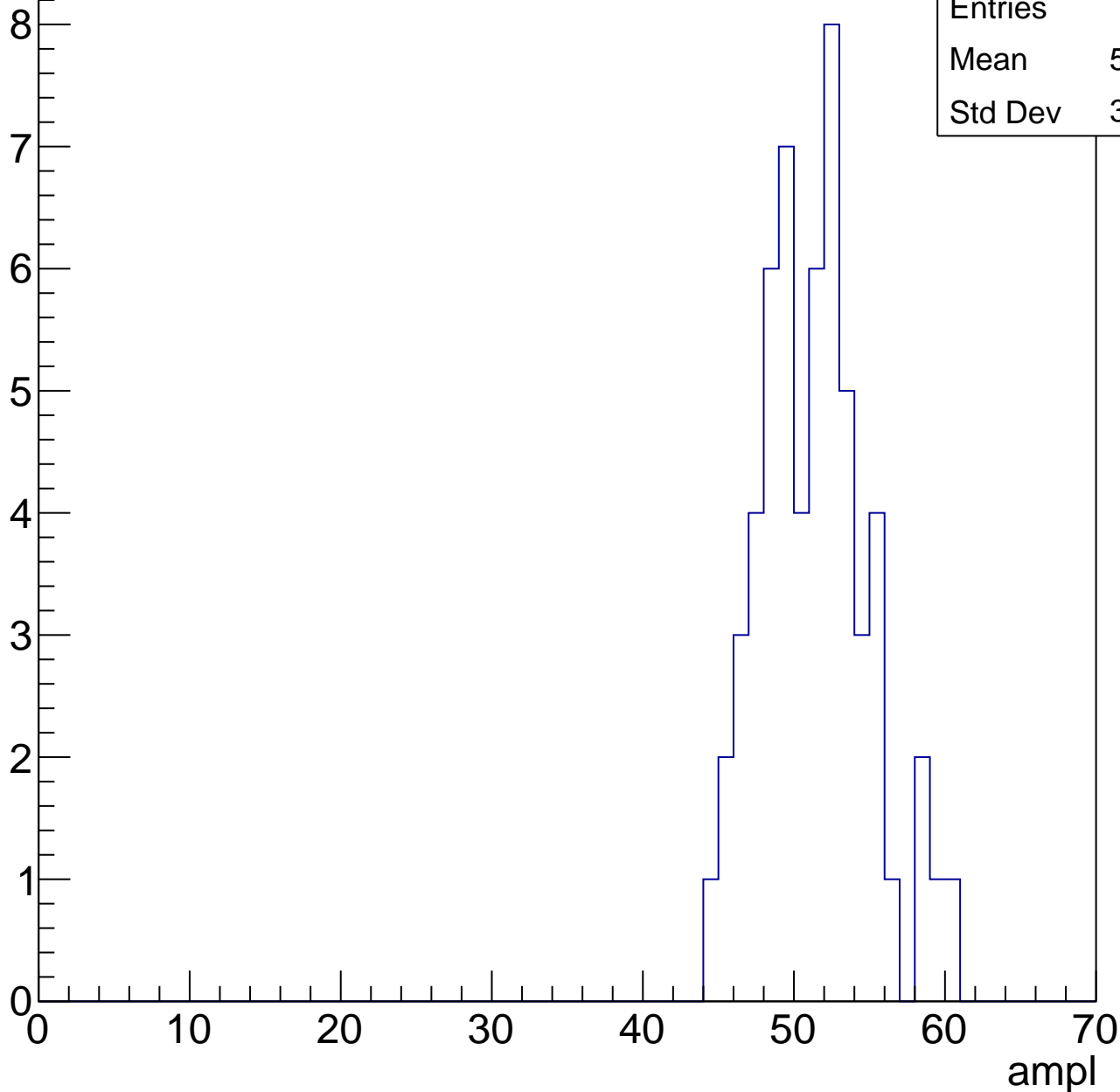


B1L100S, U25-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	50.88
Std Dev	3.553

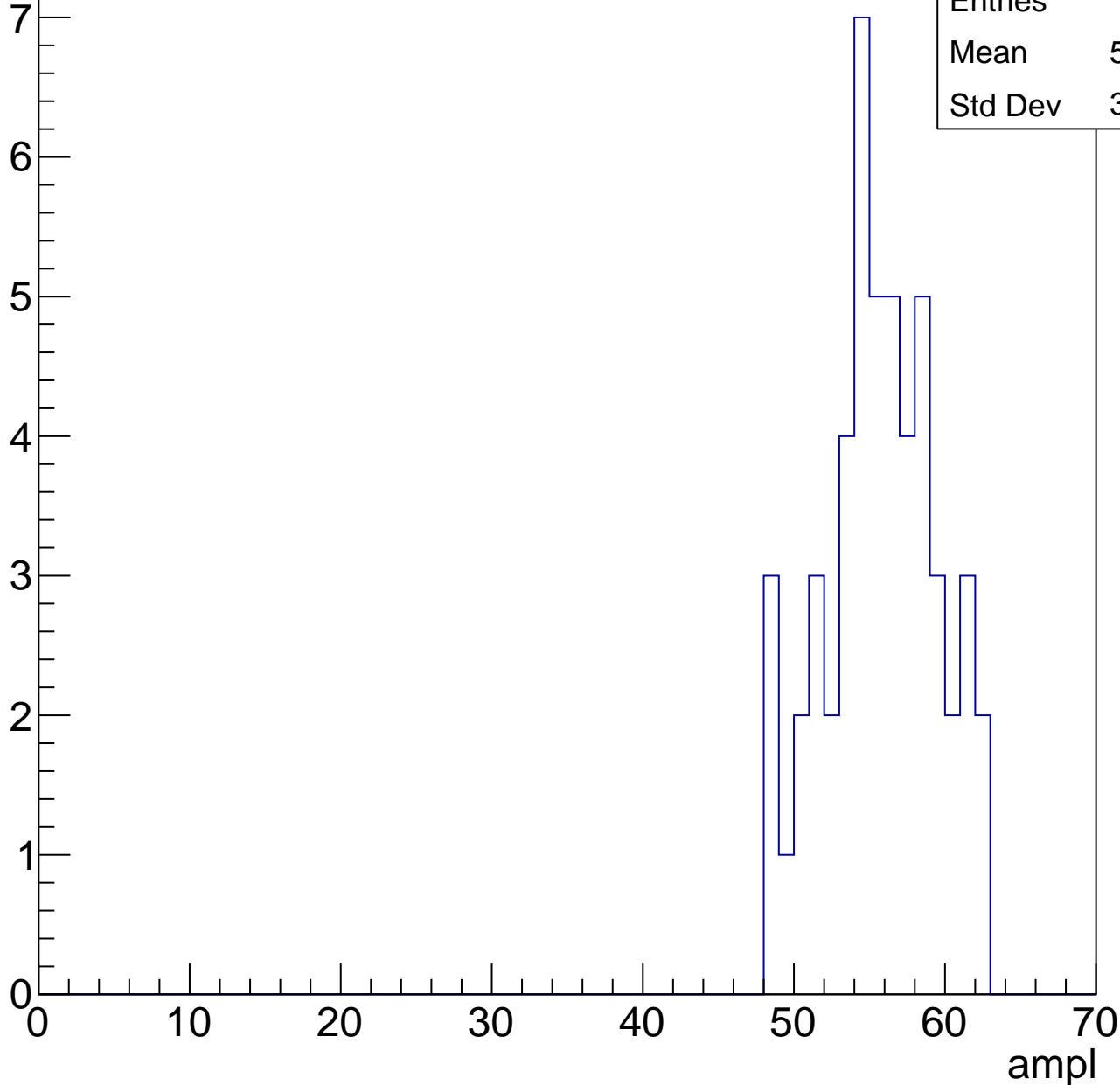


B1L100S, U25-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	55.24
Std Dev	3.676

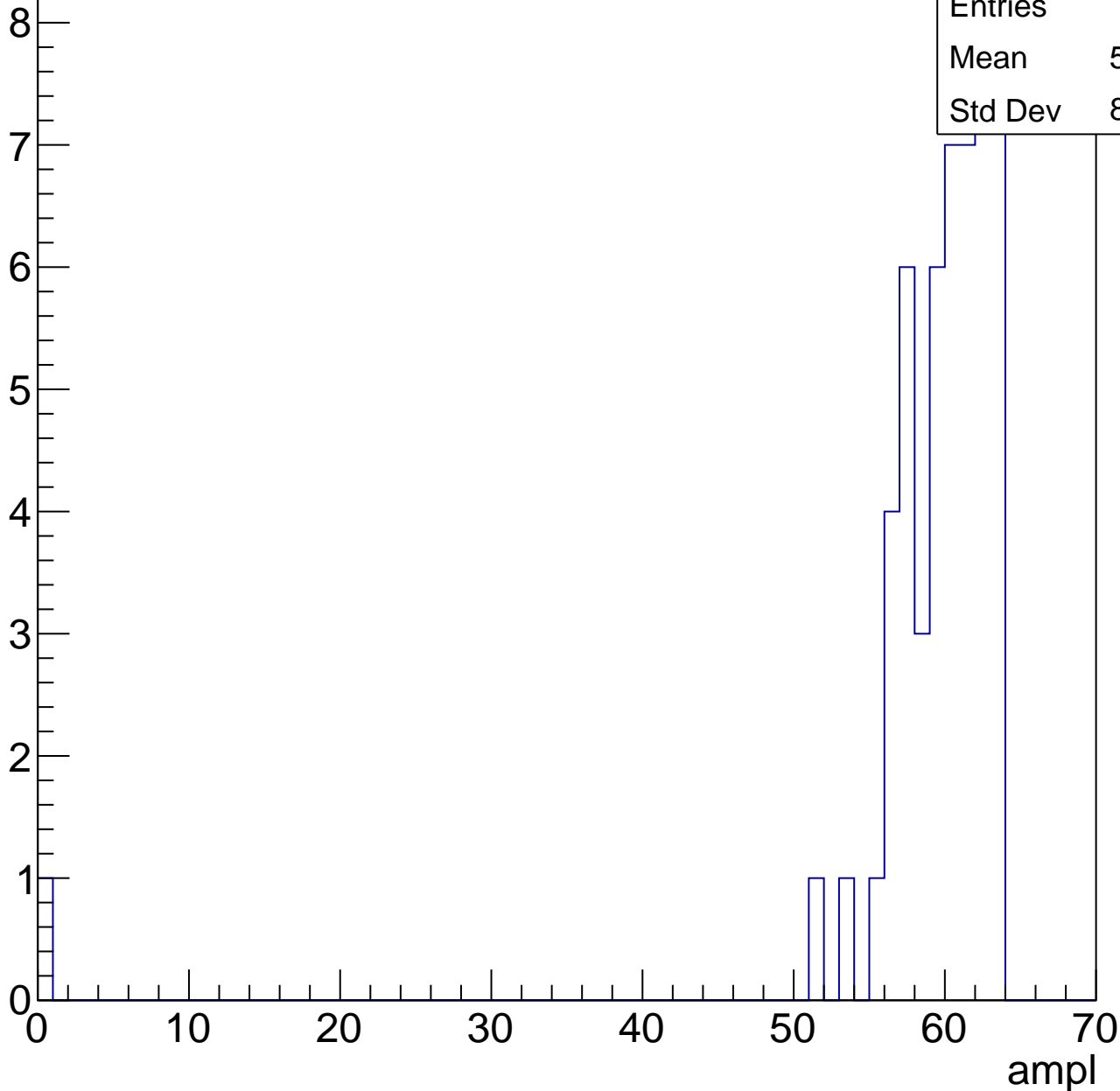


B1L100S, U25-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.49
Std Dev	8.558



B1L100S, U25-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch84, adc0

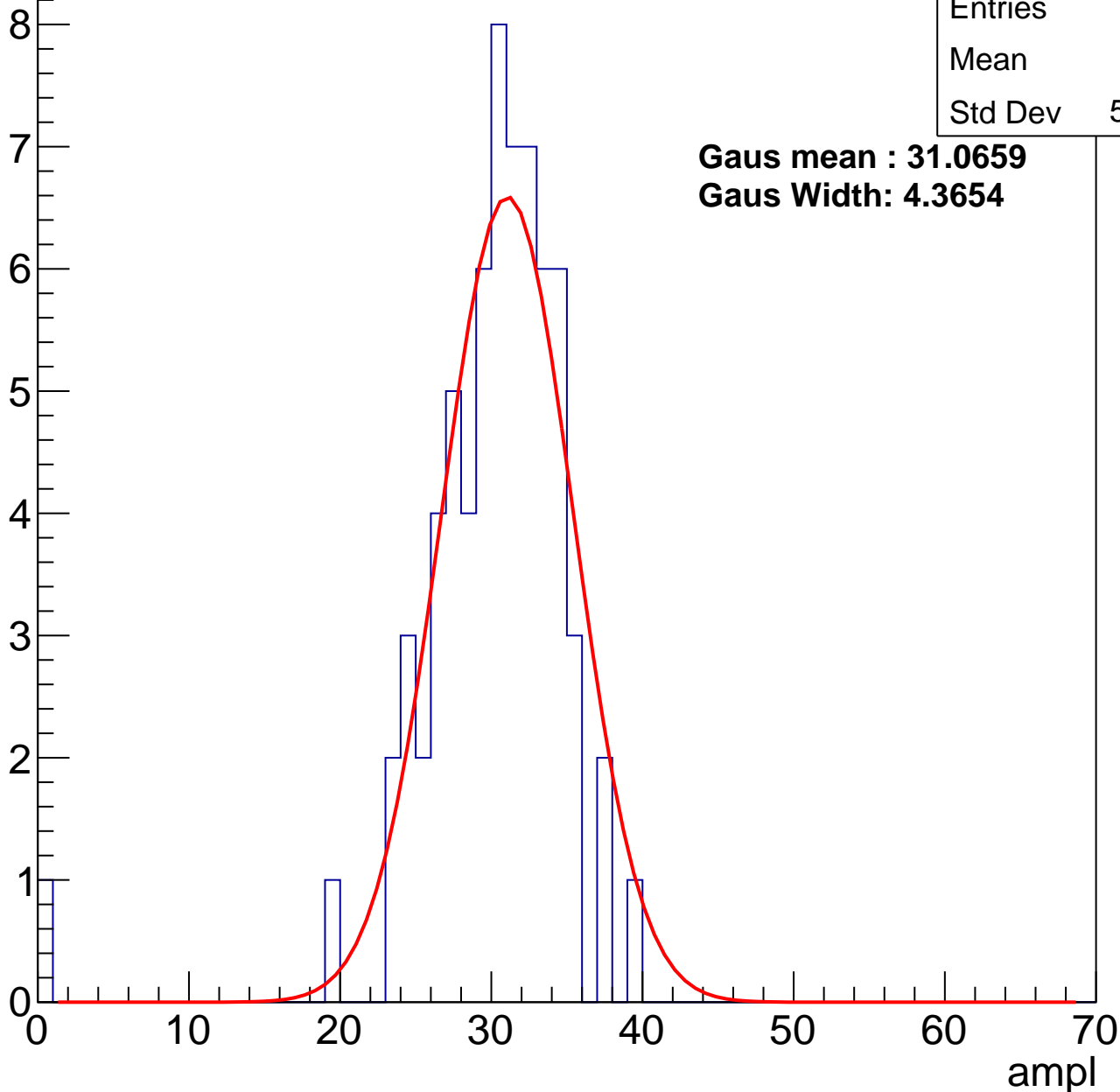
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.6
Std Dev	5.197

Gaus mean : 31.0659

Gaus Width: 4.3654



B1L100S, U25-ch84, adc1

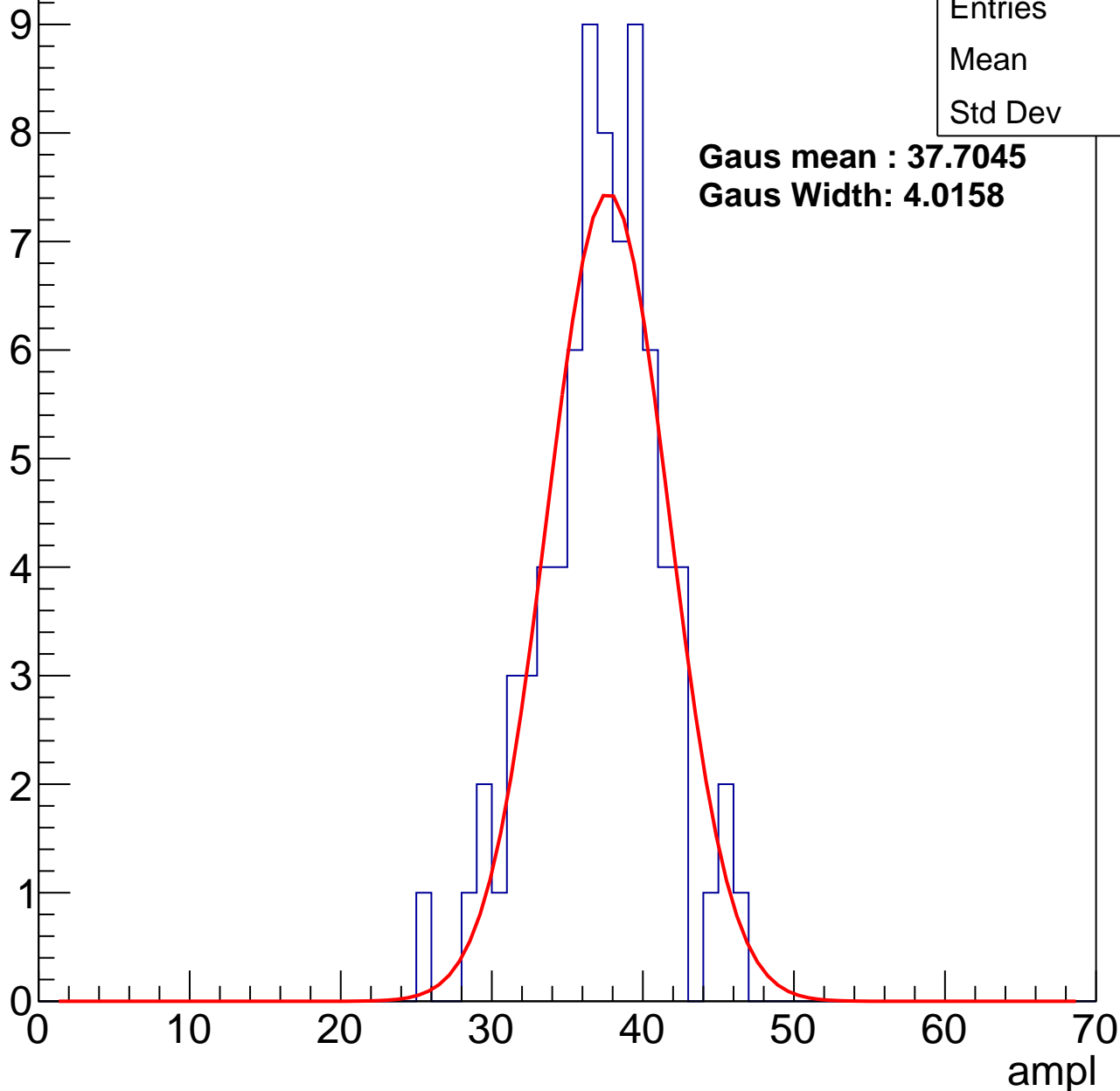
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.8
Std Dev	4.03

Gaus mean : 37.7045

Gaus Width: 4.0158



B1L100S, U25-ch84, adc2

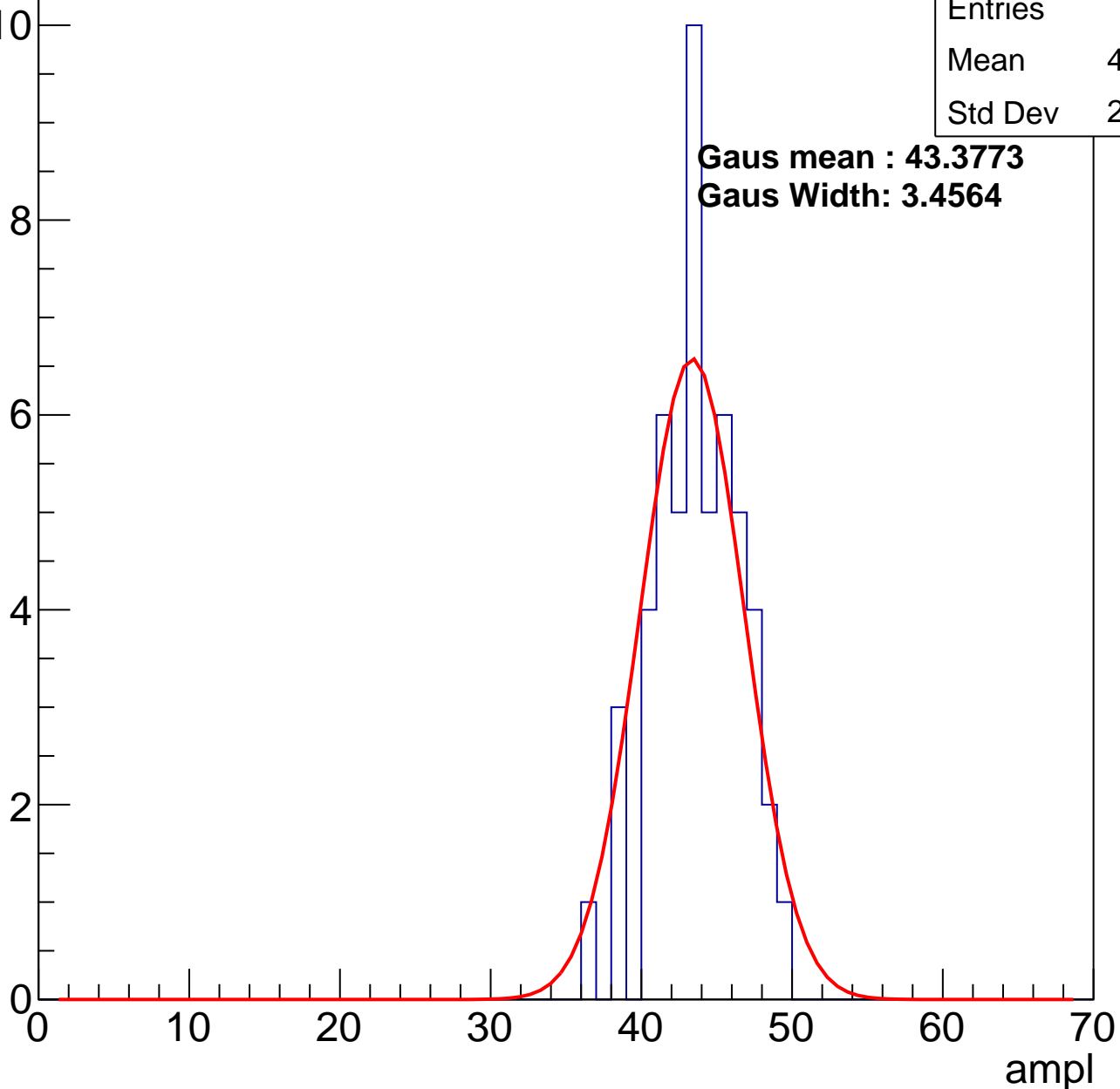
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.25
Std Dev	2.807

Gaus mean : 43.3773

Gaus Width: 3.4564

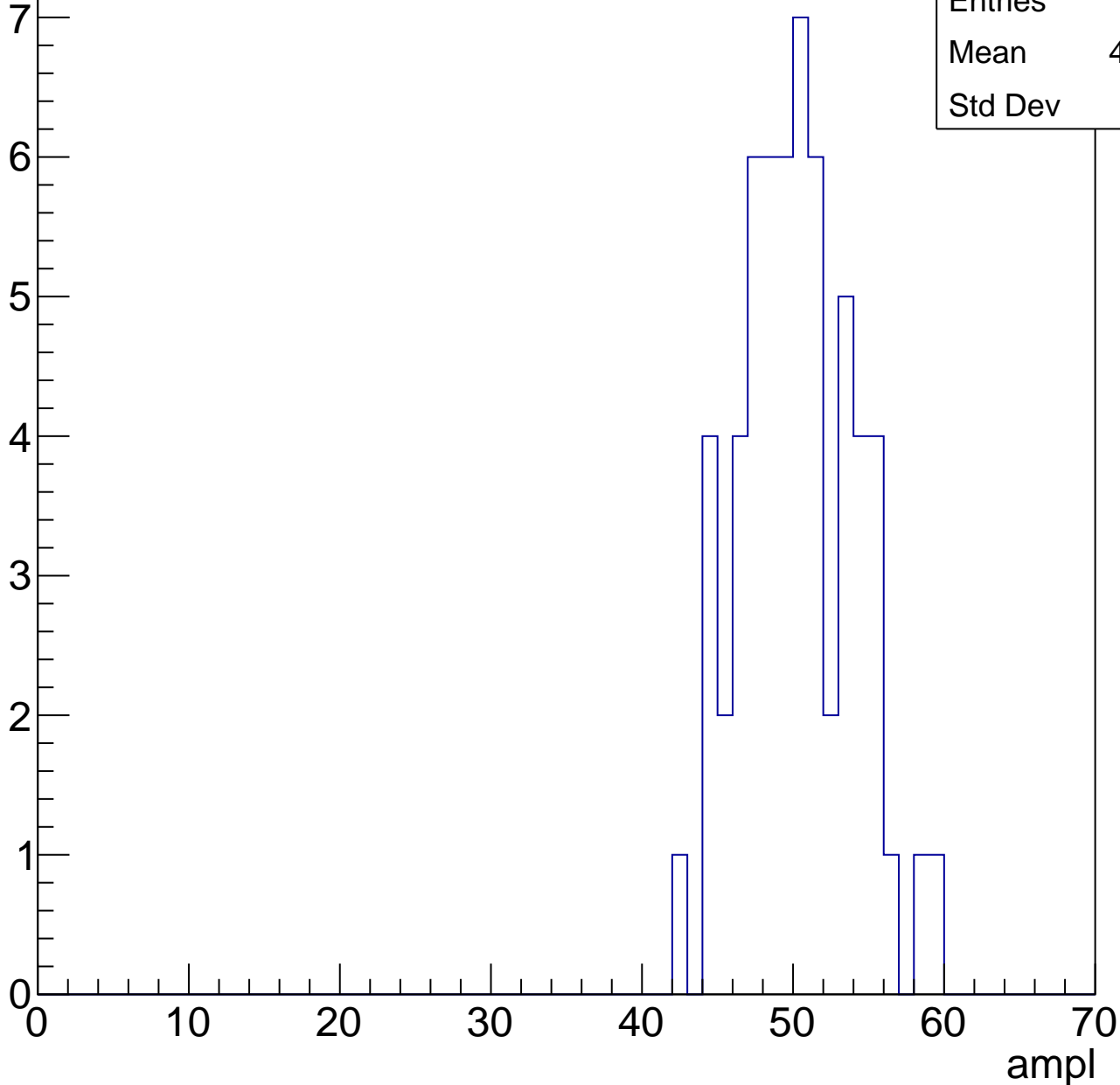


B1L100S, U25-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.83
Std Dev	3.67

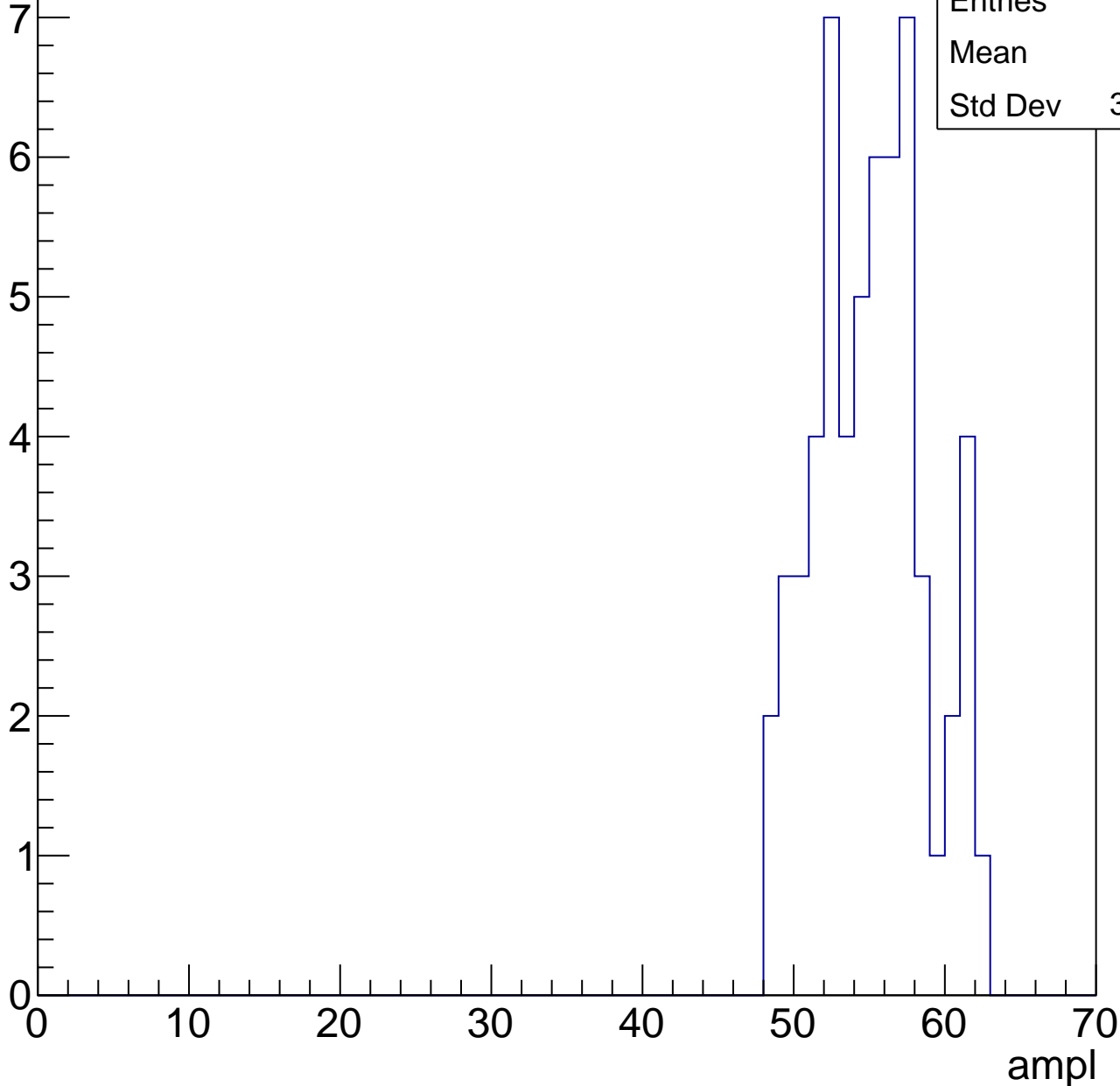


B1L100S, U25-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.6
Std Dev	3.572



B1L100S, U25-ch84, adc5

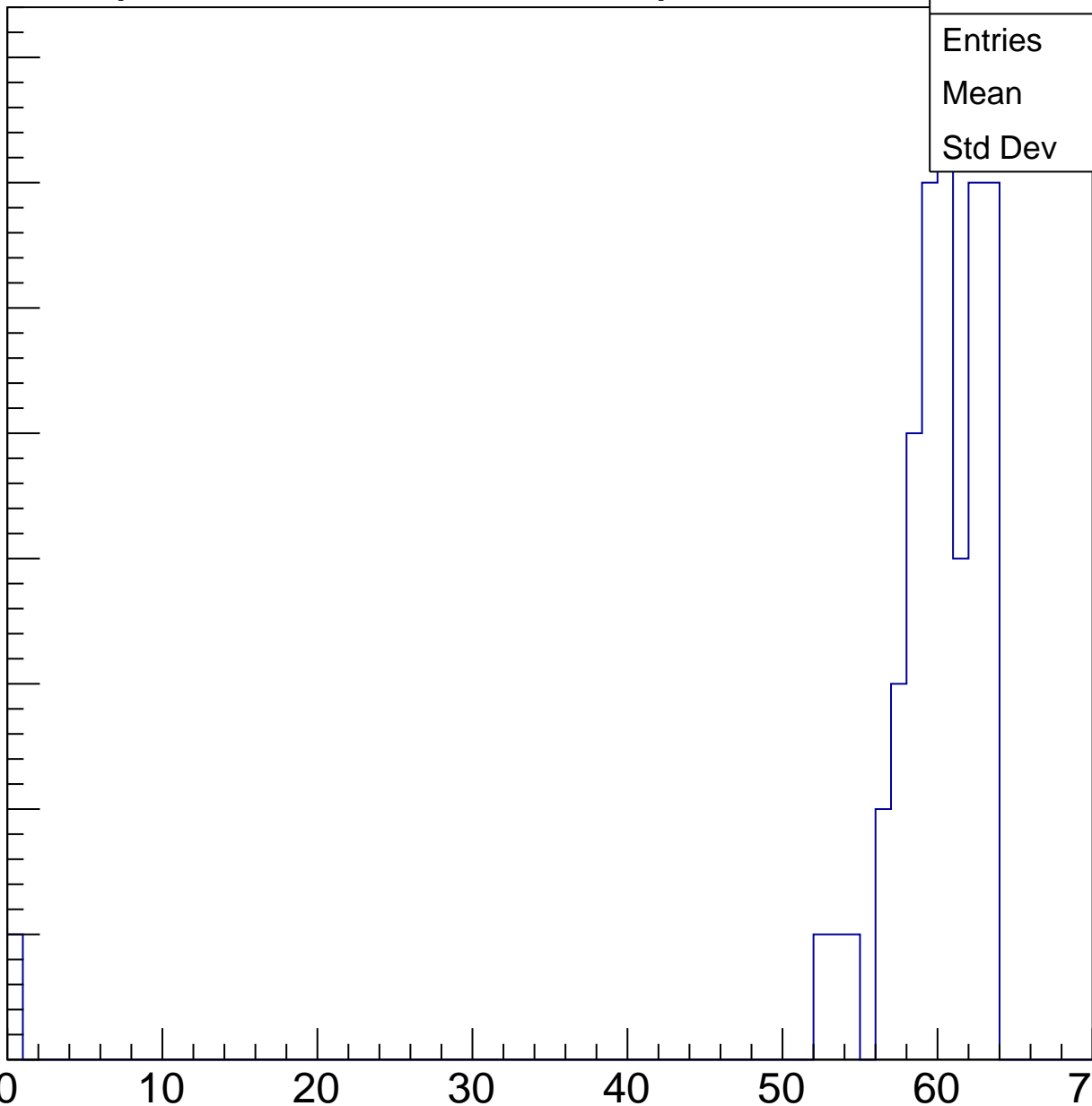
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.38
Std Dev	9

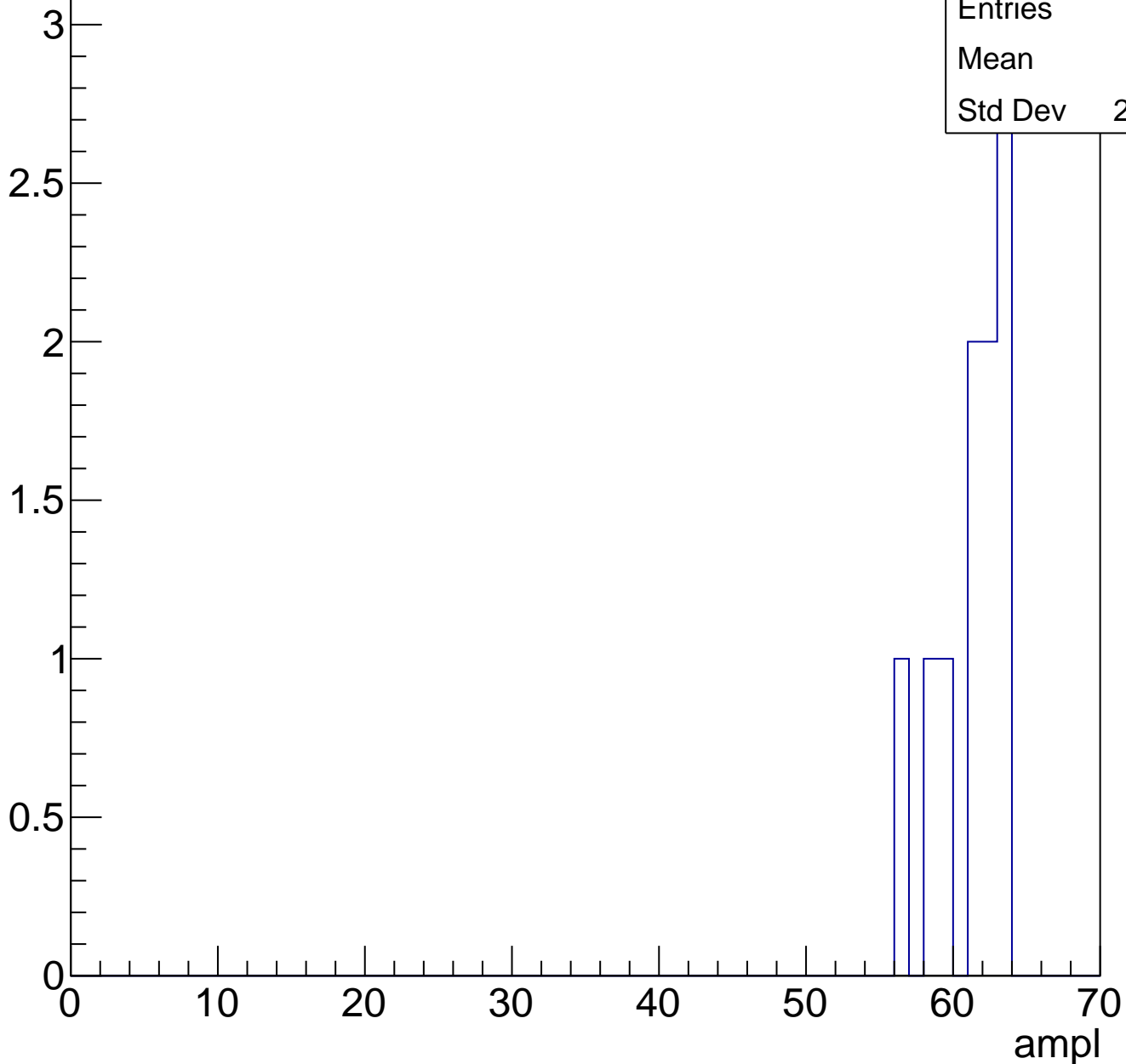
ampl



B1L100S, U25-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch85, adc0

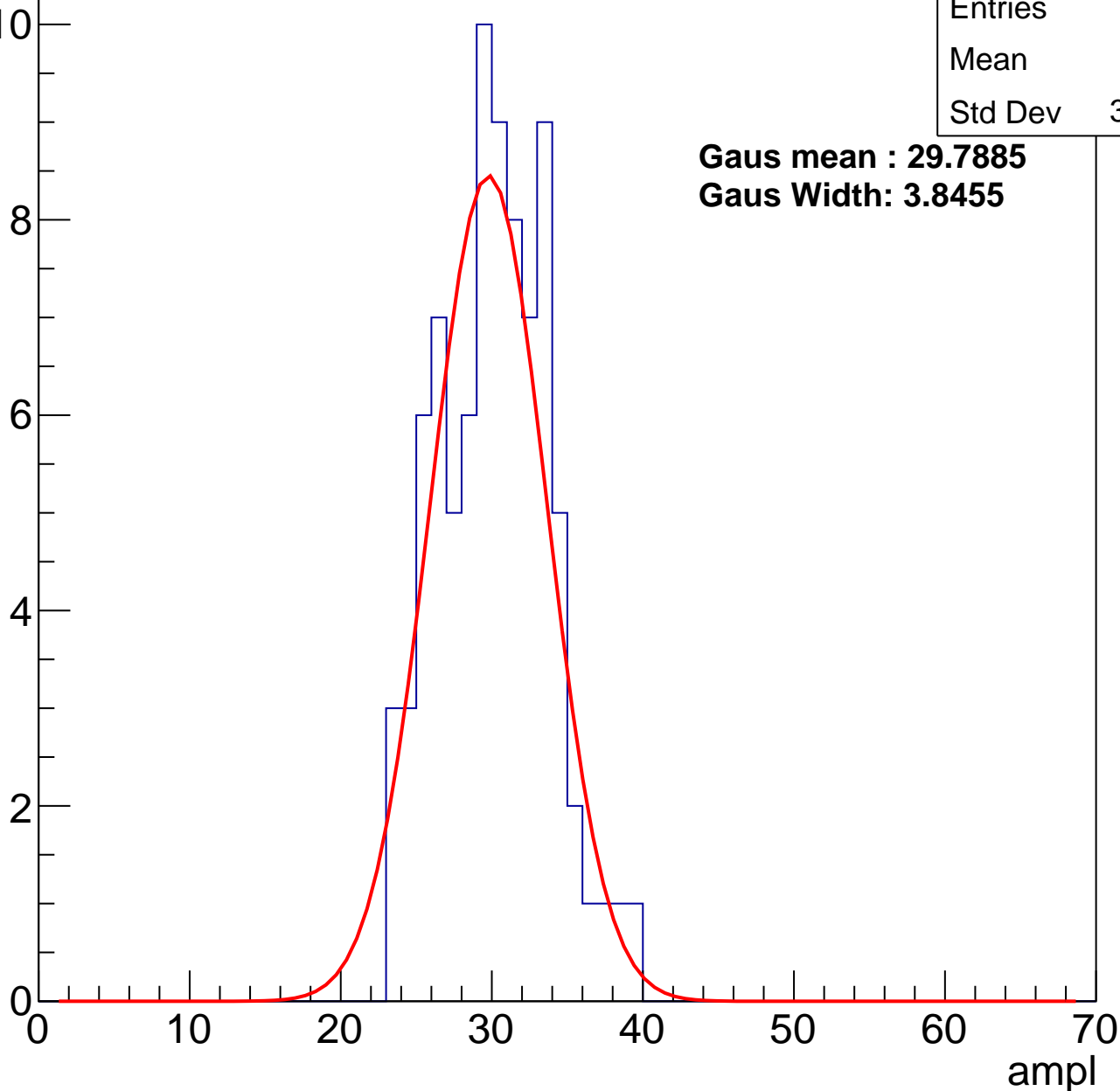
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	29.7
Std Dev	3.562

Gaus mean : 29.7885

Gaus Width: 3.8455



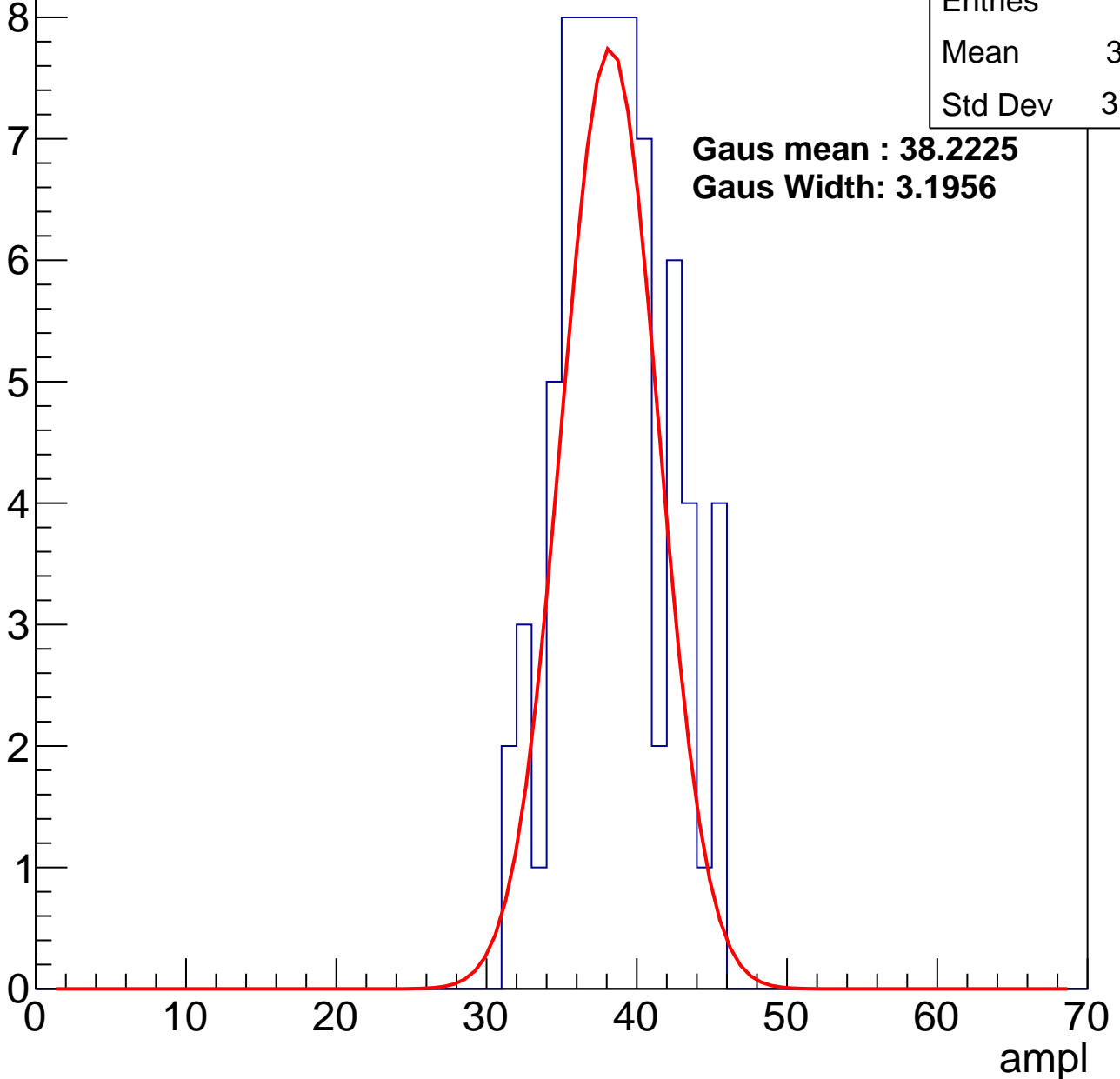
B1L100S, U25-ch85, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	38.01
Std Dev	3.474

Gaus mean : 38.2225
Gaus Width: 3.1956



B1L100S, U25-ch85, adc2

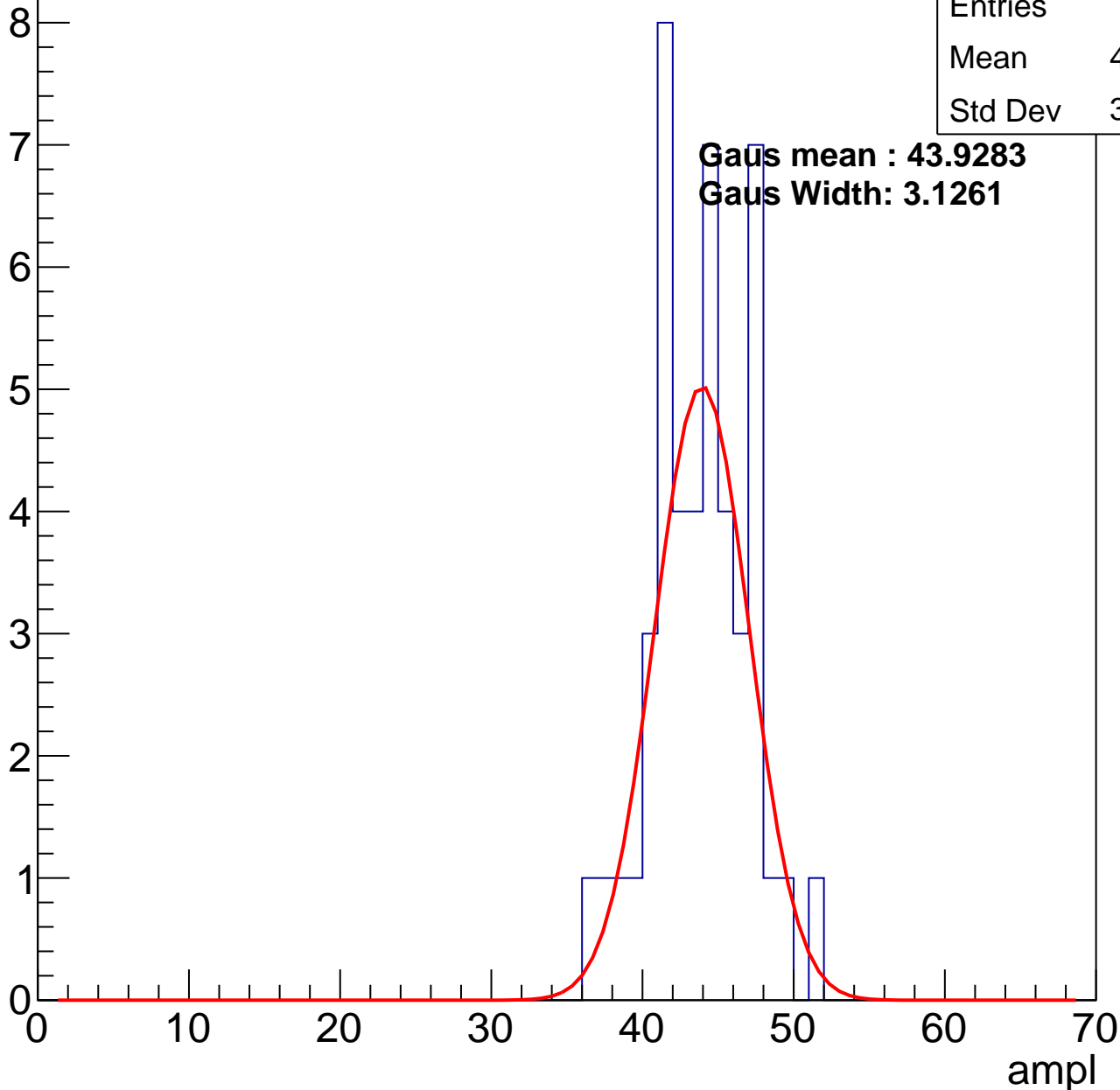
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	43.43
Std Dev	3.154

Gaus mean : 43.9283

Gaus Width: 3.1261



B1L100S, U25-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

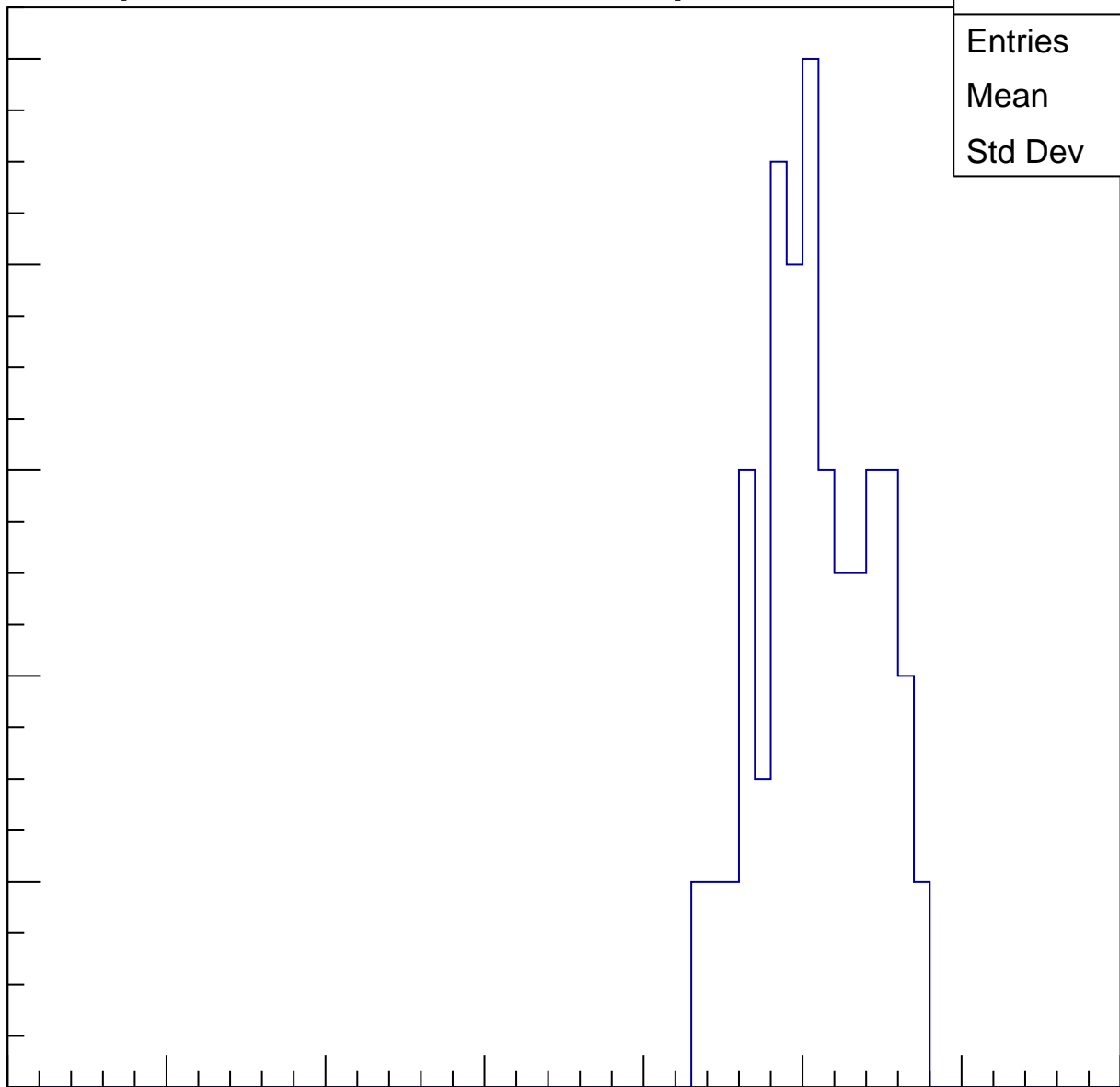
Entries	76
Mean	50.37
Std Dev	3.509

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

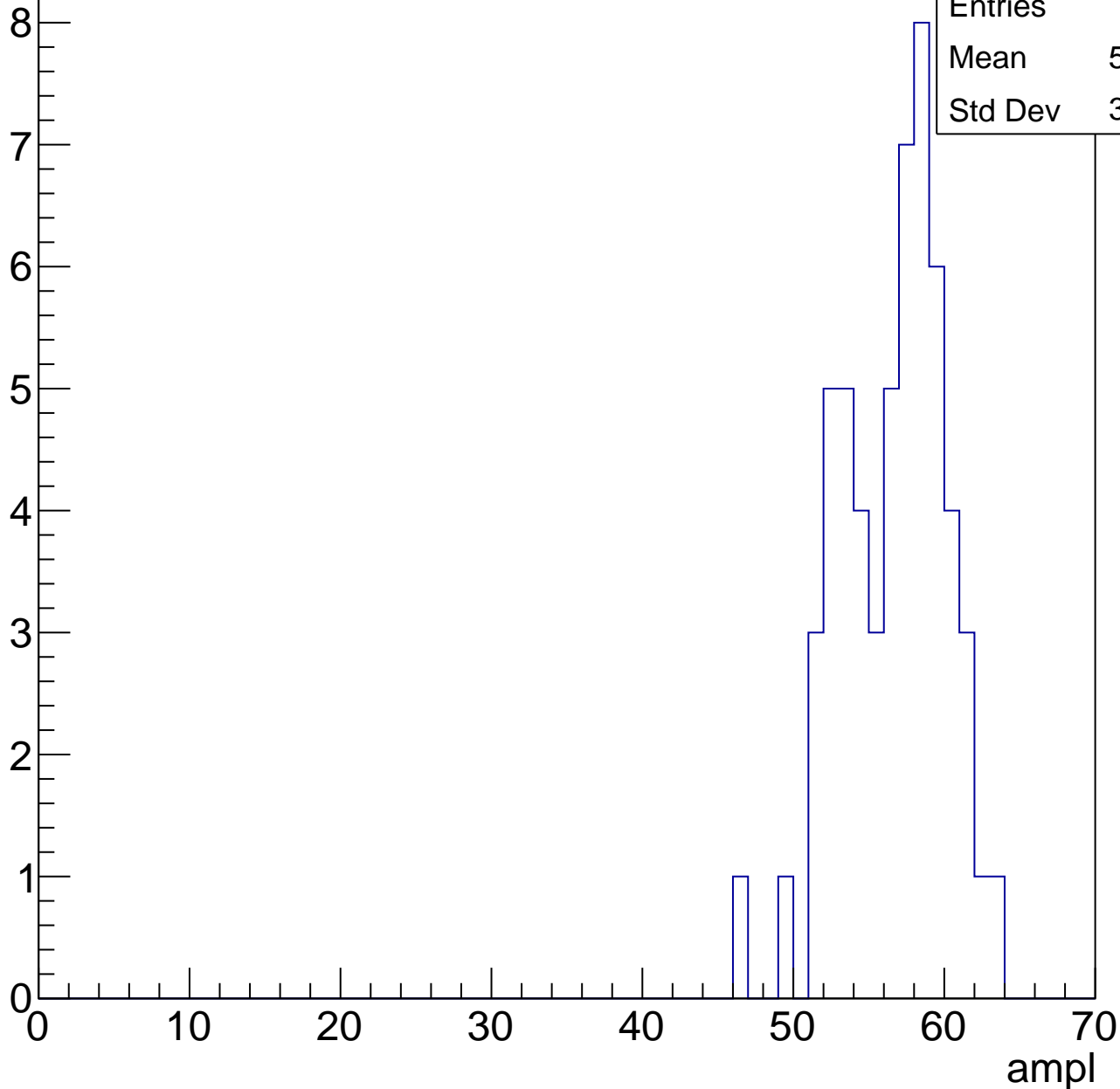


B1L100S, U25-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

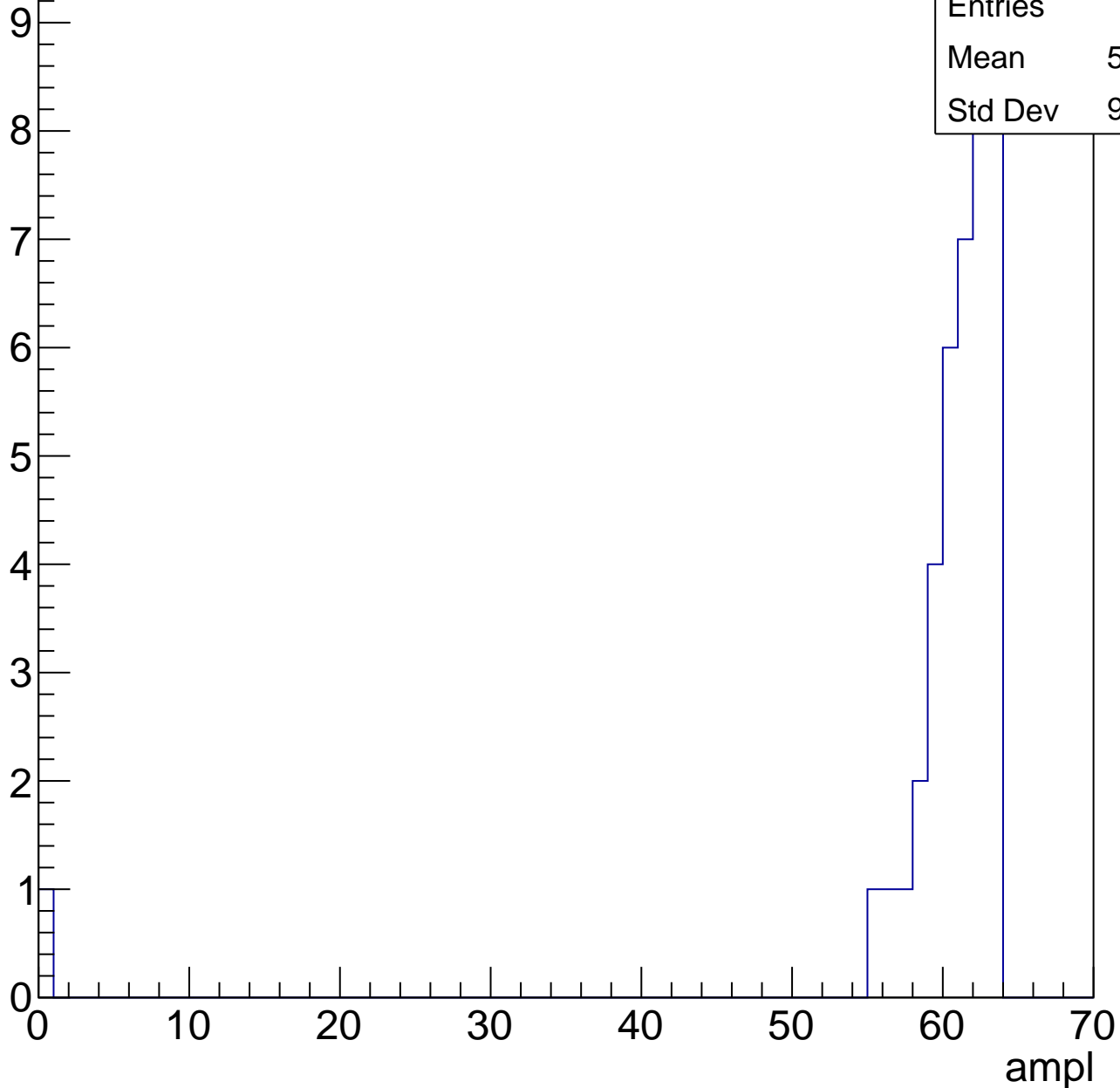
Entries	57
Mean	56.12
Std Dev	3.464



B1L100S, U25-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U25-ch86, adc0

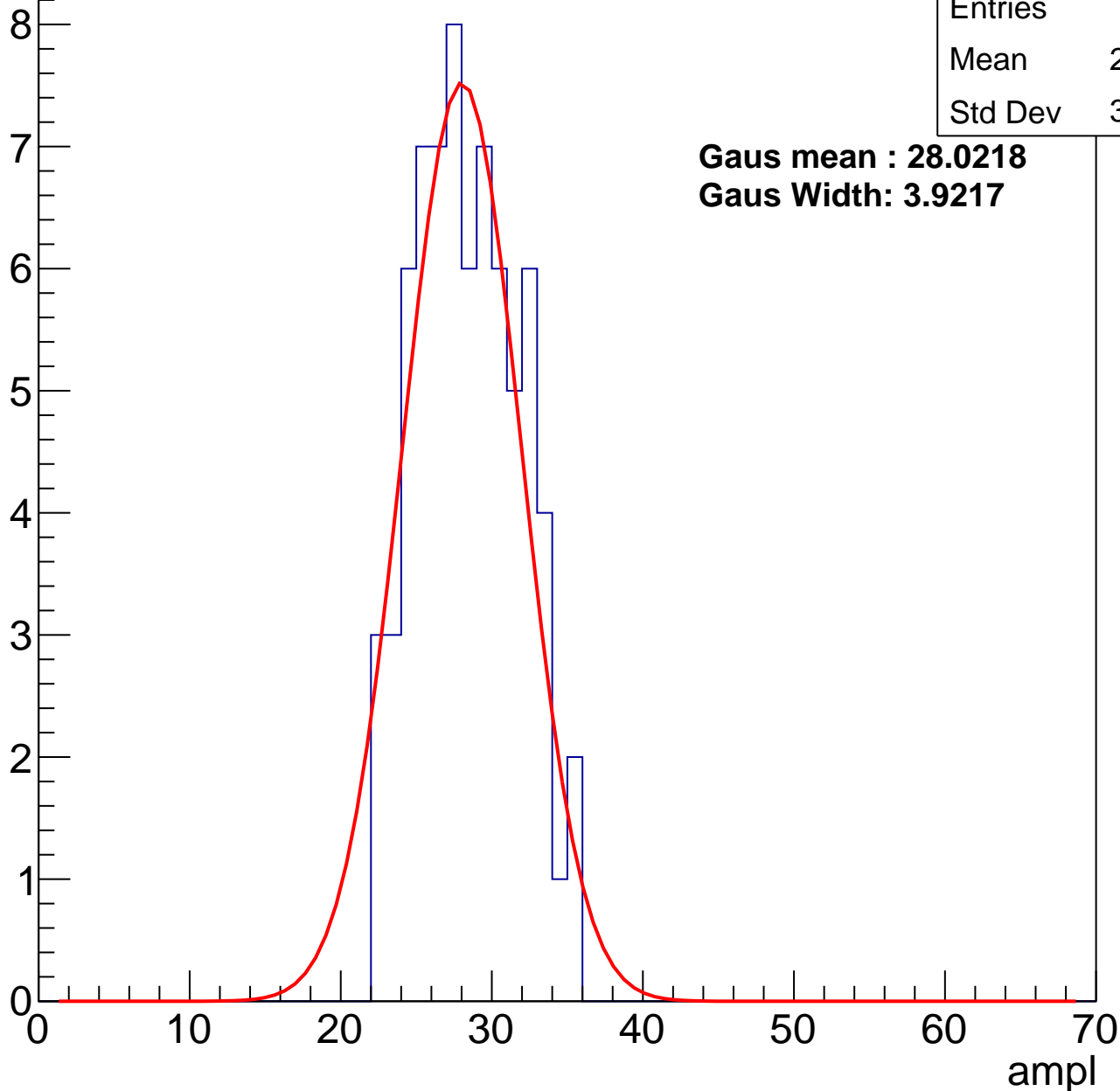
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.97
Std Dev	3.323

Gaus mean : 28.0218

Gaus Width: 3.9217



B1L100S, U25-ch86, adc1

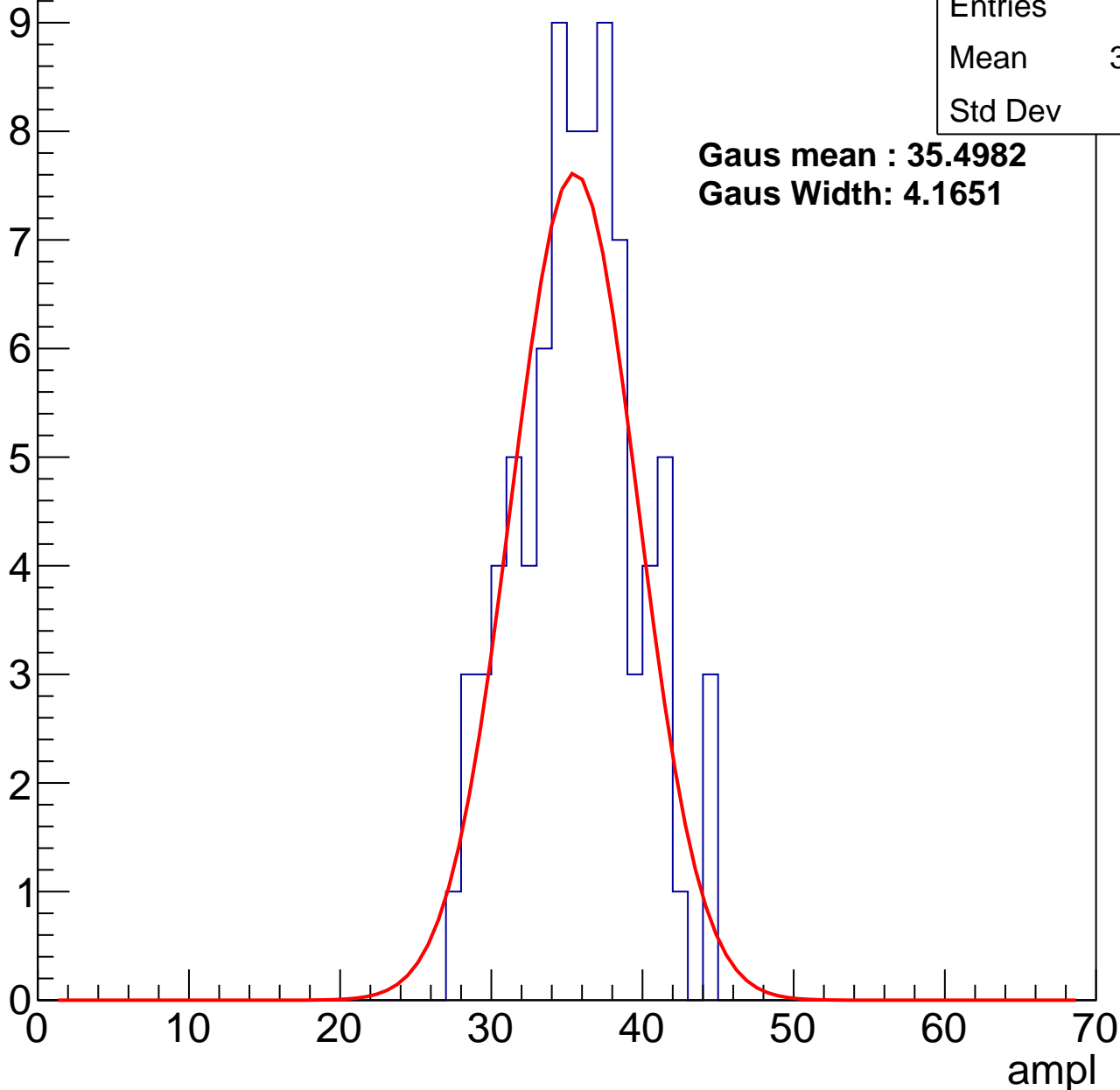
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	35.28
Std Dev	3.94

Gaus mean : 35.4982

Gaus Width: 4.1651



B1L100S, U25-ch86, adc2

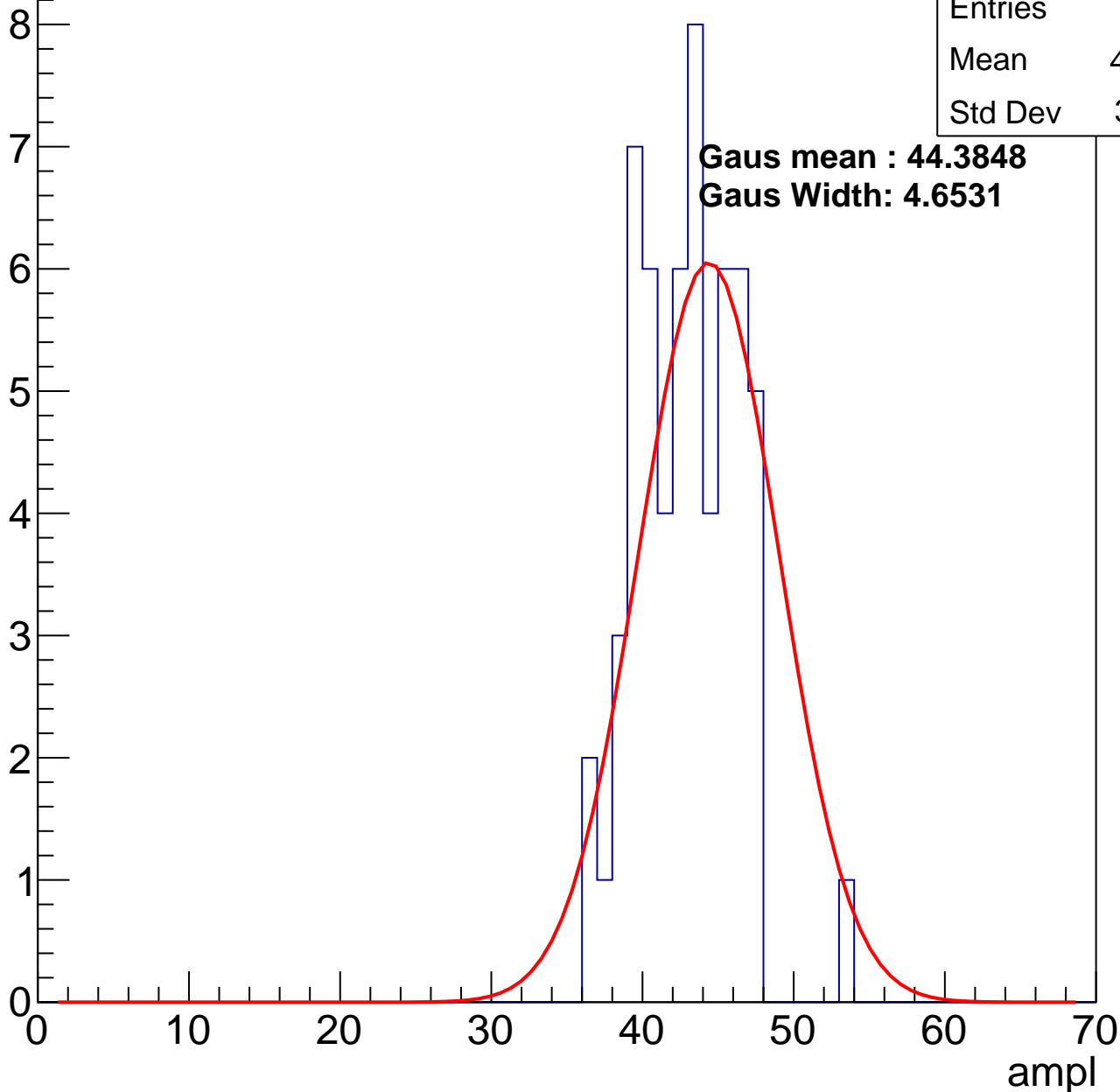
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.47
Std Dev	3.301

Gaus mean : 44.3848

Gaus Width: 4.6531



B1L100S, U25-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

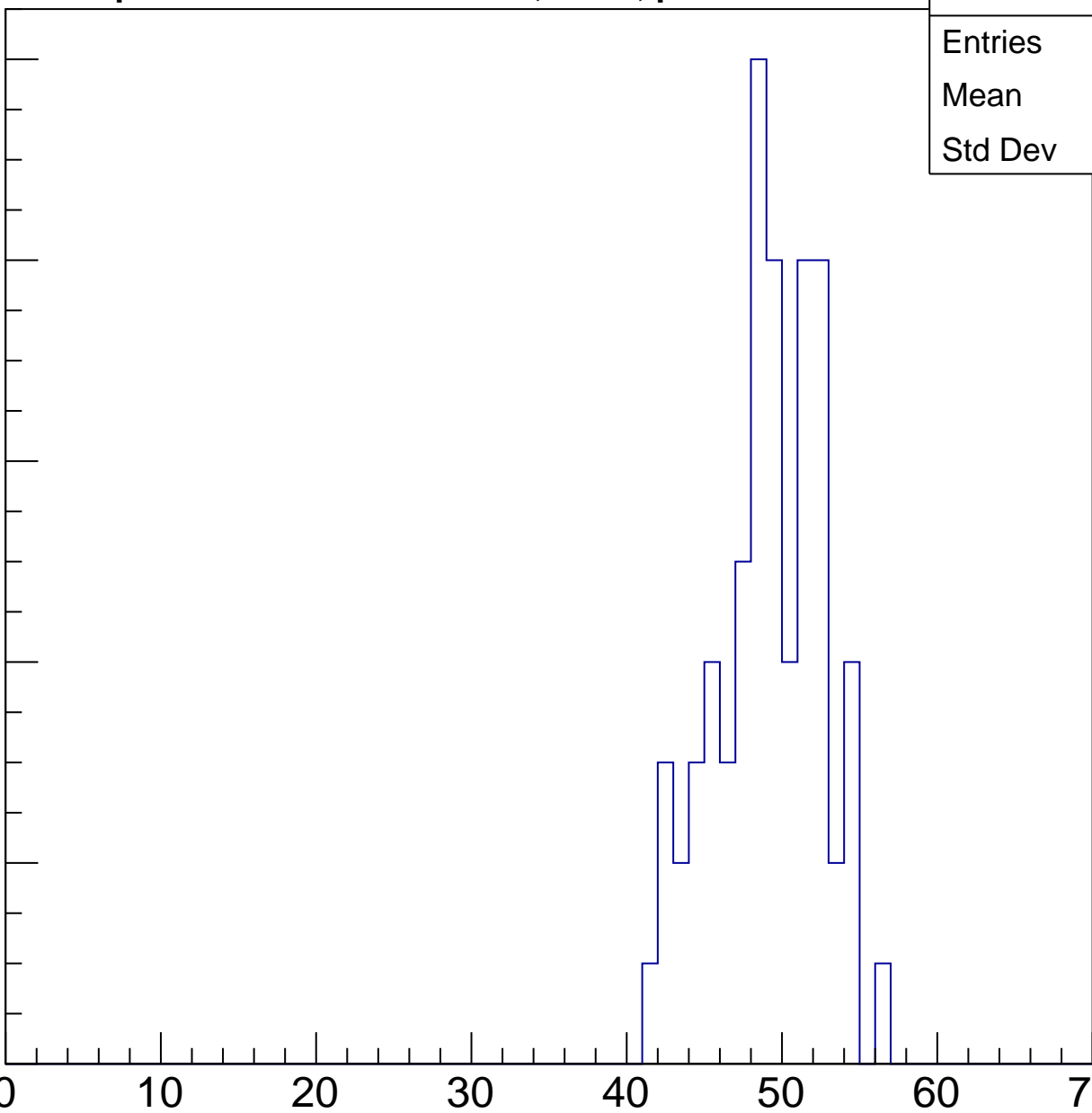
Entries	66
Mean	48.67
Std Dev	3.395

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

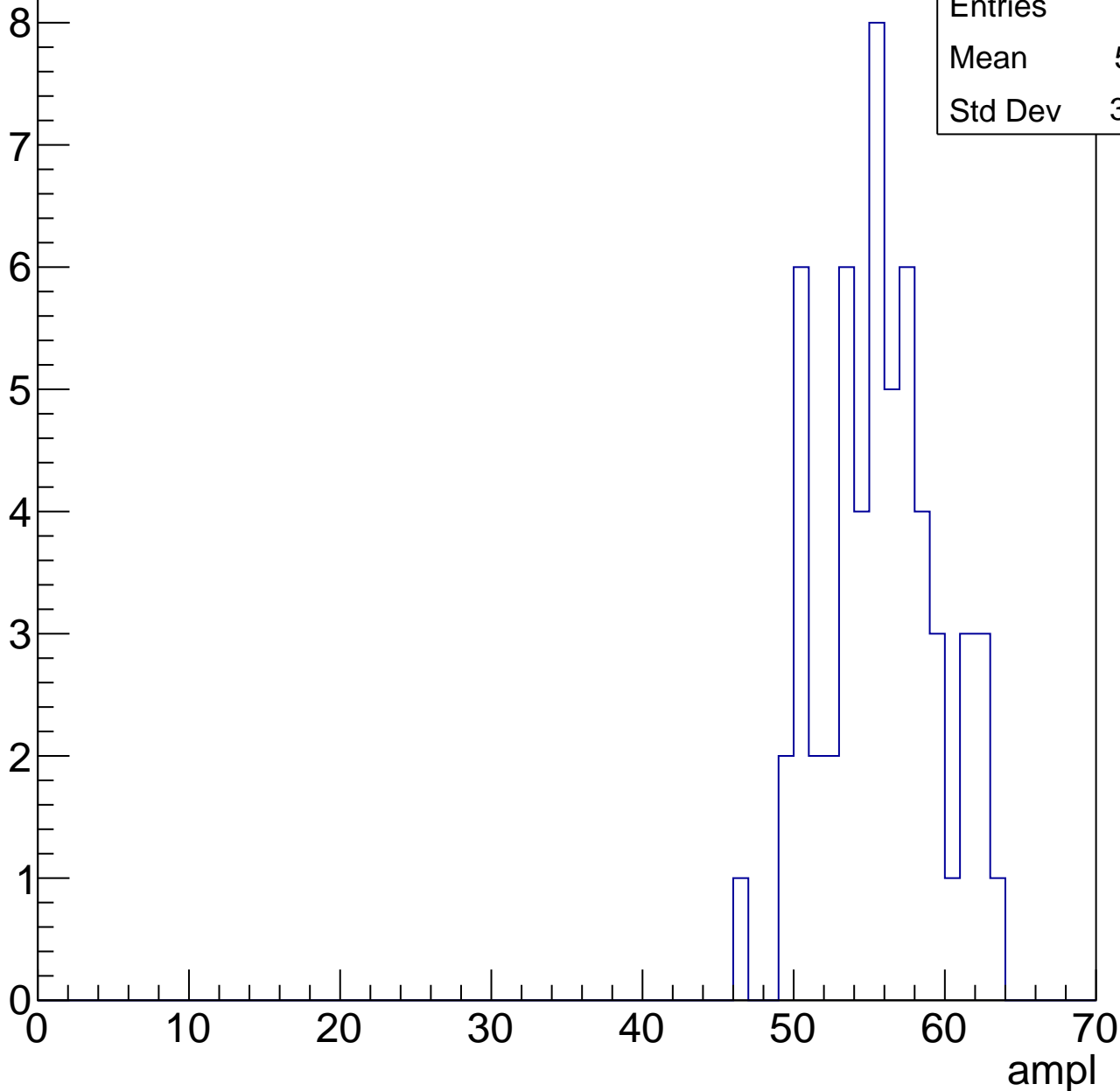


B1L100S, U25-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.21
Std Dev	3.829



B1L100S, U25-ch86, adc5

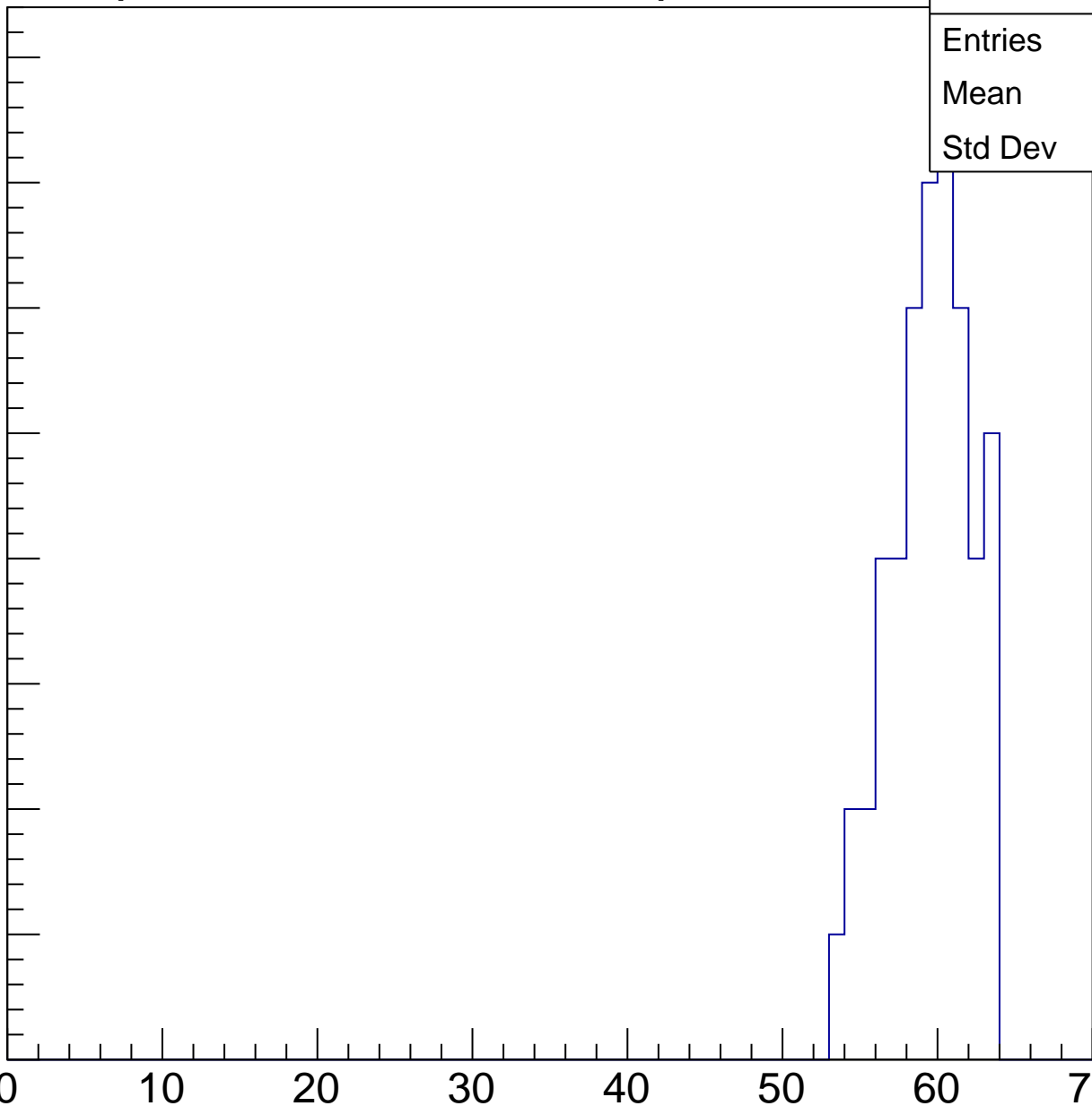
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.04
Std Dev	2.571

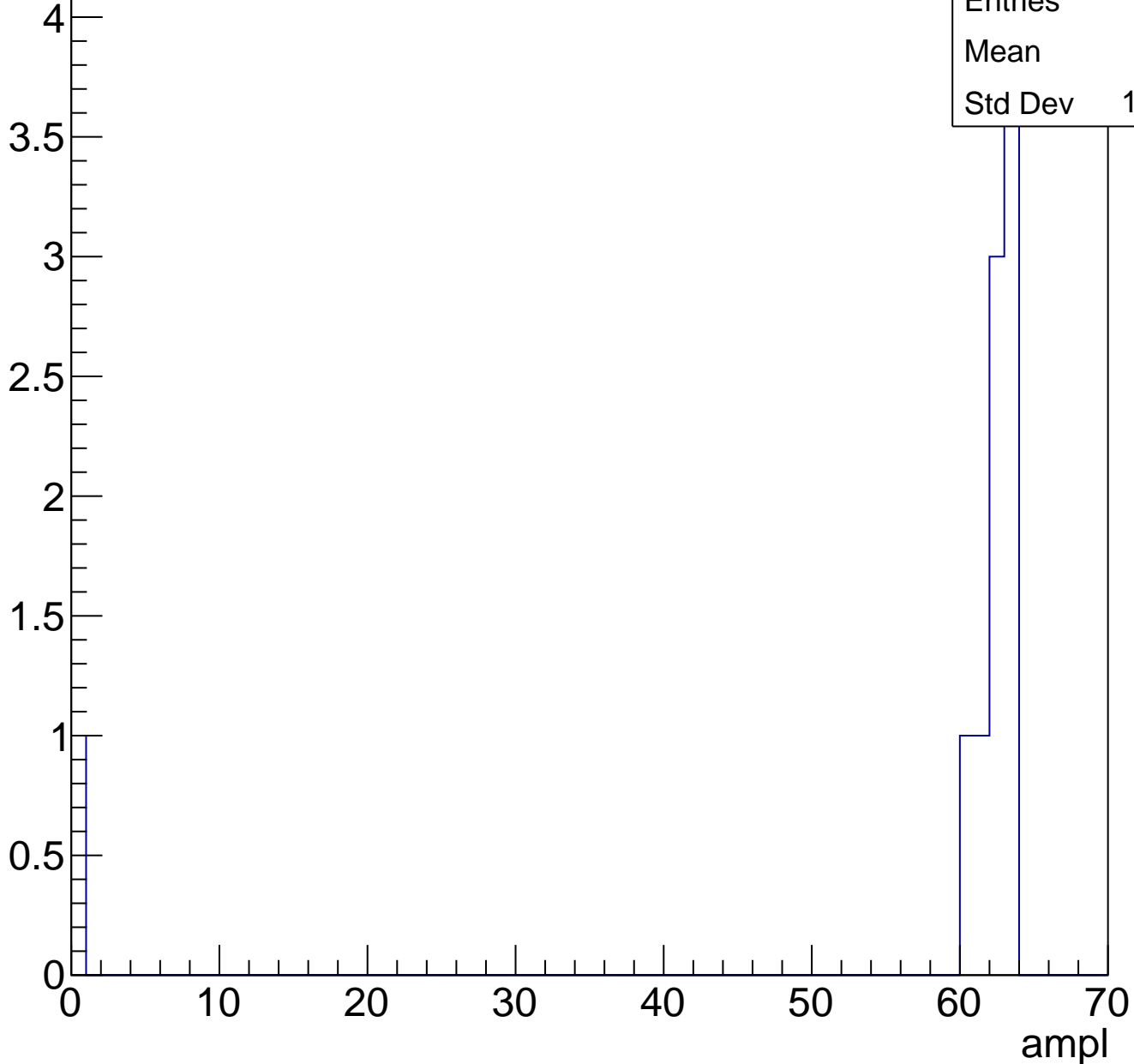
ampl



B1L100S, U25-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch87, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	29.88
Std Dev	4.766

Gaus mean : 31.1843

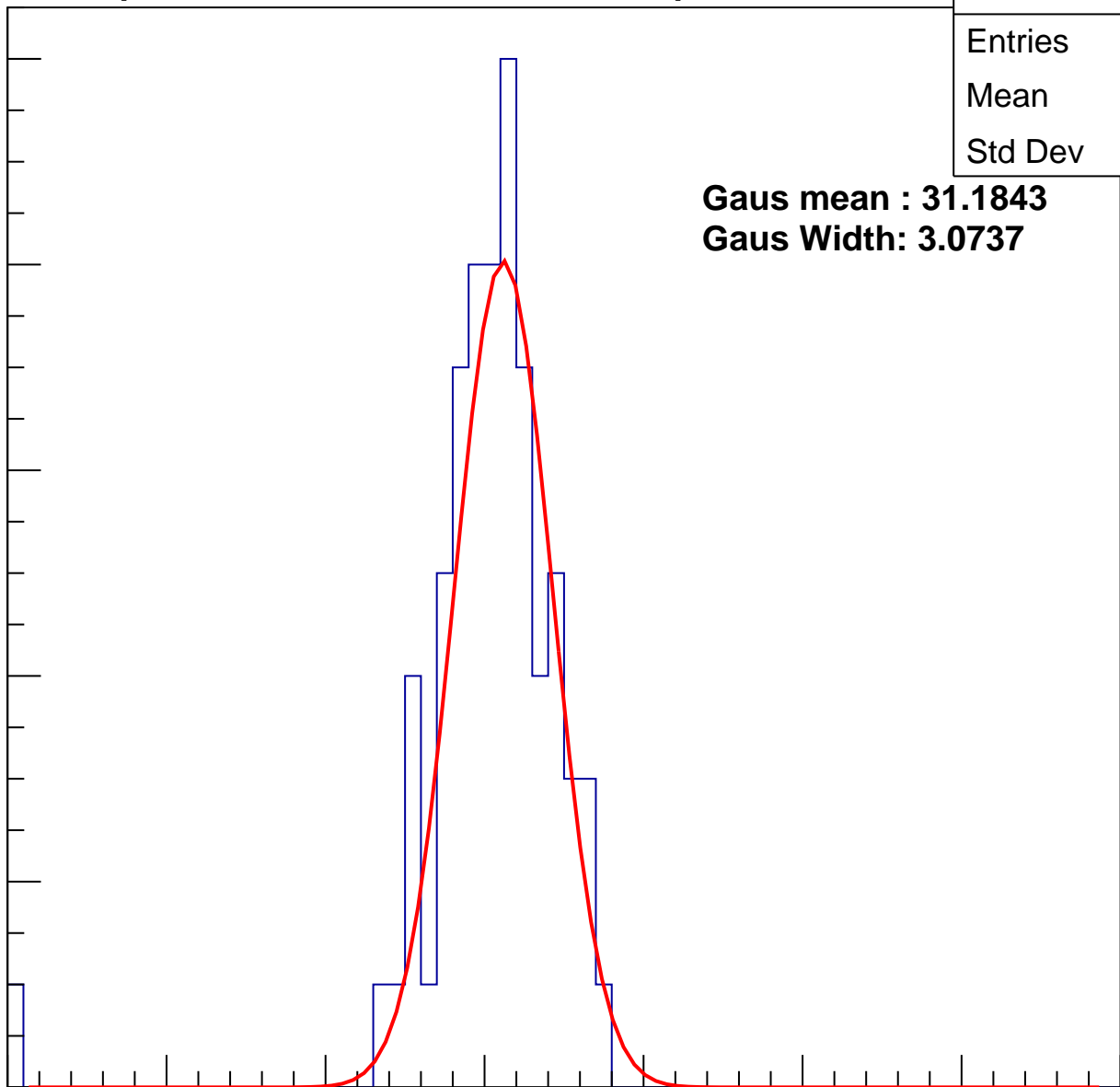
Gaus Width: 3.0737

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch87, adc1

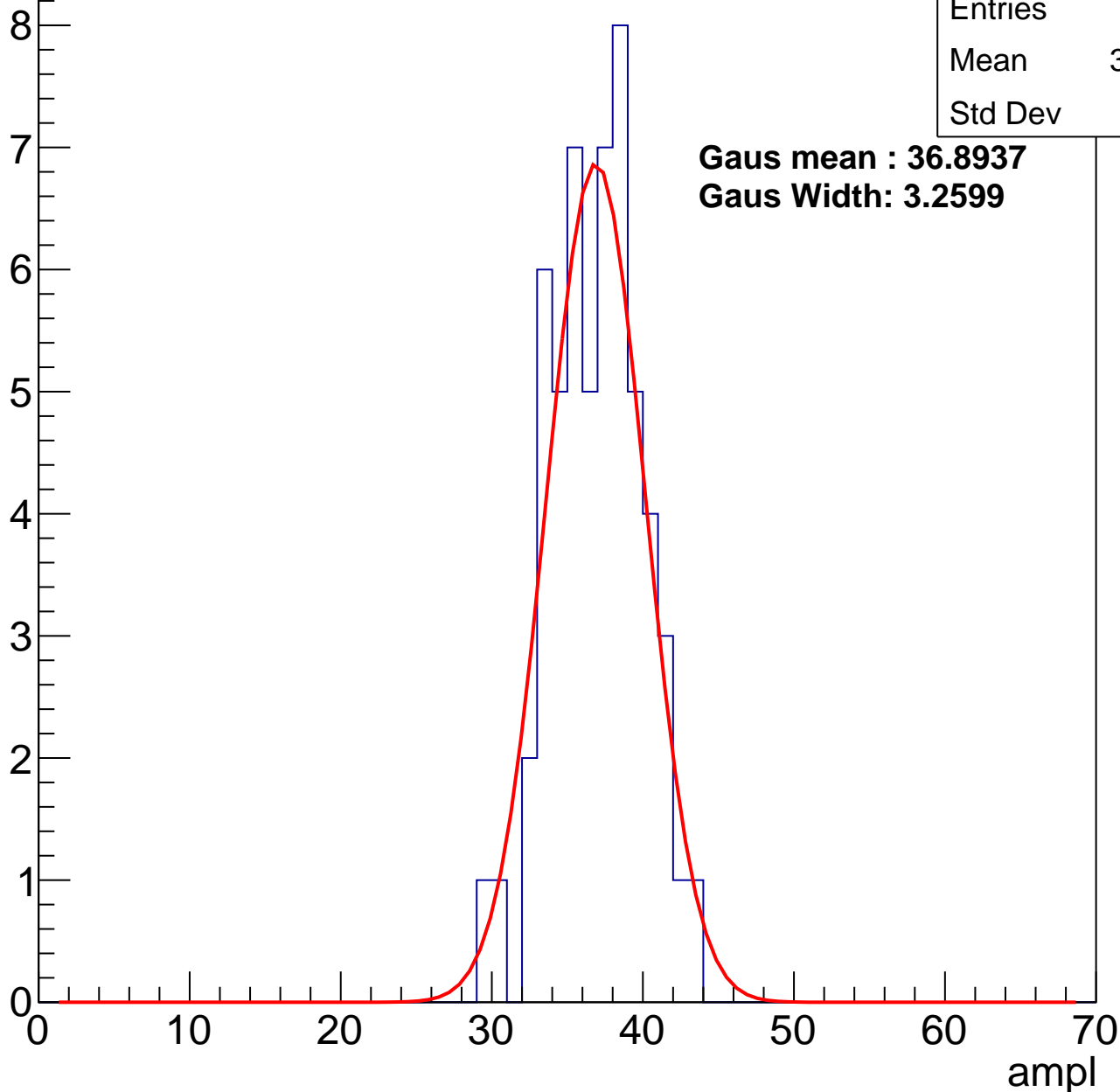
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	36.46
Std Dev	2.97

Gaus mean : 36.8937

Gaus Width: 3.2599



B1L100S, U25-ch87, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

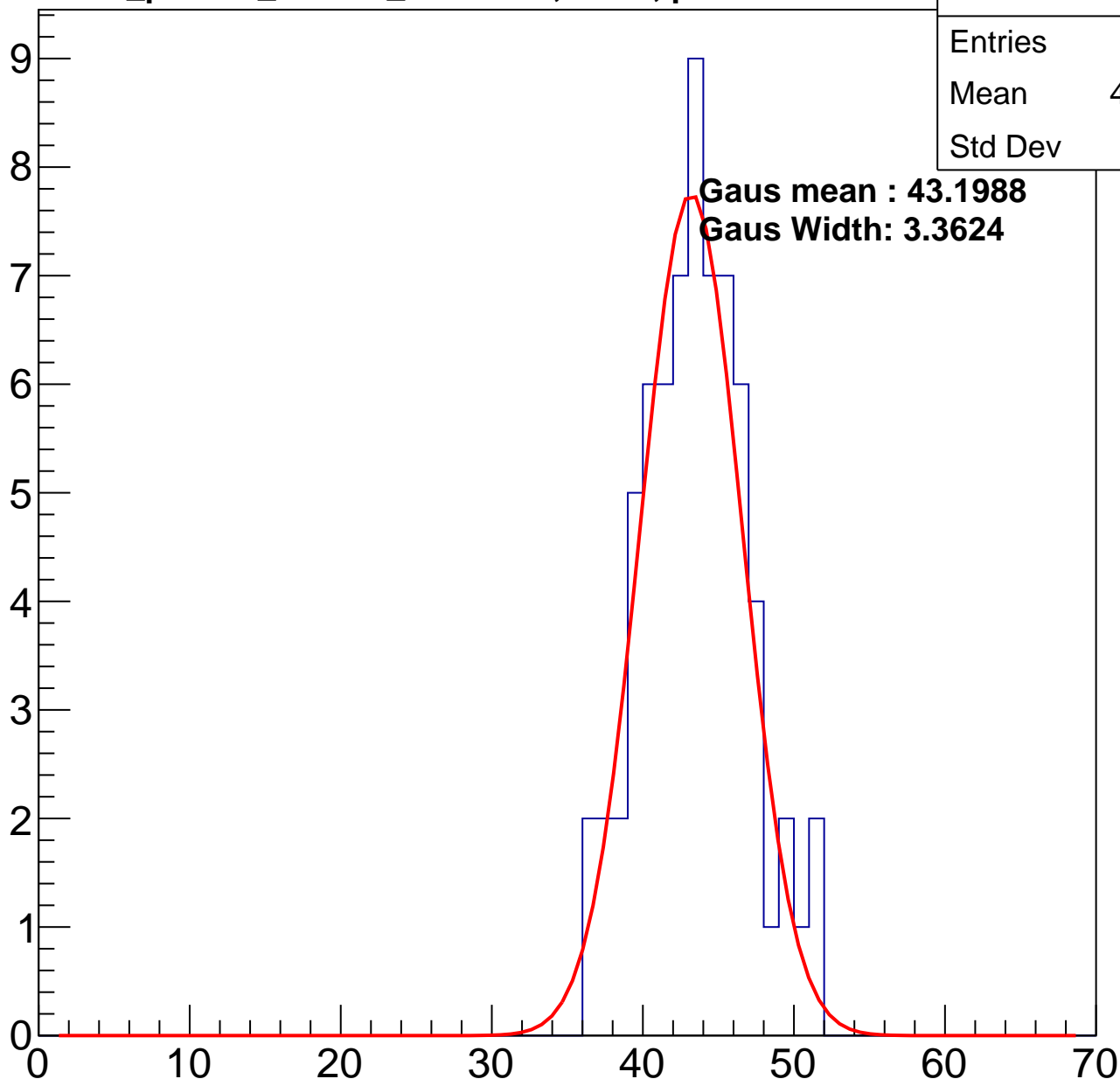
9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	43.03
Std Dev	3.43

Gaus mean : 43.1988

Gaus Width: 3.3624

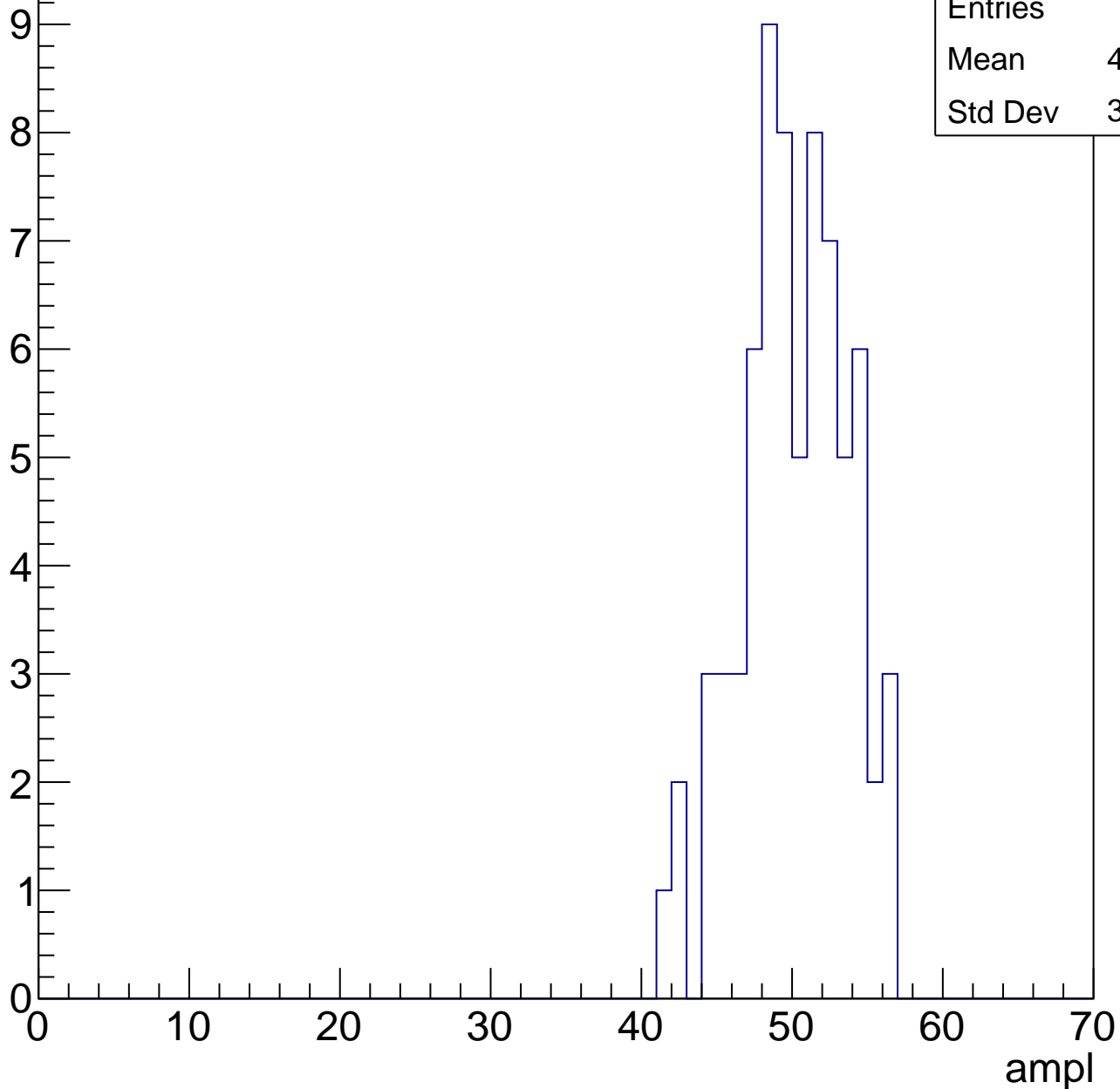
ampl



B1L100S, U25-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



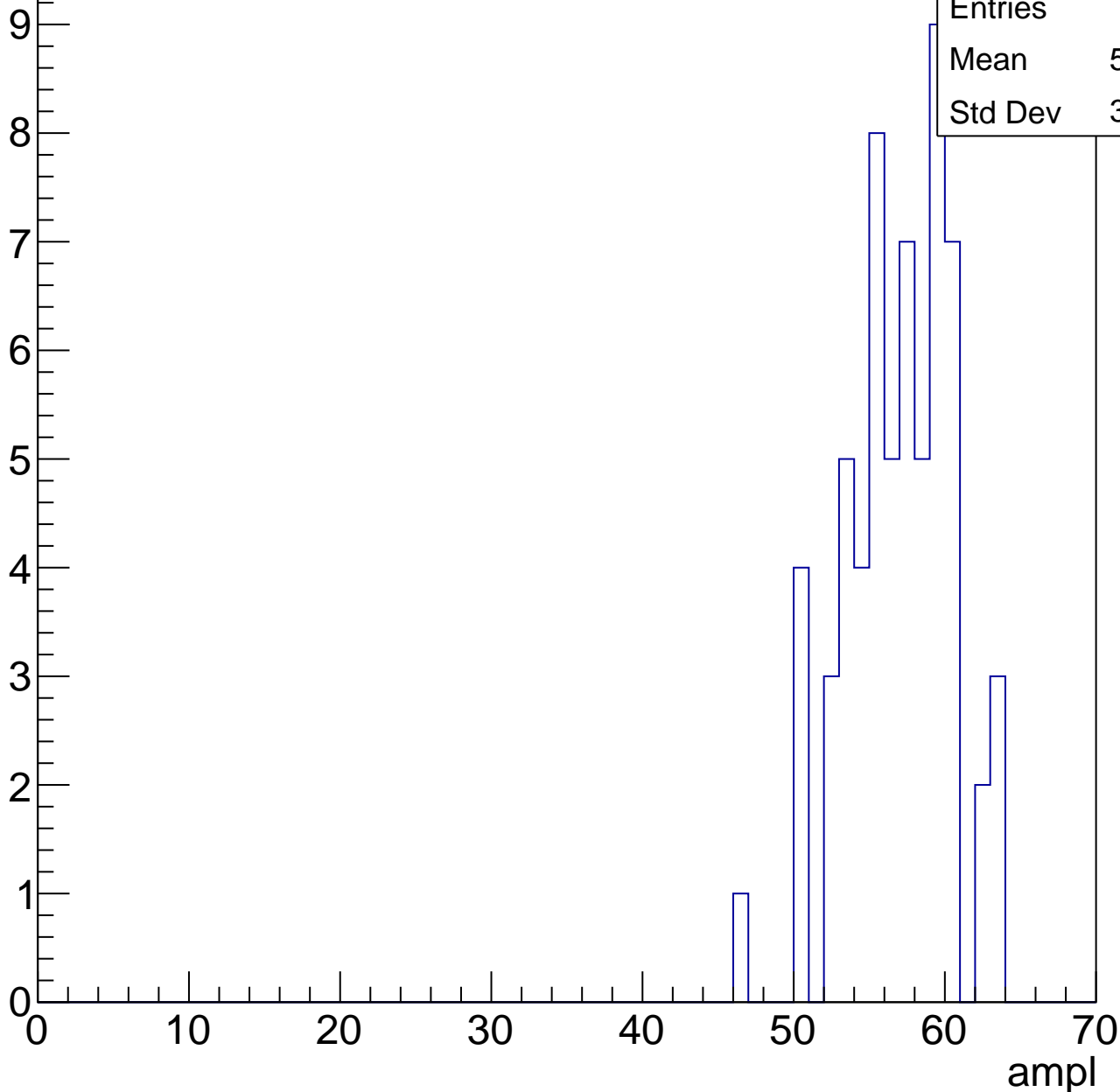
Entries	71
Mean	49.65
Std Dev	3.489

B1L100S, U25-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	56.44
Std Dev	3.536

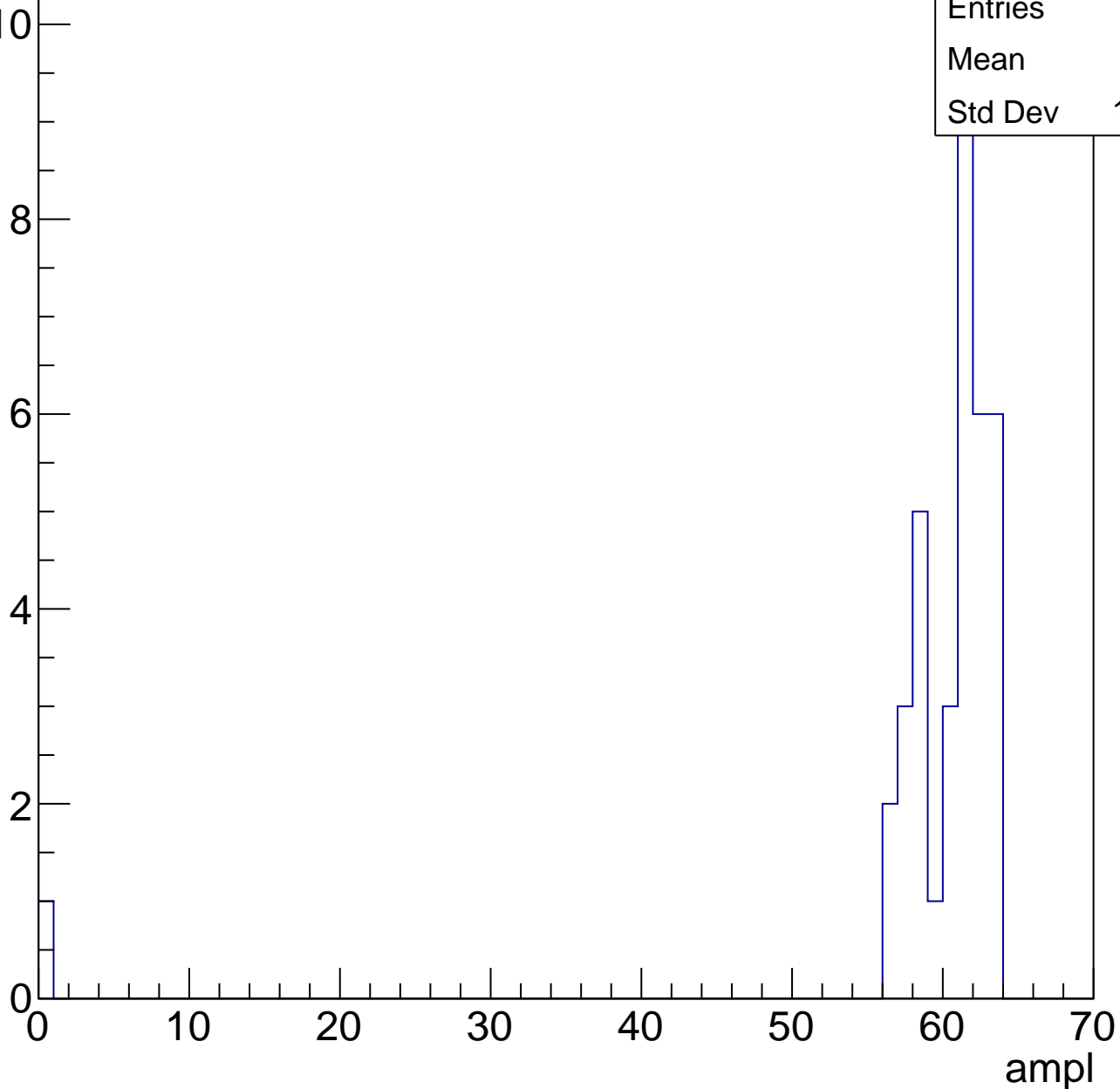


B1L100S, U25-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.7
Std Dev	10.01



B1L100S, U25-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

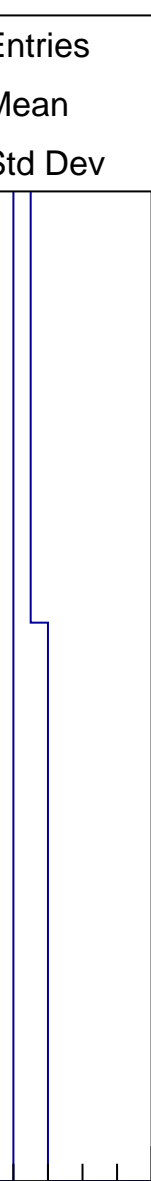
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch88, adc0

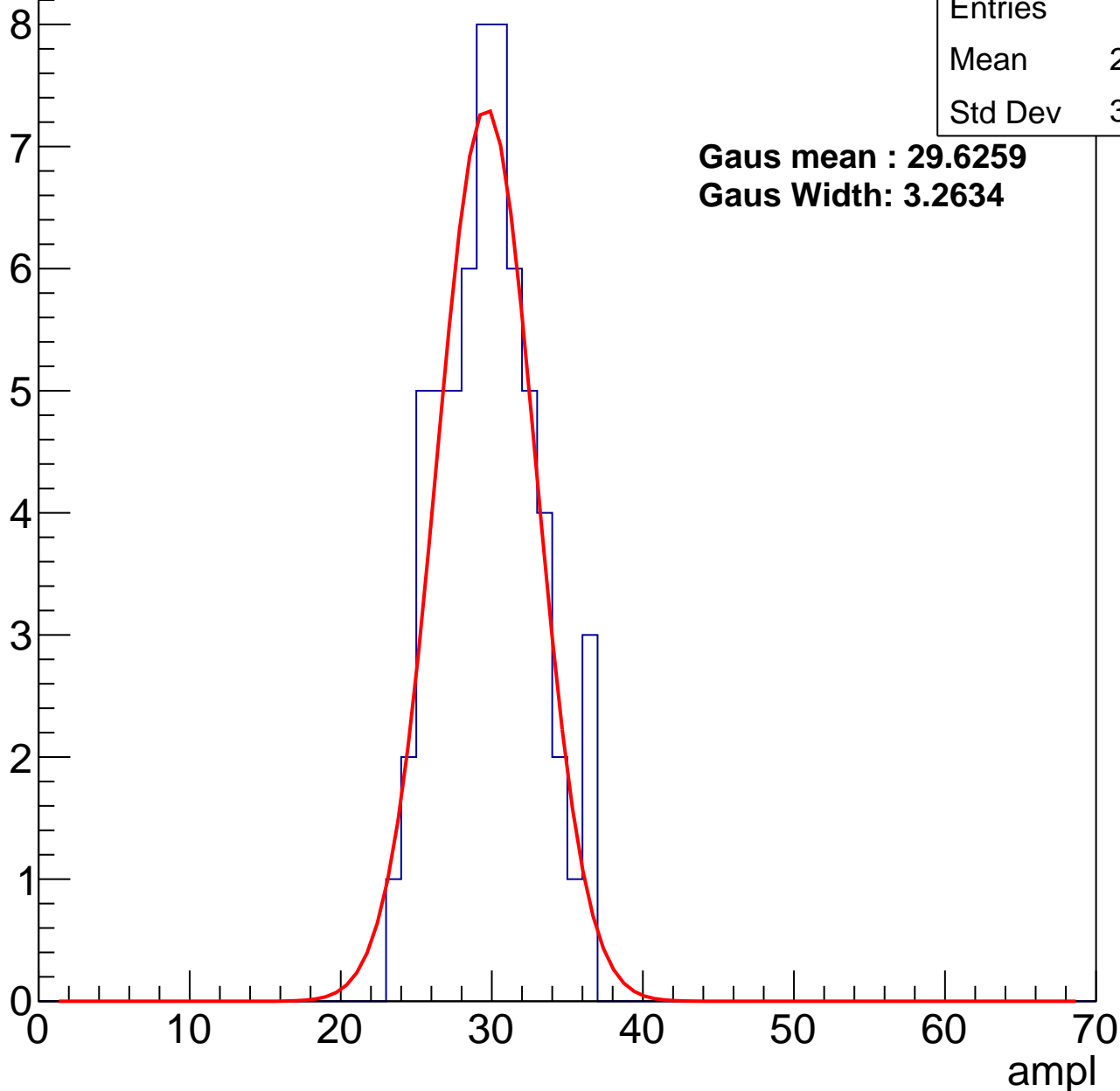
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.34
Std Dev	3.146

Gaus mean : 29.6259

Gaus Width: 3.2634



B1L100S, U25-ch88, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	36.3
Std Dev	3.744

Gaus mean : 36.8681

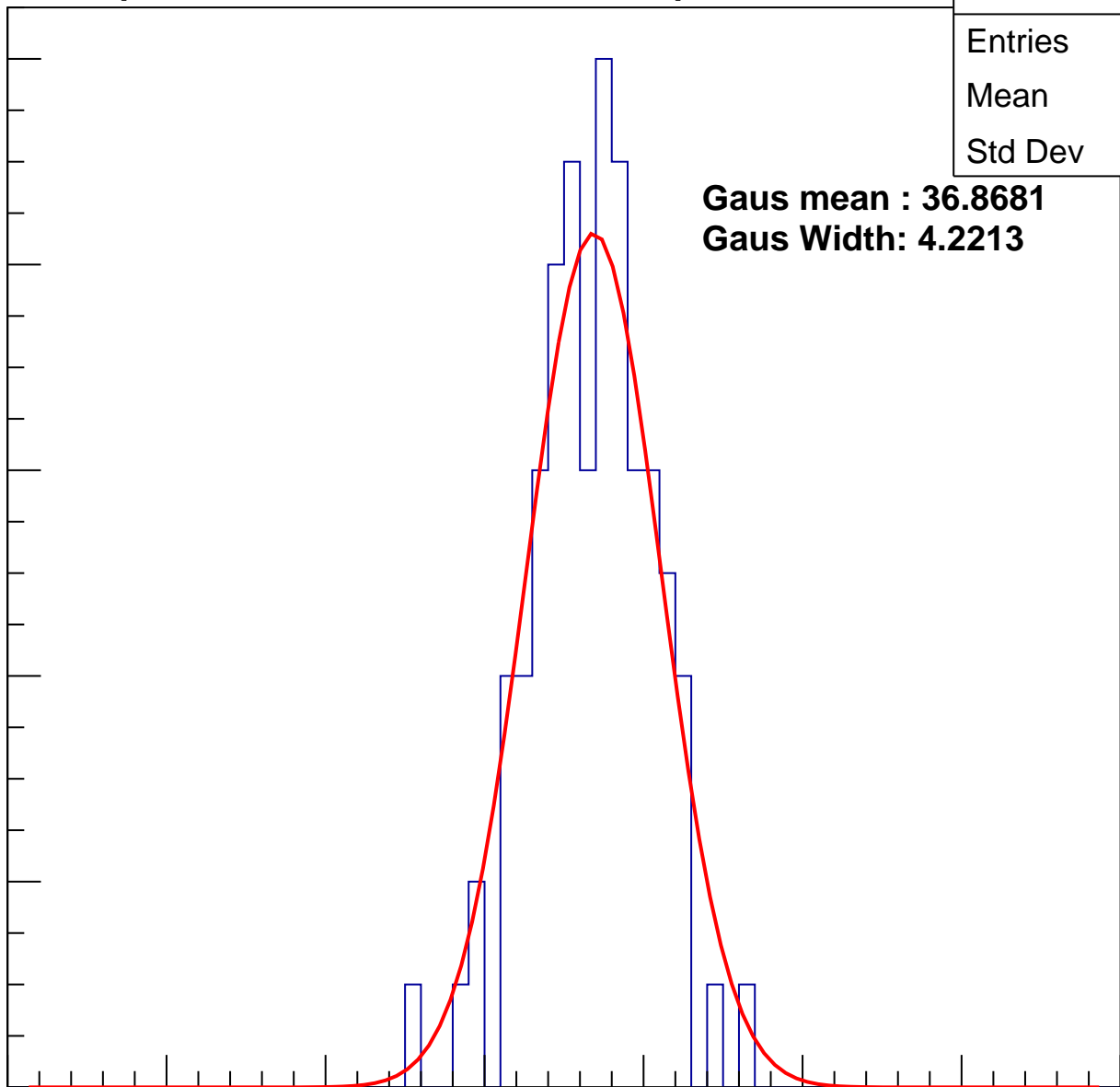
Gaus Width: 4.2213

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch88, adc2

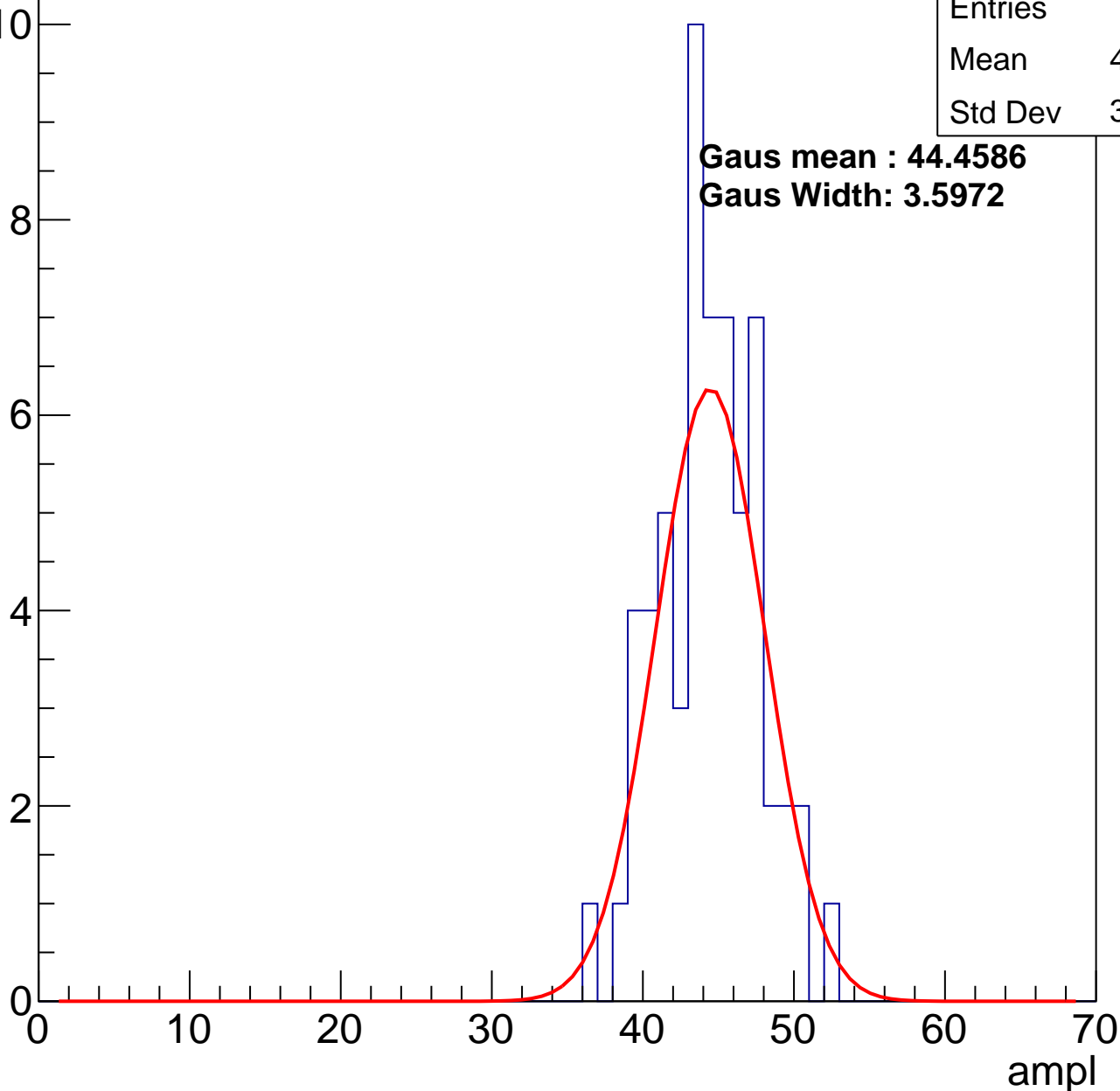
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.92
Std Dev	3.236

Gaus mean : 44.4586

Gaus Width: 3.5972

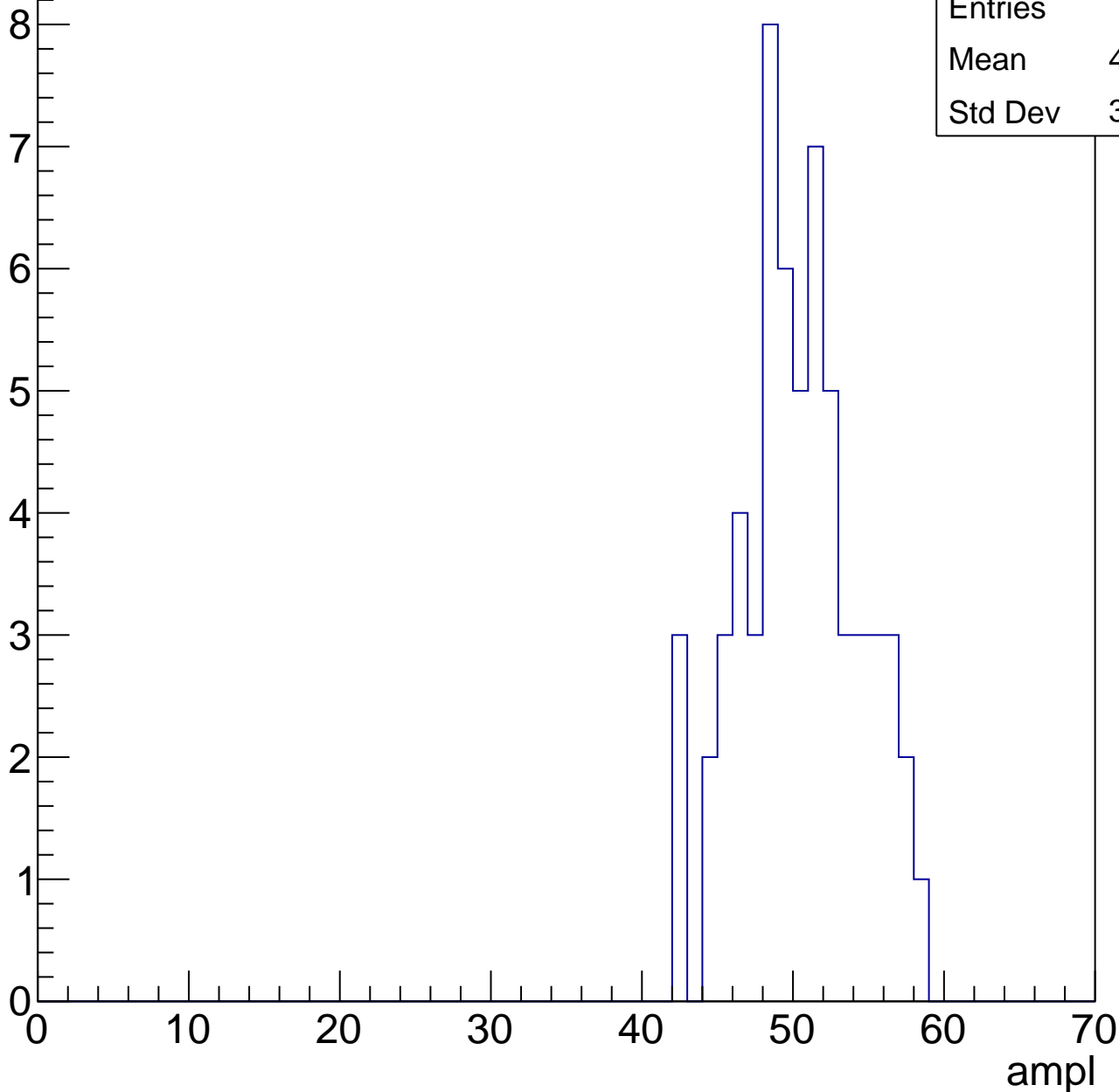


B1L100S, U25-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.92
Std Dev	3.872

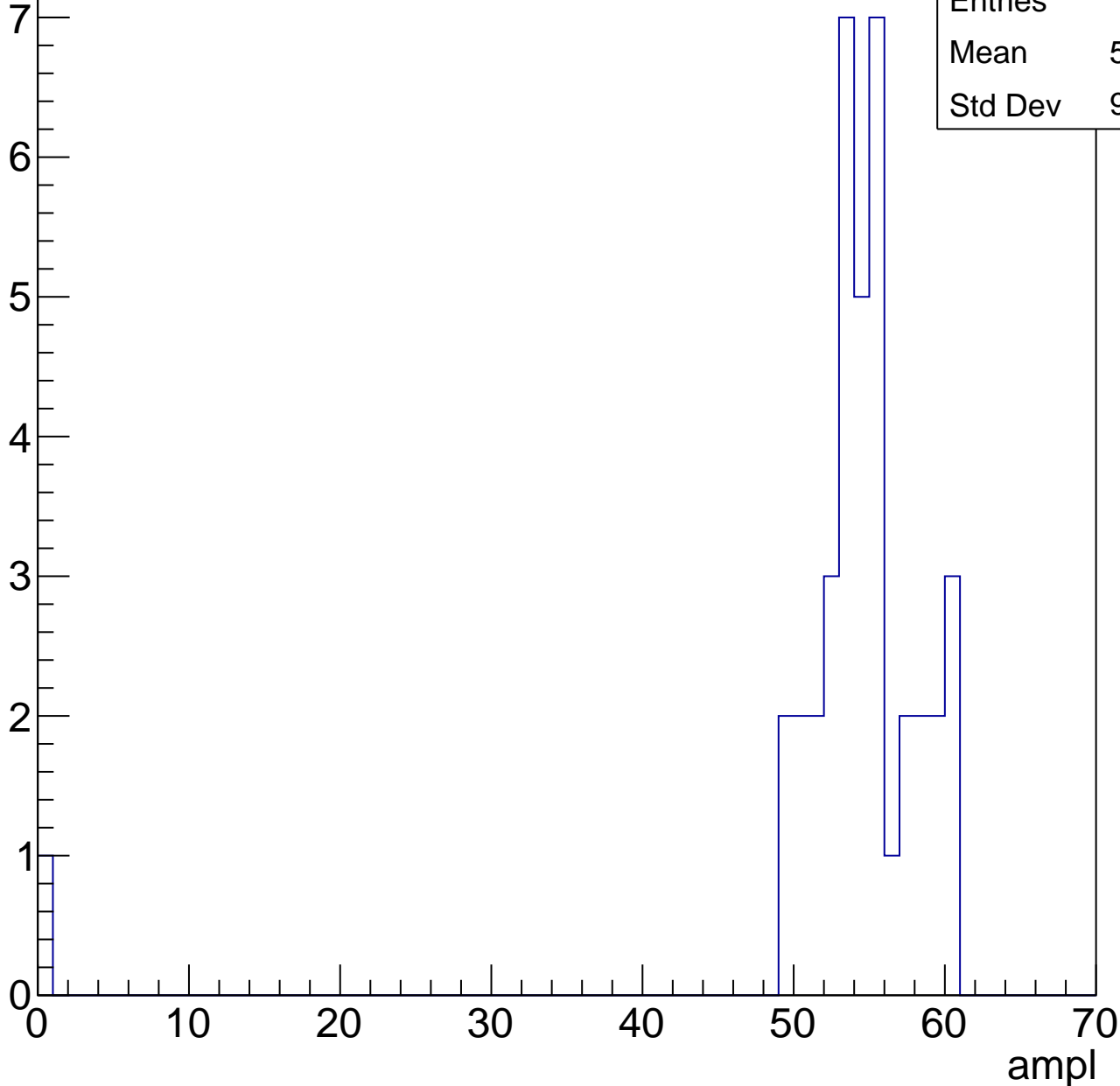


B1L100S, U25-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	52.97
Std Dev	9.077

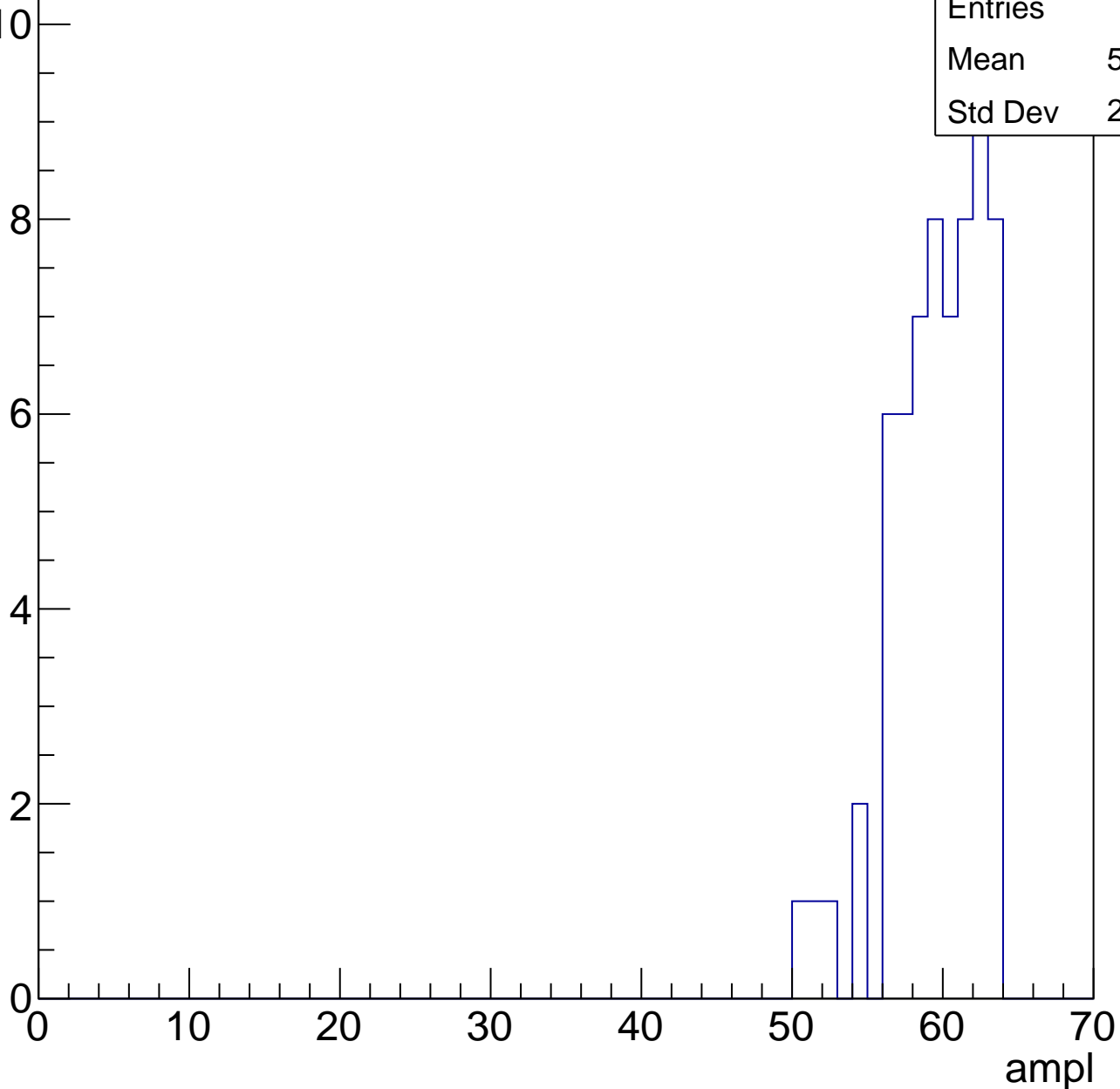


B1L100S, U25-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	59.22
Std Dev	2.995



B1L100S, U25-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	62
Std Dev	1

B1L100S, U25-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch89, adc0

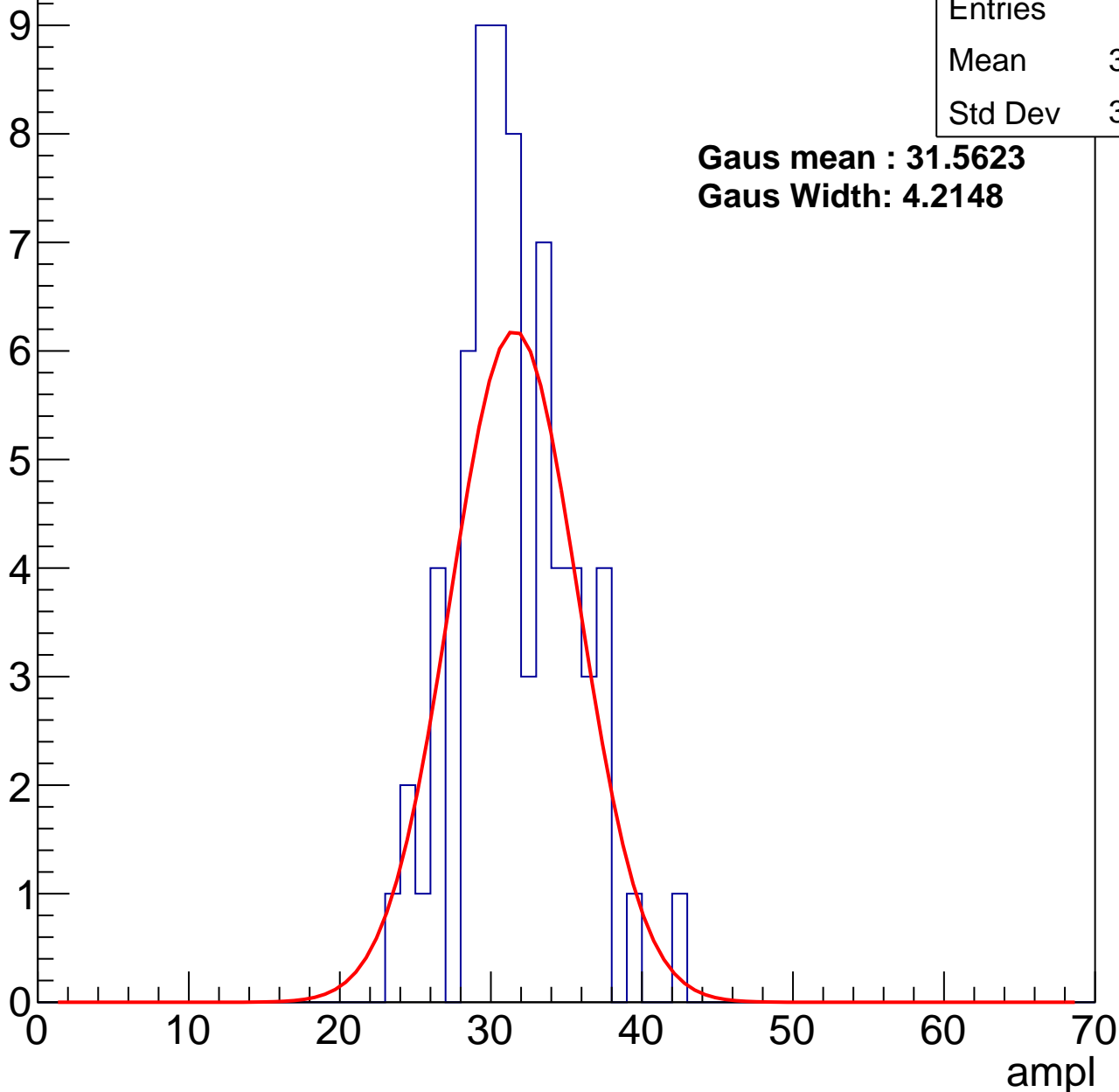
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	31.15
Std Dev	3.727

Gaus mean : 31.5623

Gaus Width: 4.2148



B1L100S, U25-ch89, adc1

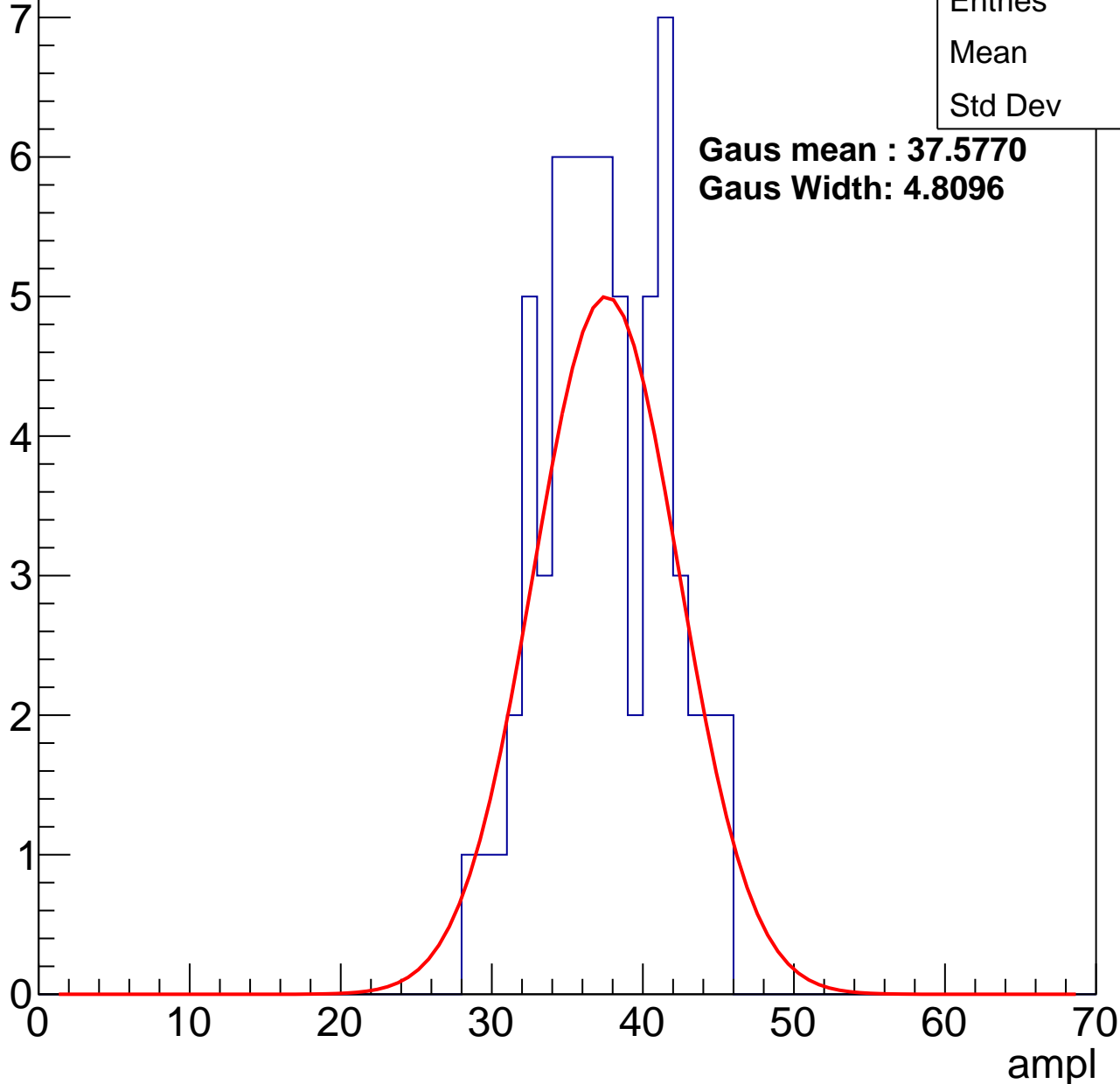
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37
Std Dev	4.05

Gaus mean : 37.5770

Gaus Width: 4.8096

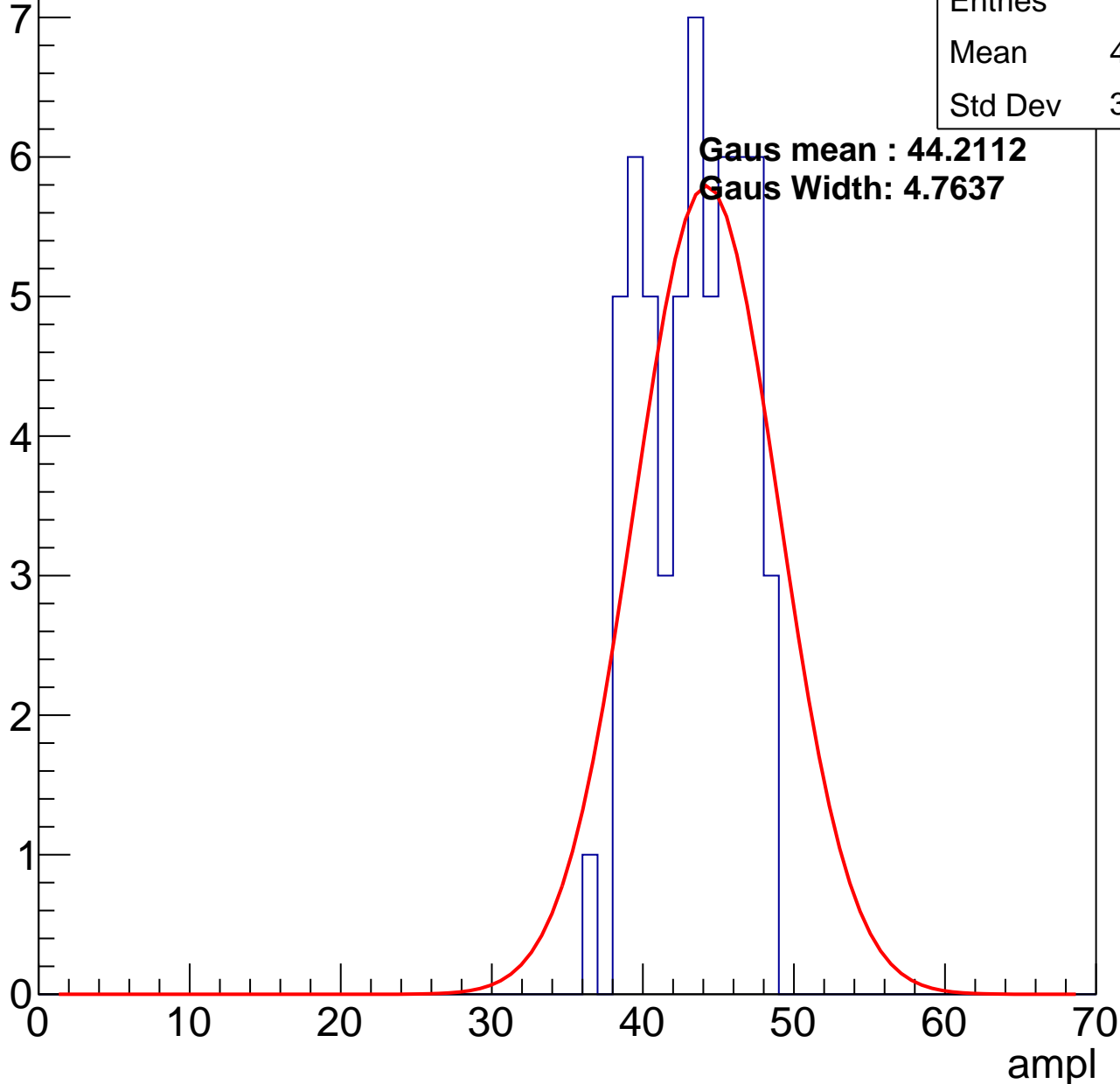


B1L100S, U25-ch89, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	42.86
Std Dev	3.176

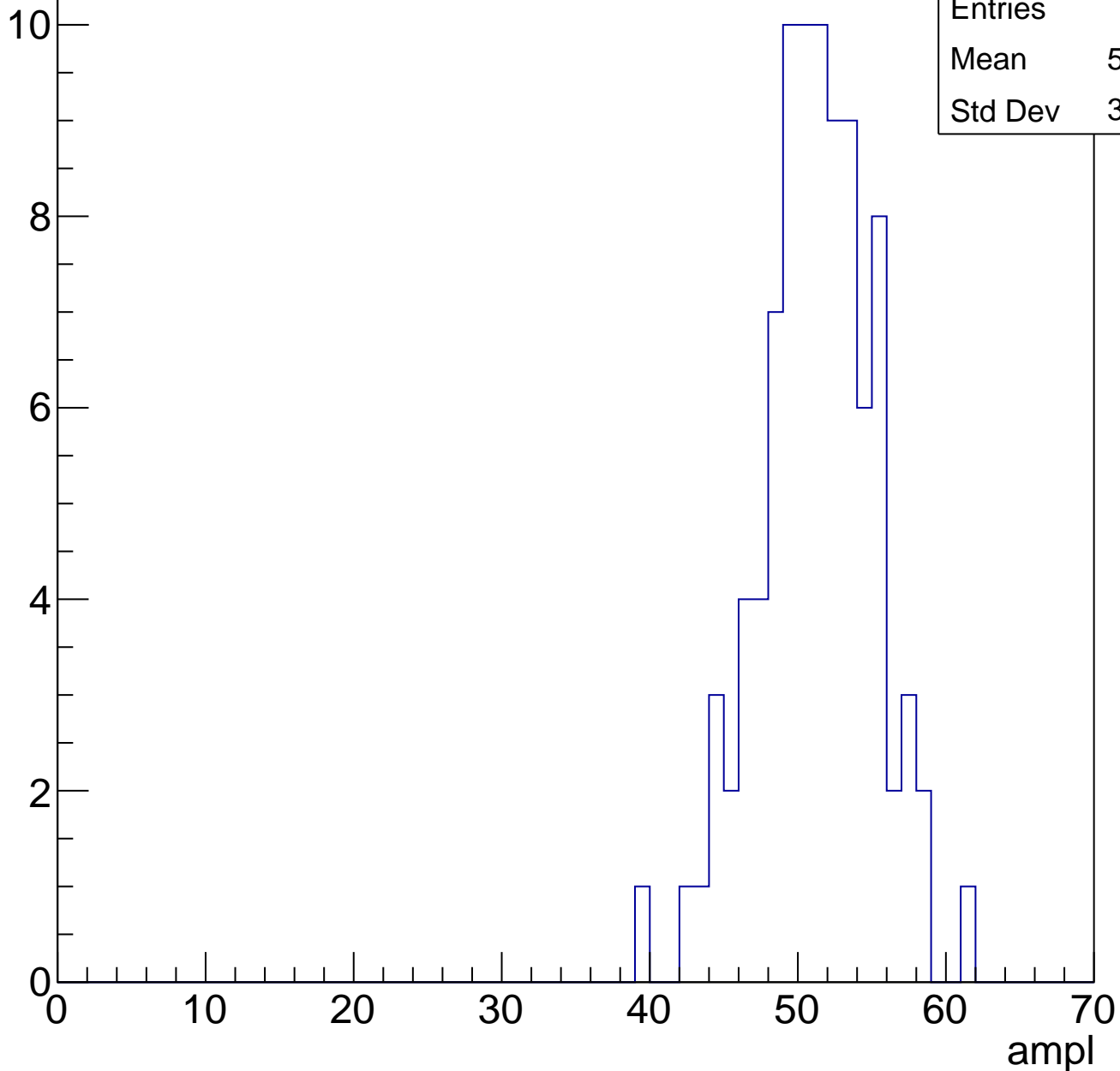


B1L100S, U25-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	93
Mean	50.78
Std Dev	3.829

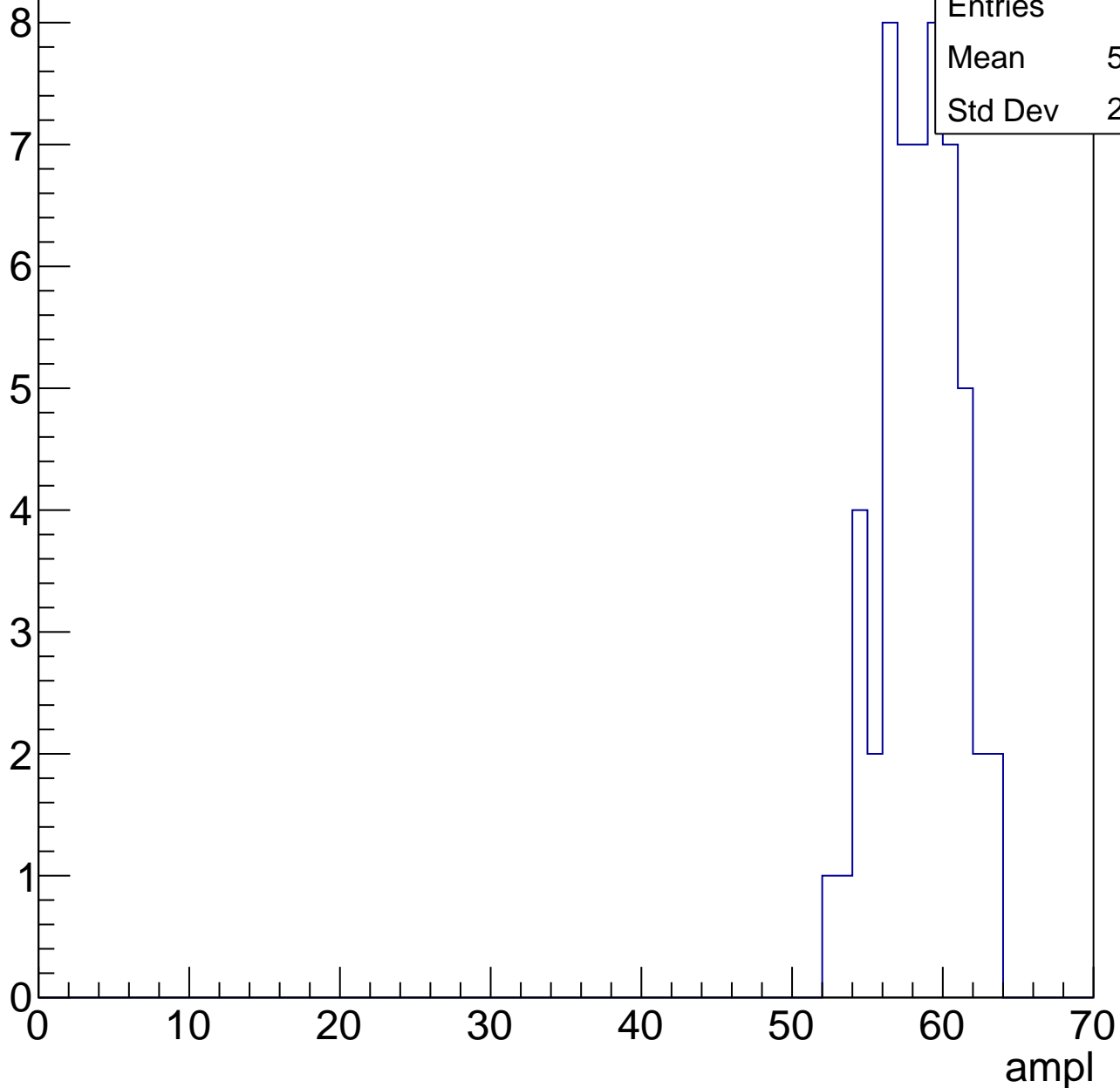
Entry



B1L100S, U25-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

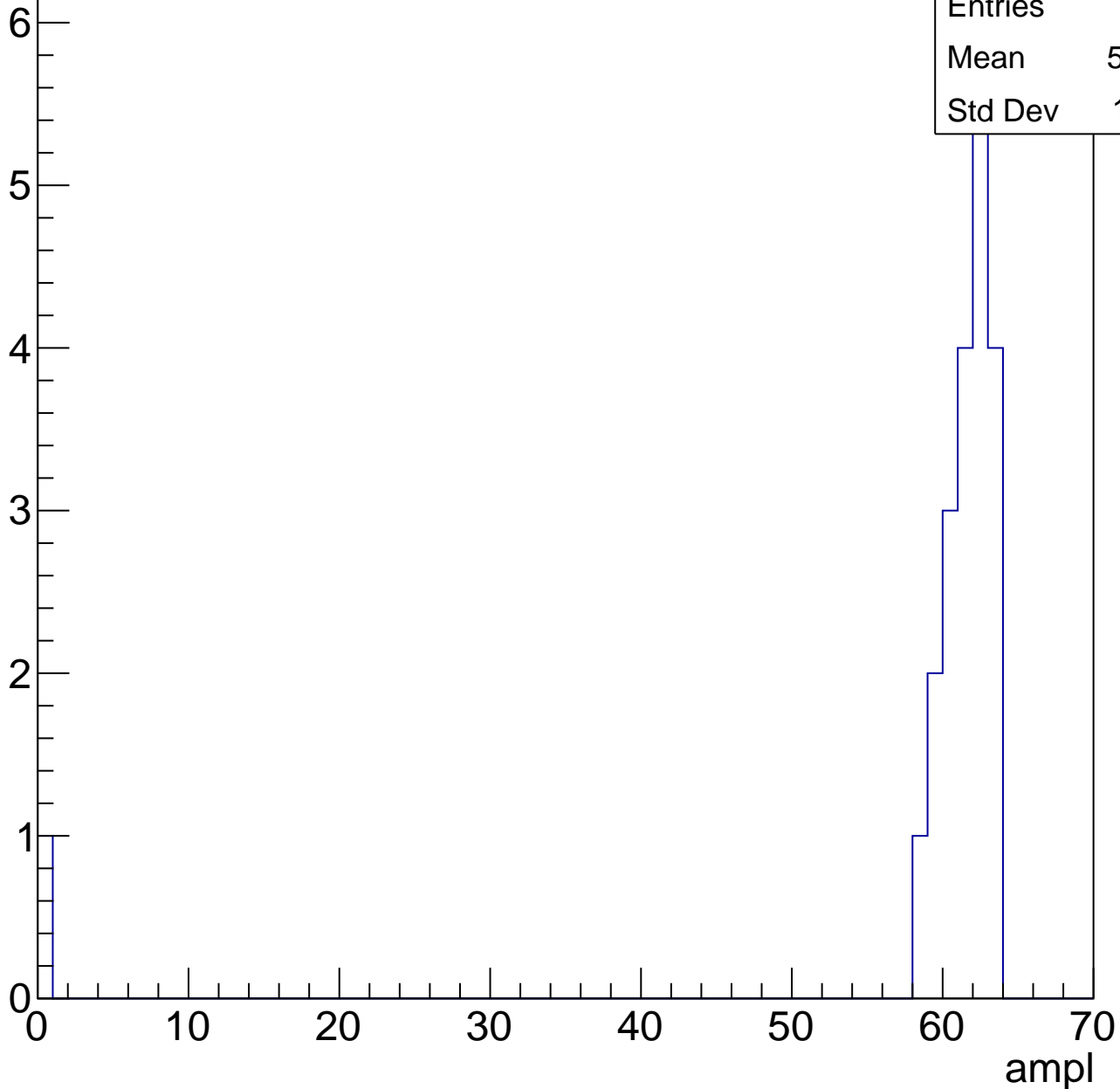


B1L100S, U25-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.29
Std Dev	13.11



B1L100S, U25-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch90, adc0

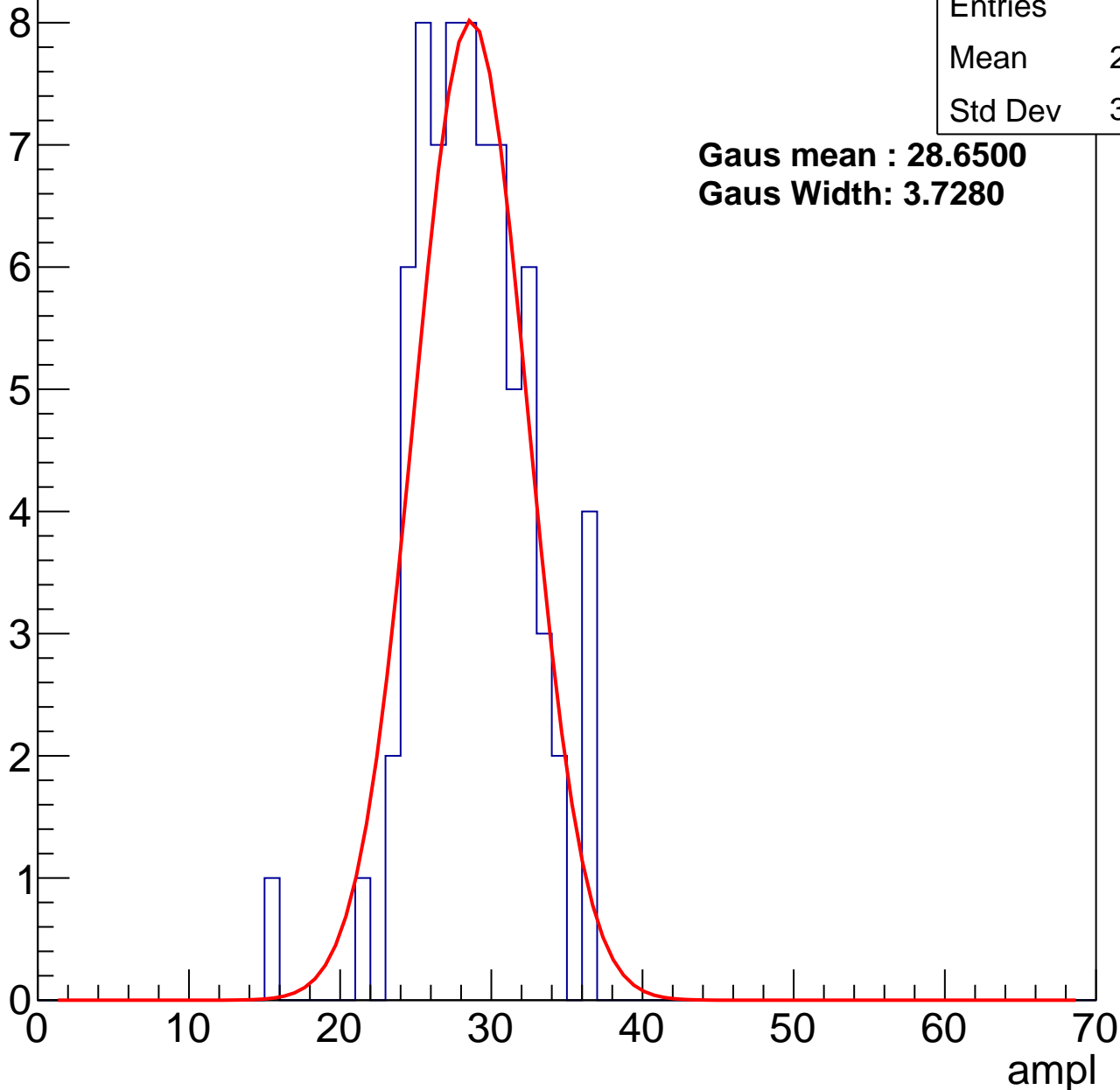
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	28.25
Std Dev	3.735

Gaus mean : 28.6500

Gaus Width: 3.7280



B1L100S, U25-ch90, adc1

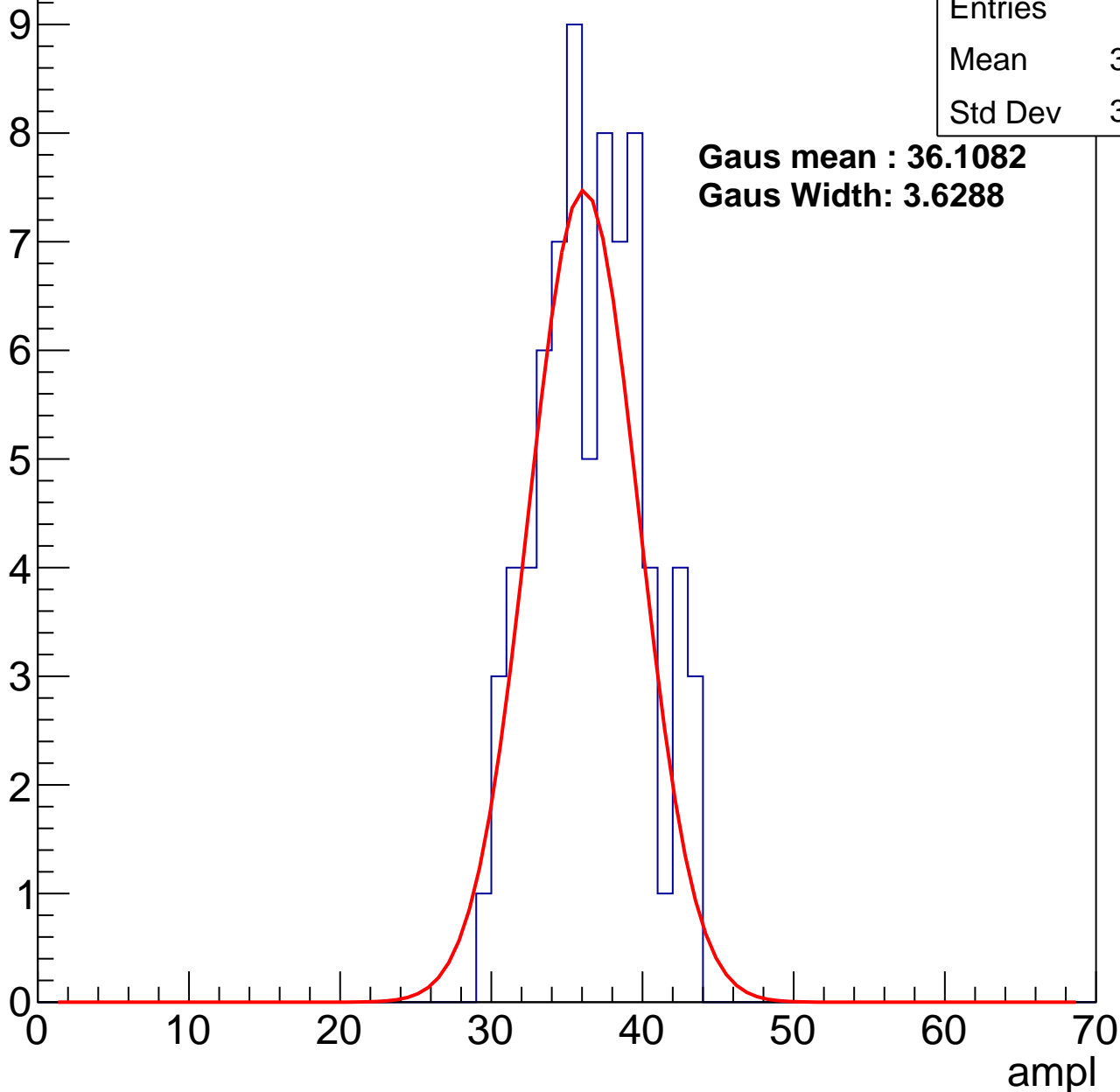
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.14
Std Dev	3.485

Gaus mean : 36.1082

Gaus Width: 3.6288



B1L100S, U25-ch90, adc2

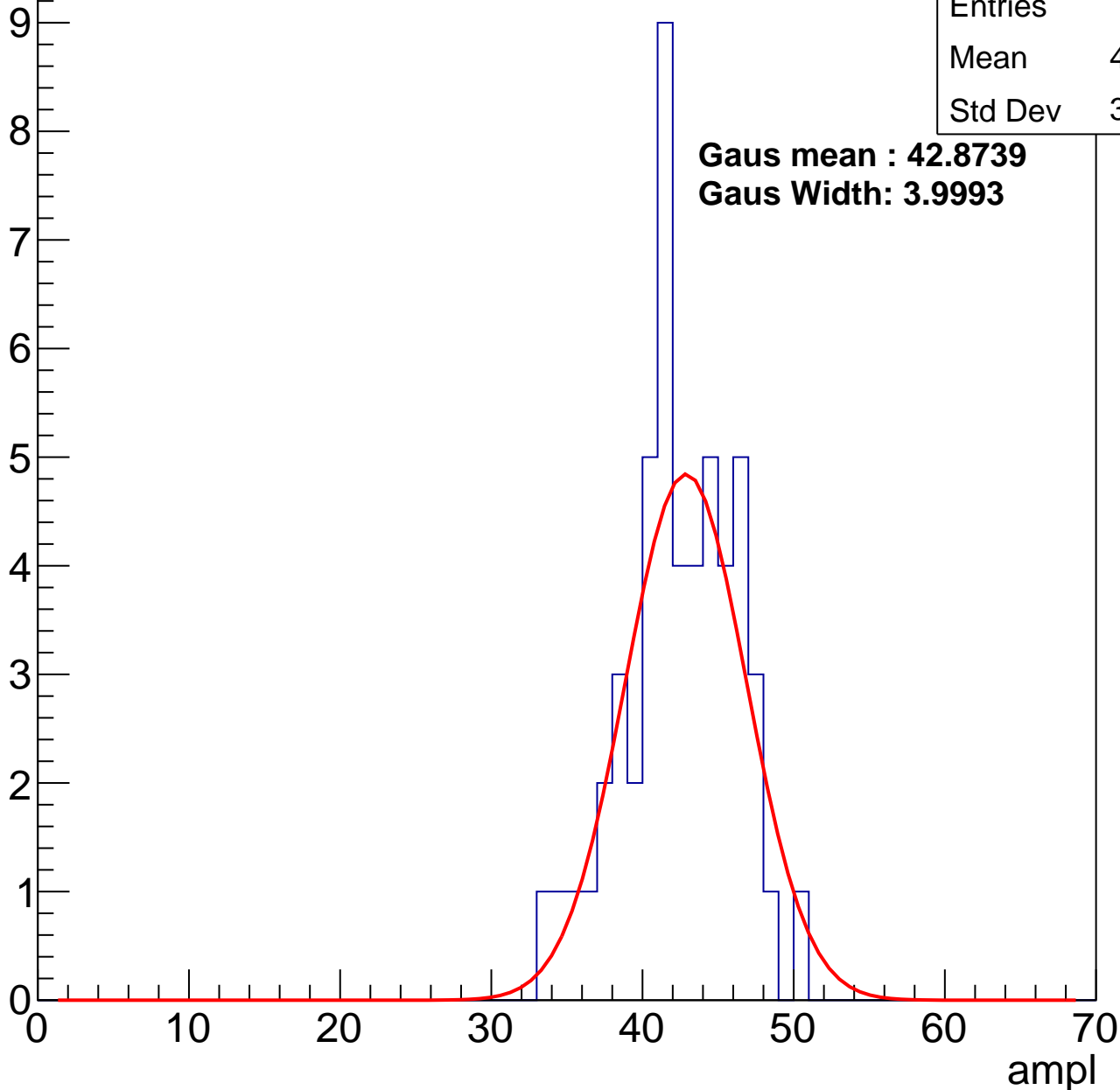
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	41.96
Std Dev	3.642

Gaus mean : 42.8739

Gaus Width: 3.9993

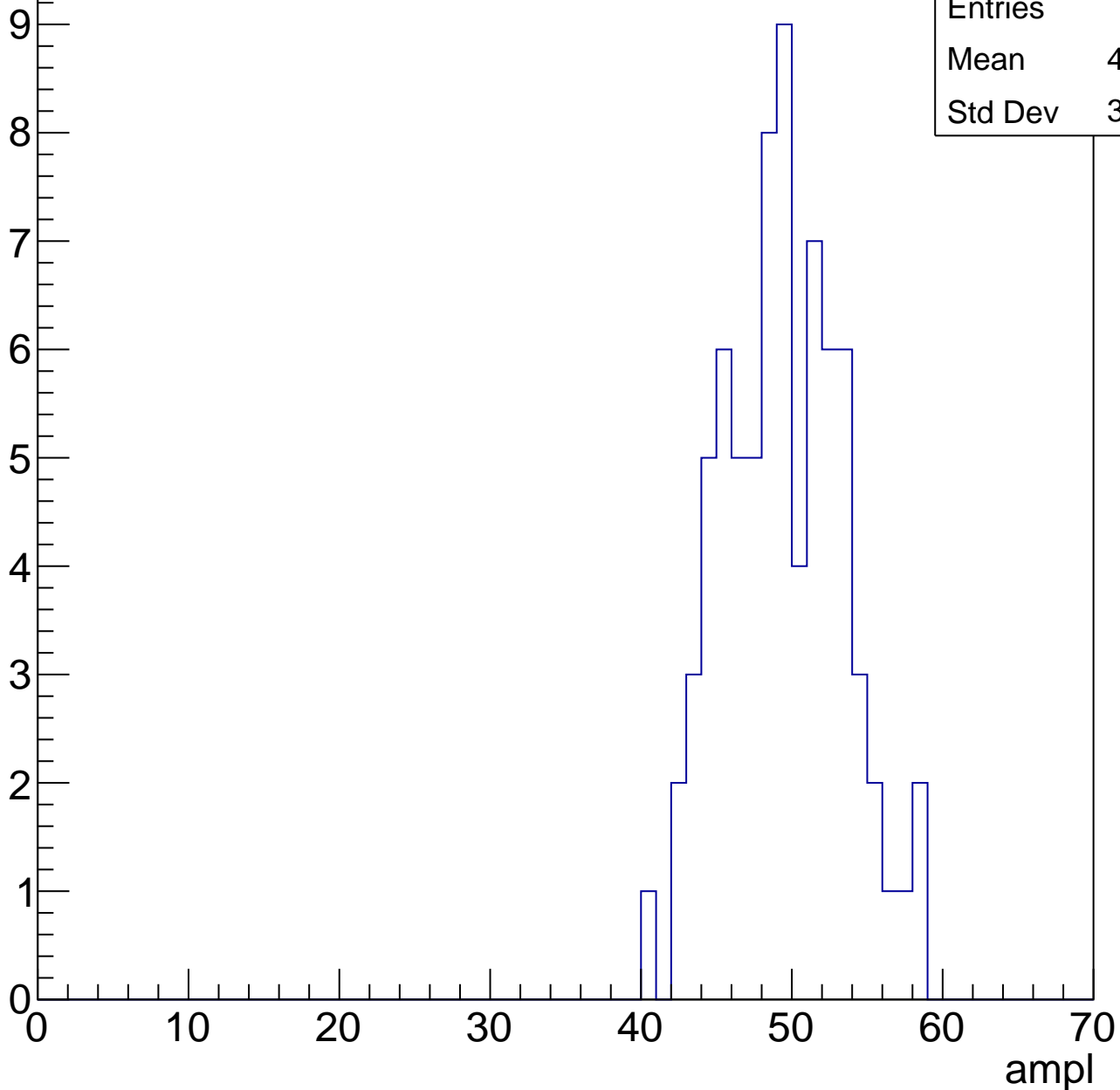


B1L100S, U25-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

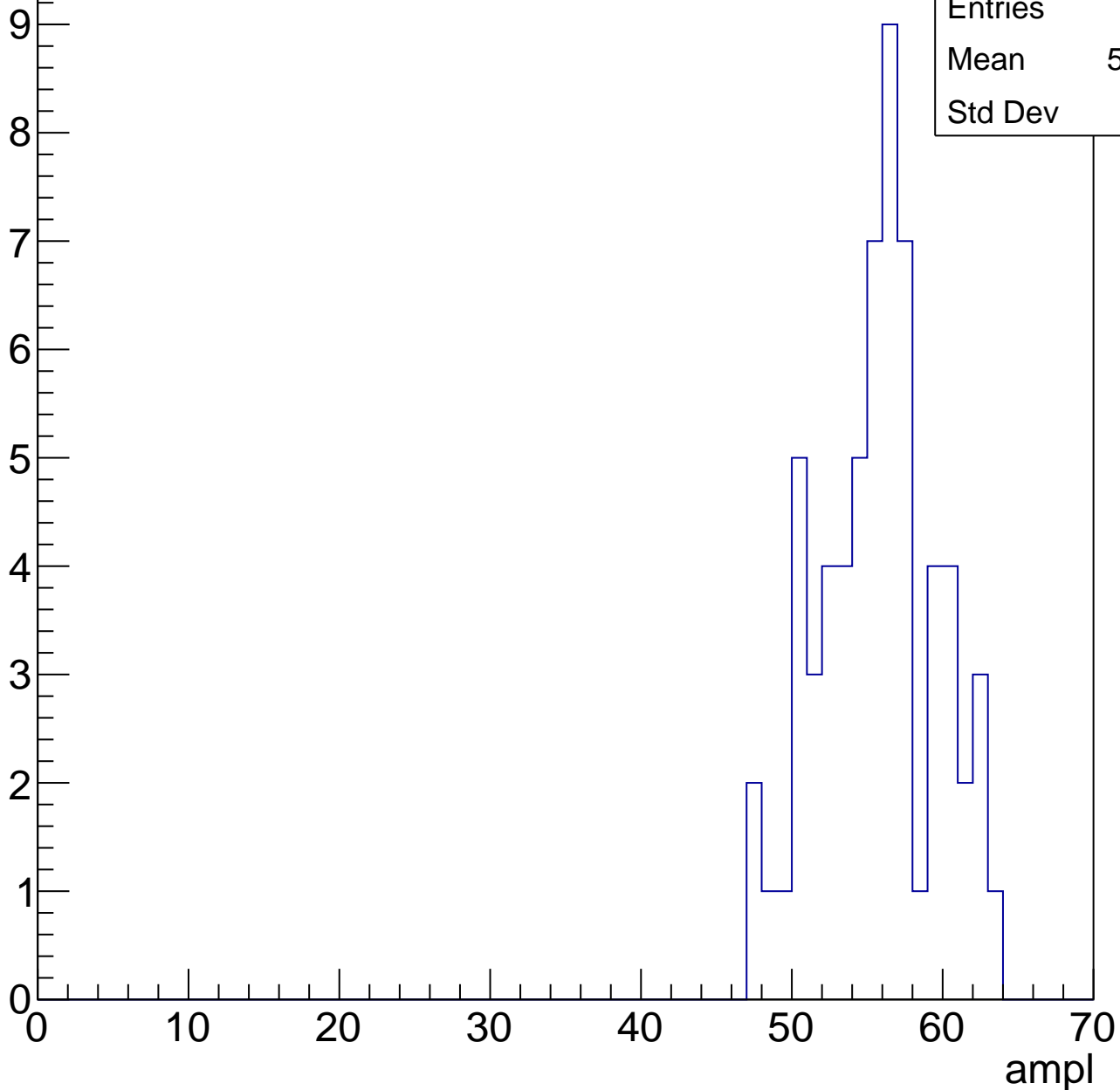
Entries	76
Mean	48.96
Std Dev	3.948



B1L100S, U25-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



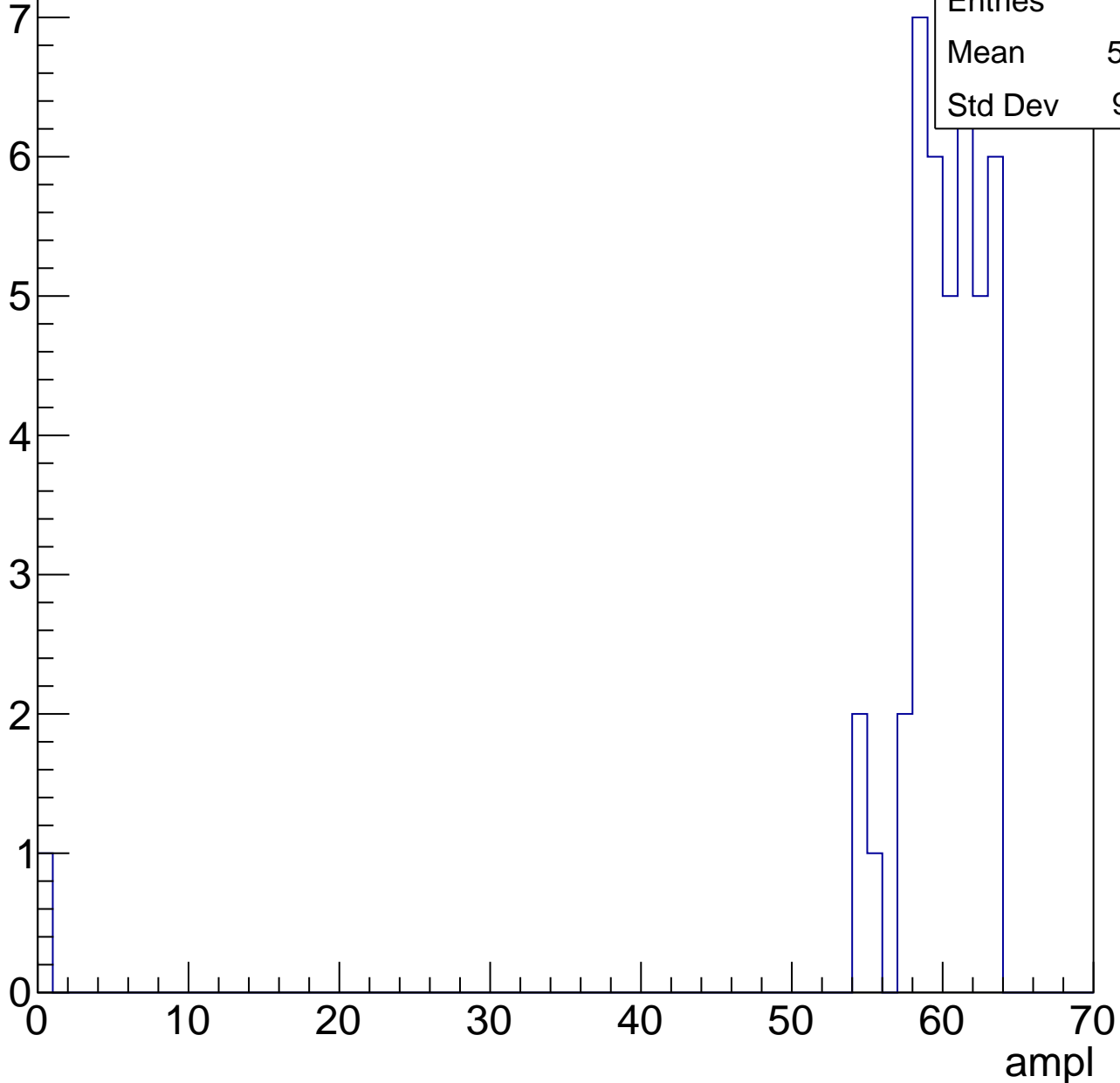
Entries	63
Mean	55.19
Std Dev	3.85

B1L100S, U25-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	58.38
Std Dev	9.411



B1L100S, U25-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch91, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	28.94
Std Dev	4.552

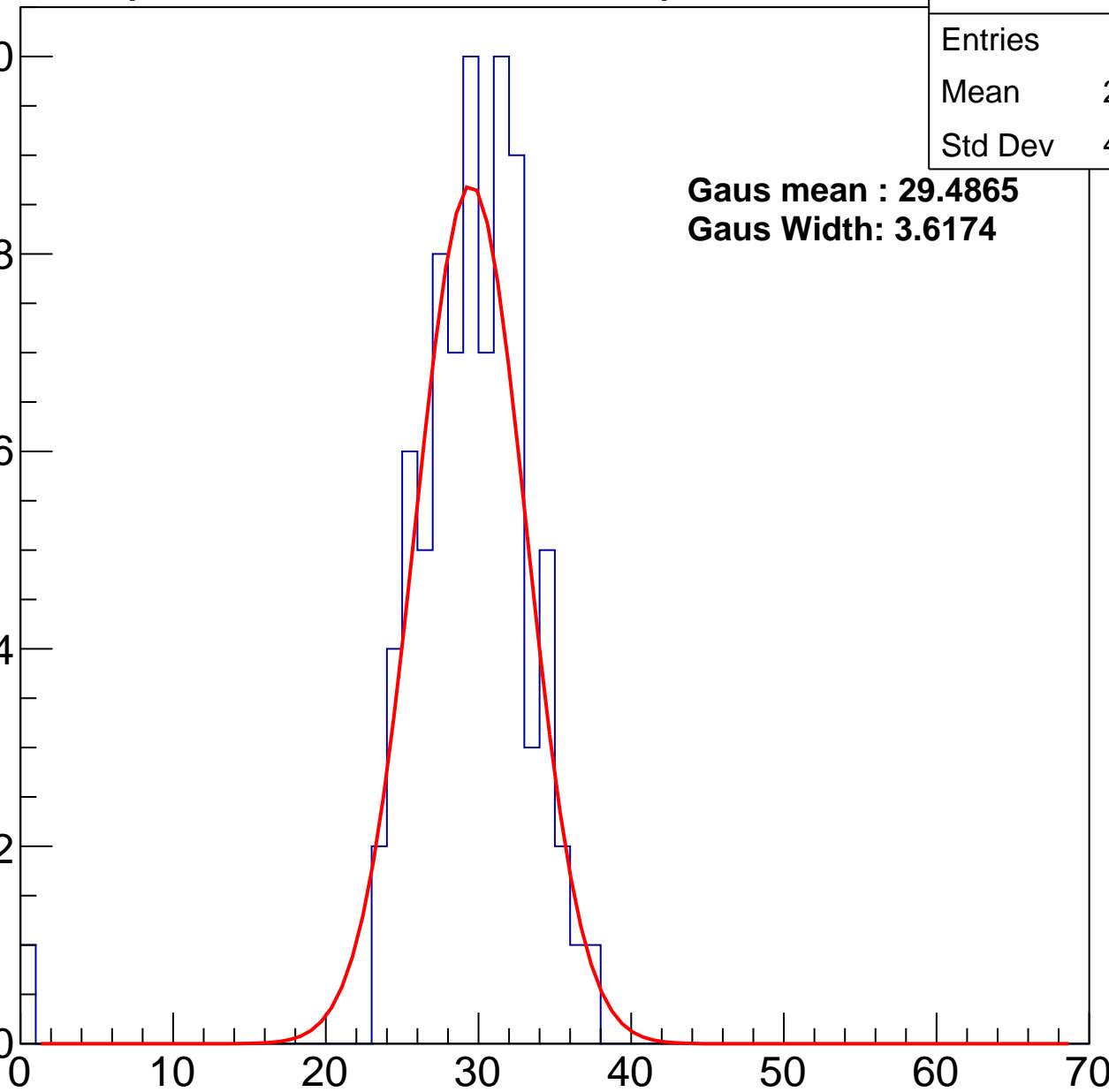
Gaus mean : 29.4865

Gaus Width: 3.6174

Entry

10
8
6
4
2
0

ampl



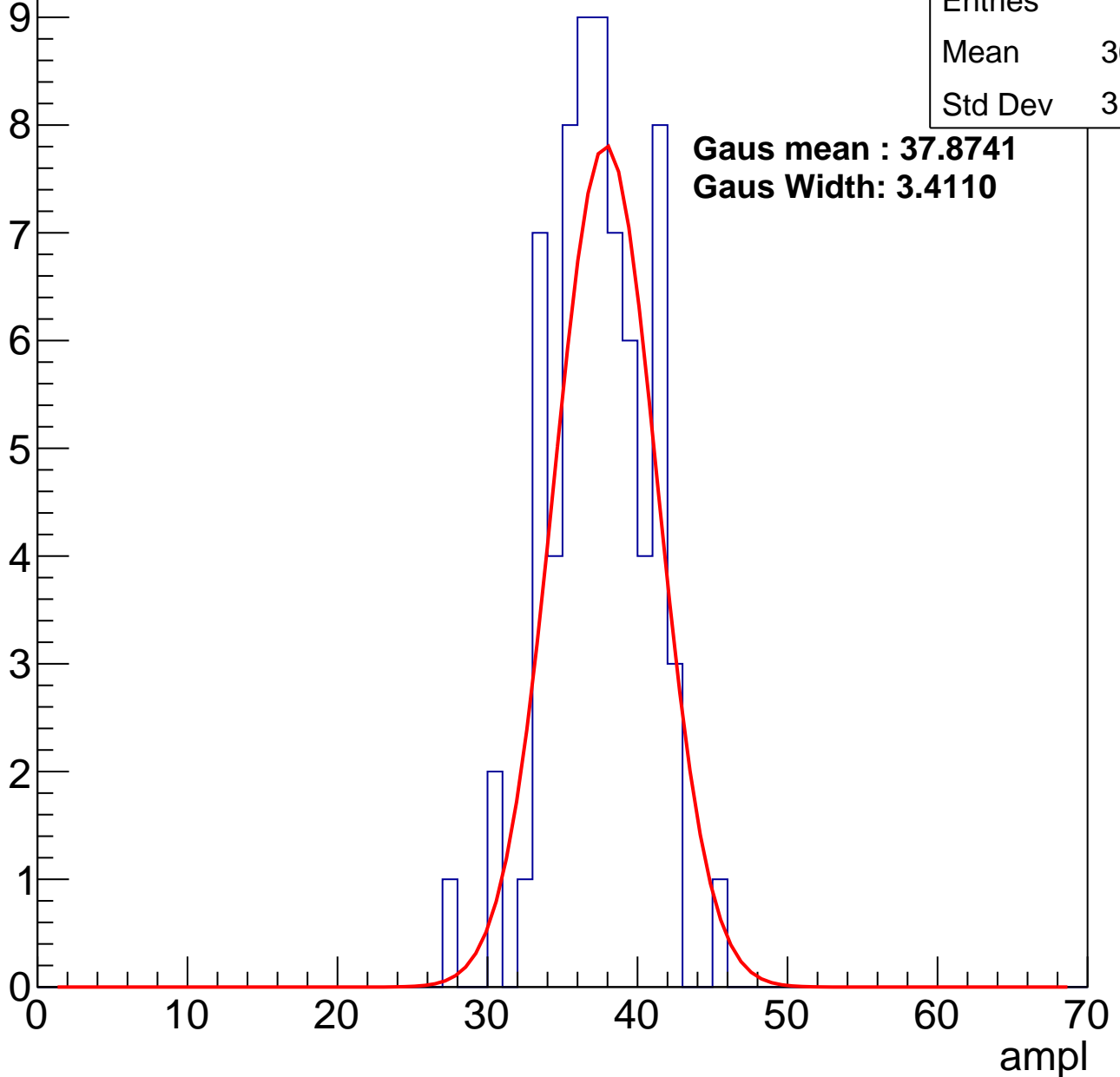
B1L100S, U25-ch91, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.89
Std Dev	3.263

Gaus mean : 37.8741
Gaus Width: 3.4110



B1L100S, U25-ch91, adc2

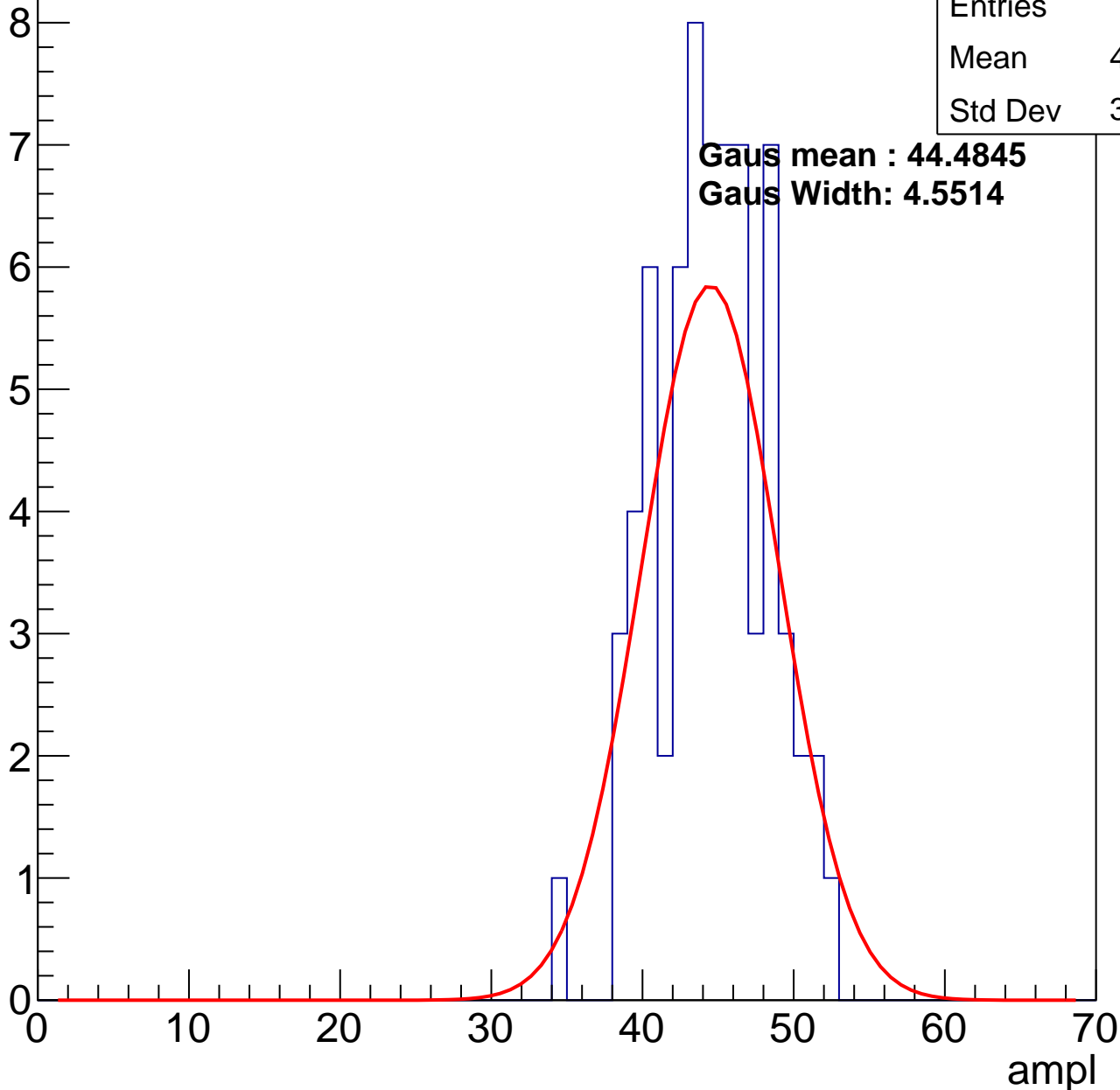
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.13
Std Dev	3.683

Gaus mean : 44.4845

Gaus Width: 4.5514

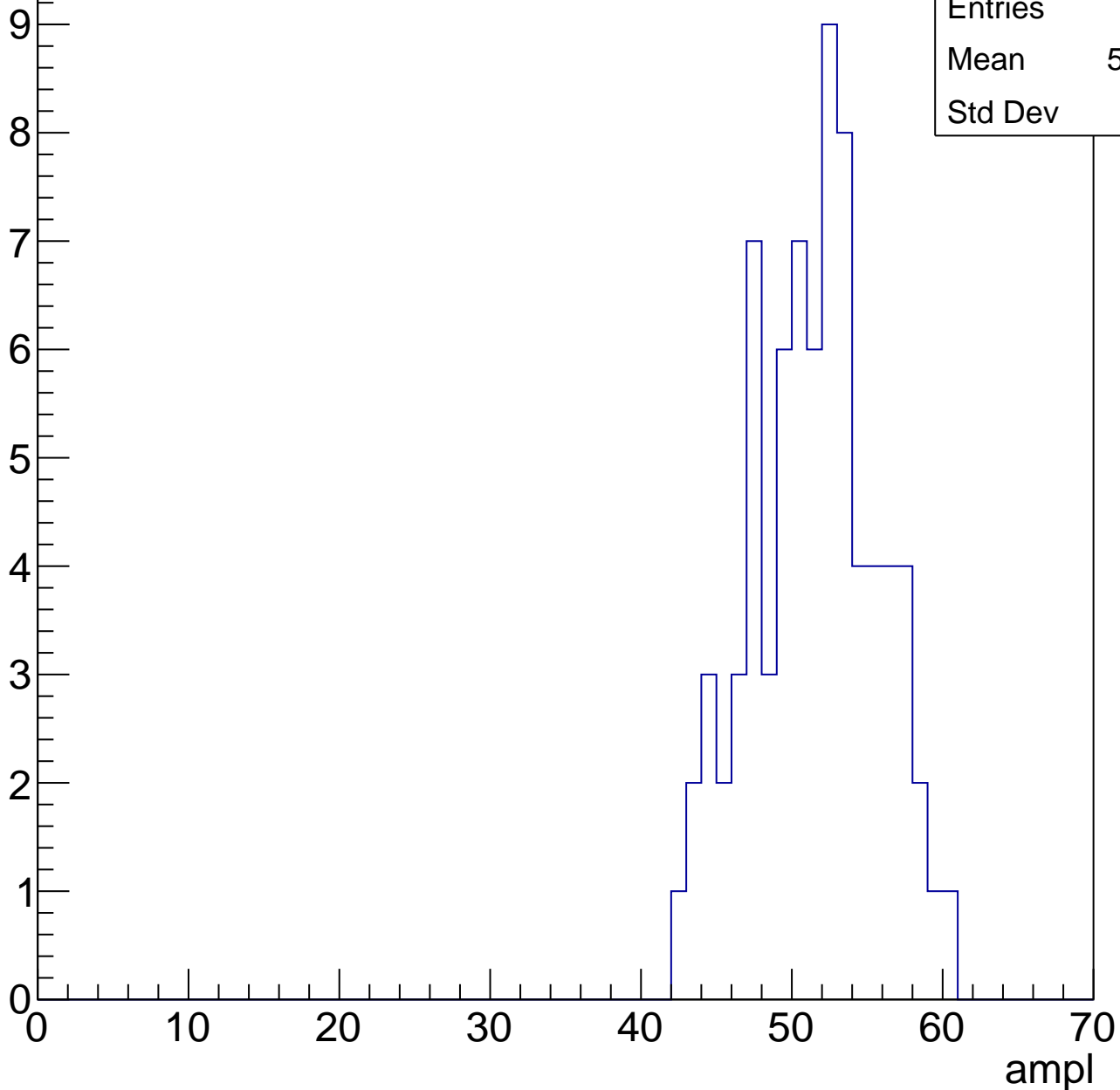


B1L100S, U25-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

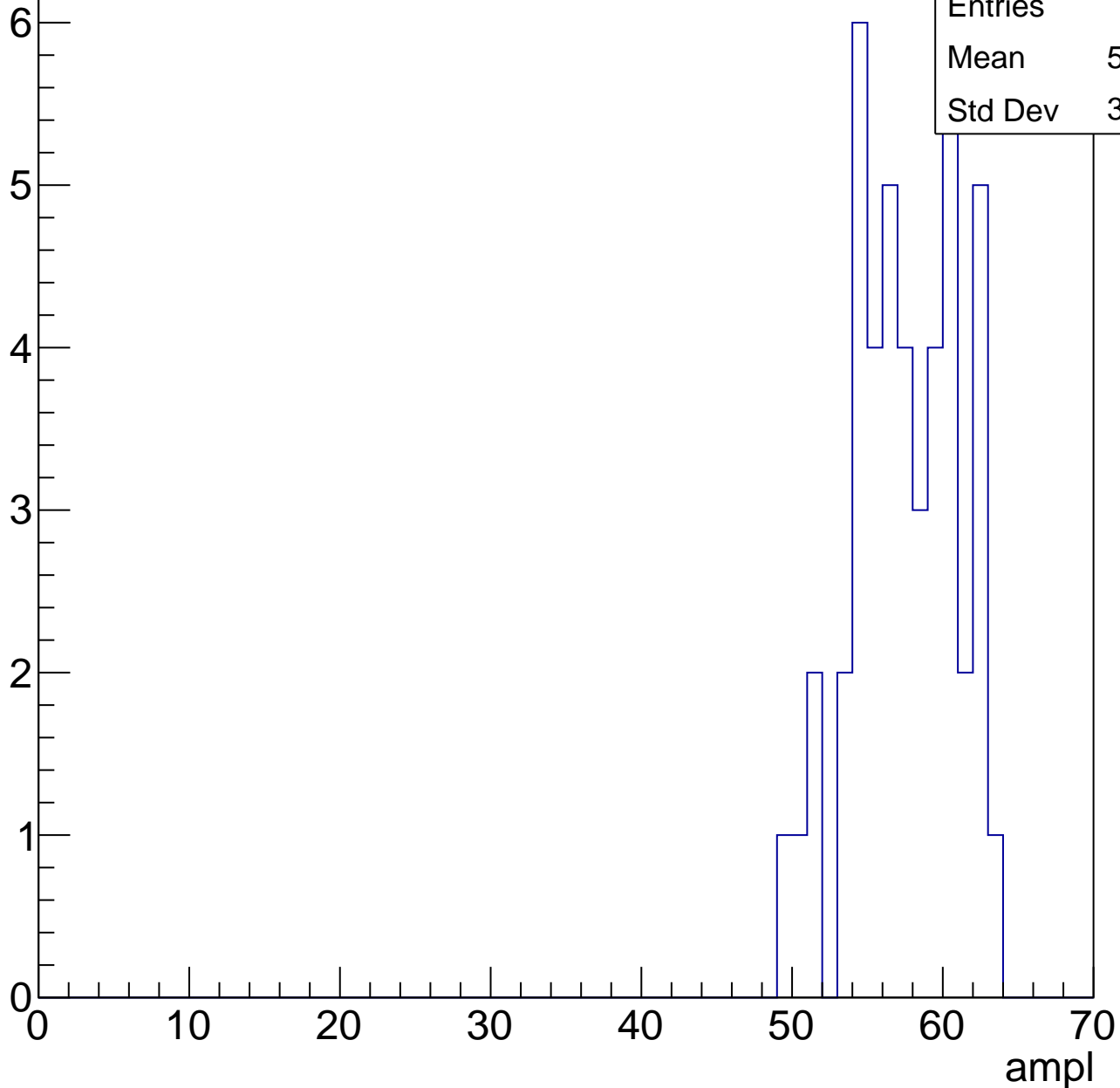
Entries	77
Mean	50.99
Std Dev	4.11



B1L100S, U25-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	46
Mean	57.04
Std Dev	3.483

B1L100S, U25-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	37
Mean	58.43
Std Dev	10.01

ampl

0

10

20

30

40

50

60

70

B1L100S, U25-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L100S, U25-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch92, adc0

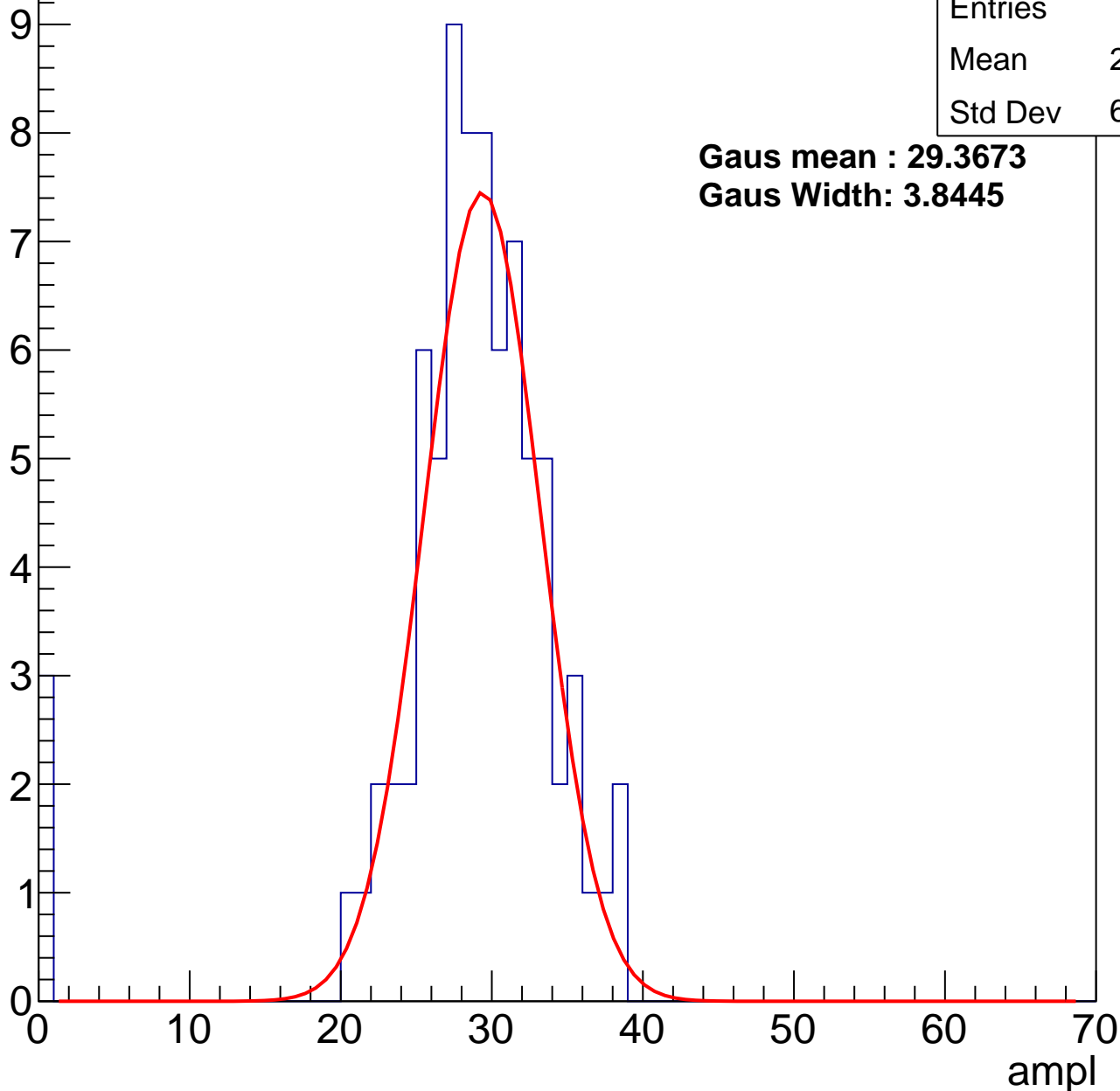
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	27.87
Std Dev	6.717

Gaus mean : 29.3673

Gaus Width: 3.8445



B1L100S, U25-ch92, adc1

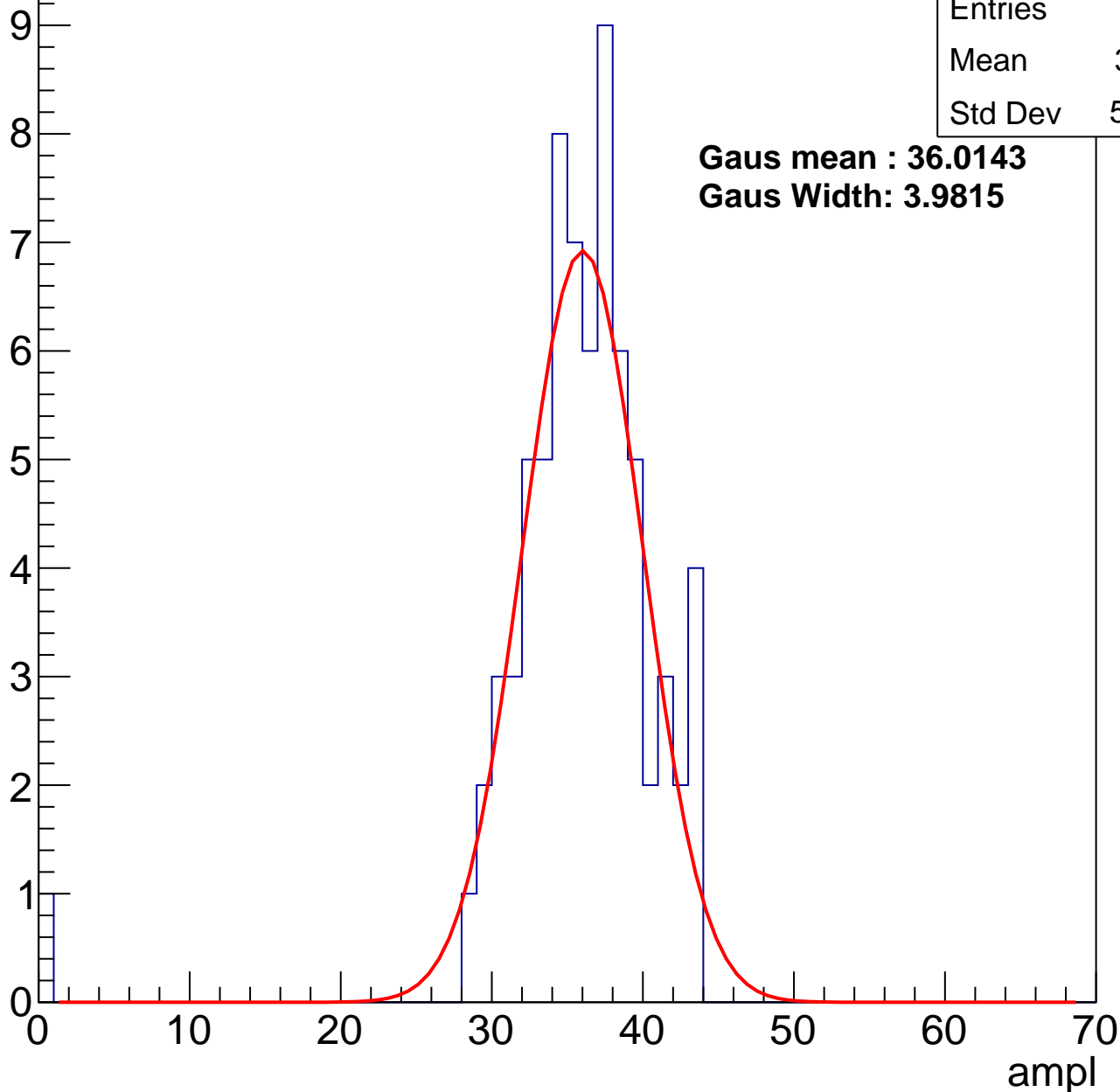
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	35.31
Std Dev	5.557

Gaus mean : 36.0143

Gaus Width: 3.9815



B1L100S, U25-ch92, adc2

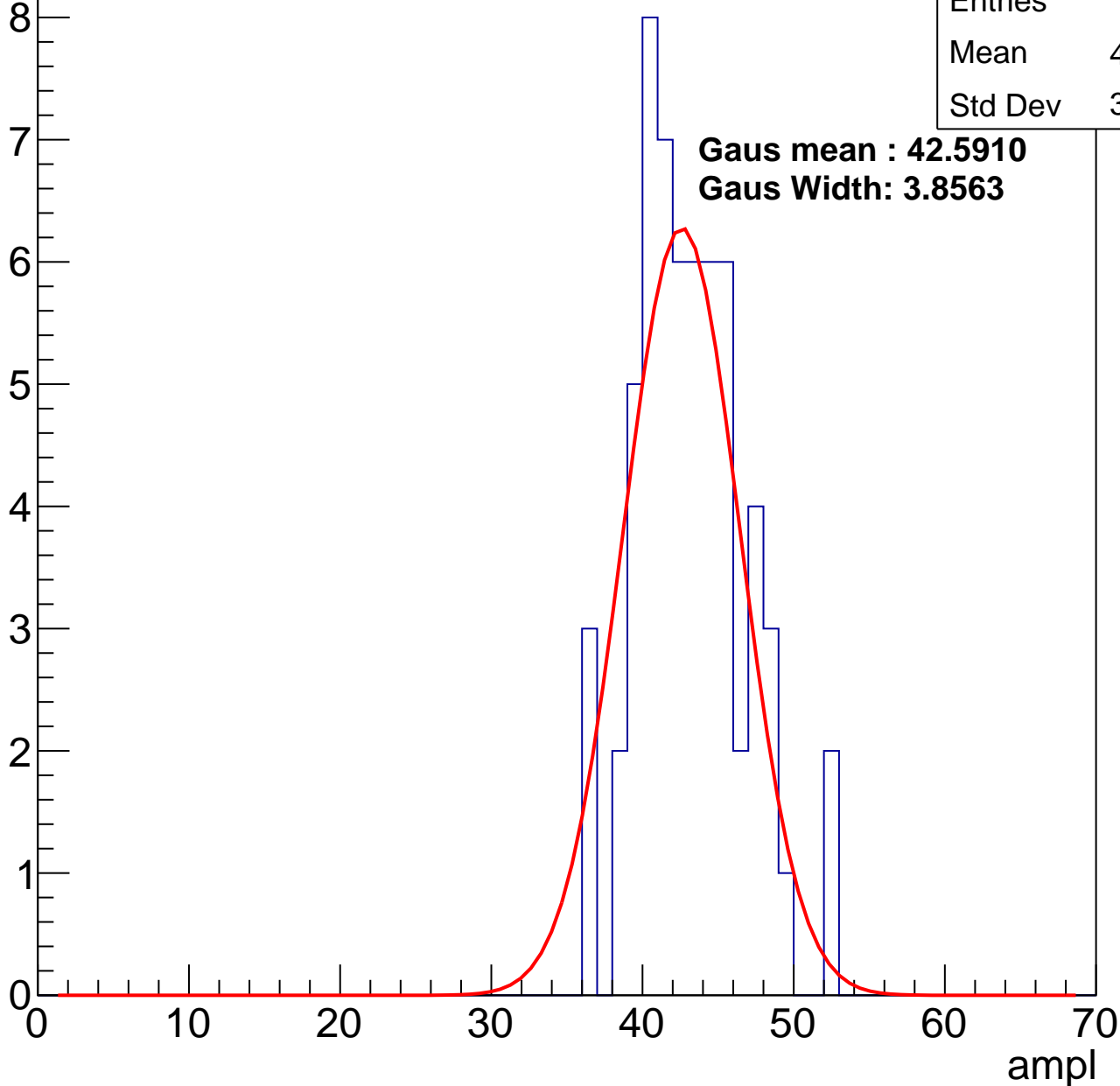
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.74
Std Dev	3.539

Gaus mean : 42.5910

Gaus Width: 3.8563

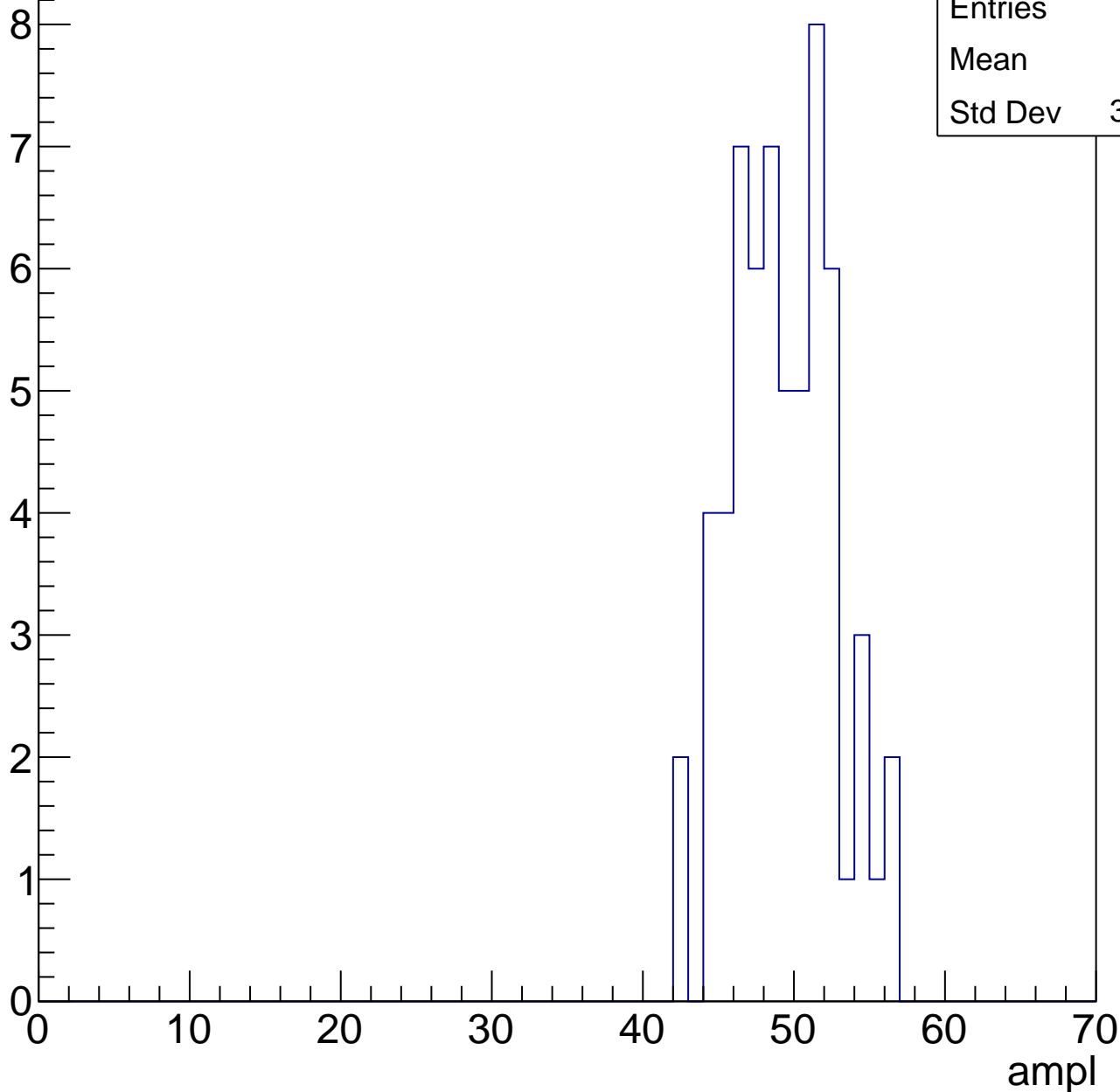


B1L100S, U25-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48.8
Std Dev	3.313

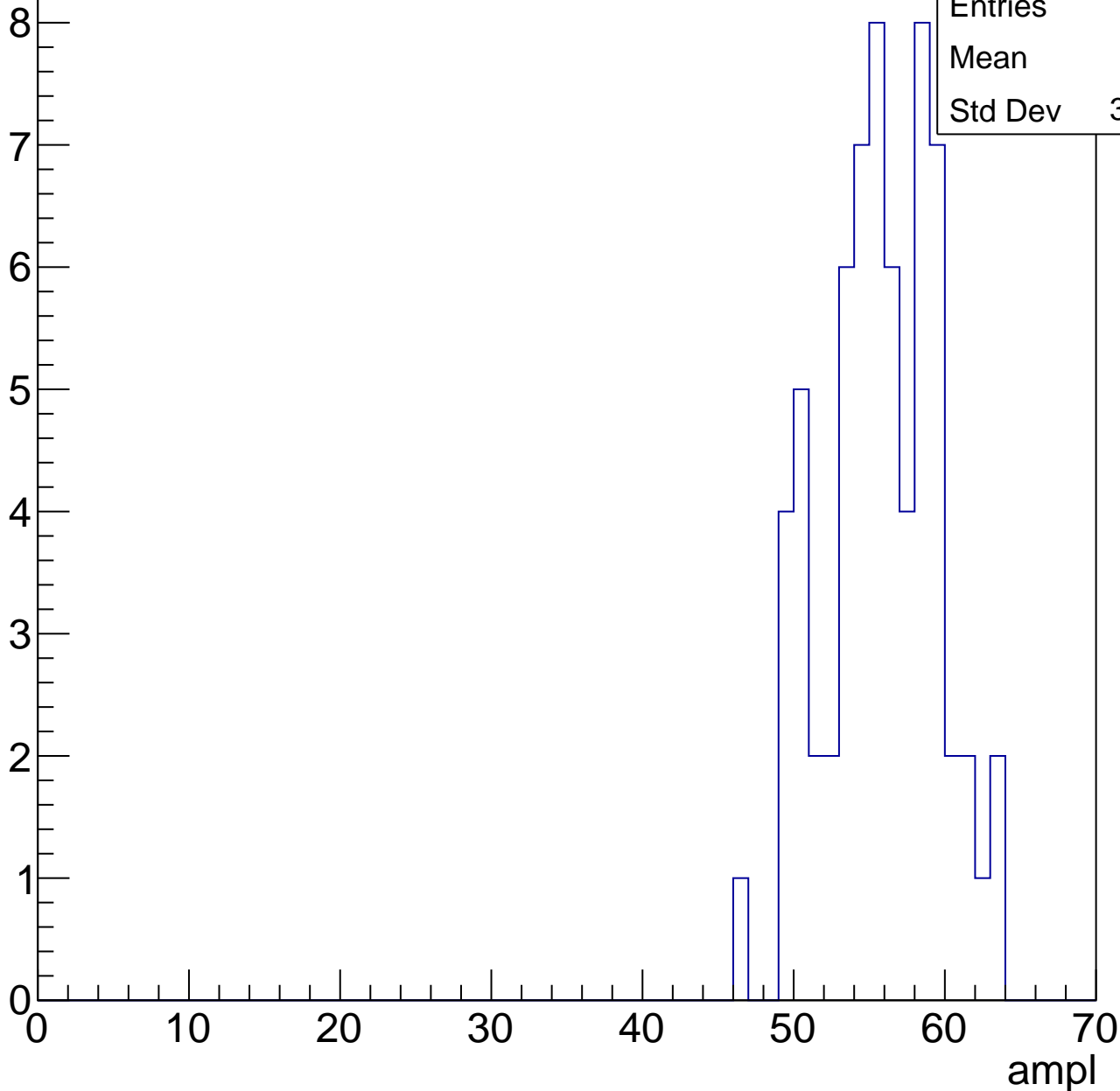


B1L100S, U25-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	55.3
Std Dev	3.726

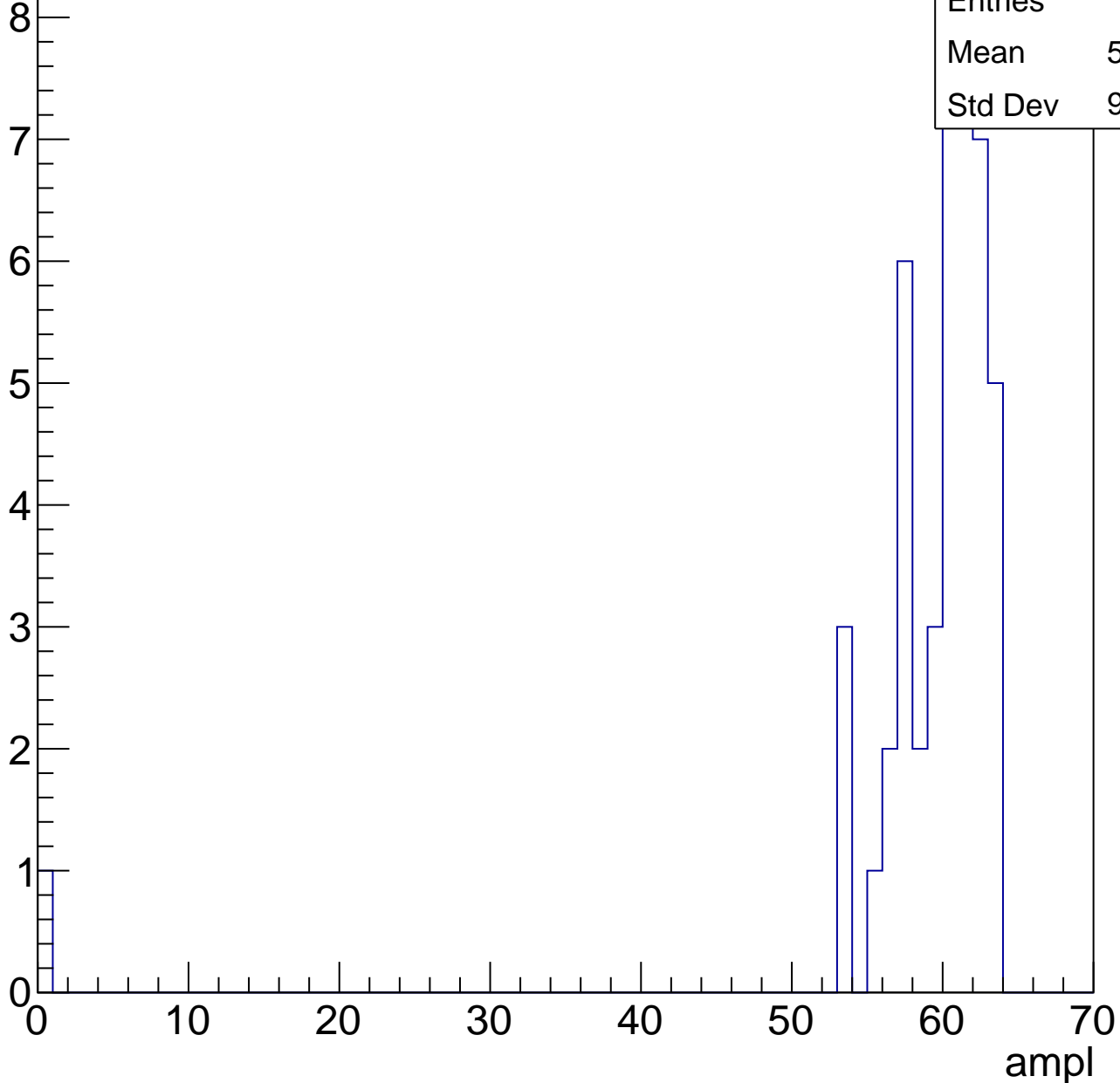


B1L100S, U25-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.22
Std Dev	9.094



B1L100S, U25-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch93, adc0

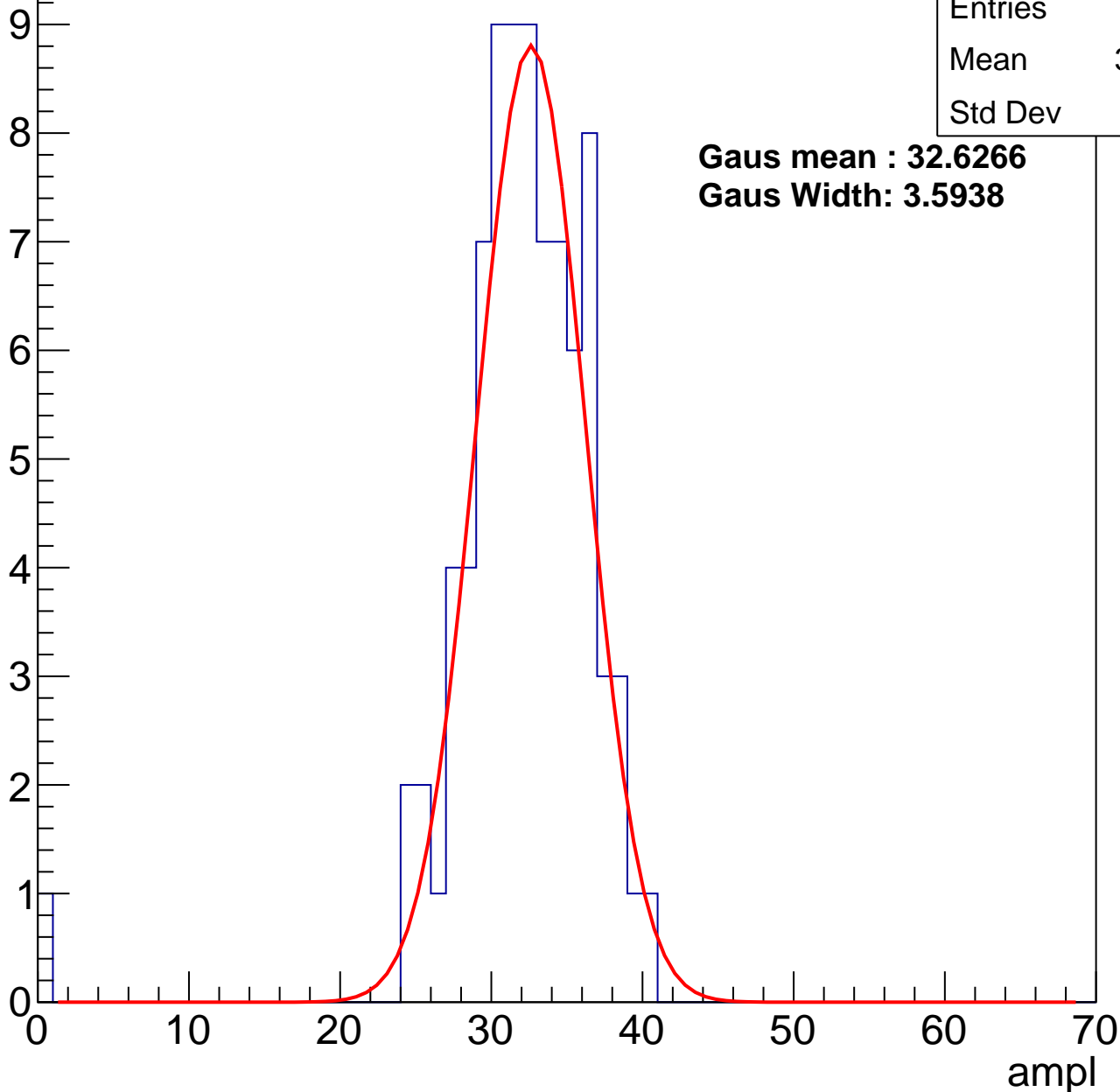
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	31.61
Std Dev	4.95

Gaus mean : 32.6266

Gaus Width: 3.5938



B1L100S, U25-ch93, adc1

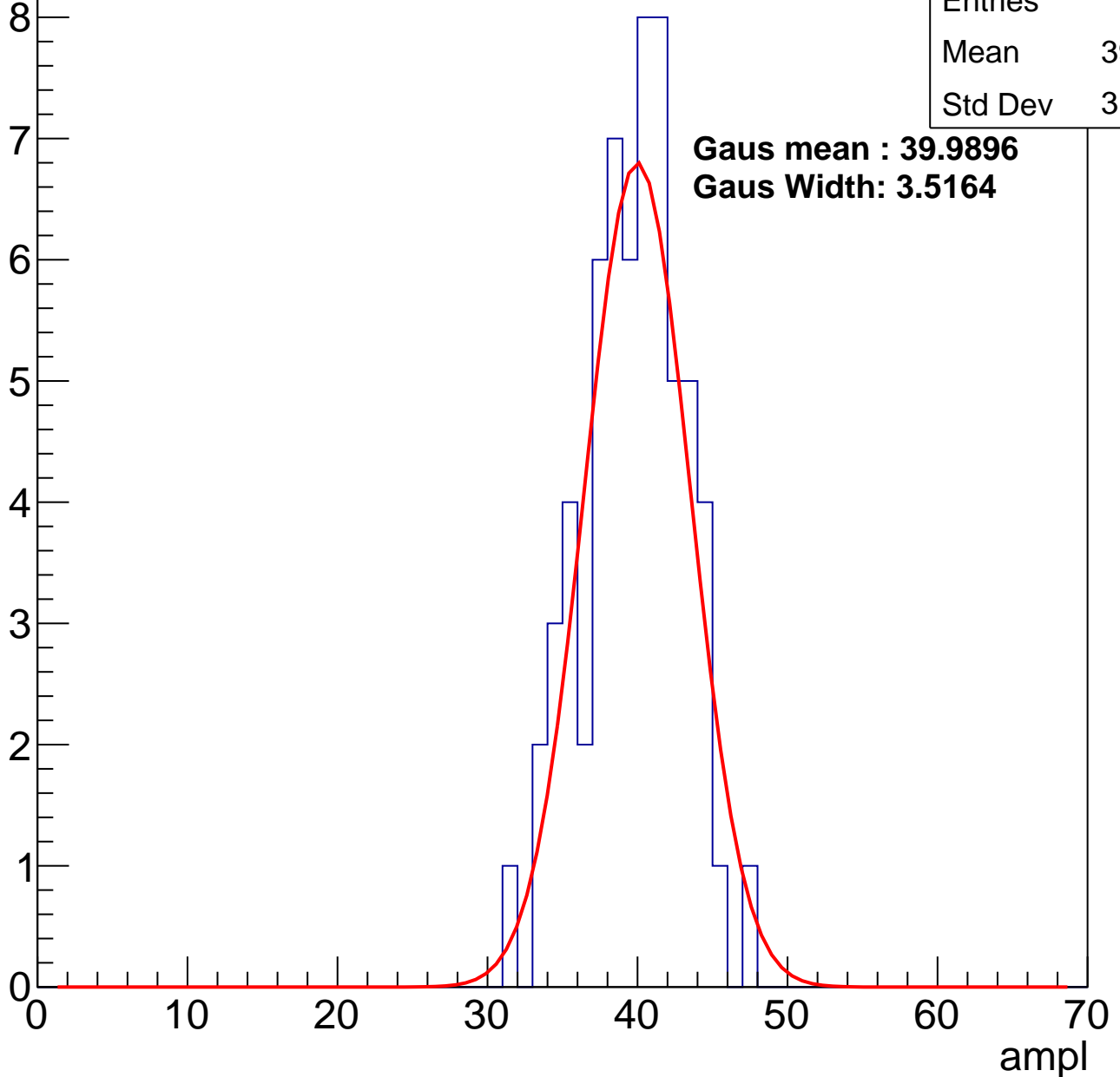
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	39.27
Std Dev	3.296

Gaus mean : 39.9896

Gaus Width: 3.5164



B1L100S, U25-ch93, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	46.28
Std Dev	3.73

Gaus mean : 46.7692

Gaus Width: 3.5005

10

8

6

4

2

0

0

10

20

30

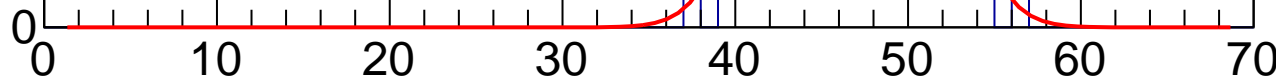
40

50

60

70

ampl

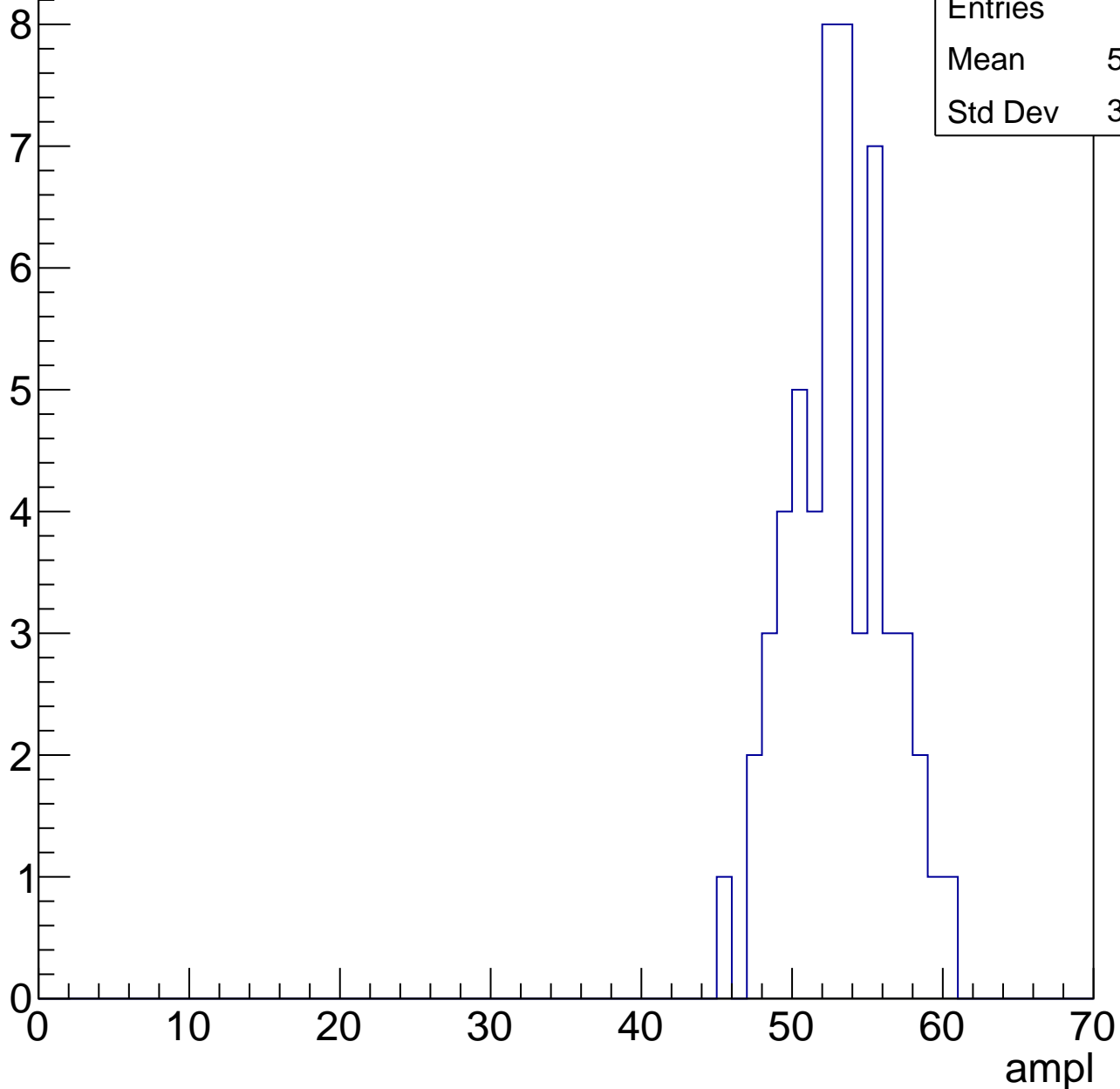


B1L100S, U25-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	52.62
Std Dev	3.239

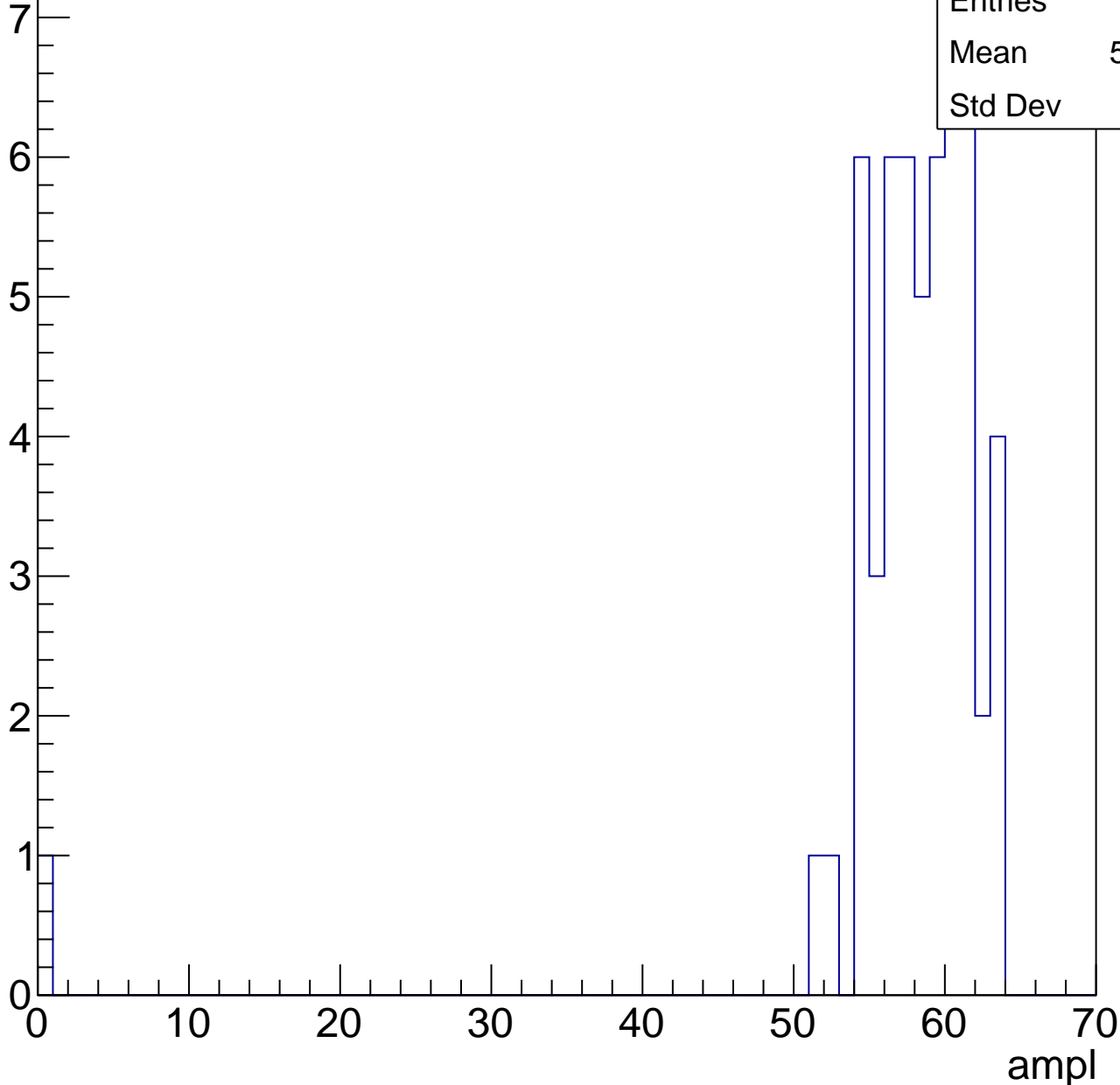


B1L100S, U25-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.04
Std Dev	8.29

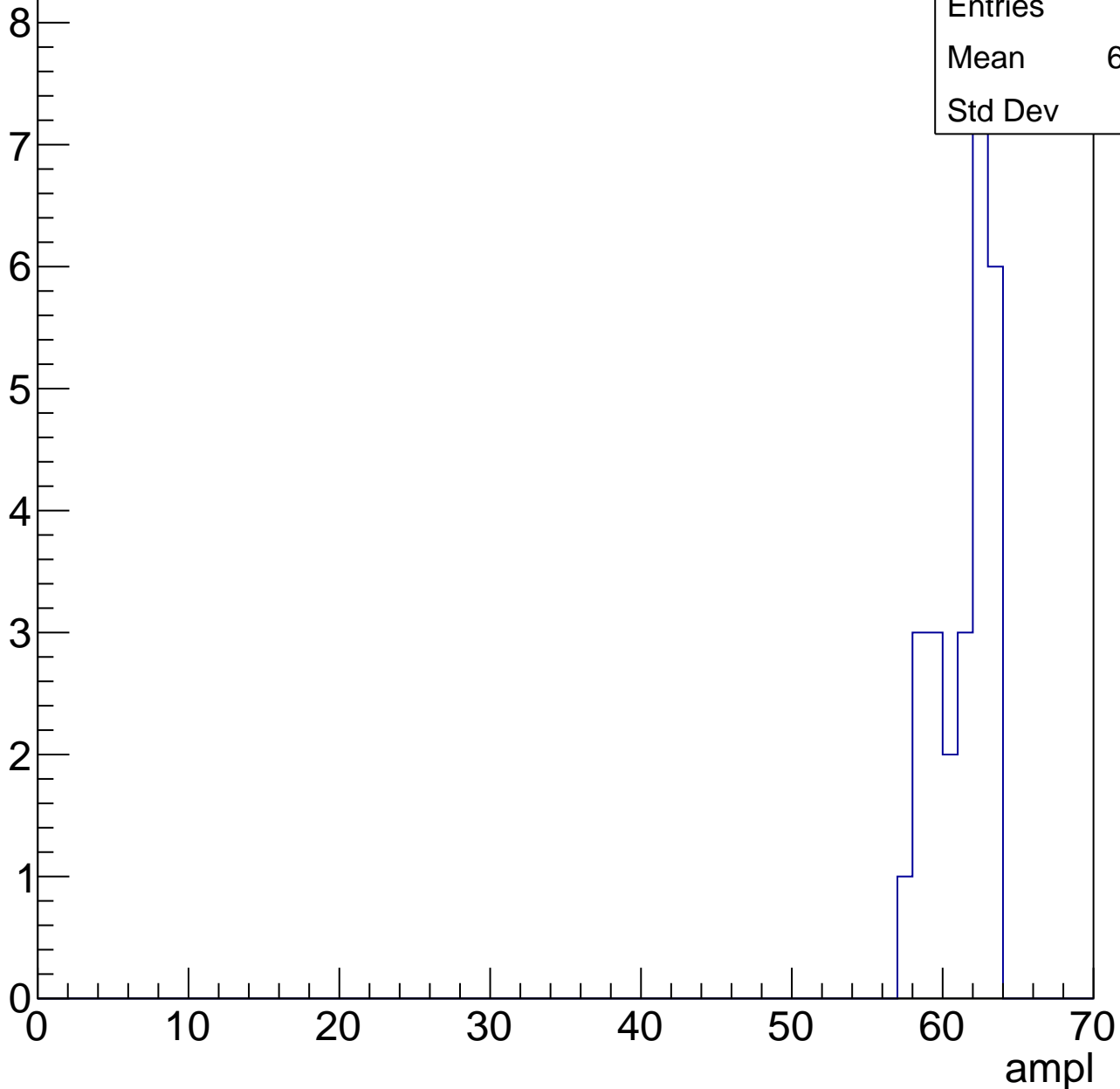


B1L100S, U25-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	60.96
Std Dev	1.85



B1L100S, U25-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U25-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch94, adc0

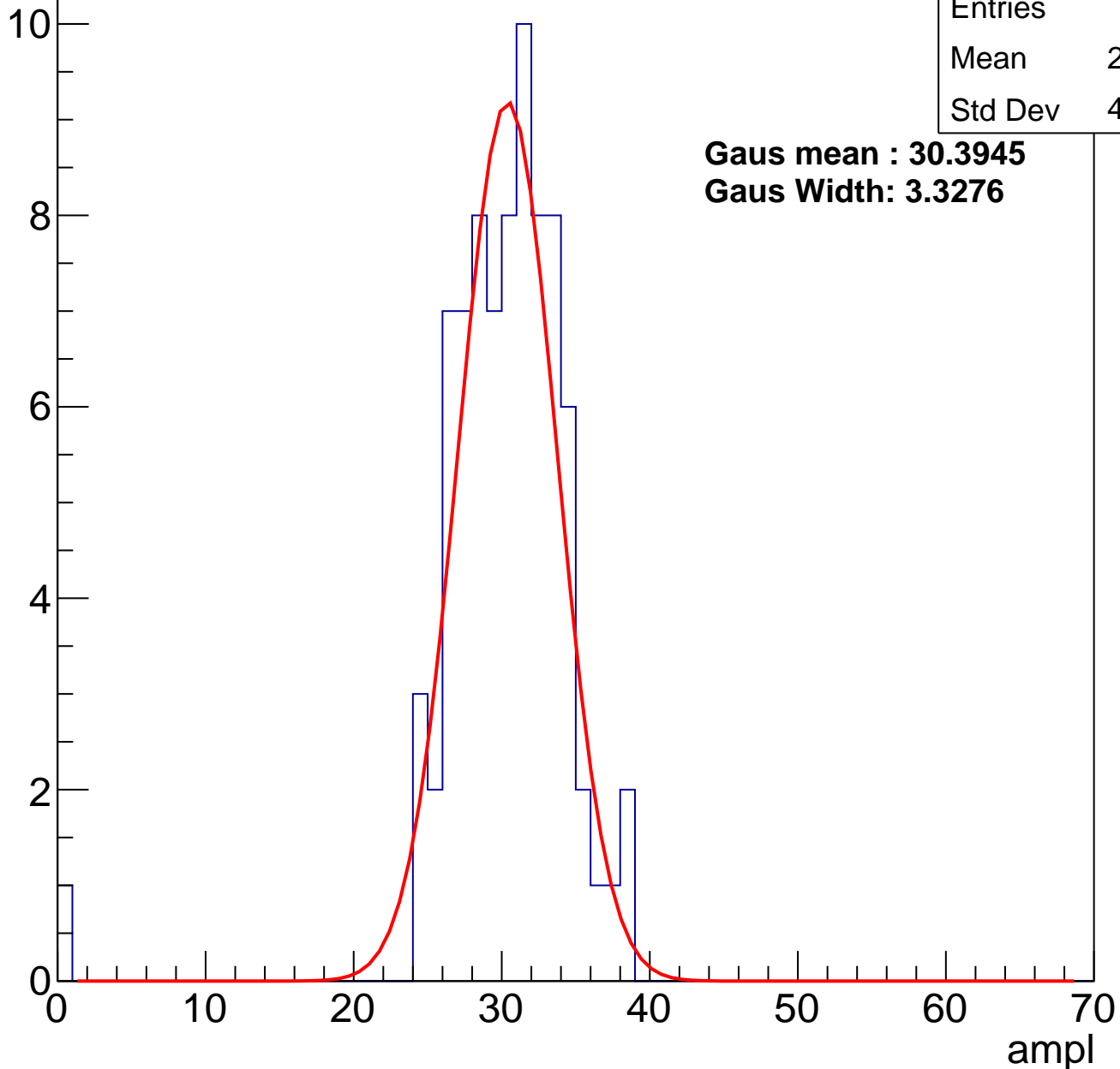
calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	29.79
Std Dev	4.637

Gaus mean : 30.3945

Gaus Width: 3.3276

Entry



B1L100S, U25-ch94, adc1

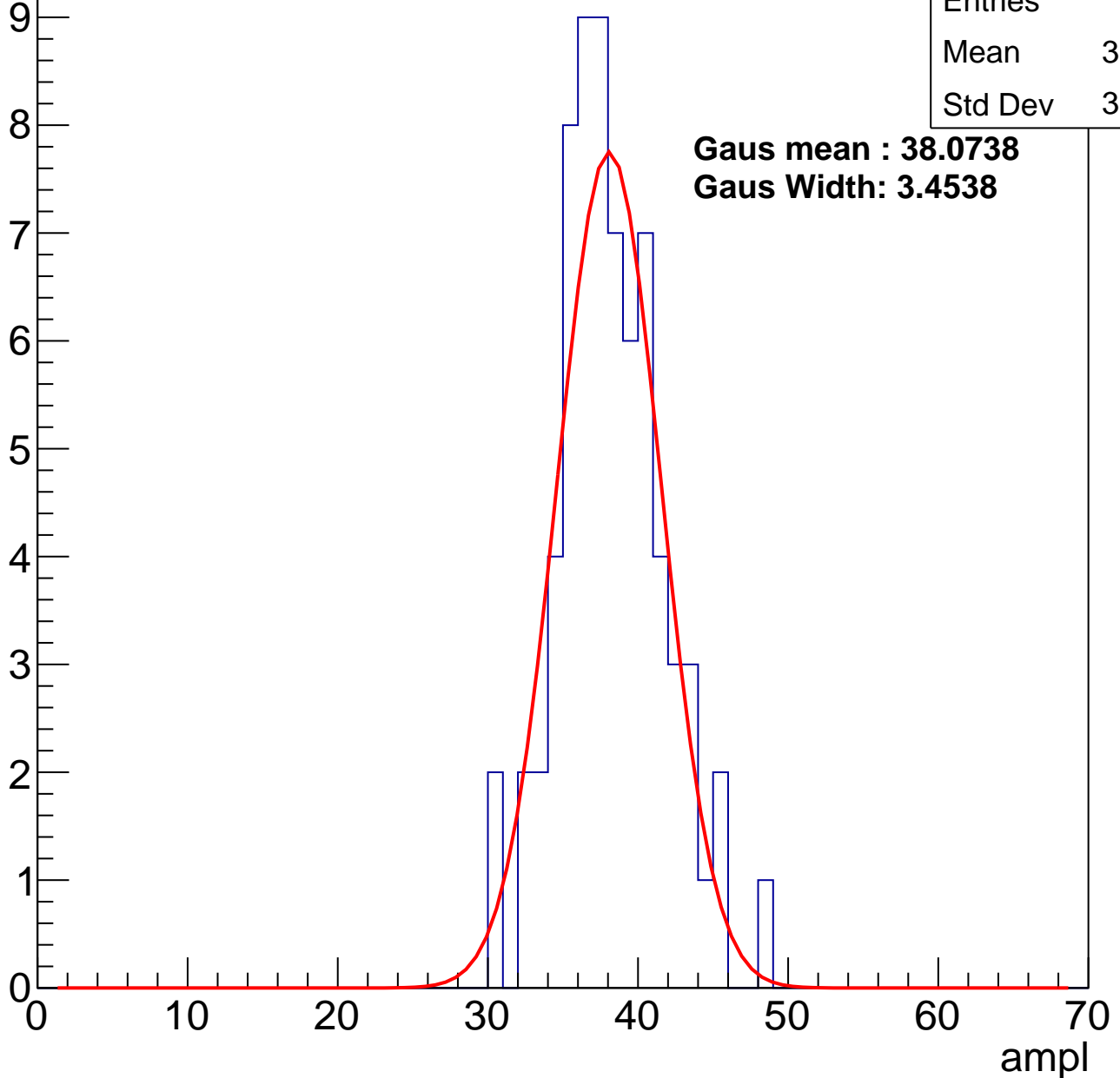
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.77
Std Dev	3.494

Gaus mean : 38.0738

Gaus Width: 3.4538



B1L100S, U25-ch94, adc2

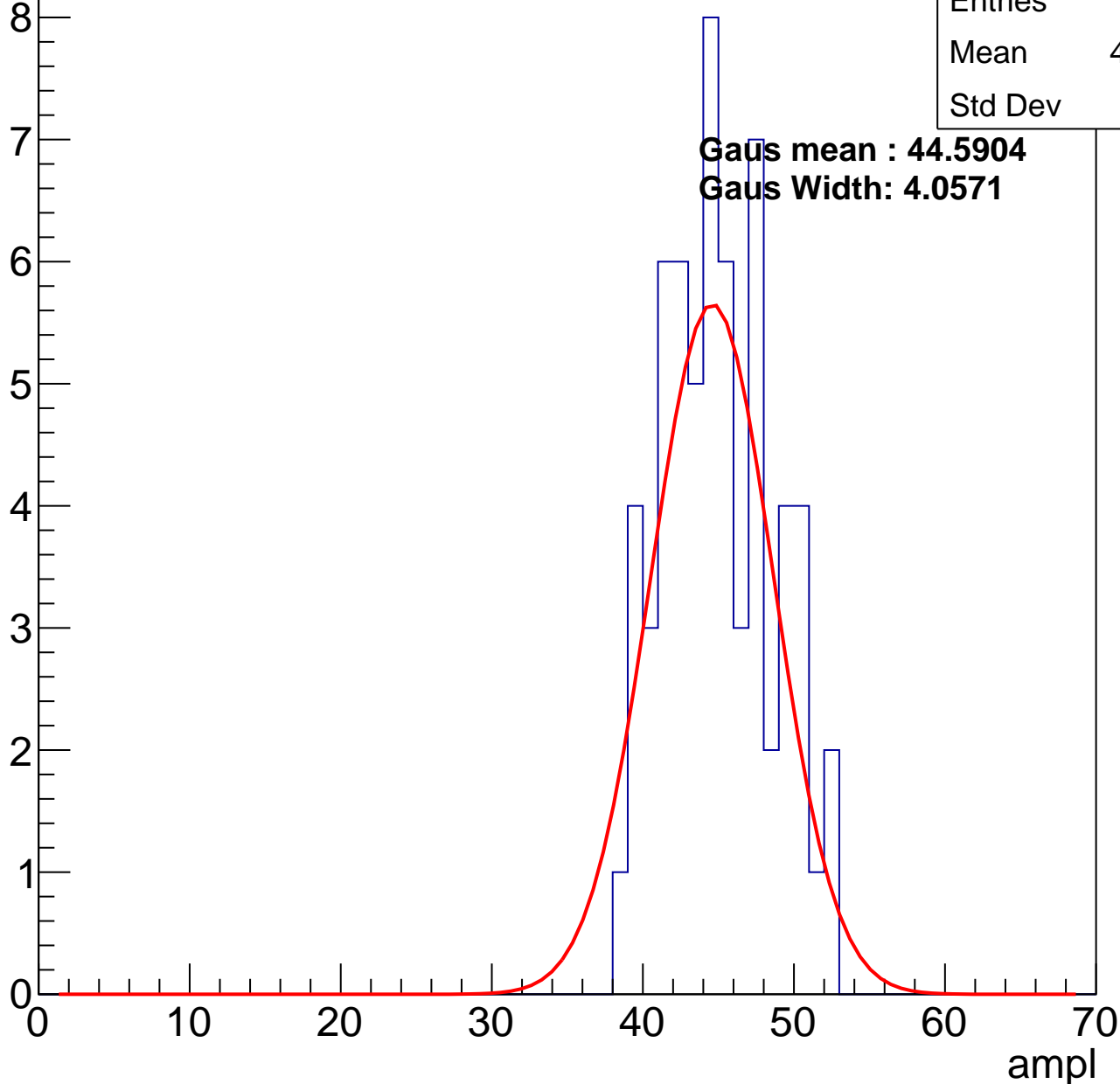
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.56
Std Dev	3.55

Gaus mean : 44.5904

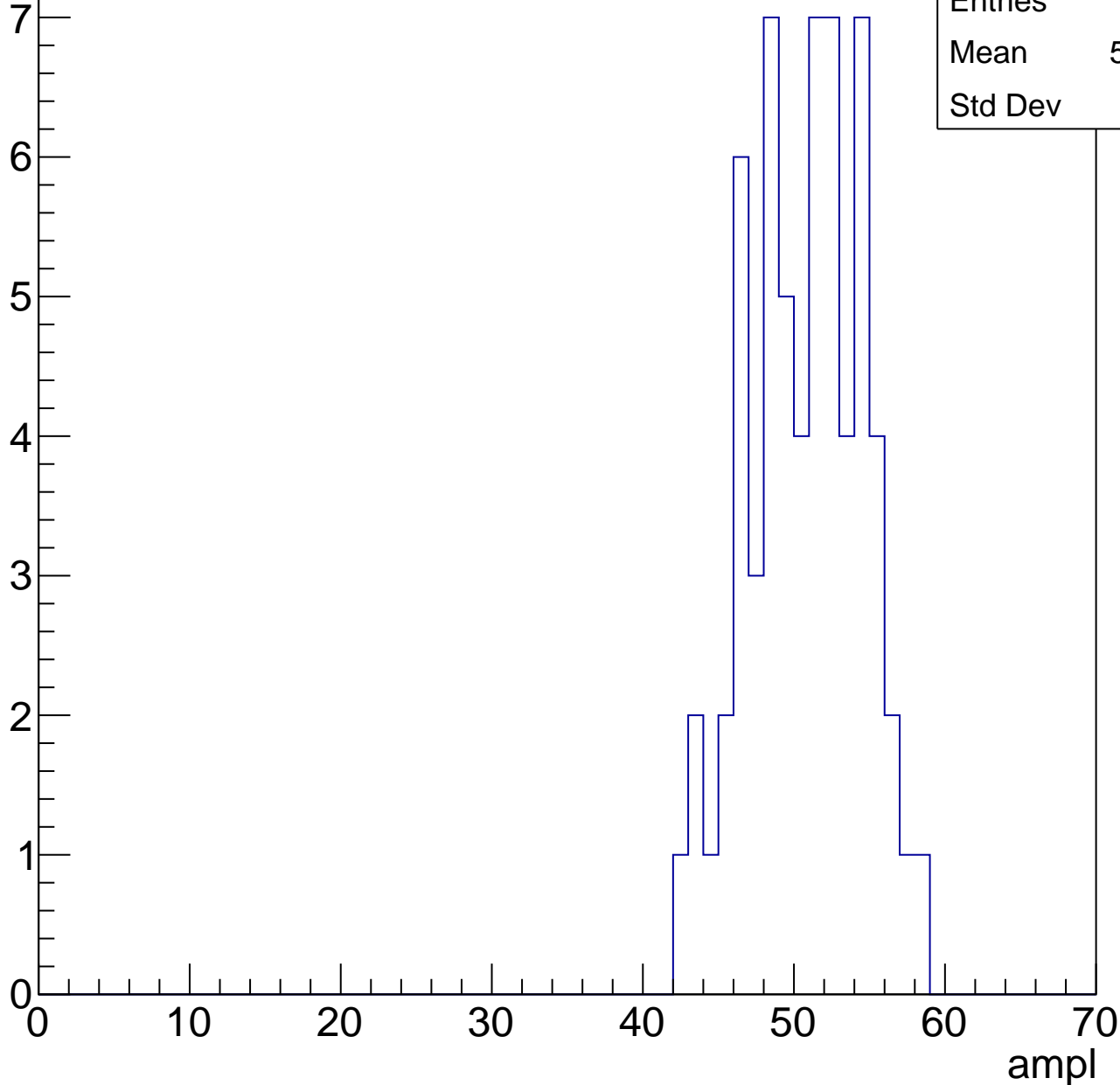
Gaus Width: 4.0571



B1L100S, U25-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



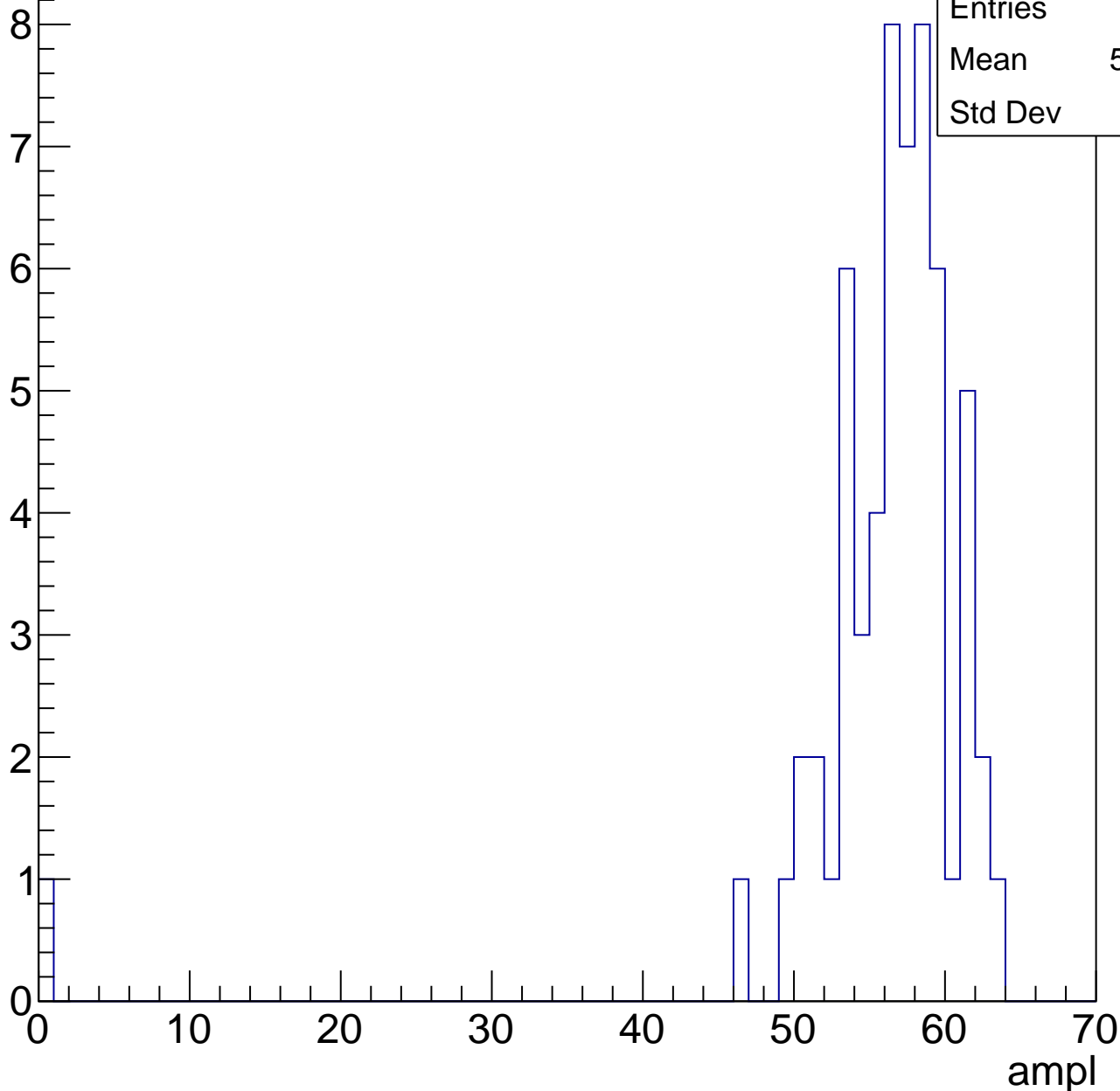
Entries	64
Mean	50.28
Std Dev	3.68

B1L100S, U25-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

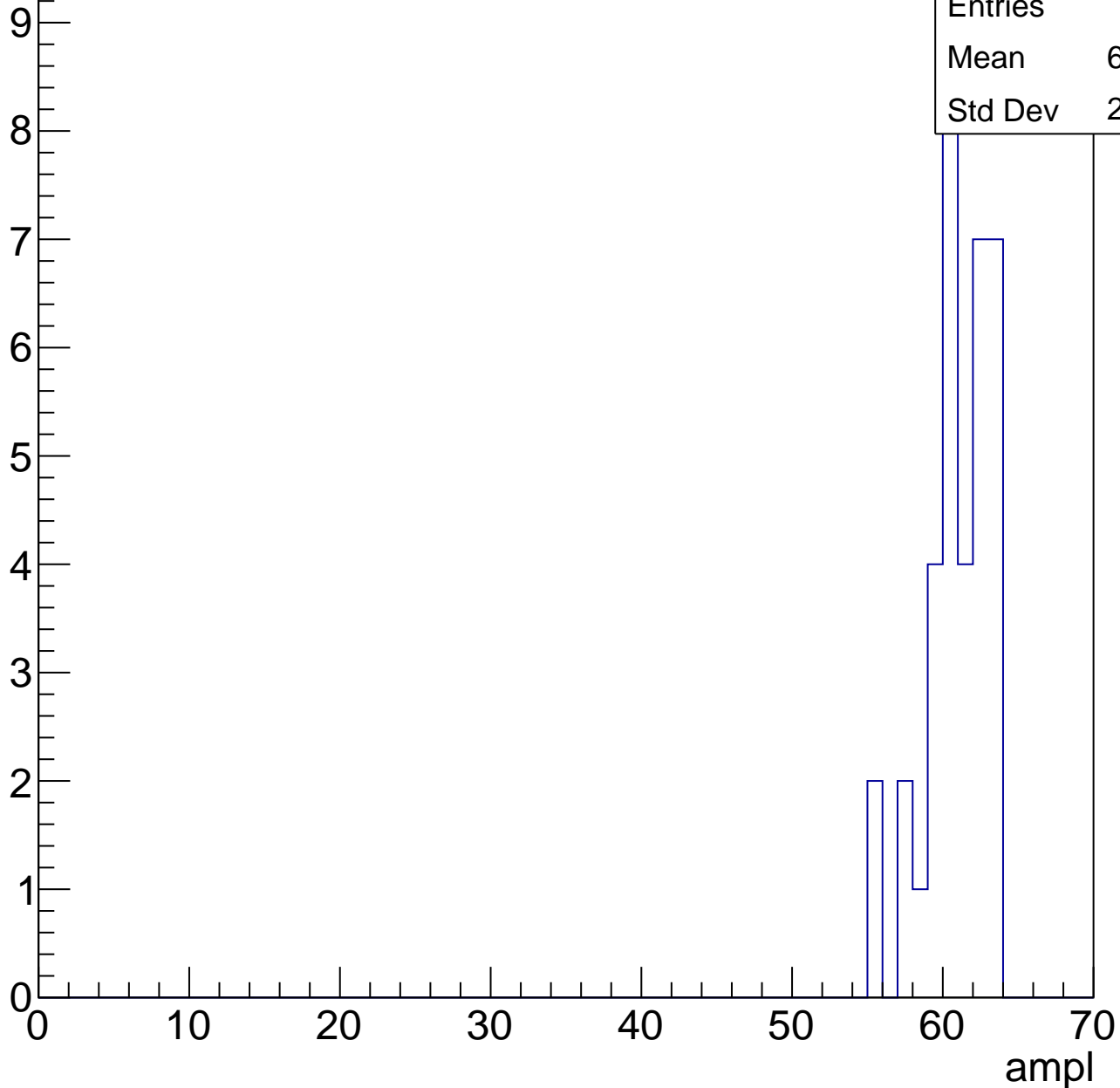
Entries	59
Mean	55.36
Std Dev	8.05



B1L100S, U25-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	36
Mean	60.47
Std Dev	2.128

B1L100S, U25-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch94, adc7

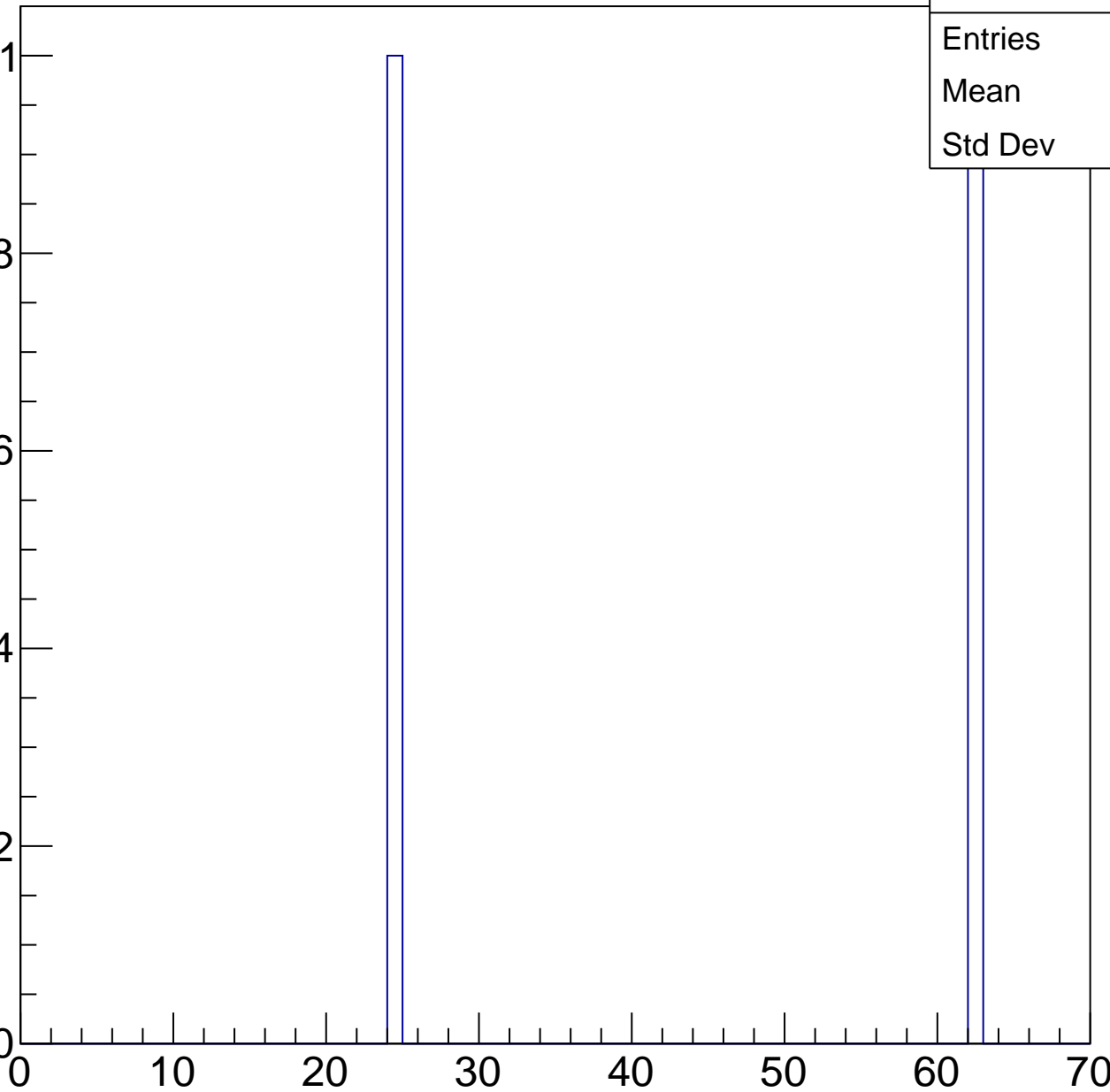
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	19

ampl



B1L100S, U25-ch95, adc0

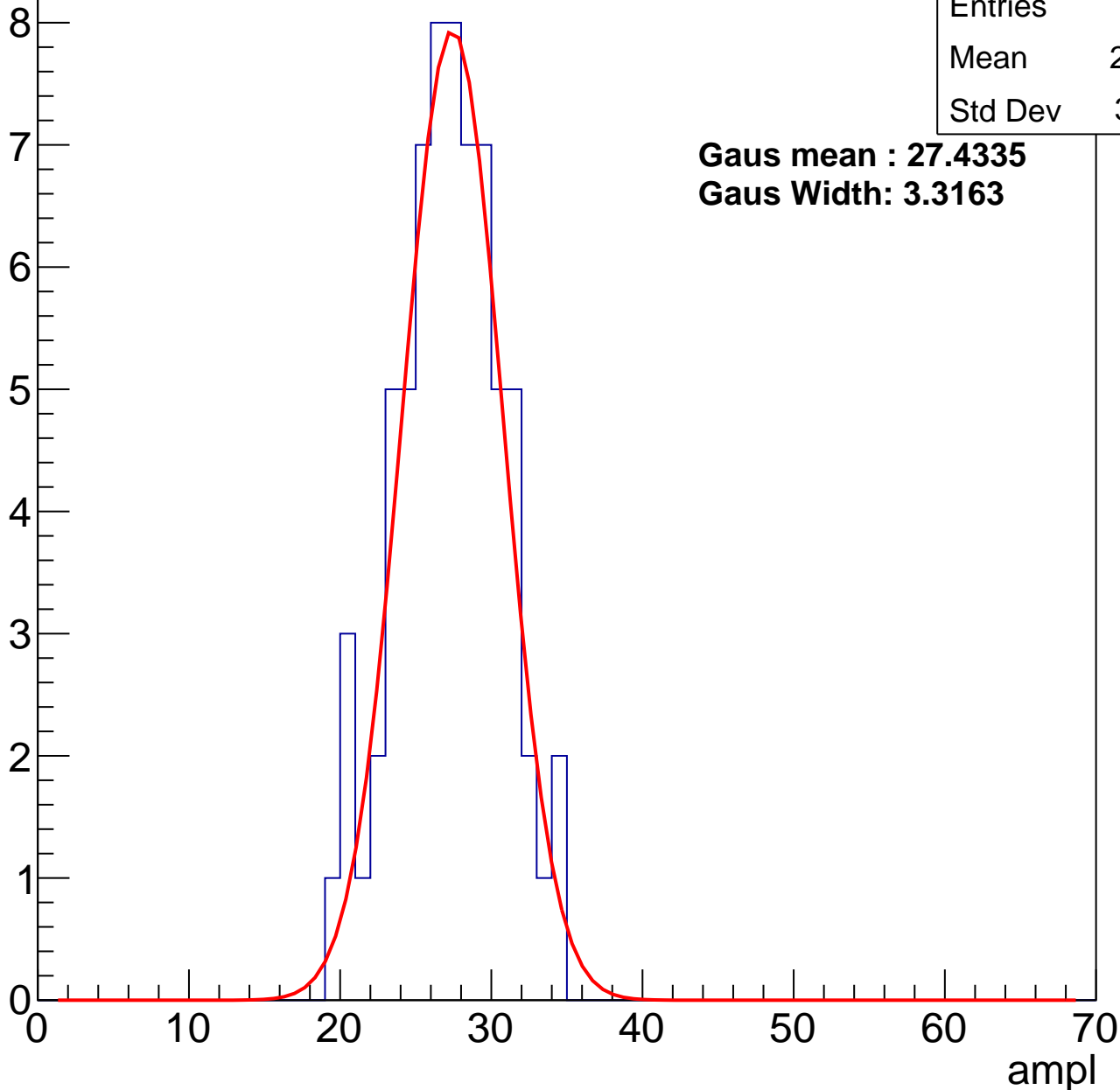
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	26.77
Std Dev	3.401

Gaus mean : 27.4335

Gaus Width: 3.3163



B1L100S, U25-ch95, adc1

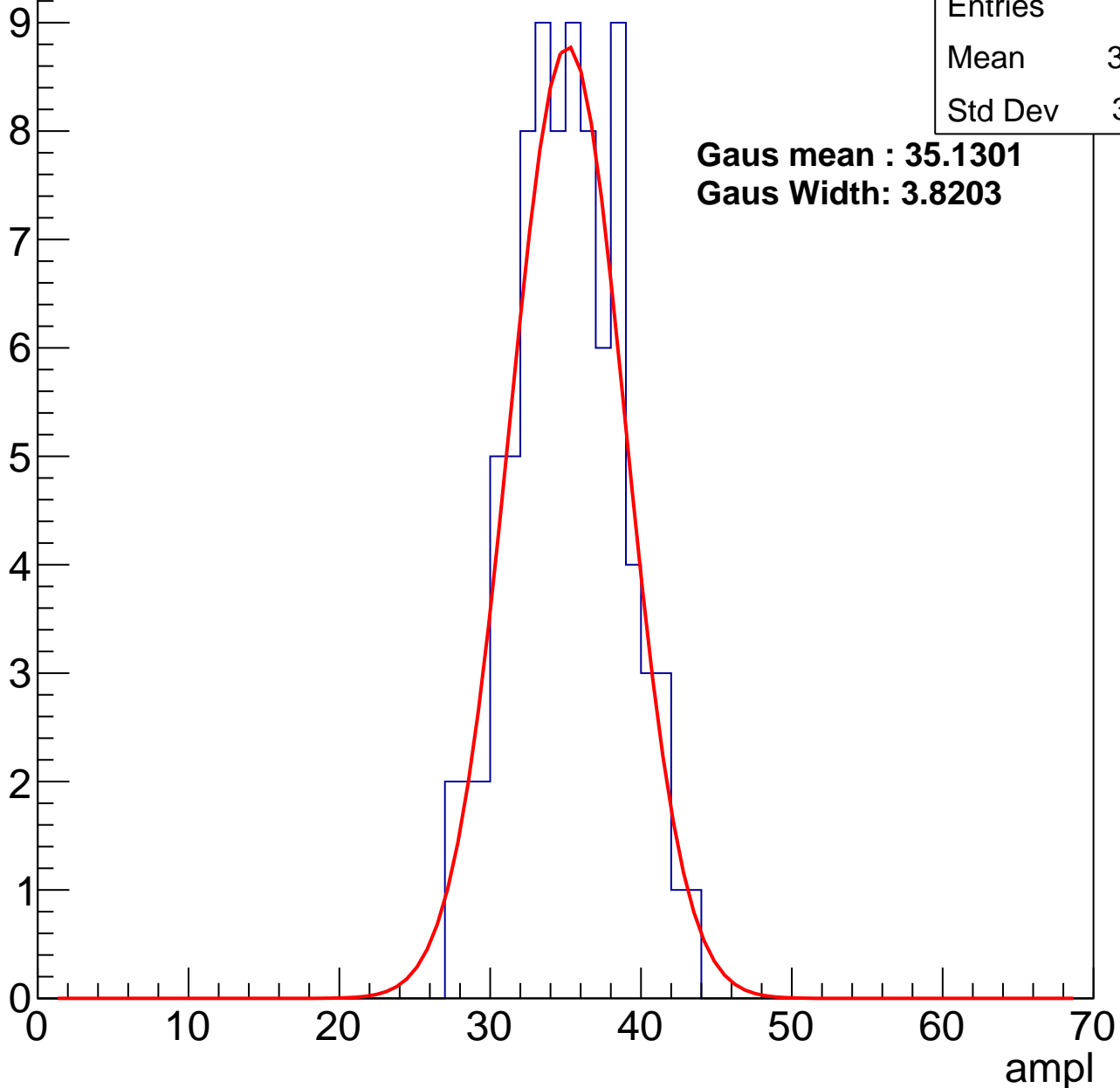
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	34.69
Std Dev	3.571

Gaus mean : 35.1301

Gaus Width: 3.8203



B1L100S, U25-ch95, adc2

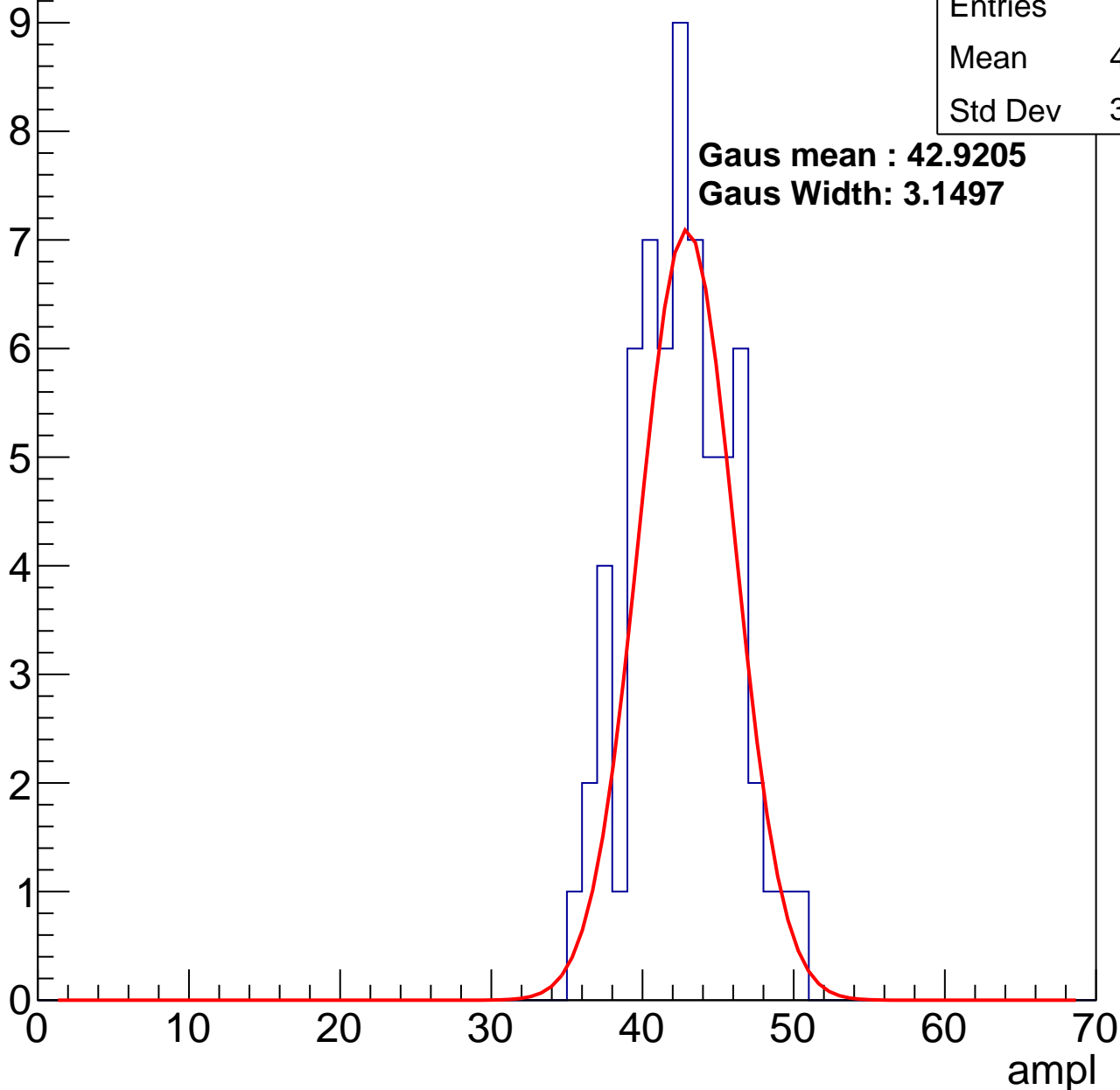
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.09
Std Dev	3.287

Gaus mean : 42.9205

Gaus Width: 3.1497

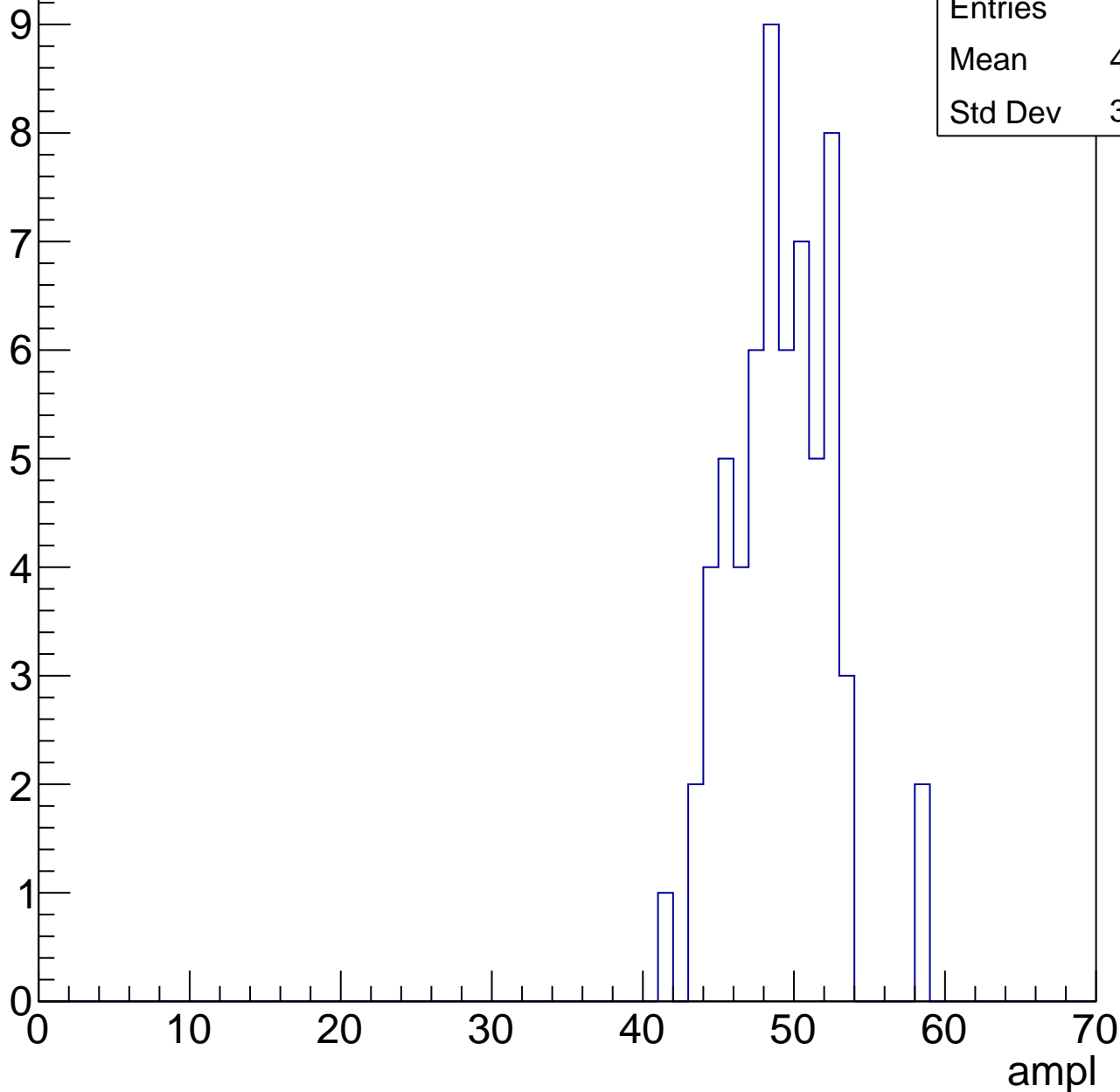


B1L100S, U25-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	48.65
Std Dev	3.327



B1L100S, U25-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	54.54
Std Dev	3.478

Entry

10

8

6

4

2

0

0

10

20

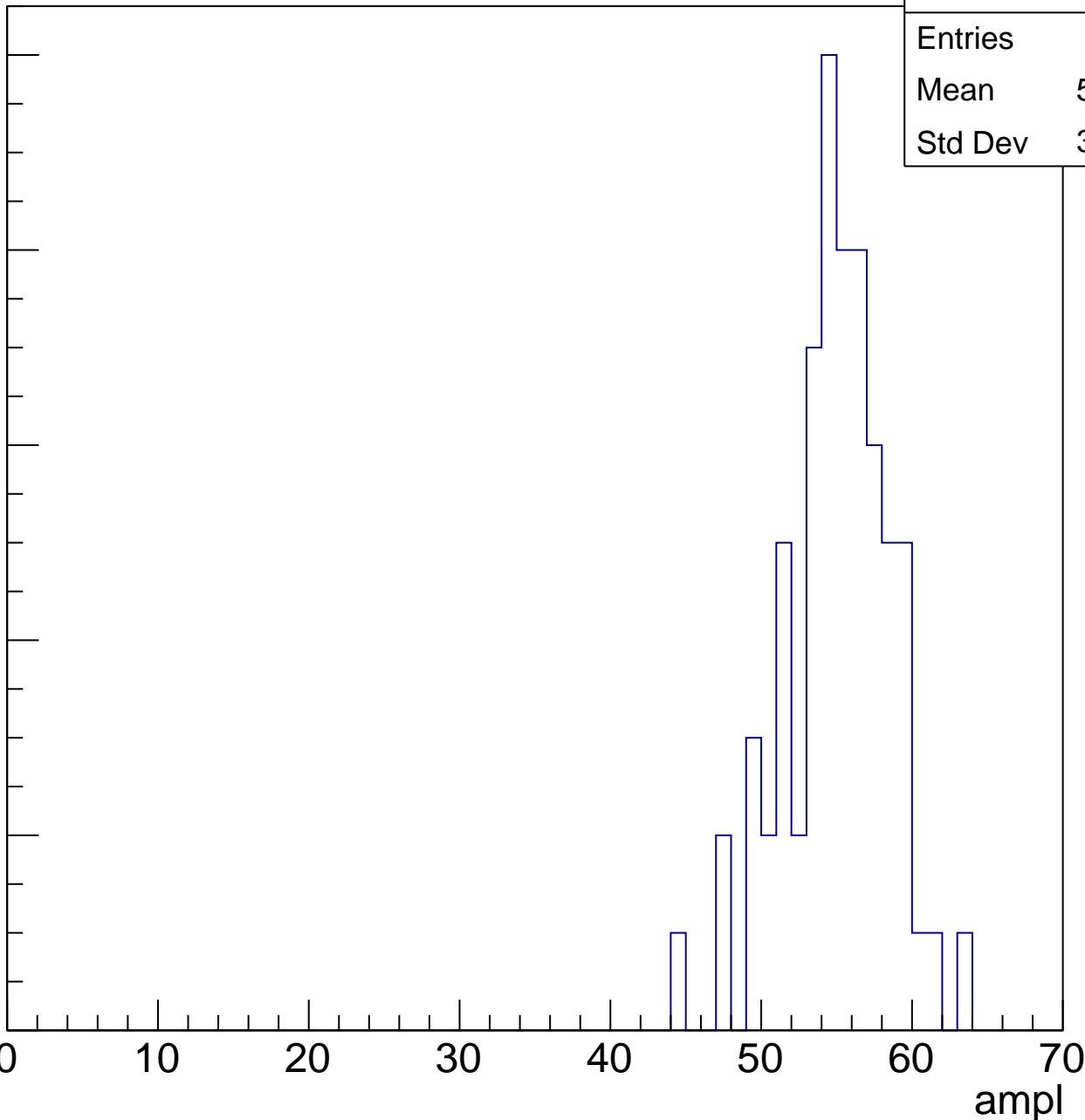
30

40

50

60

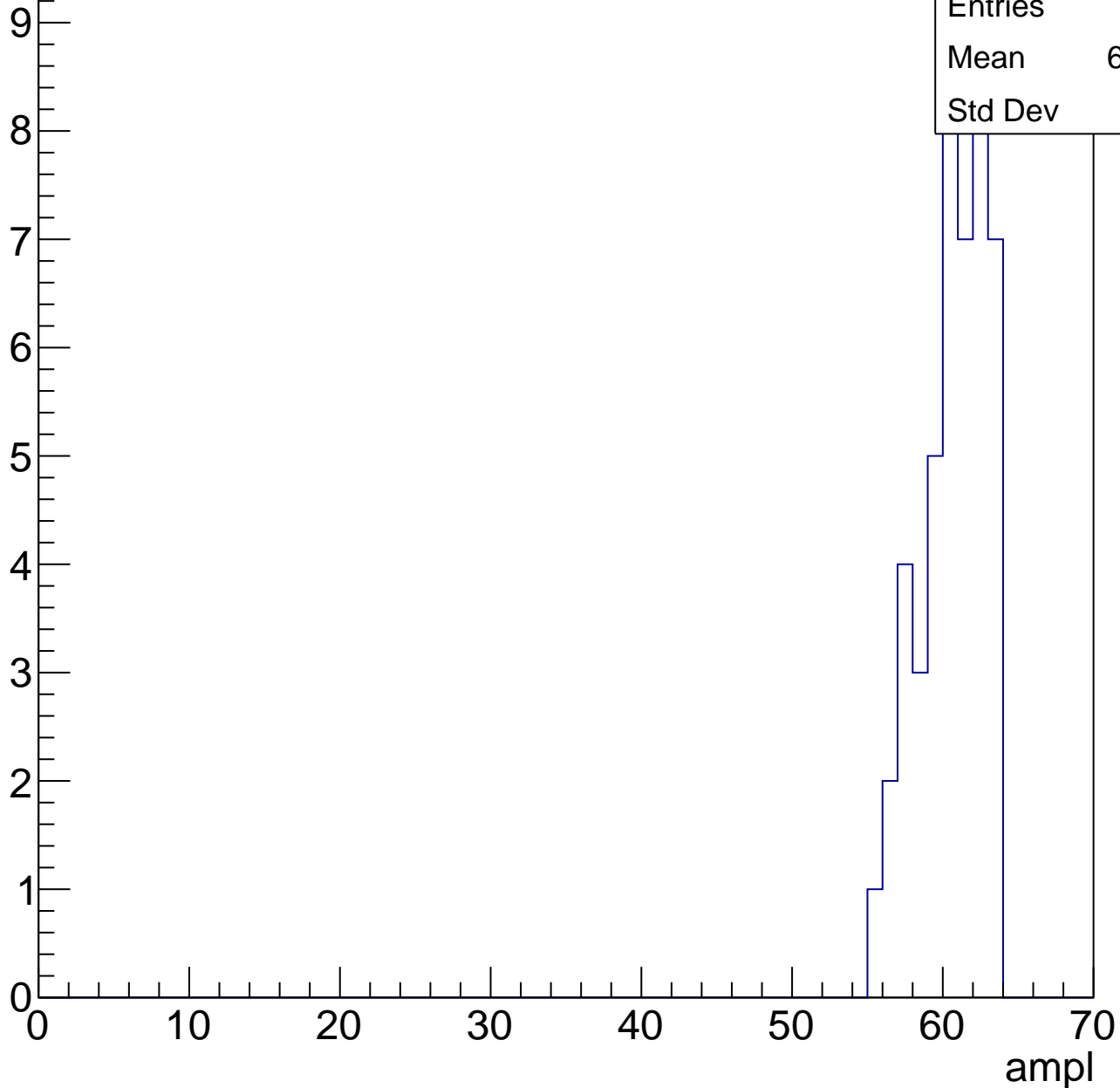
ampl



B1L100S, U25-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

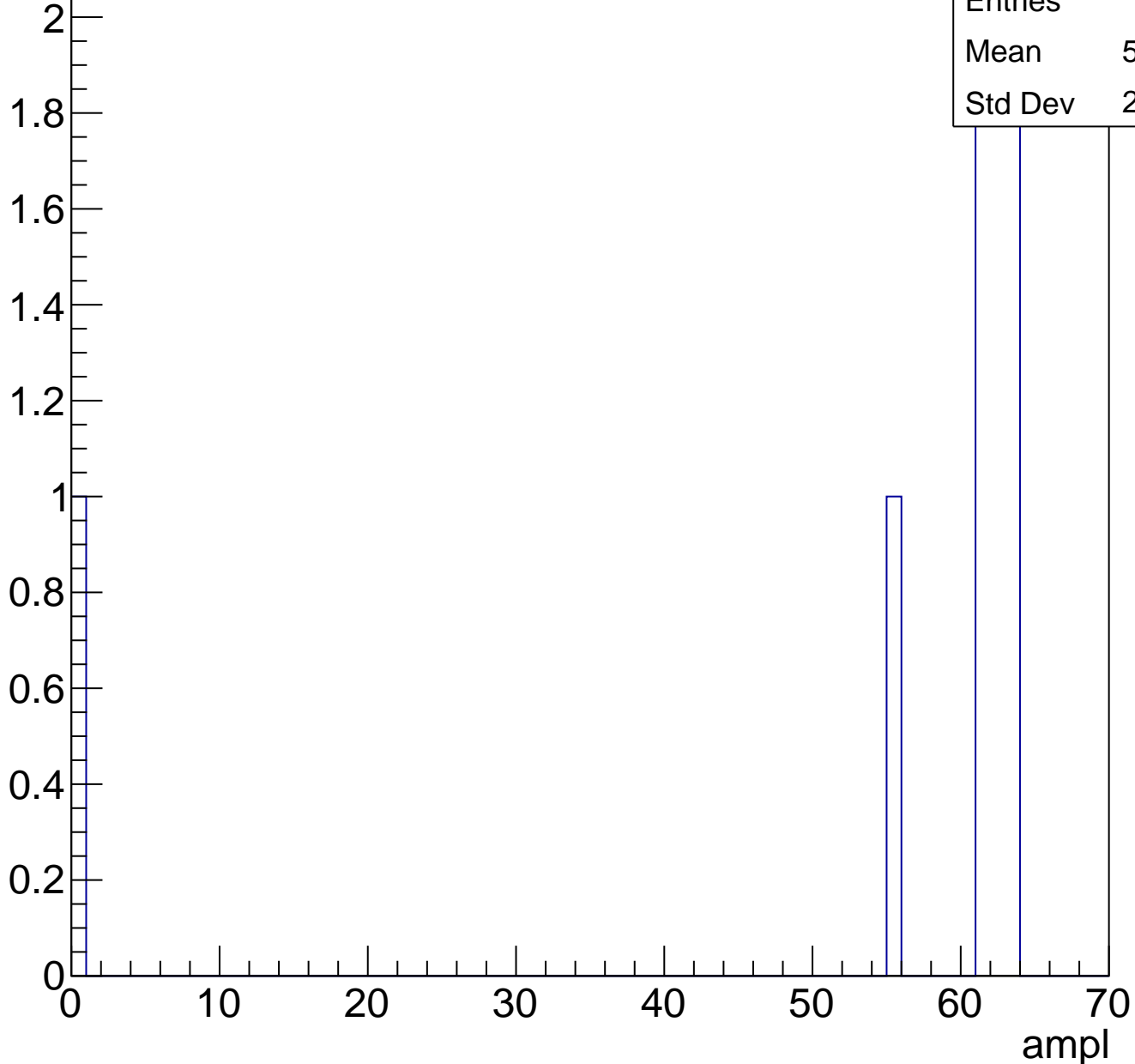
Entry



B1L100S, U25-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch96, adc0

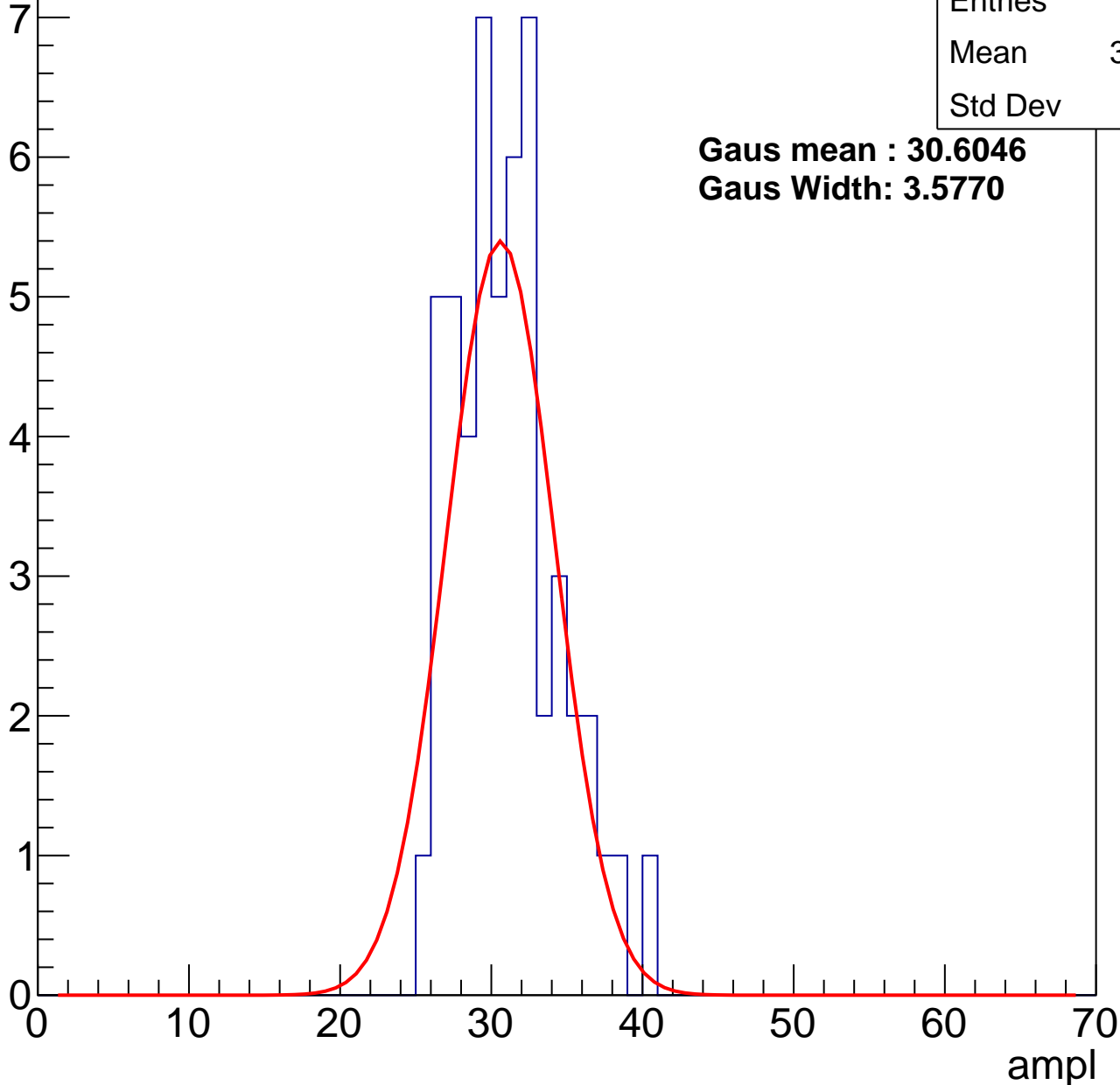
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	30.58
Std Dev	3.37

Gaus mean : 30.6046

Gaus Width: 3.5770



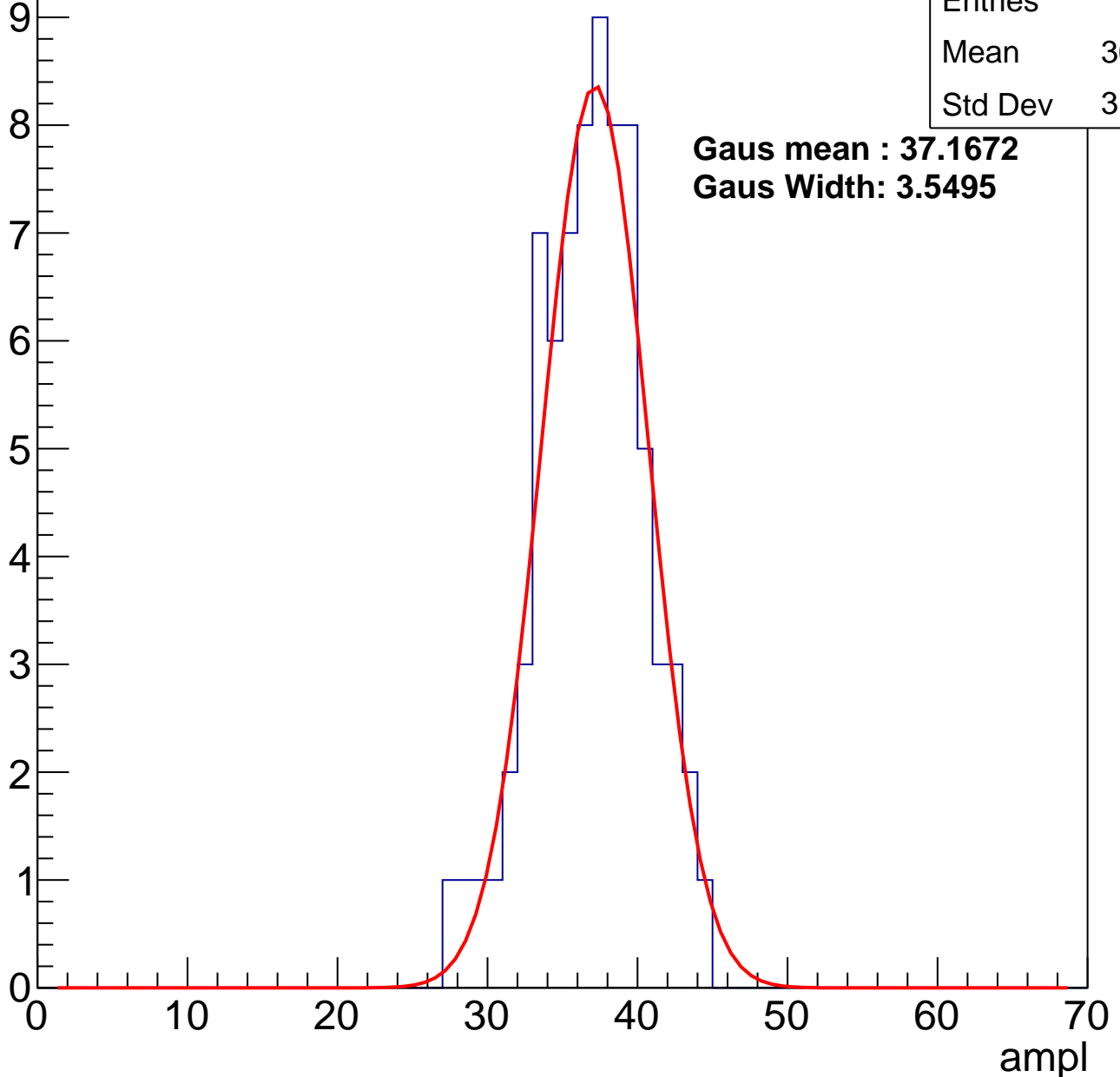
B1L100S, U25-ch96, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.42
Std Dev	3.529

Gaus mean : 37.1672
Gaus Width: 3.5495



B1L100S, U25-ch96, adc2

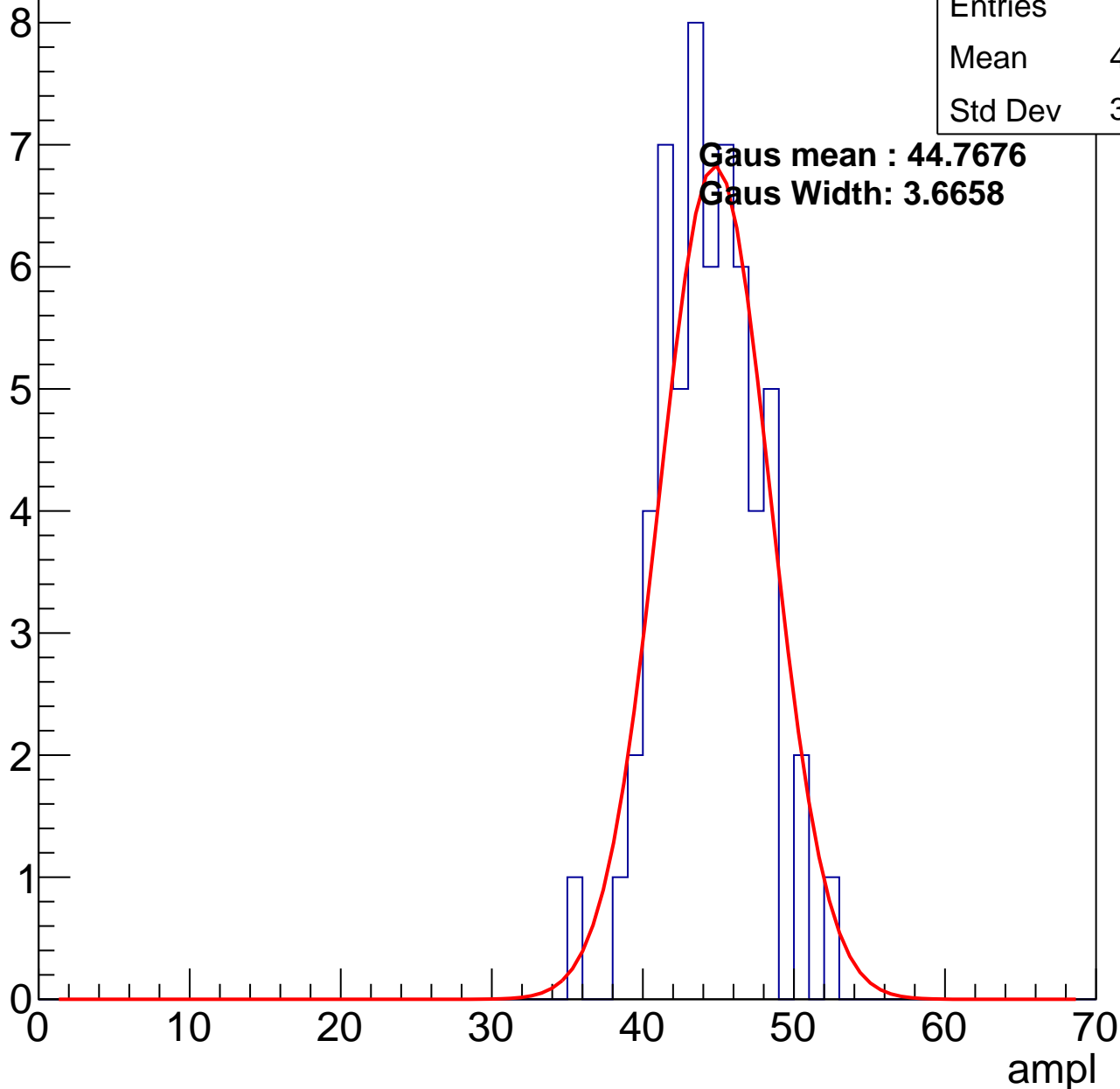
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.85
Std Dev	3.199

Gaus mean : 44.7676

Gaus Width: 3.6658

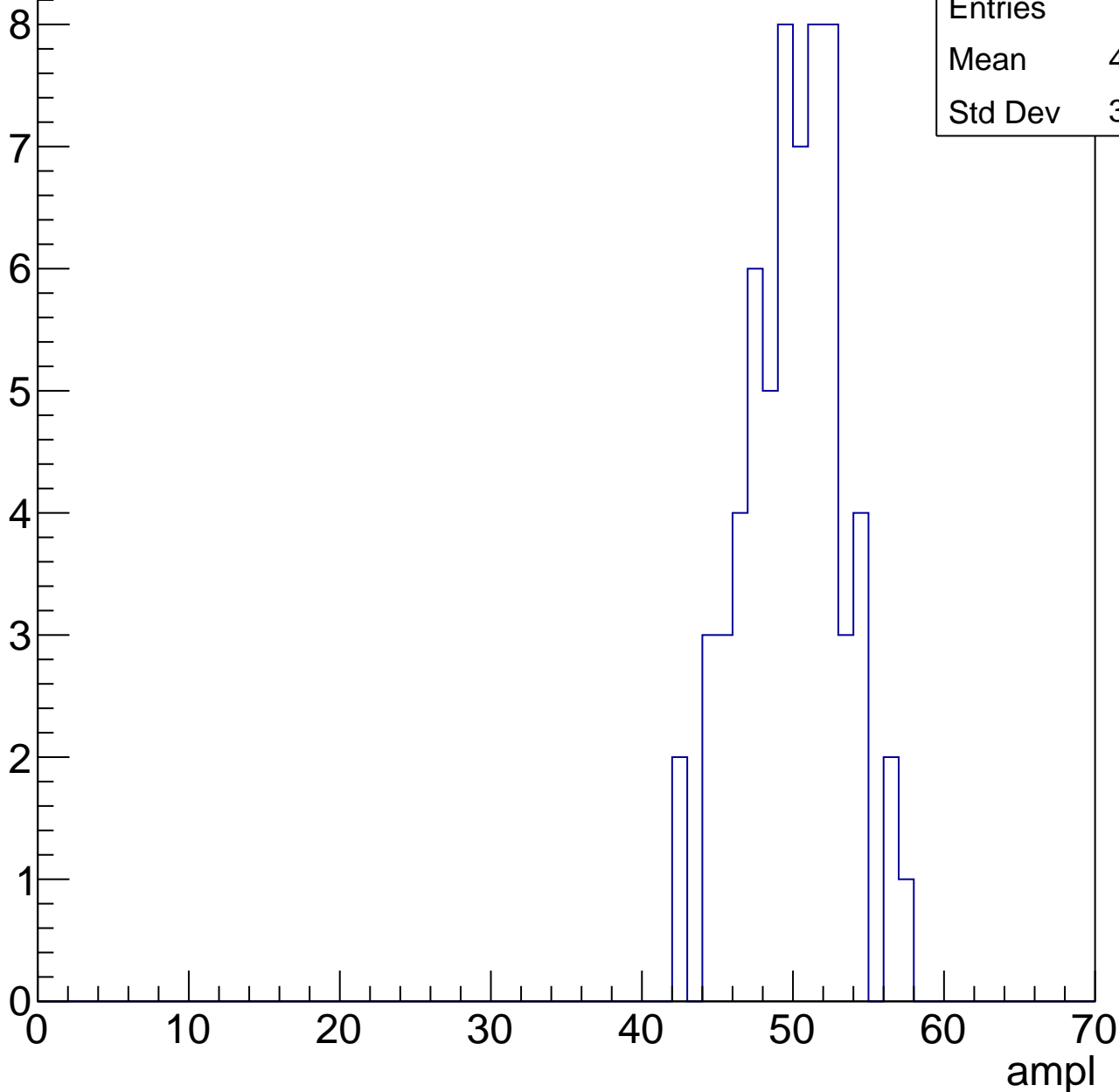


B1L100S, U25-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

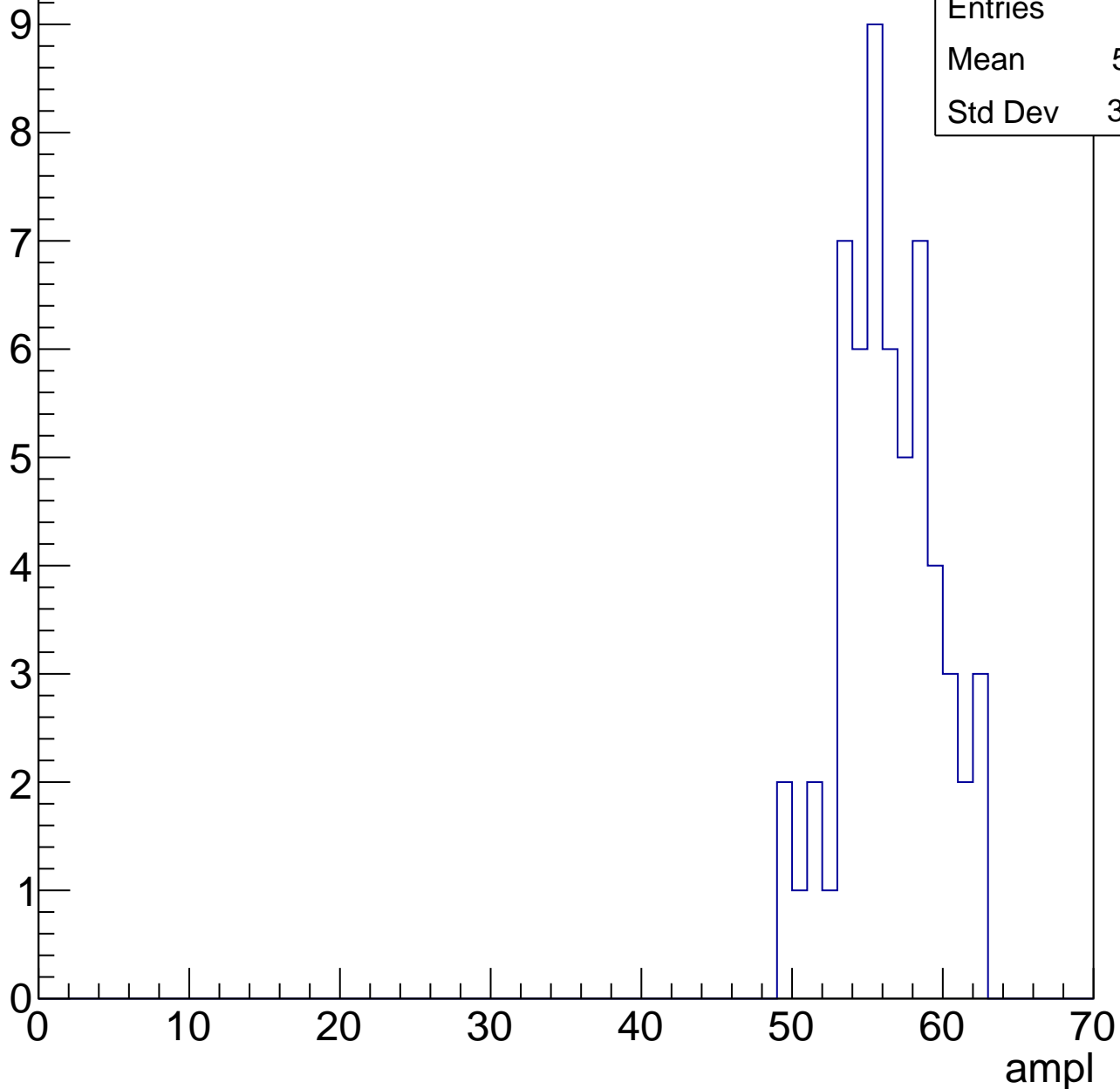
Entries	64
Mean	49.48
Std Dev	3.288



B1L100S, U25-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

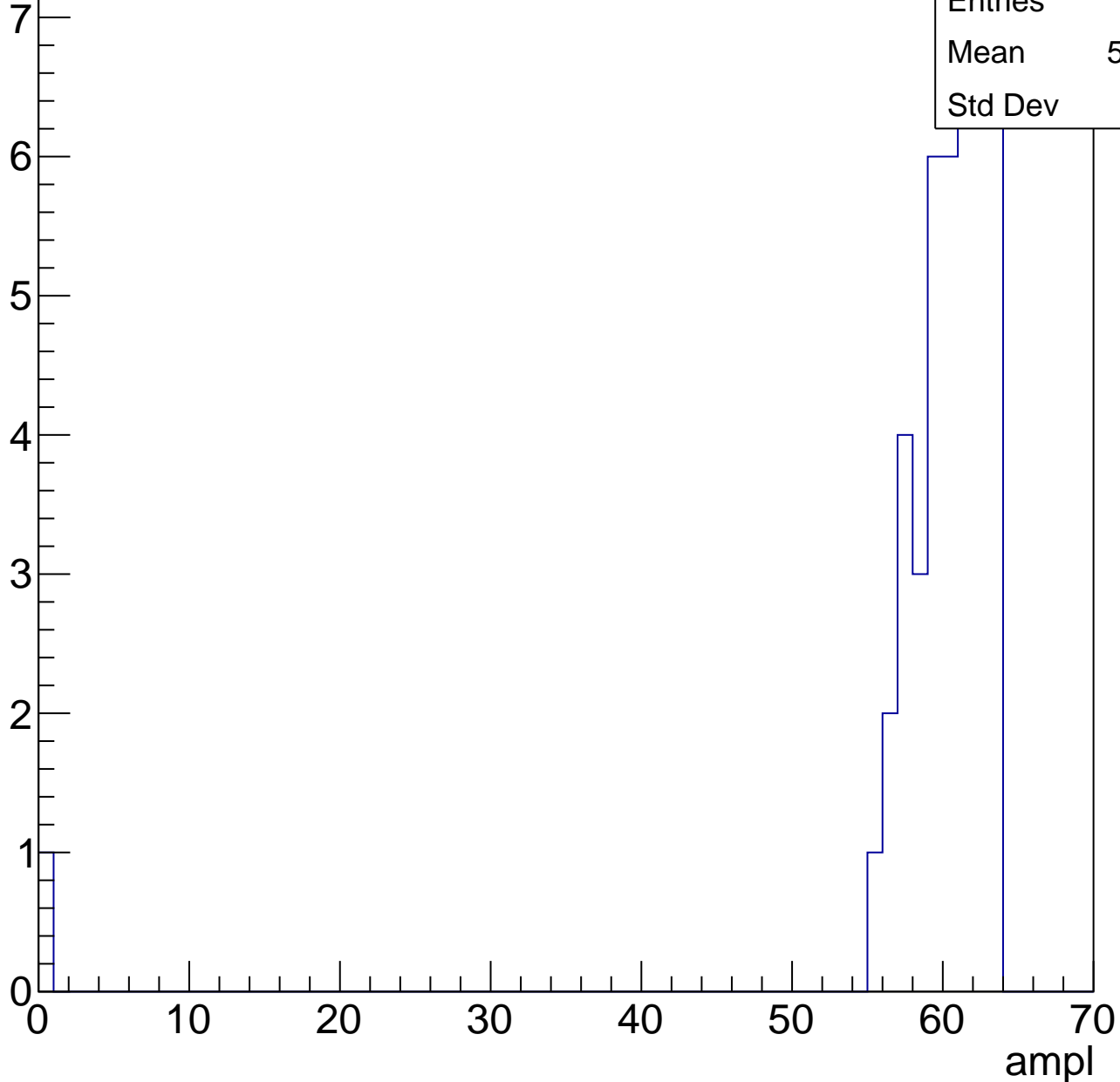


Entries	58
Mean	55.91
Std Dev	3.136

B1L100S, U25-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

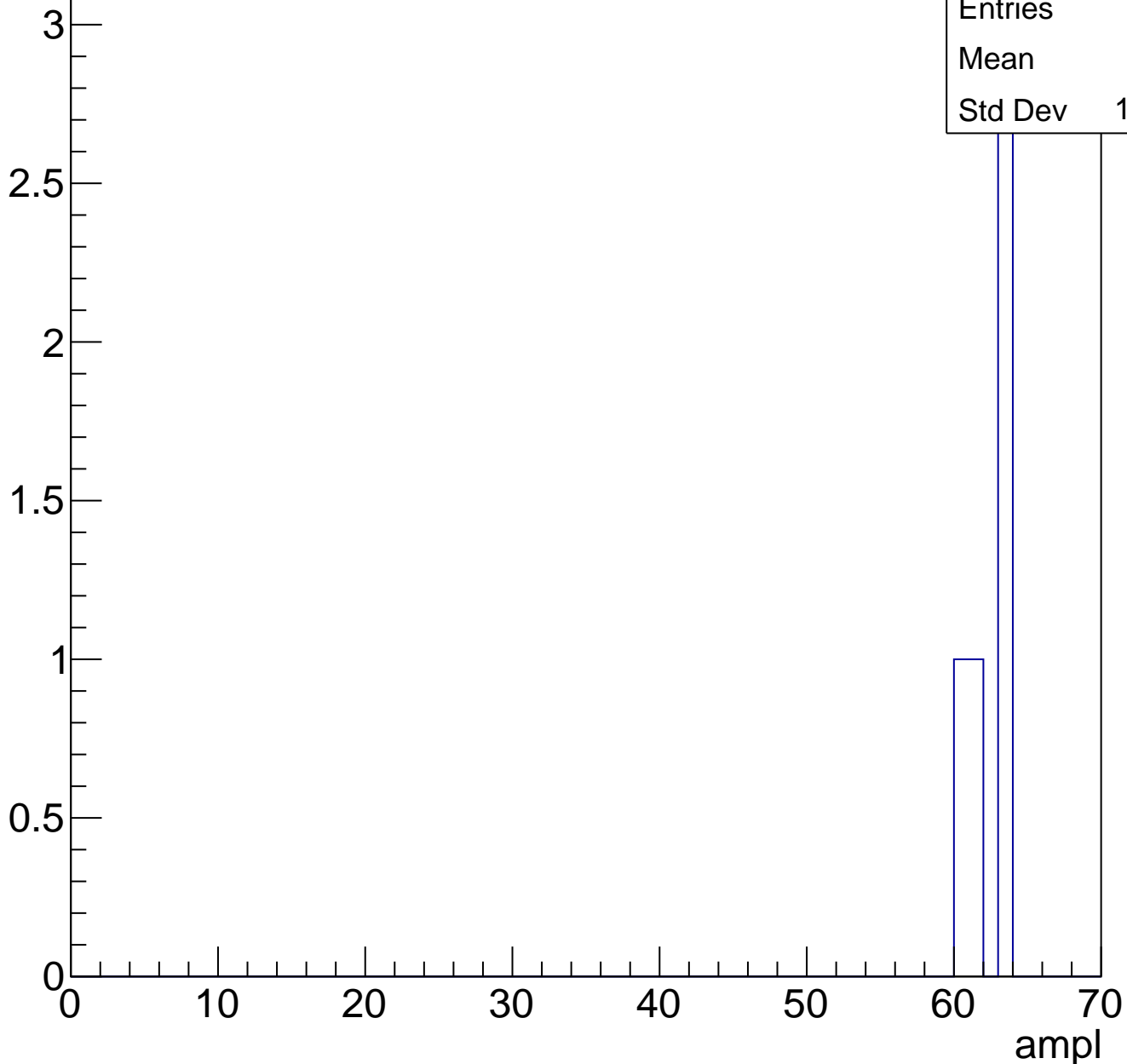
Entry



B1L100S, U25-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch97, adc0

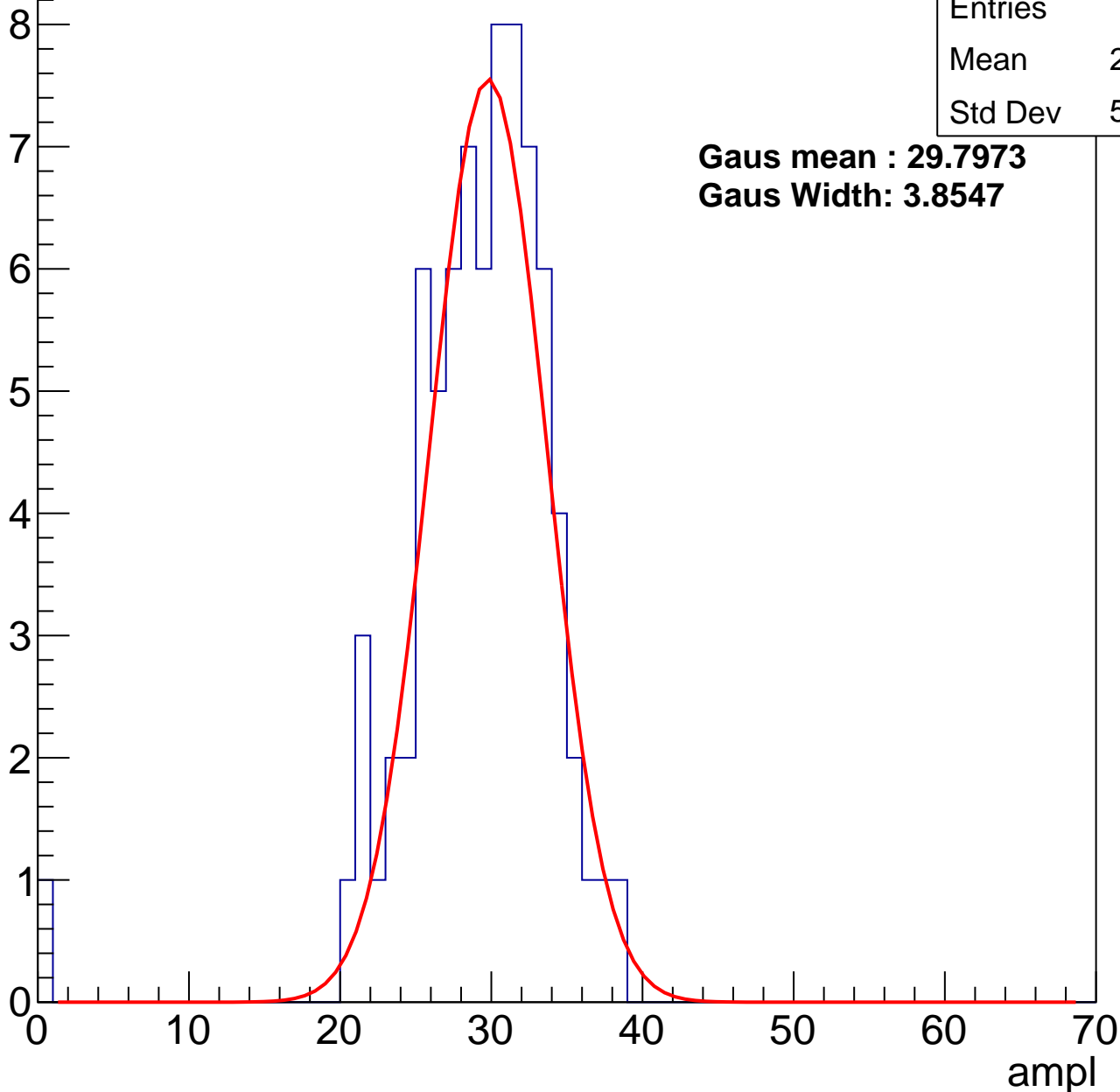
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.69
Std Dev	5.082

Gaus mean : 29.7973

Gaus Width: 3.8547



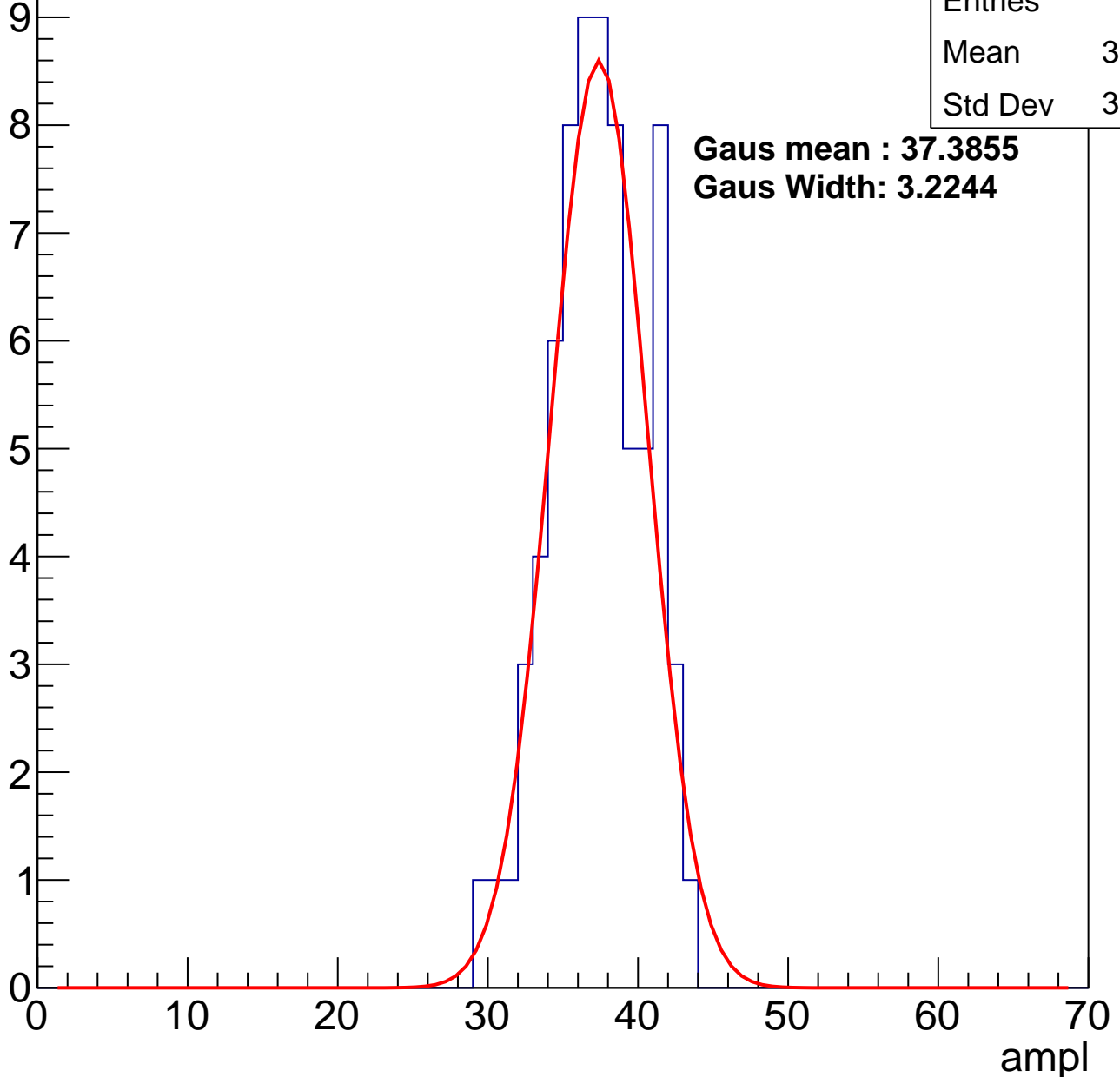
B1L100S, U25-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

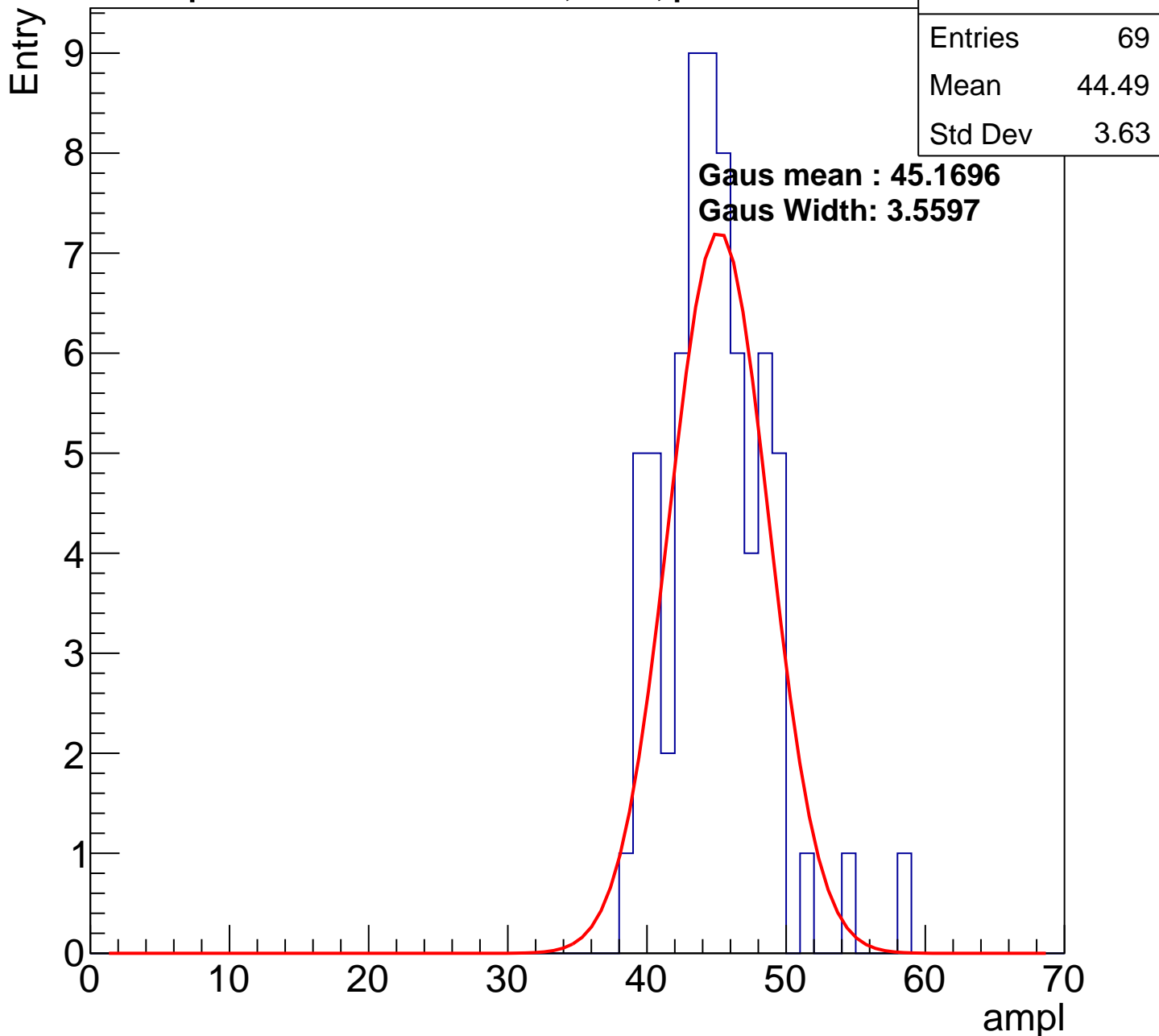
Entries	72
Mean	36.88
Std Dev	3.104

Gaus mean : 37.3855
Gaus Width: 3.2244



B1L100S, U25-ch97, adc2

calib_packv5_042523_0143.root, FC#4, port A2

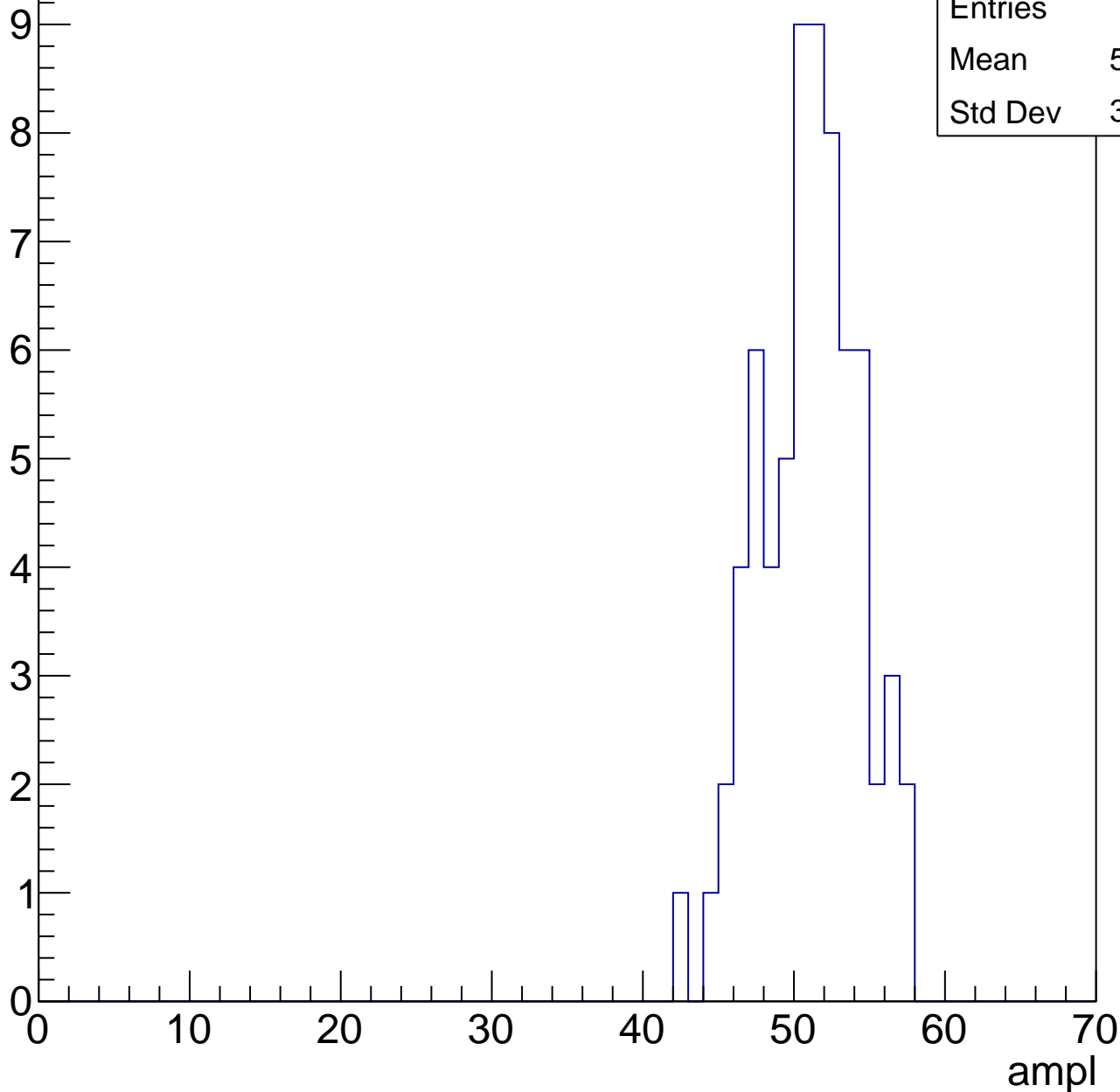


B1L100S, U25-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.56
Std Dev	3.242

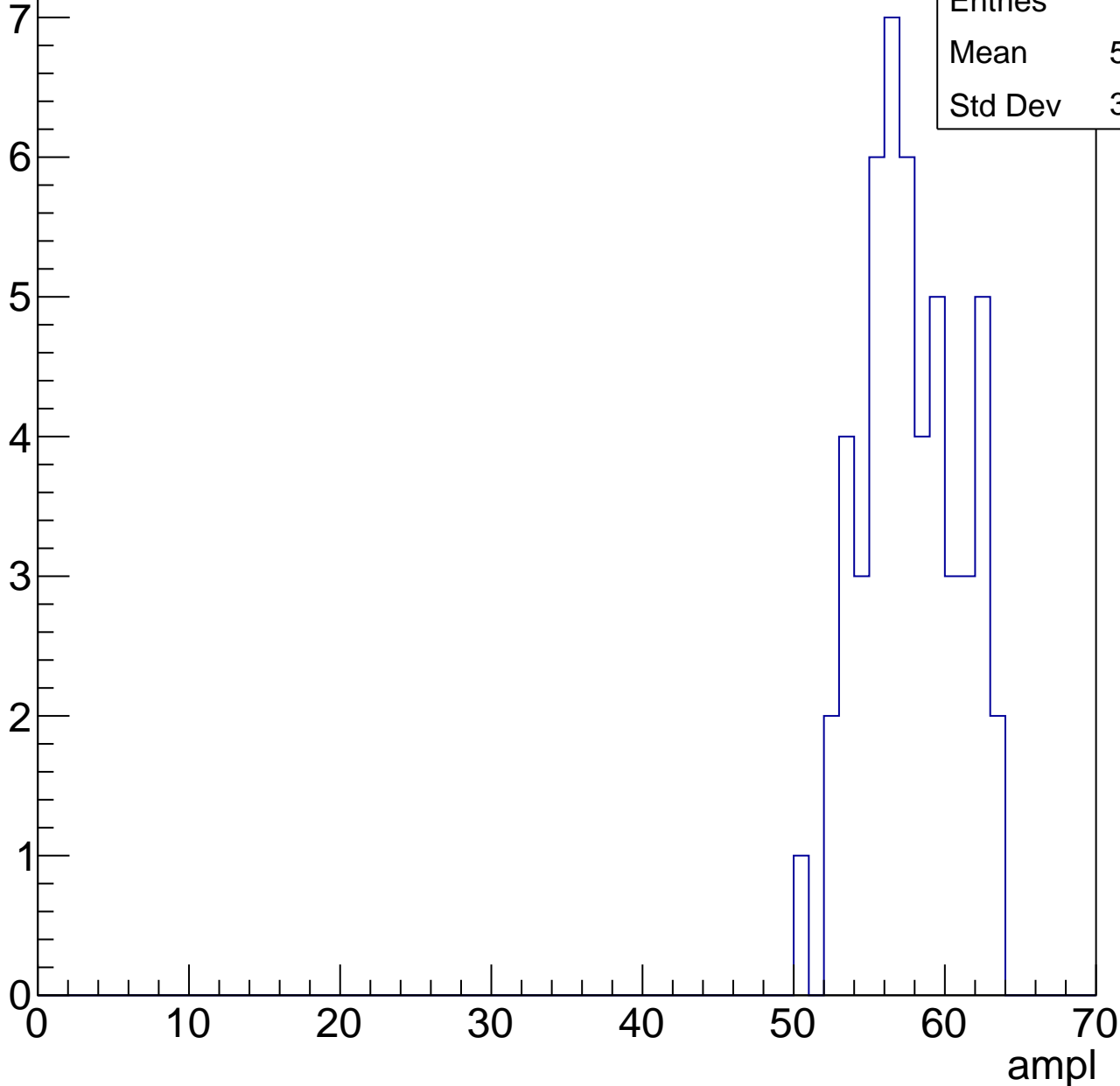


B1L100S, U25-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

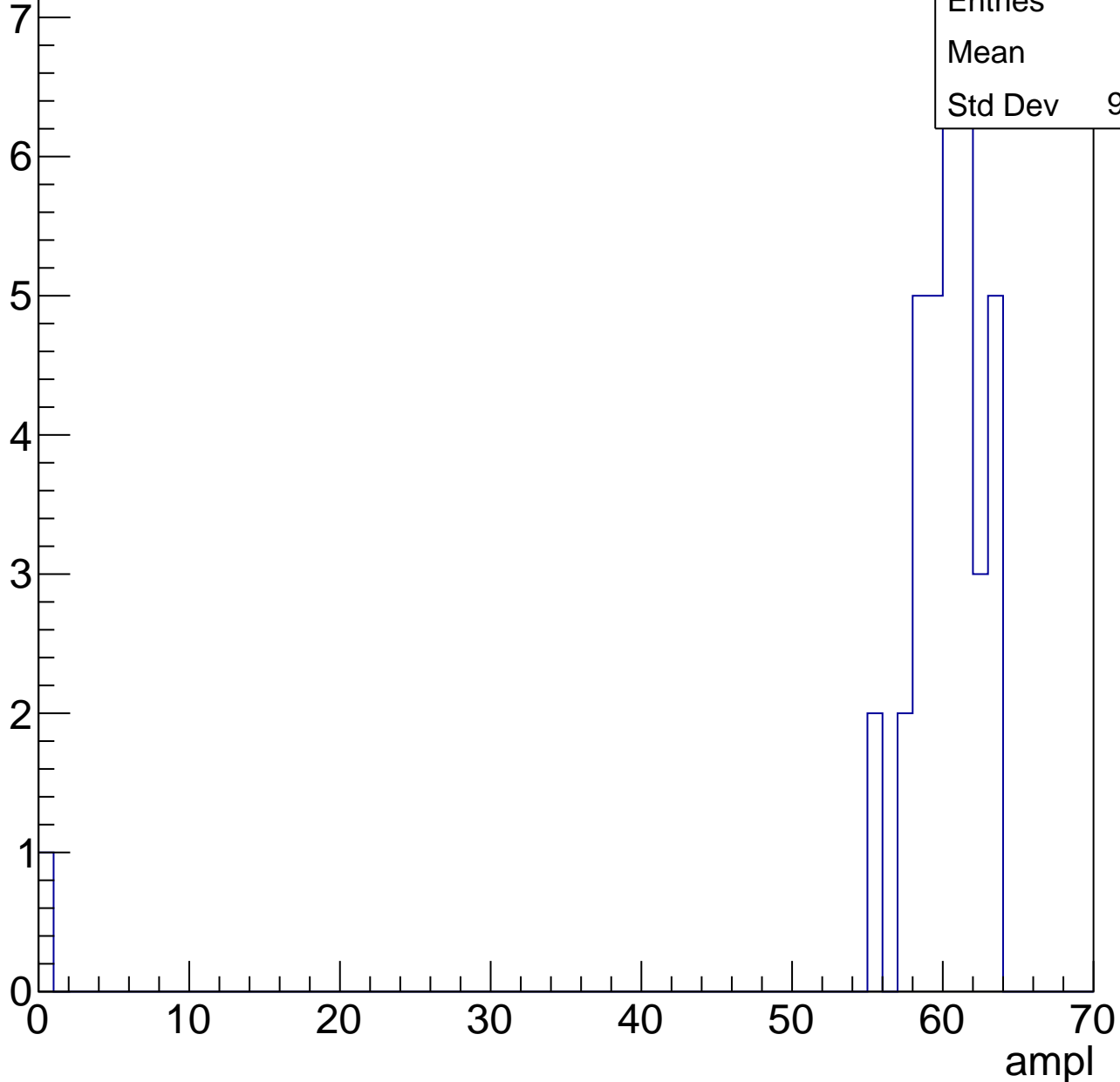
Entries	51
Mean	57.22
Std Dev	3.177



B1L100S, U25-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch98, adc0

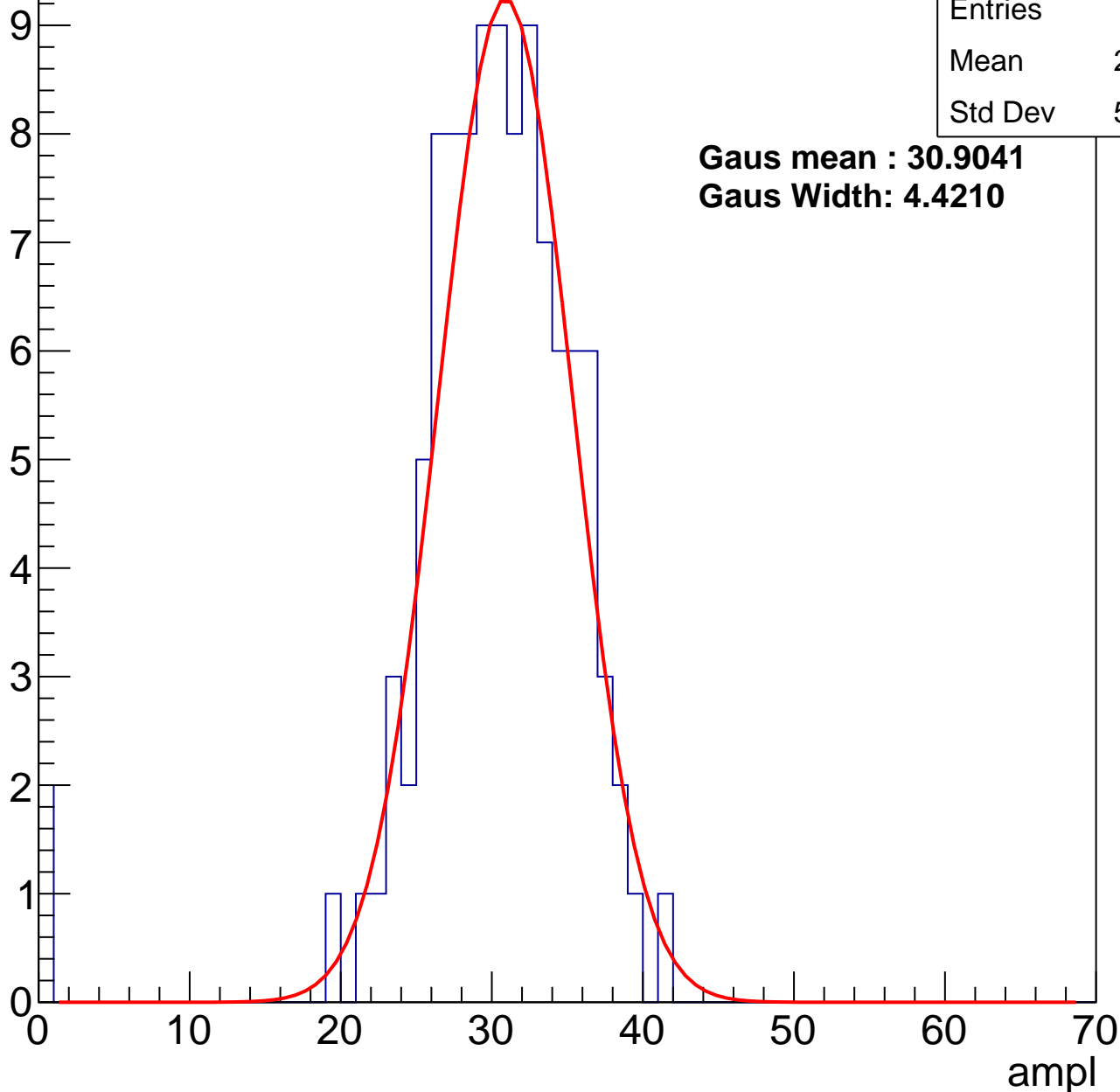
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	106
Mean	29.69
Std Dev	5.897

Gaus mean : 30.9041

Gaus Width: 4.4210



B1L100S, U25-ch98, adc1

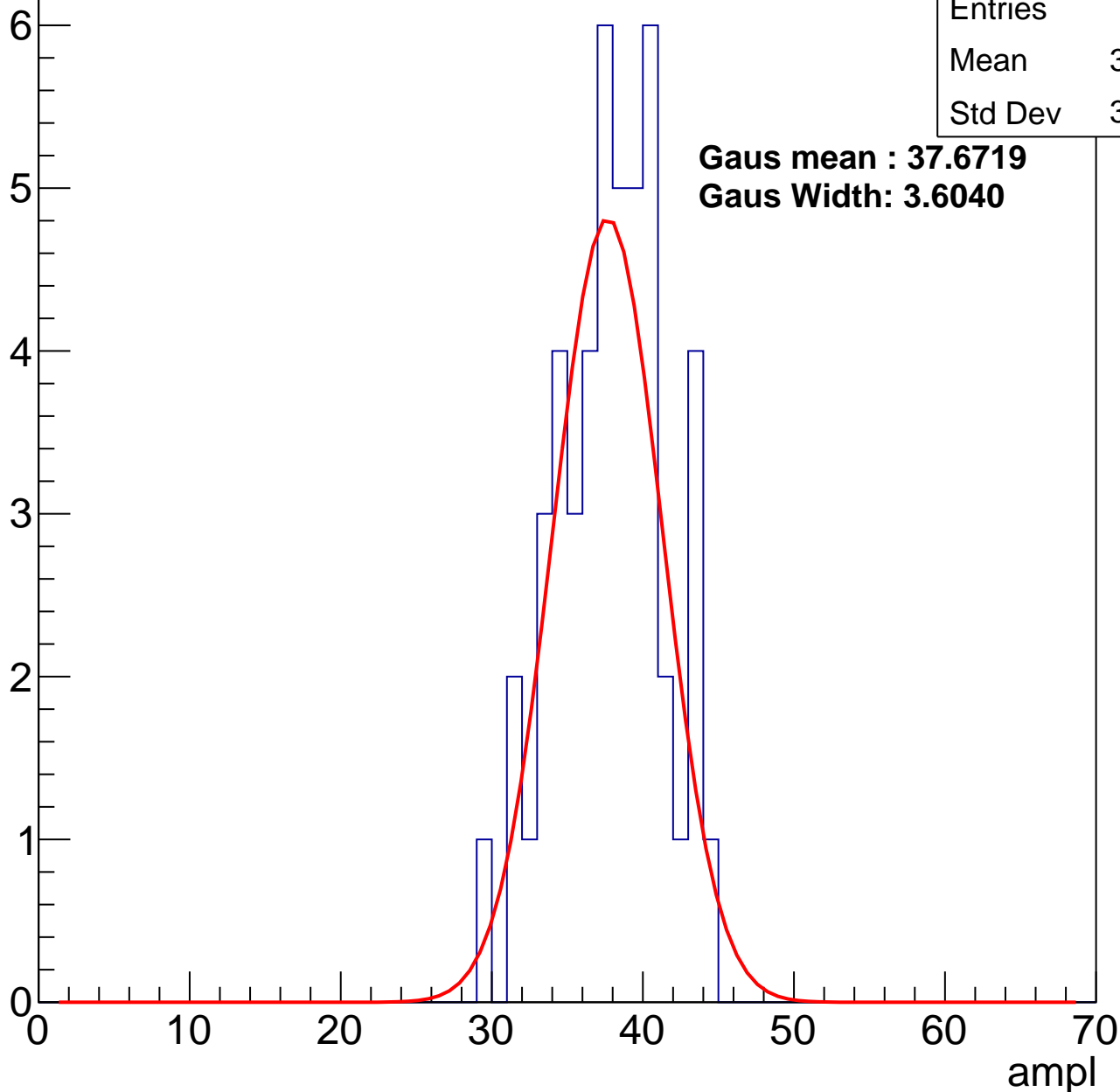
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	37.38
Std Dev	3.486

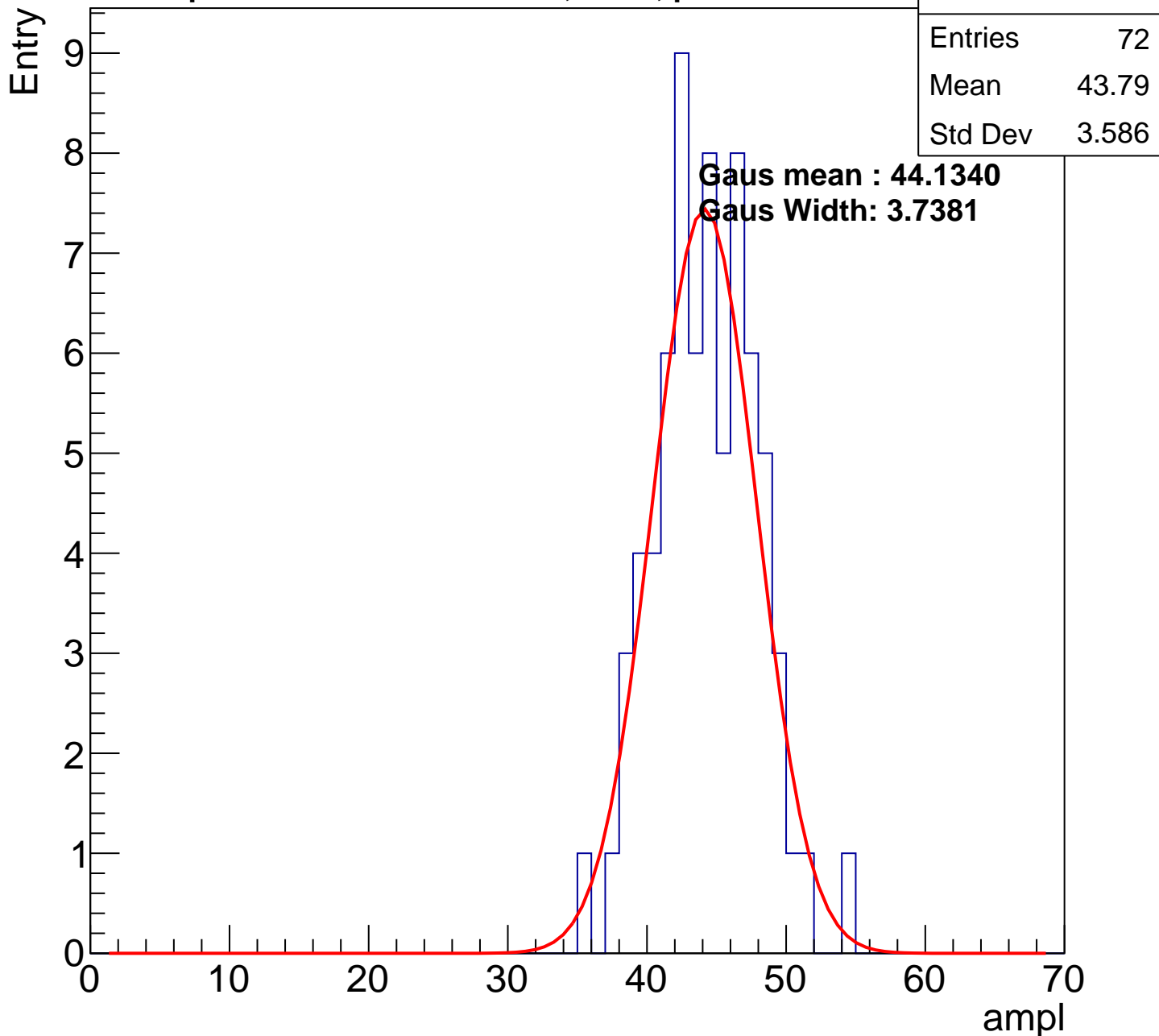
Gaus mean : 37.6719

Gaus Width: 3.6040



B1L100S, U25-ch98, adc2

calib_packv5_042523_0143.root, FC#4, port A2

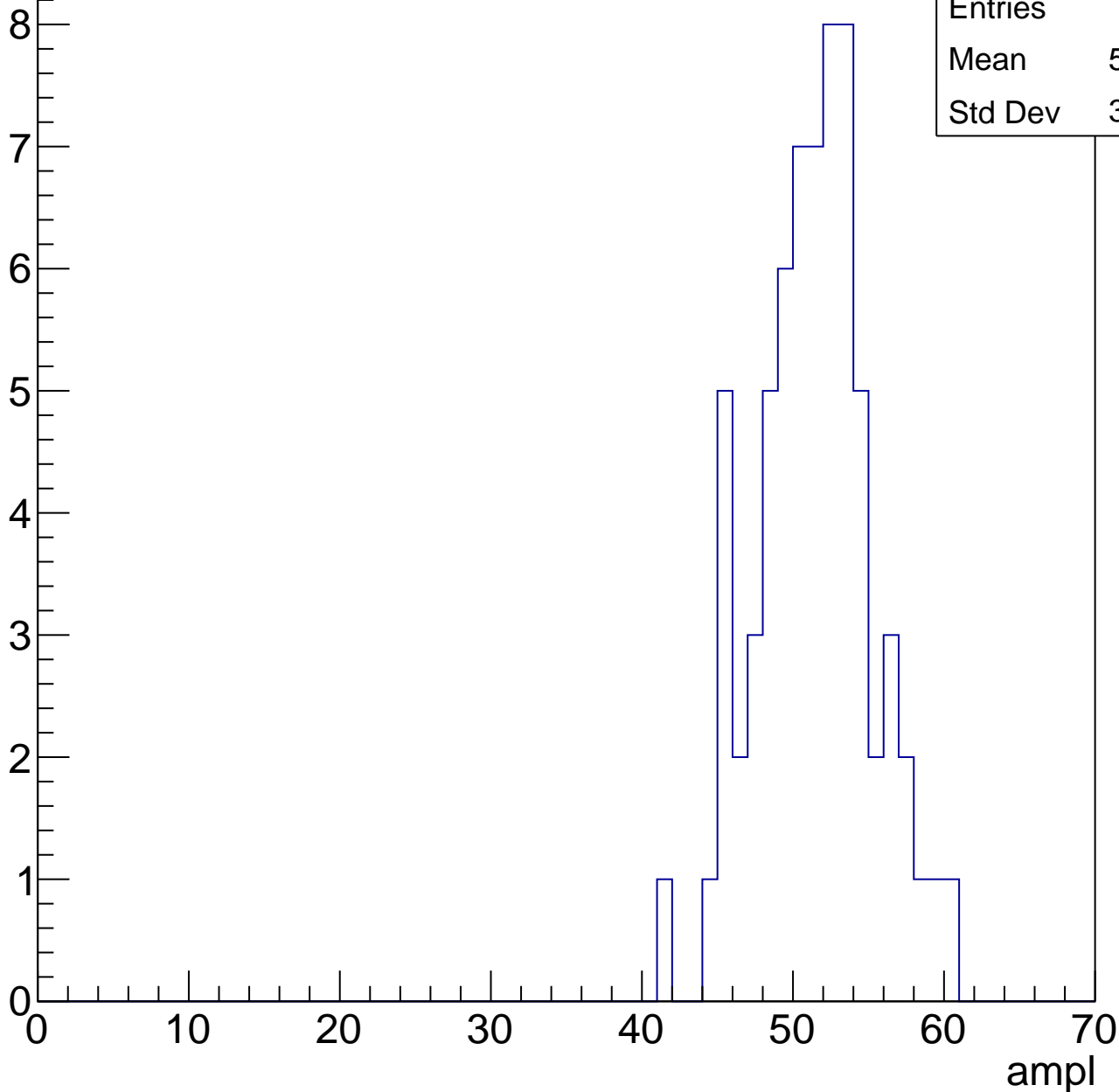


B1L100S, U25-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.93
Std Dev	3.759

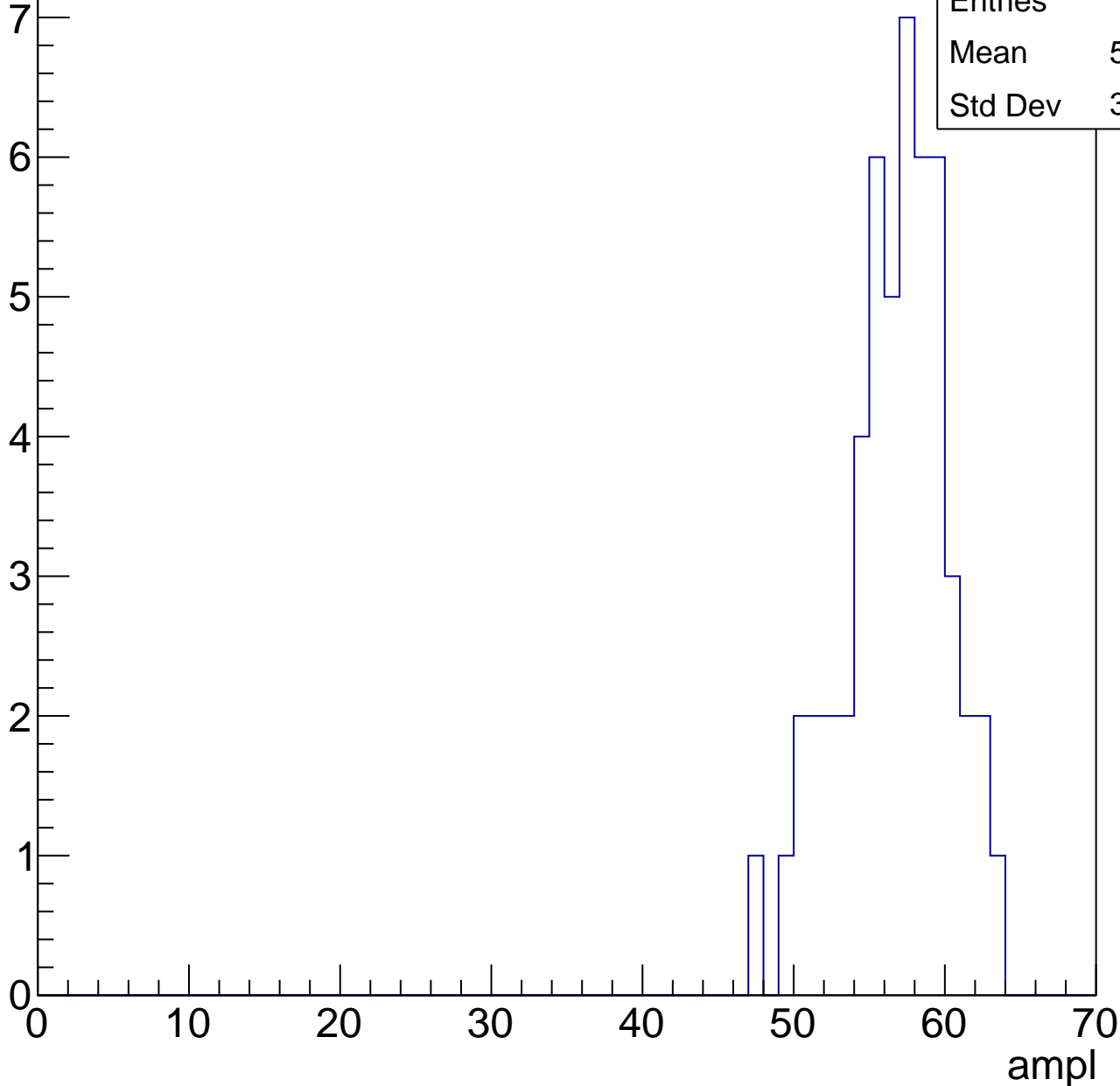


B1L100S, U25-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

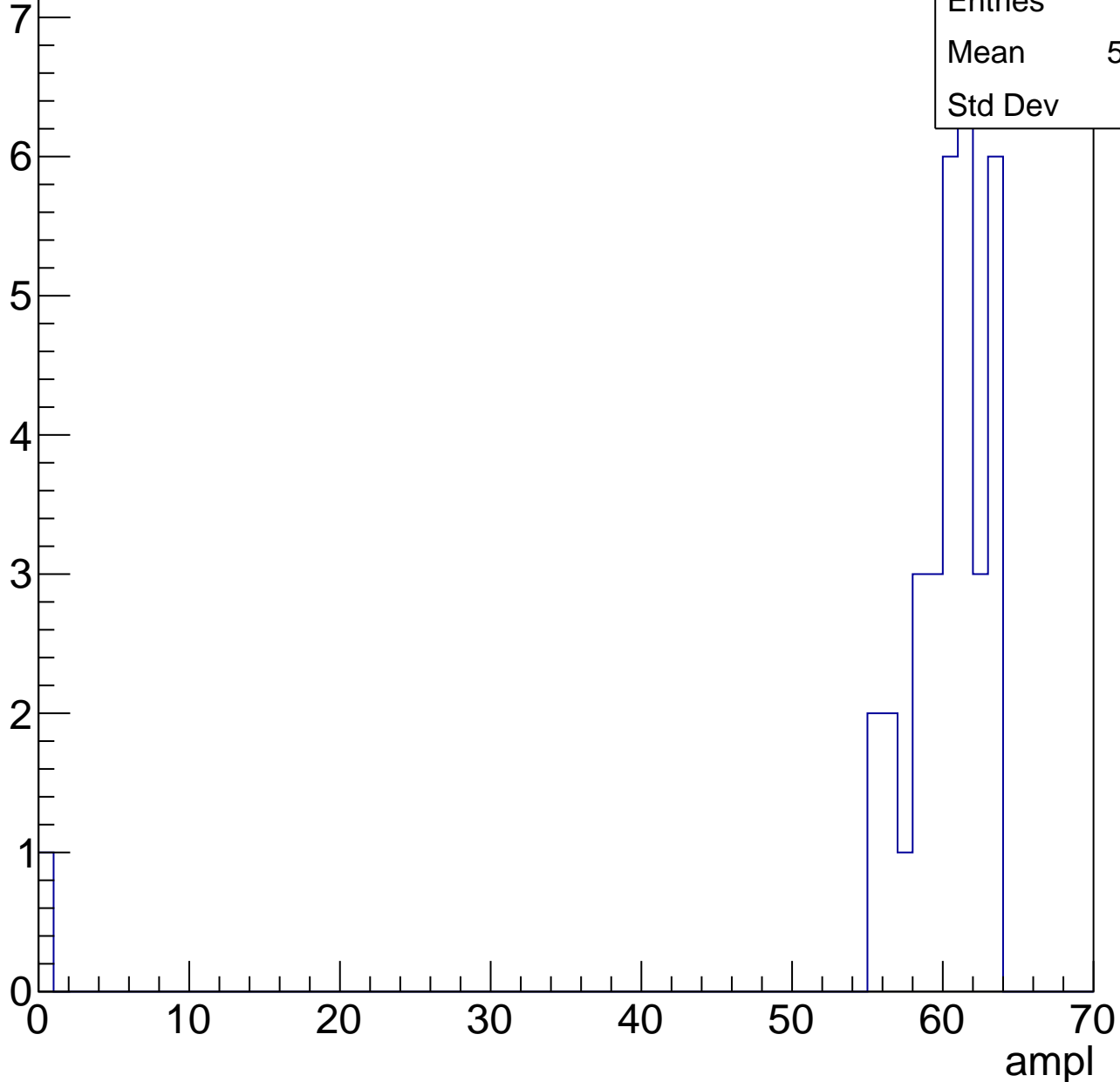
Entries	52
Mean	56.23
Std Dev	3.468



B1L100S, U25-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

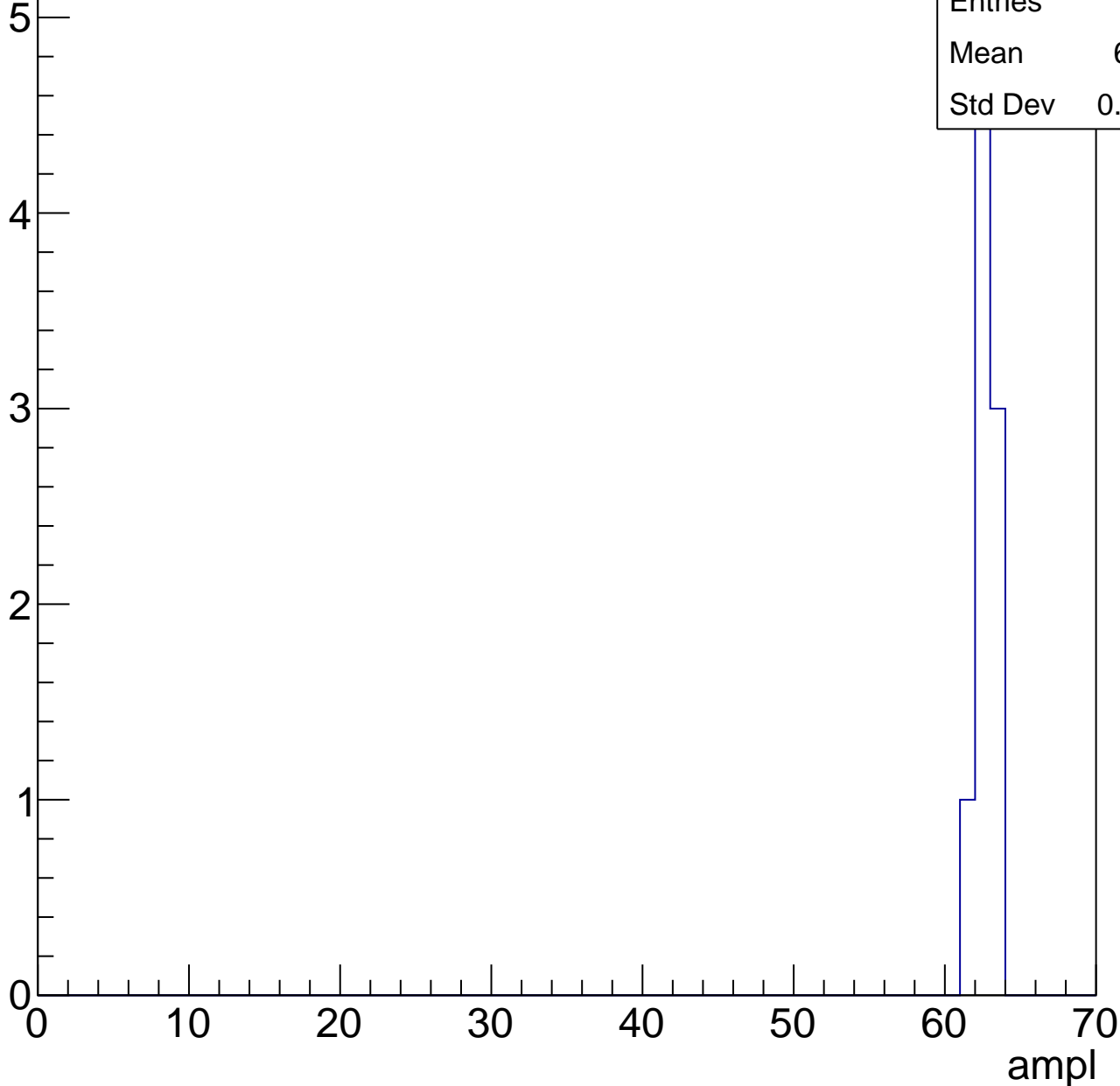


B1L100S, U25-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	62.22
Std Dev	0.6285



B1L100S, U25-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch99, adc0

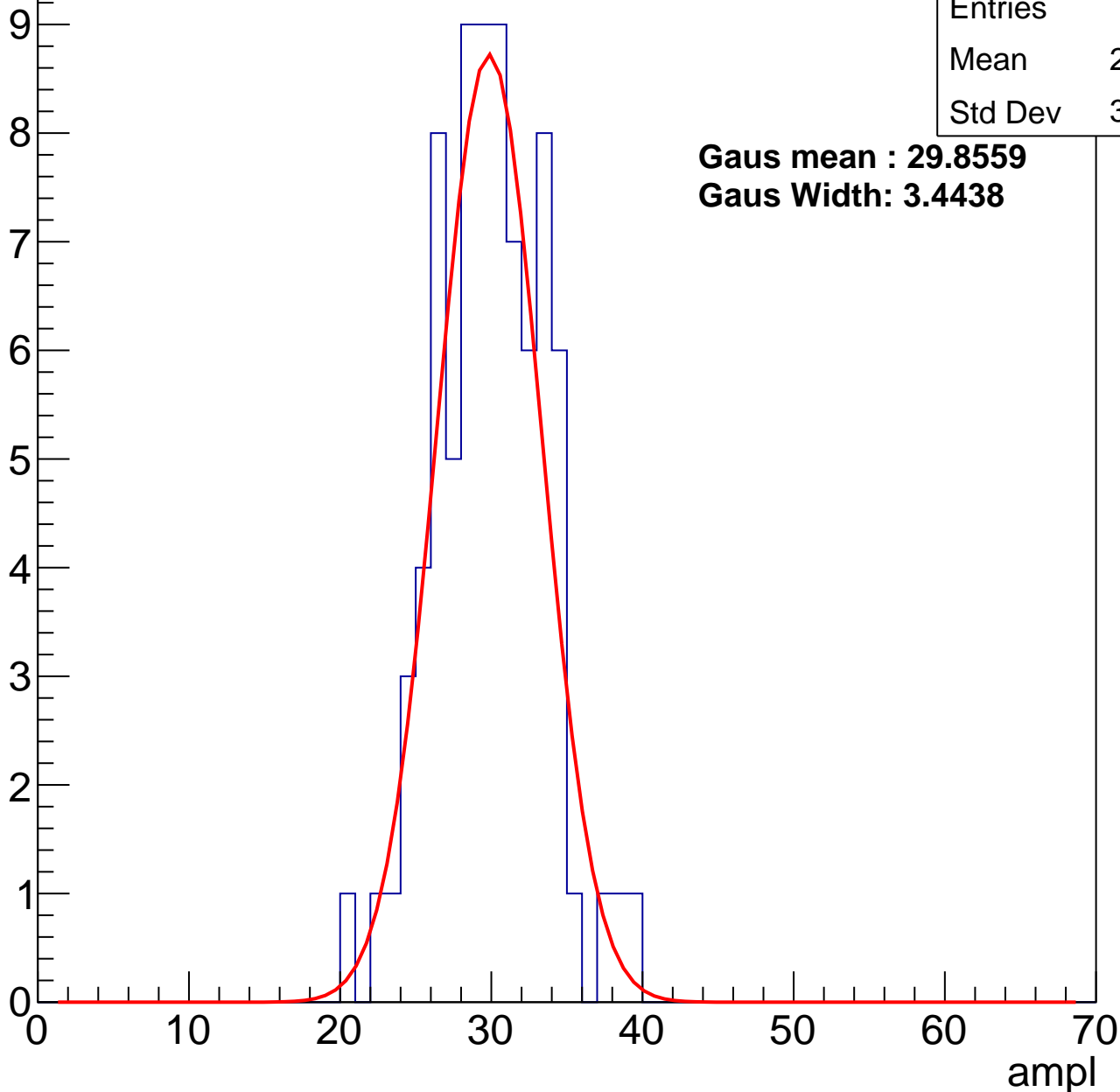
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	29.49
Std Dev	3.577

Gaus mean : 29.8559

Gaus Width: 3.4438



B1L100S, U25-ch99, adc1

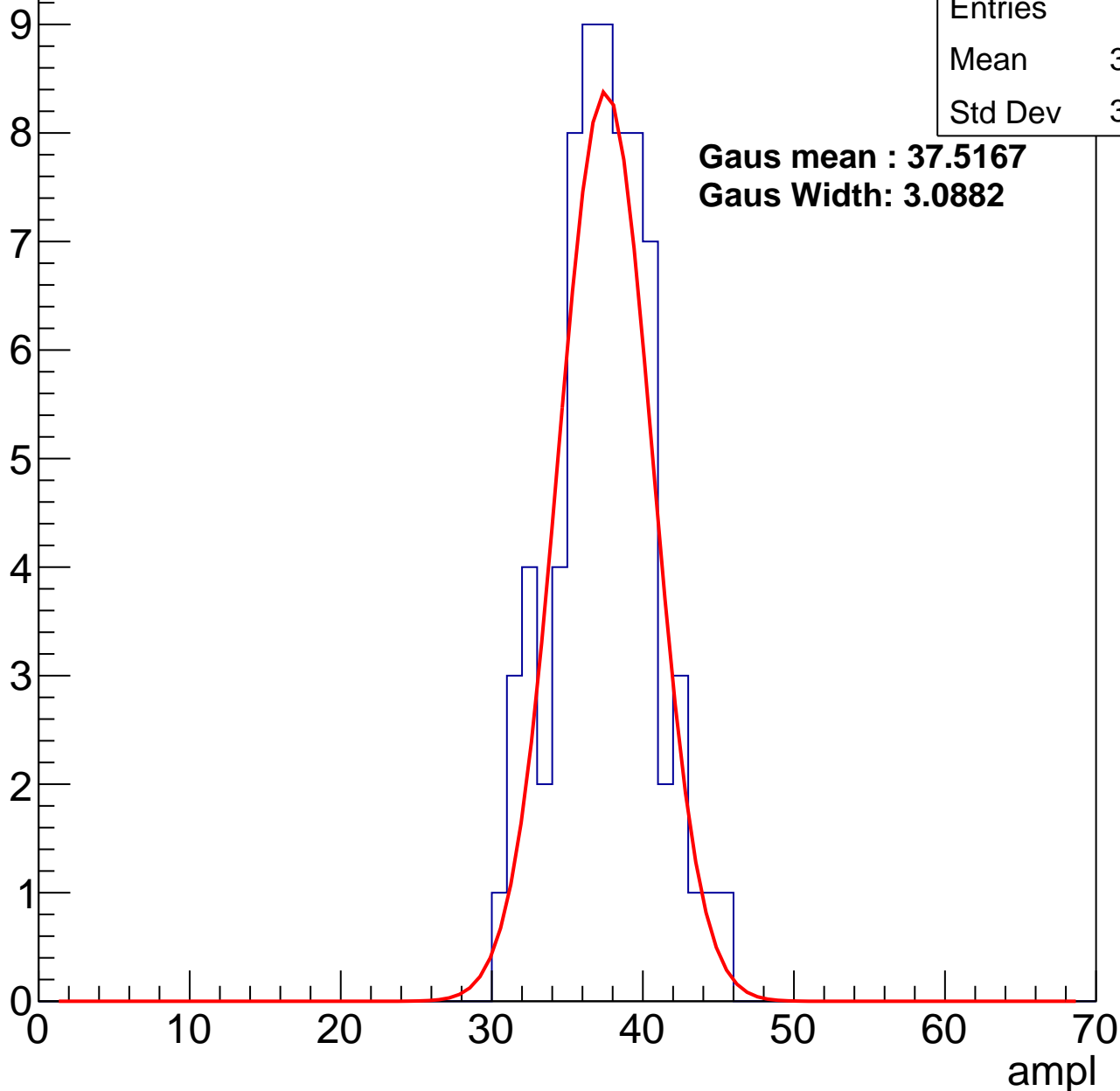
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.99
Std Dev	3.195

Gaus mean : 37.5167

Gaus Width: 3.0882



B1L100S, U25-ch99, adc2

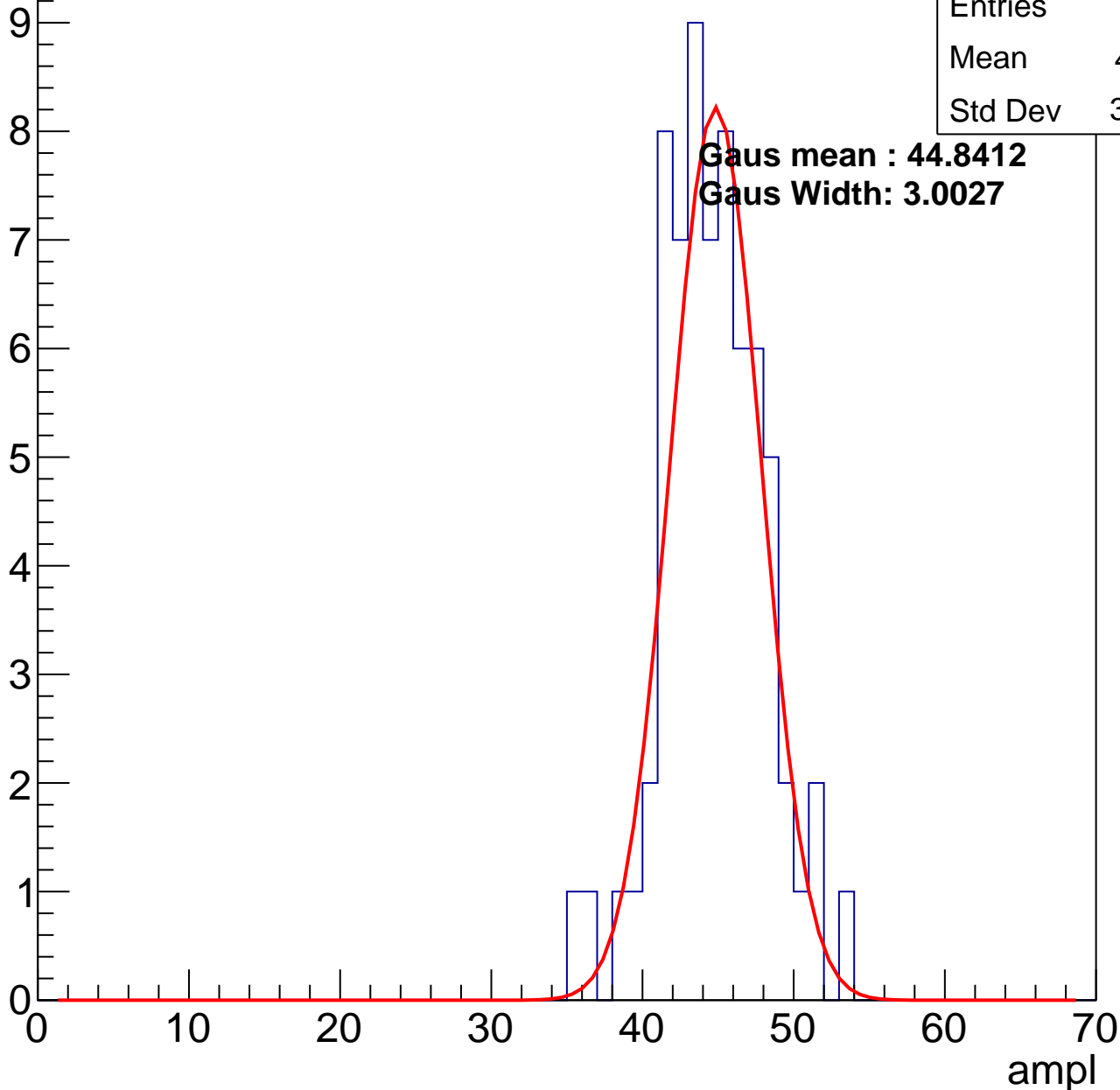
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.21
Std Dev	3.376

Gaus mean : 44.8412

Gaus Width: 3.0027

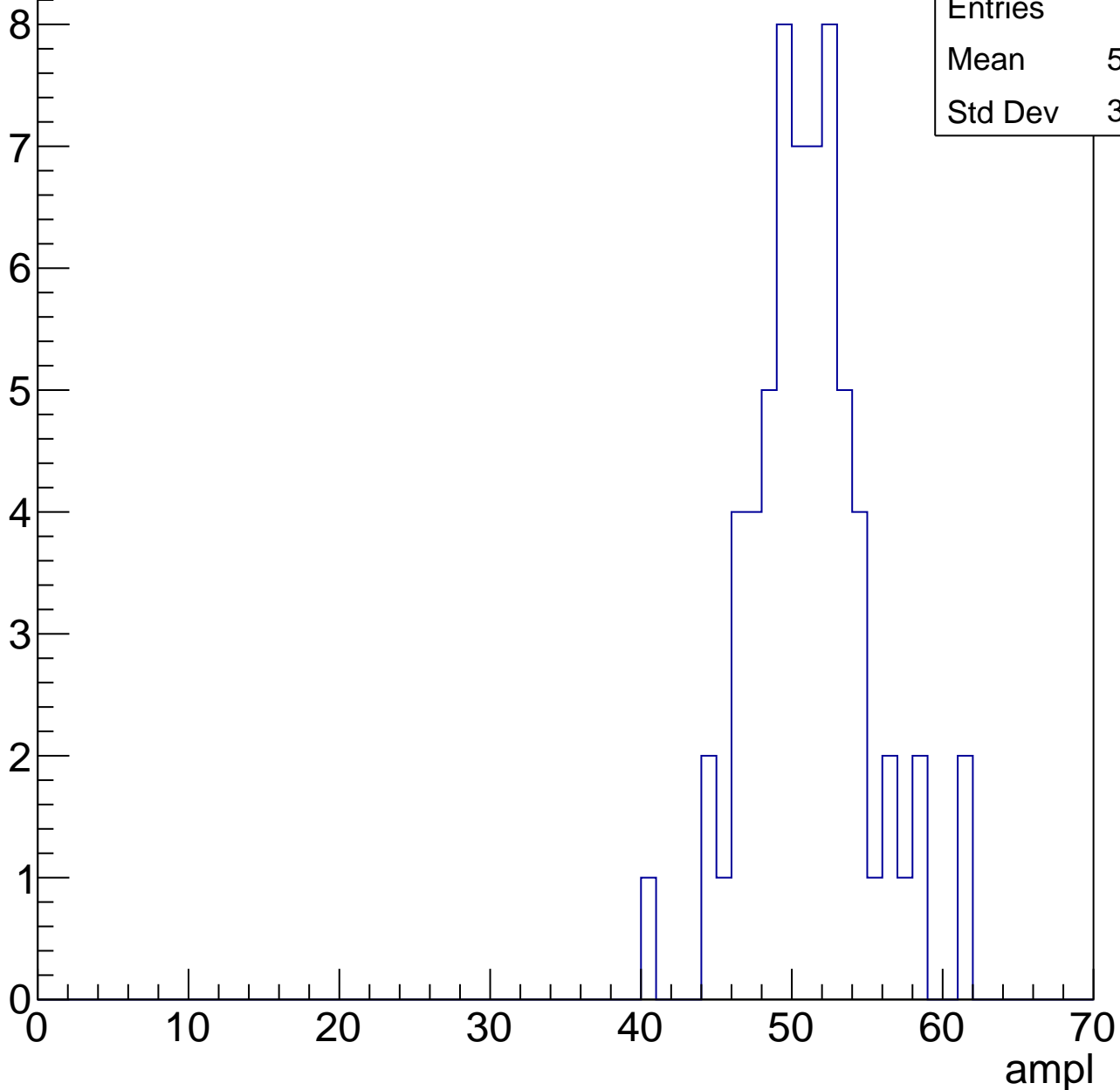


B1L100S, U25-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

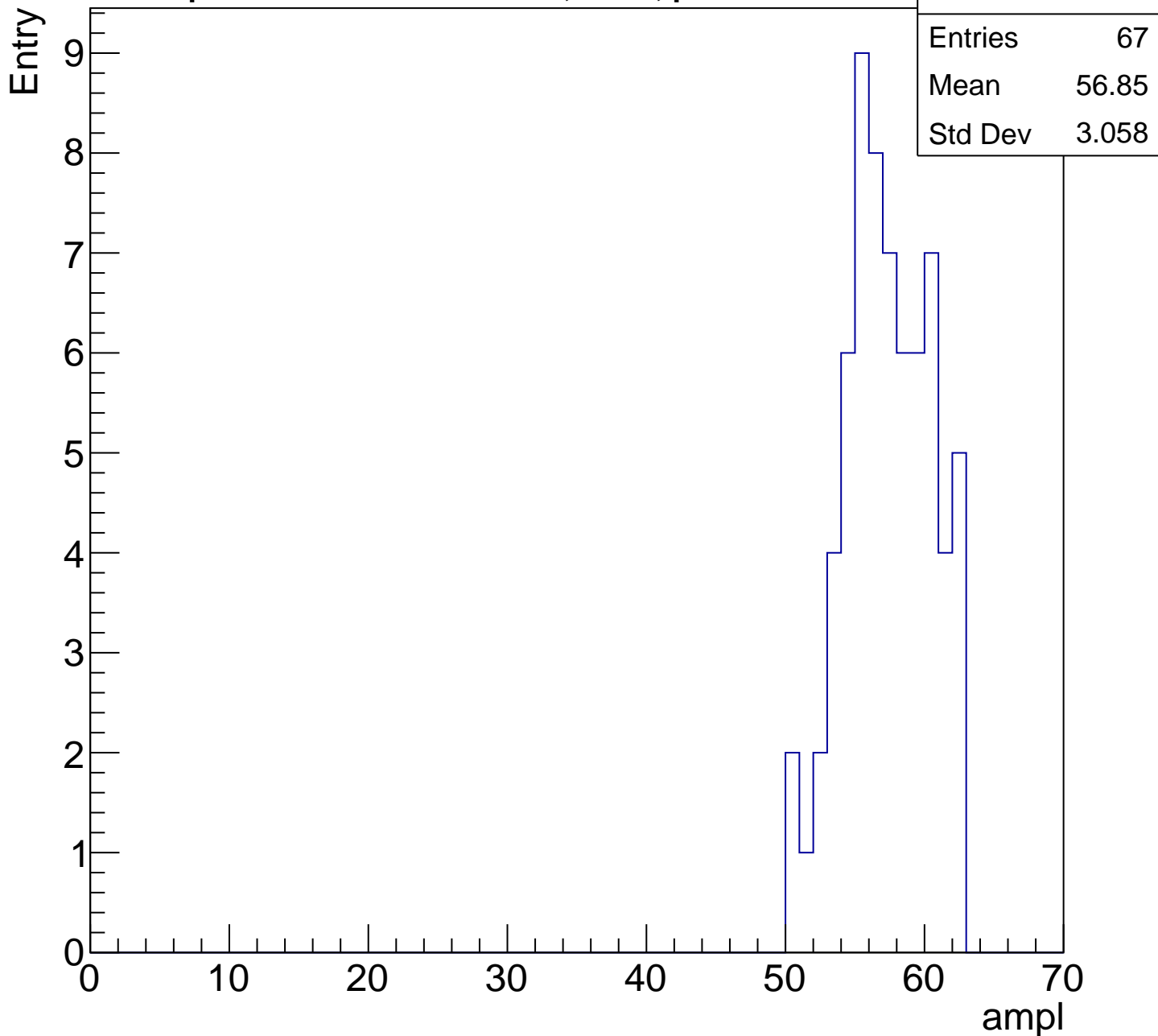
Entry

Entries	64
Mean	50.67
Std Dev	3.873



B1L100S, U25-ch99, adc4

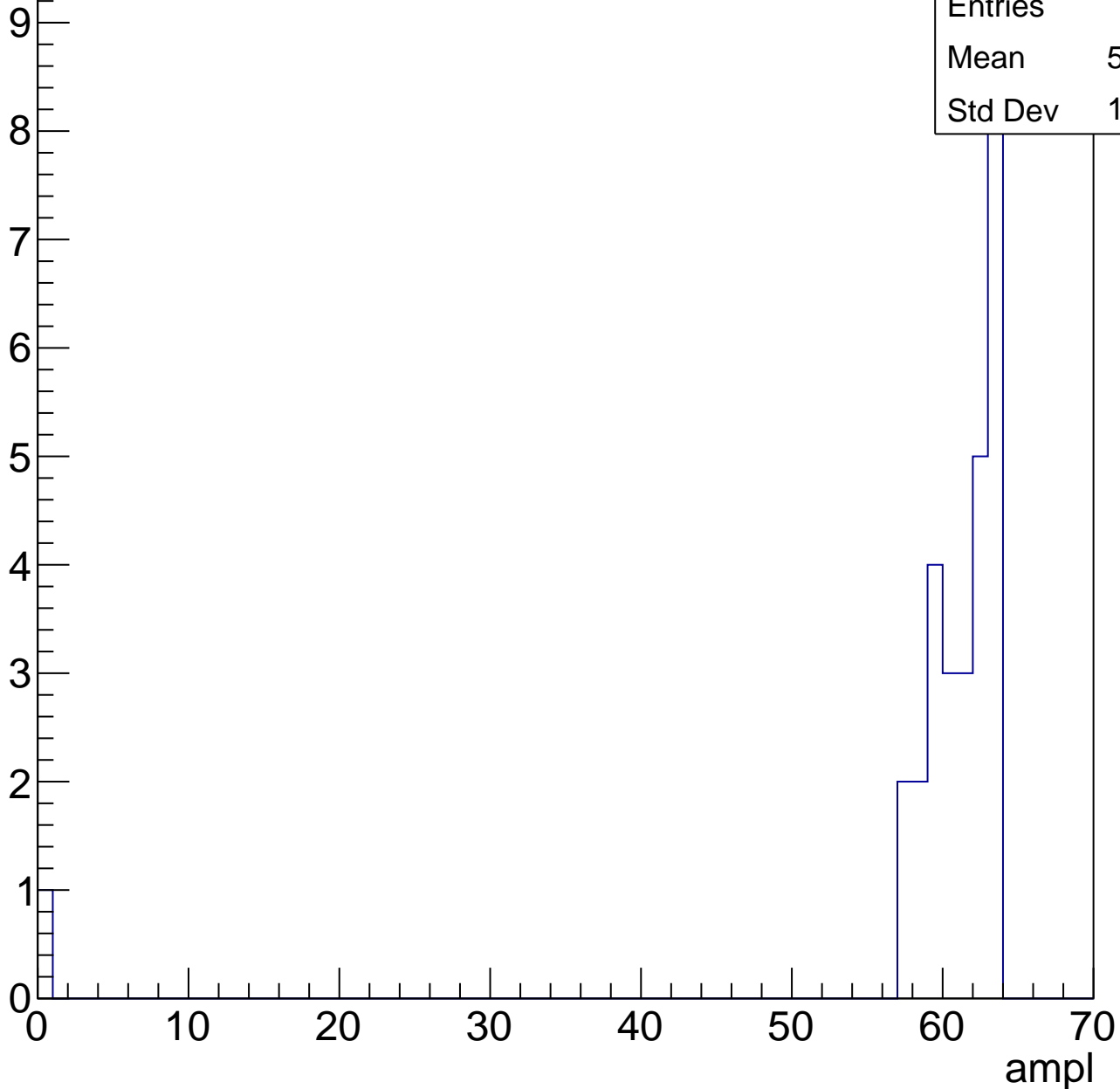
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U25-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch100, adc0

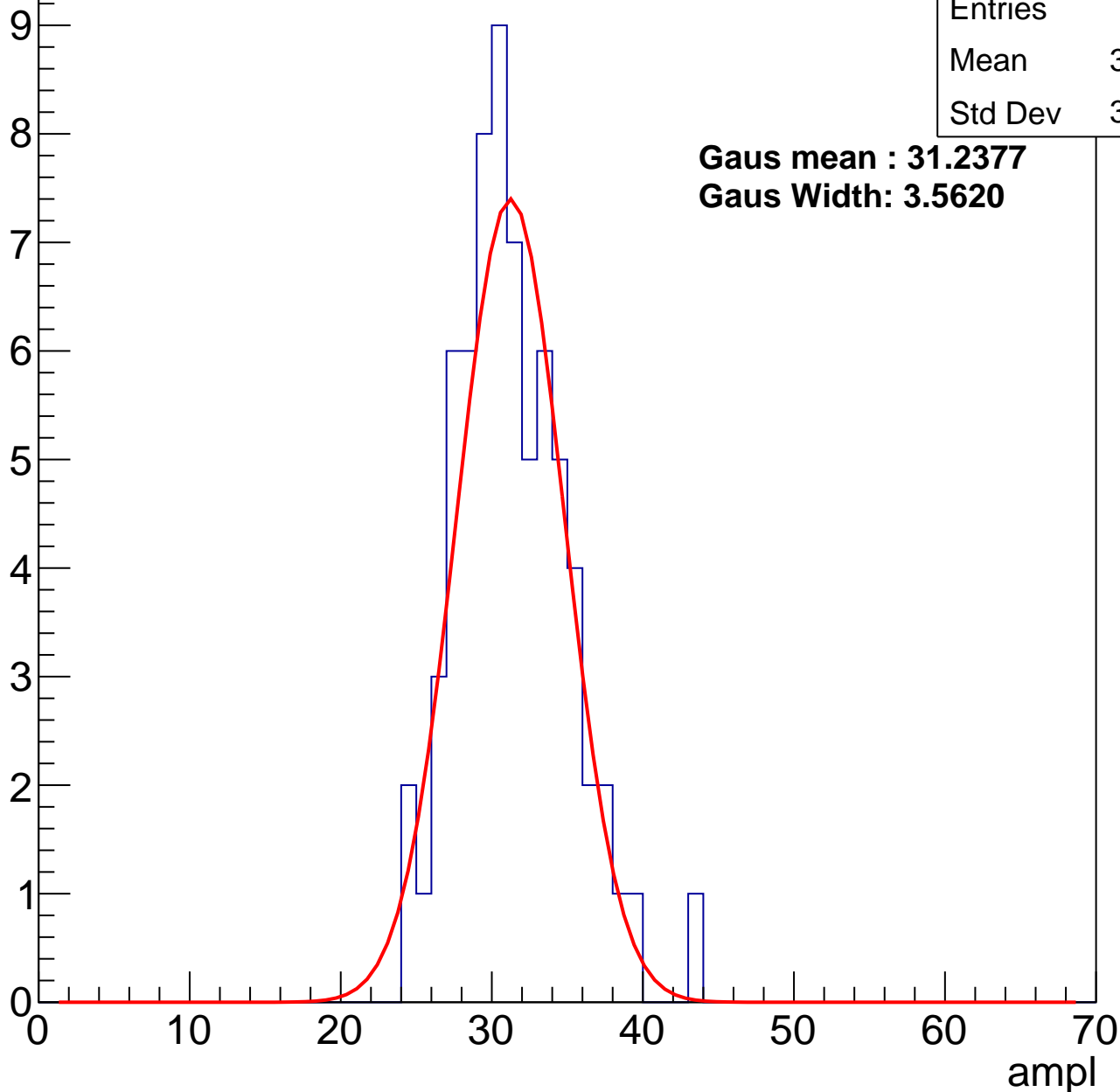
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.93
Std Dev	3.645

Gaus mean : 31.2377

Gaus Width: 3.5620



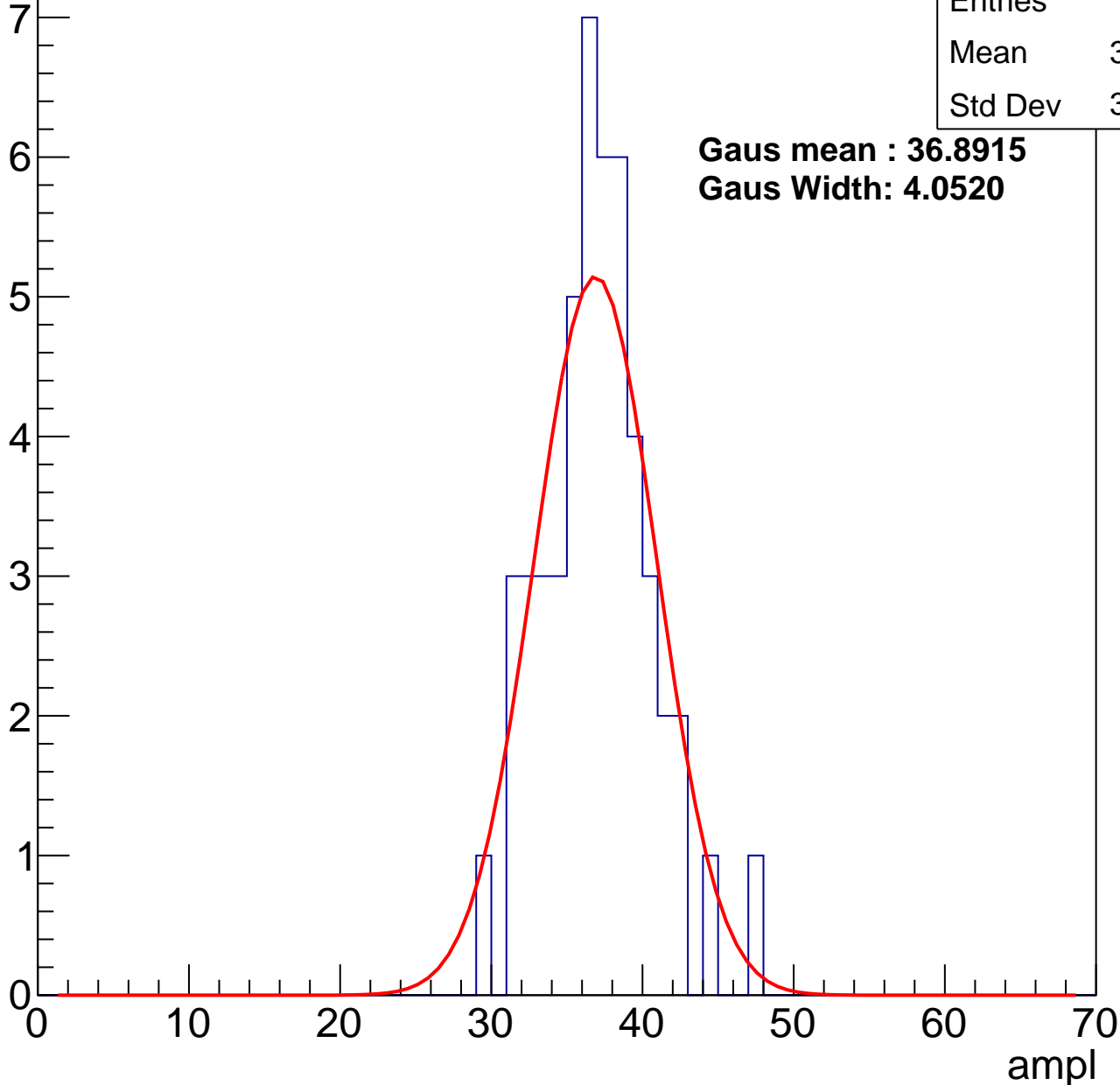
B1L100S, U25-ch100, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	36.58
Std Dev	3.522

Gaus mean : 36.8915
Gaus Width: 4.0520



B1L100S, U25-ch100, adc2

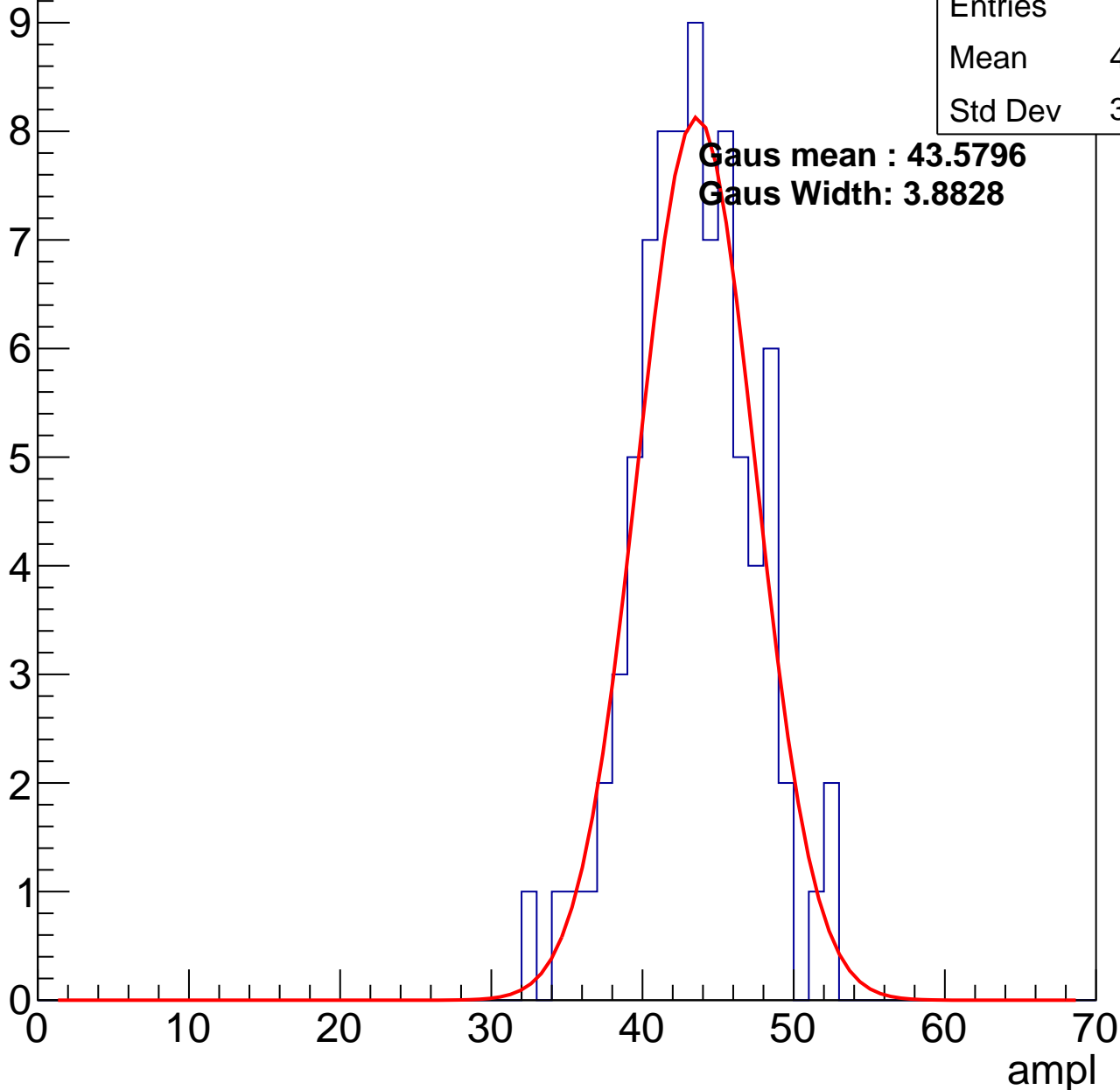
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	42.94
Std Dev	3.914

Gaus mean : 43.5796

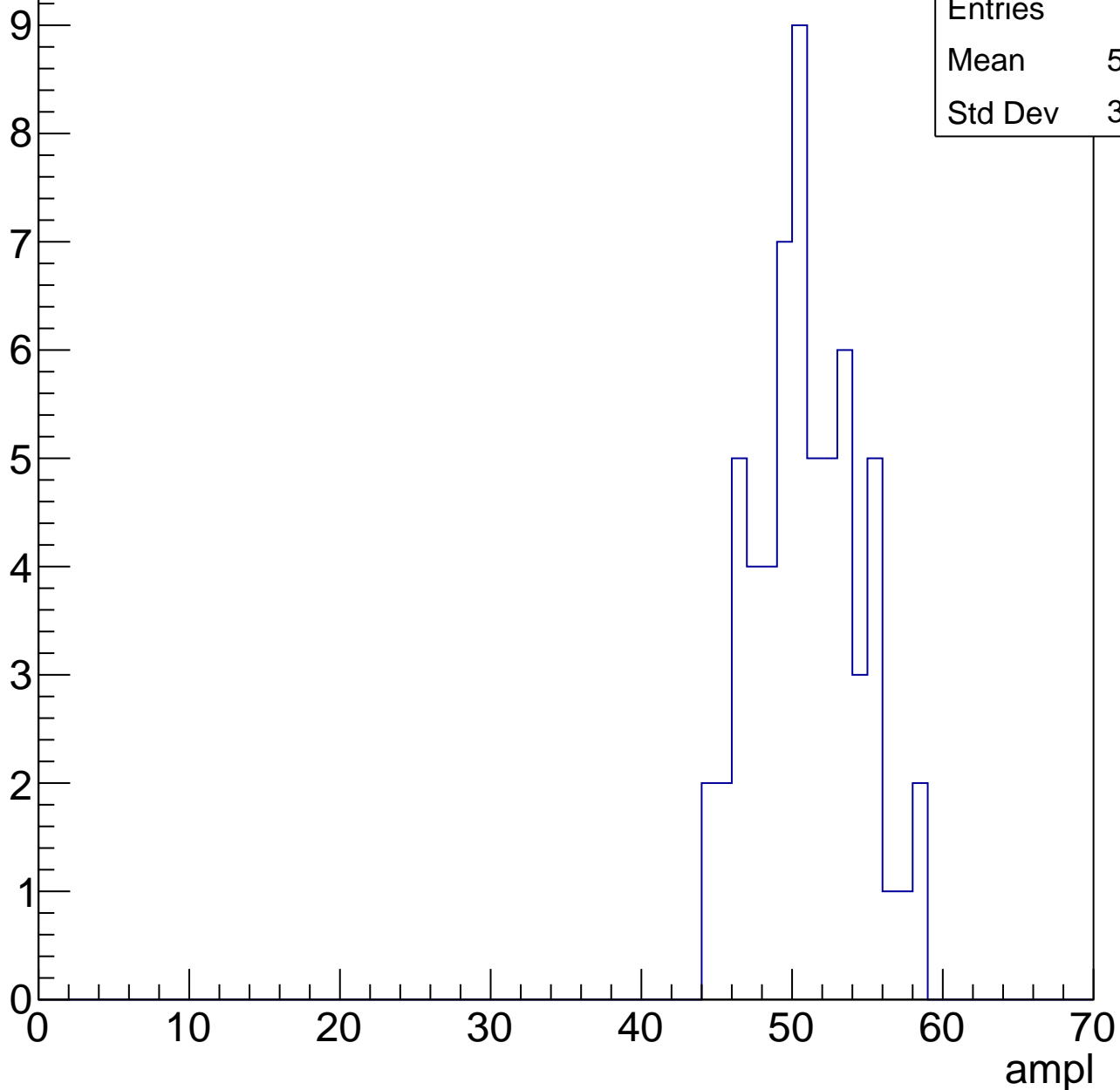
Gaus Width: 3.8828



B1L100S, U25-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

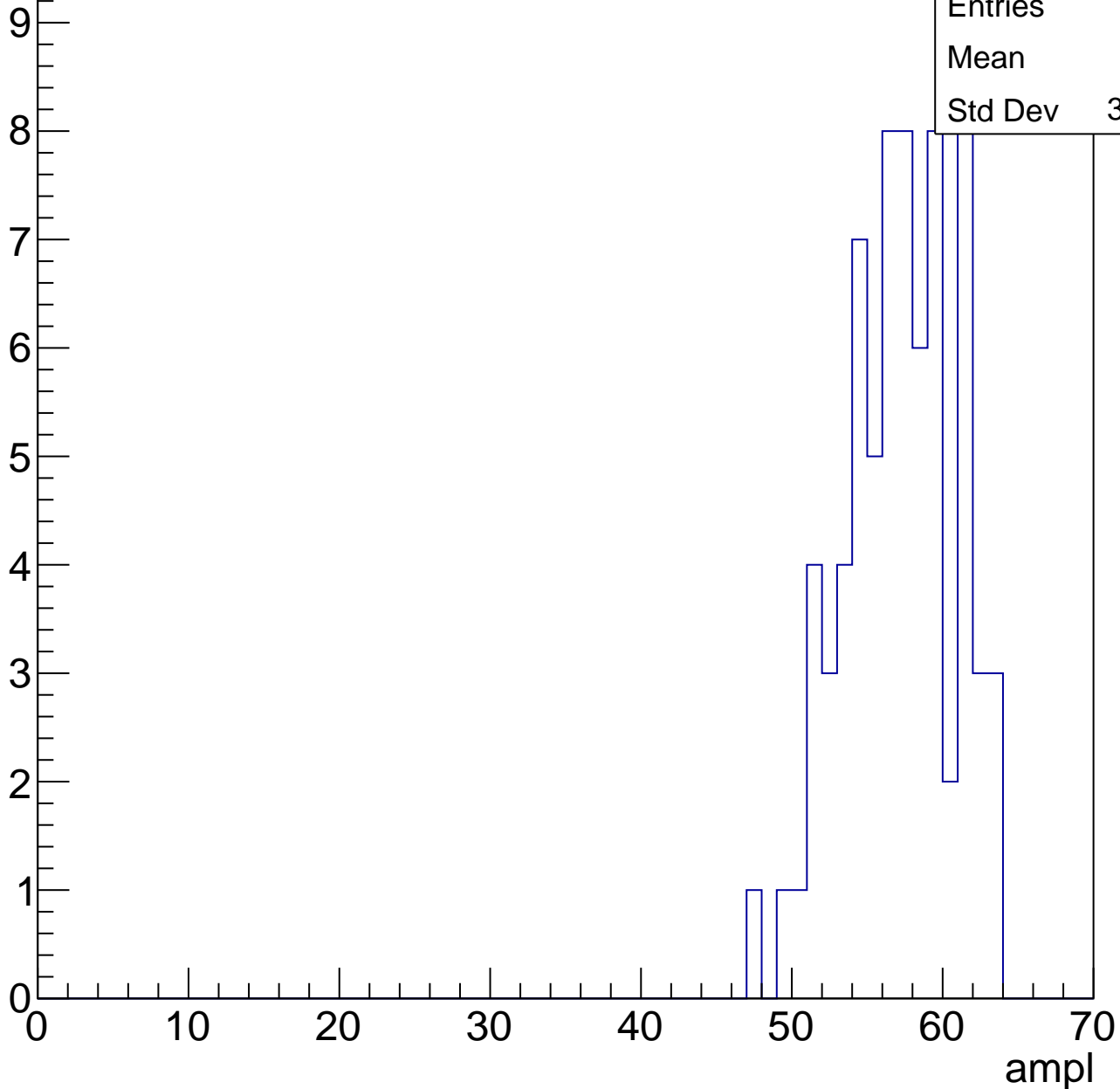


B1L100S, U25-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	56.7
Std Dev	3.633

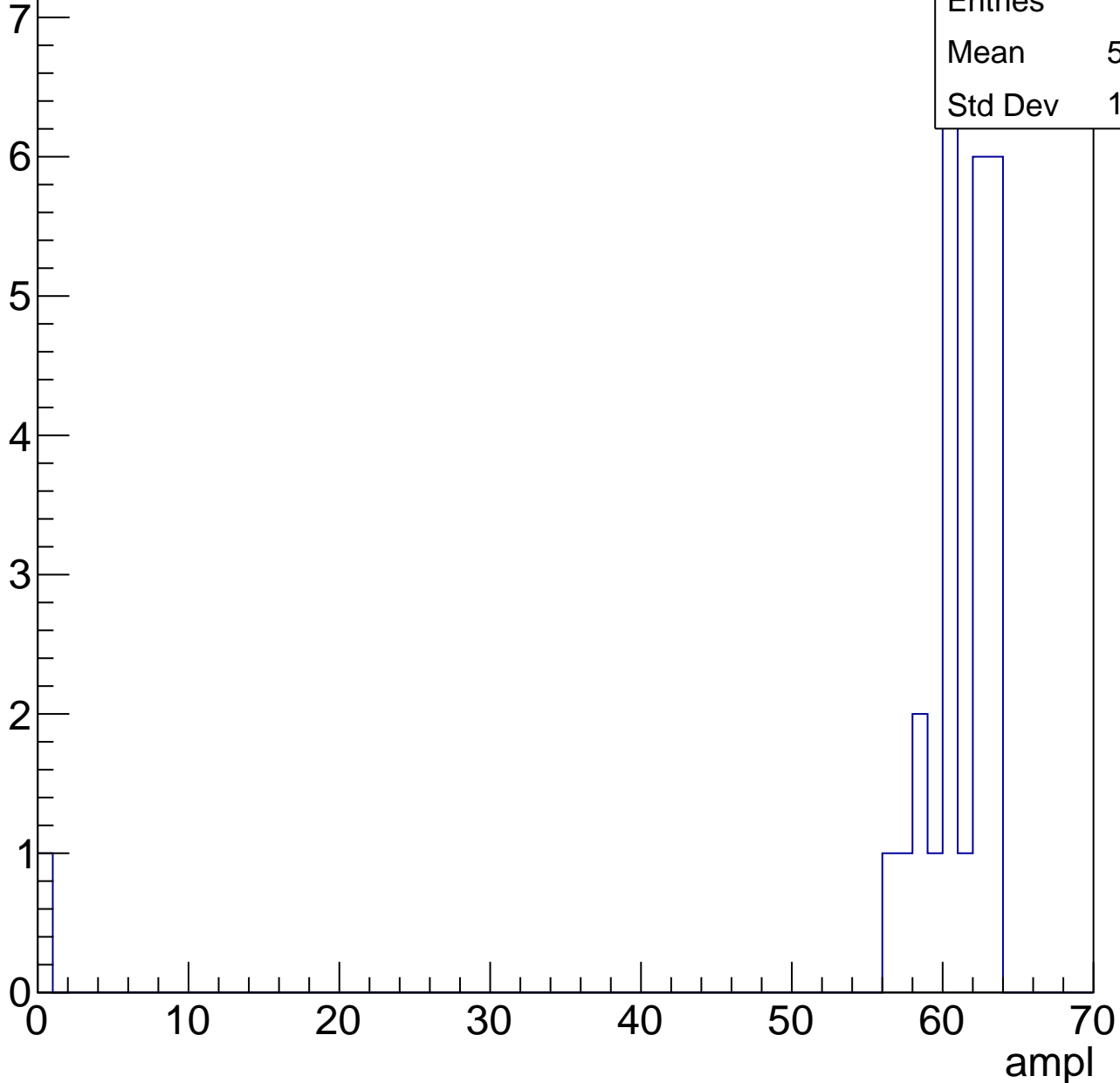


B1L100S, U25-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

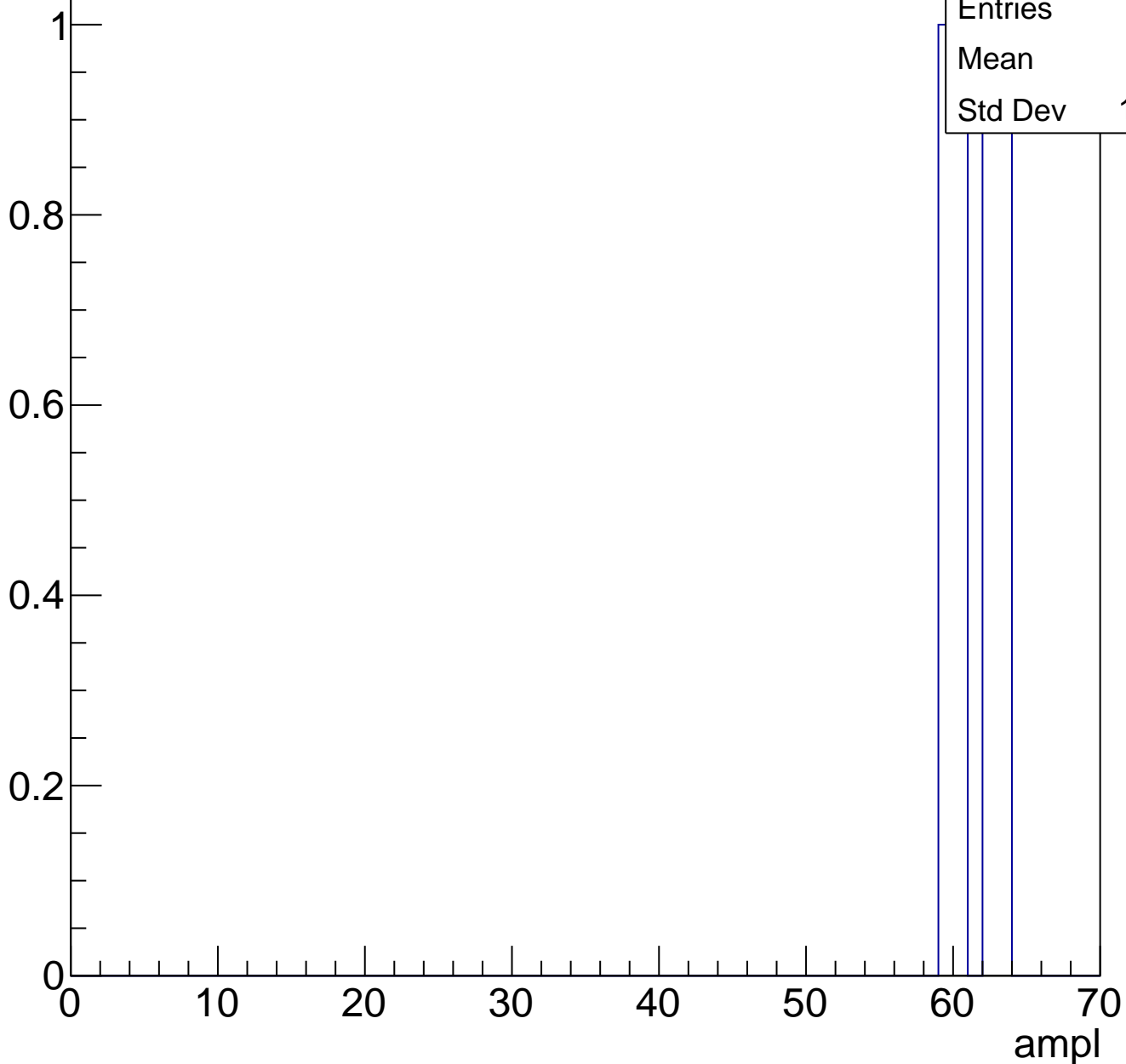
Entries	26
Mean	58.42
Std Dev	11.85



B1L100S, U25-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch101, adc0

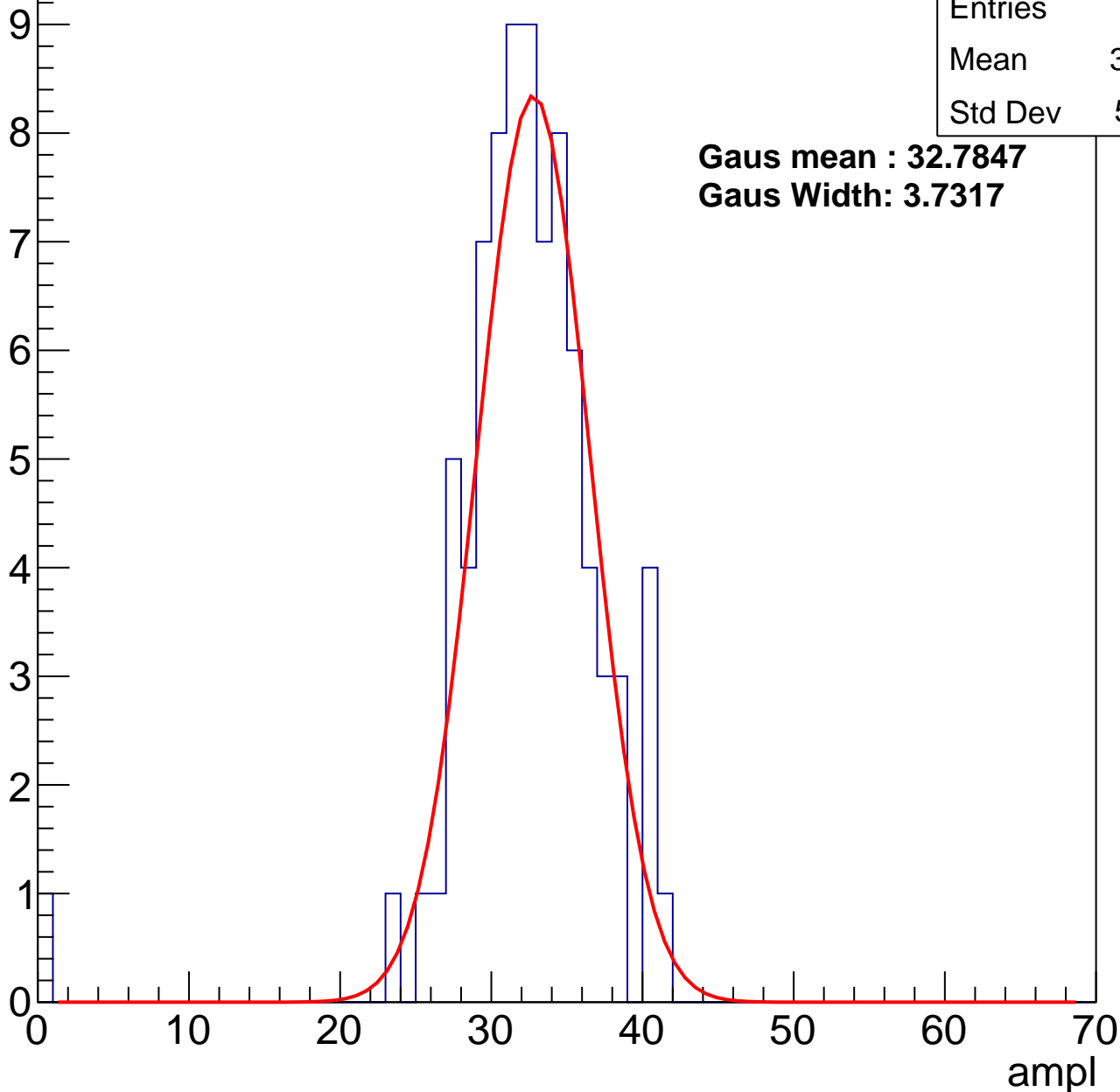
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	31.88
Std Dev	5.131

Gaus mean : 32.7847

Gaus Width: 3.7317



B1L100S, U25-ch101, adc1

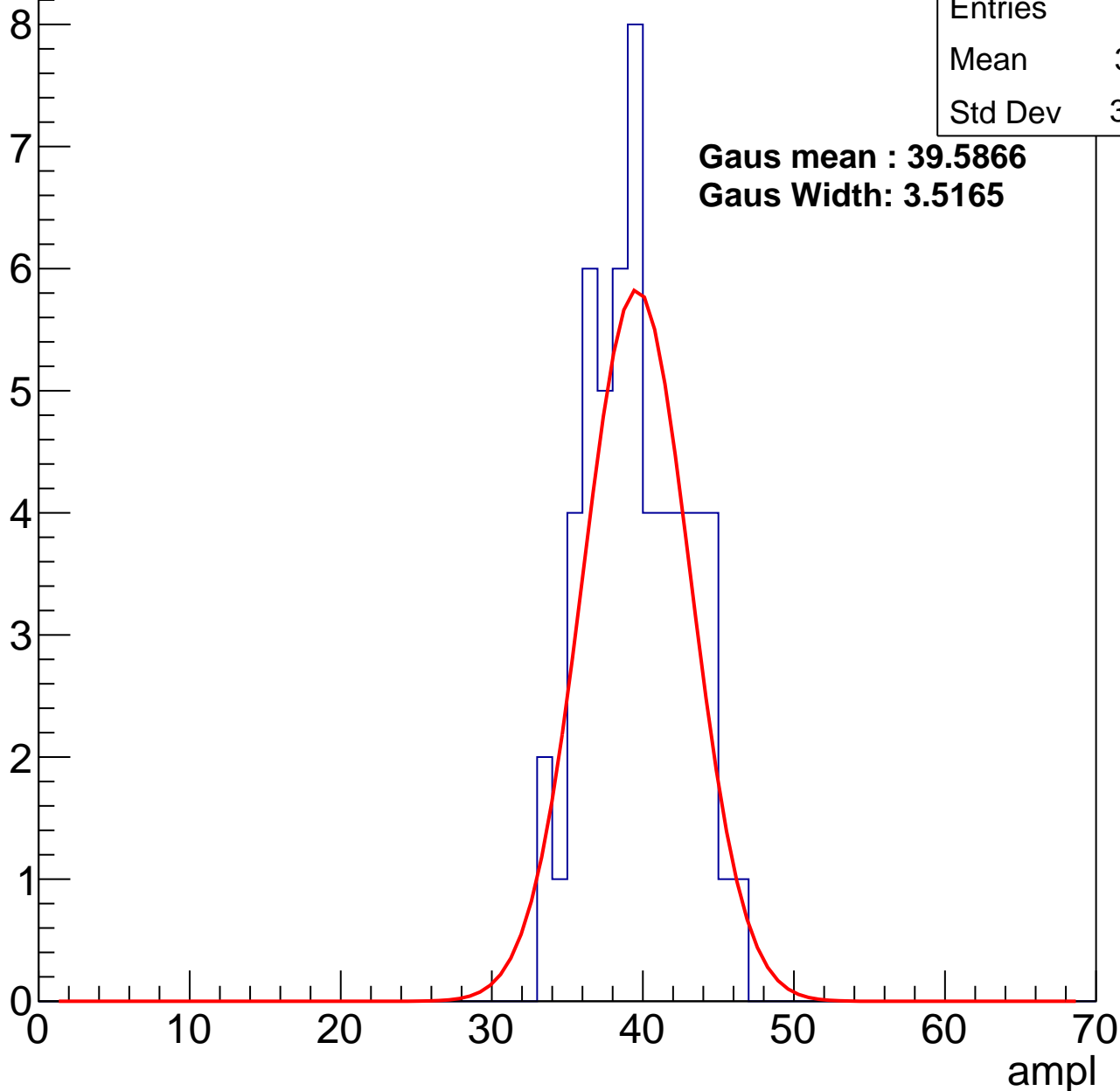
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	39.11
Std Dev	3.178

Gaus mean : 39.5866

Gaus Width: 3.5165



B1L100S, U25-ch101, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	45.45
Std Dev	3.538

Gaus mean : 46.1167

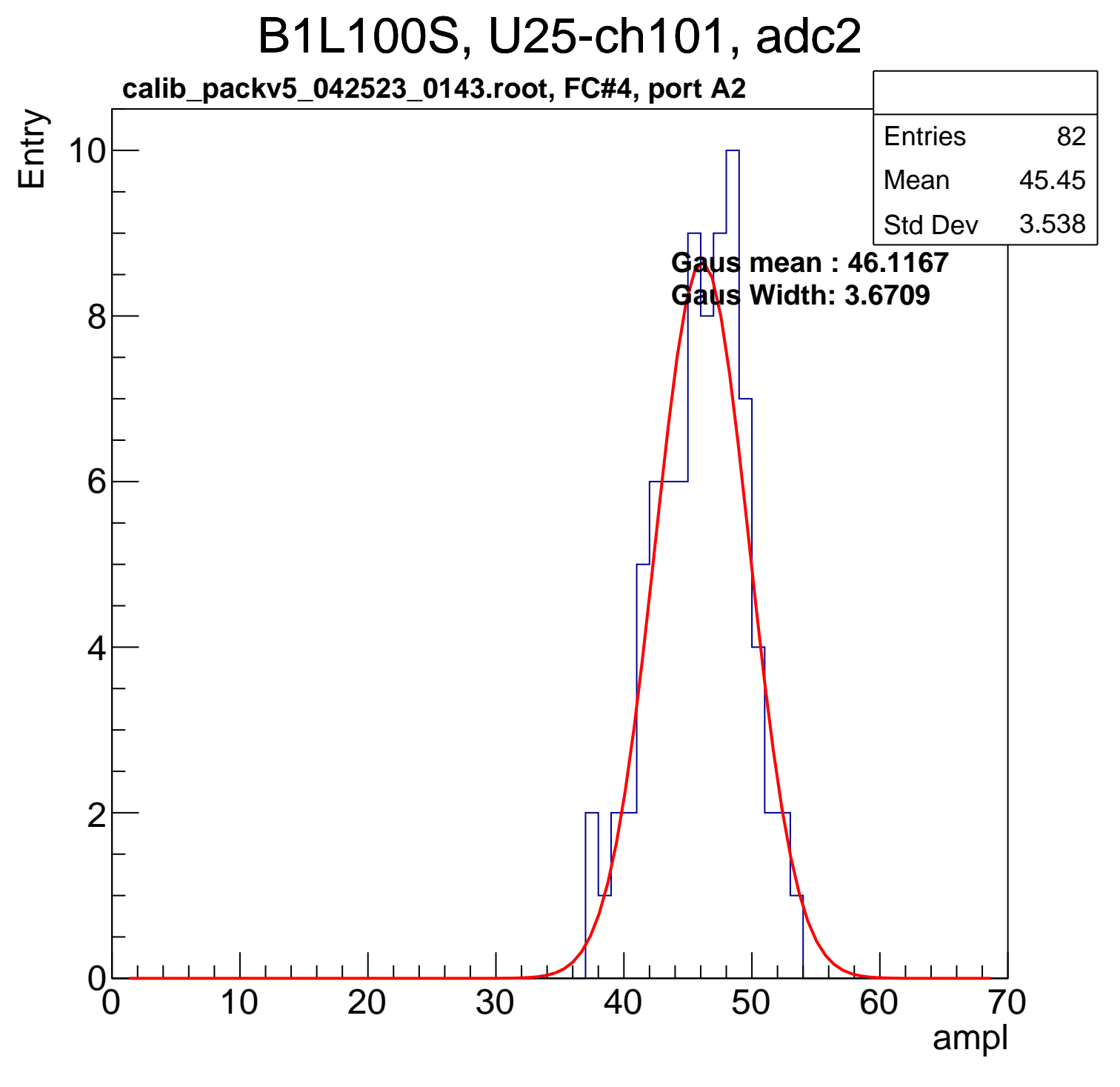
Gaus Width: 3.6709

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

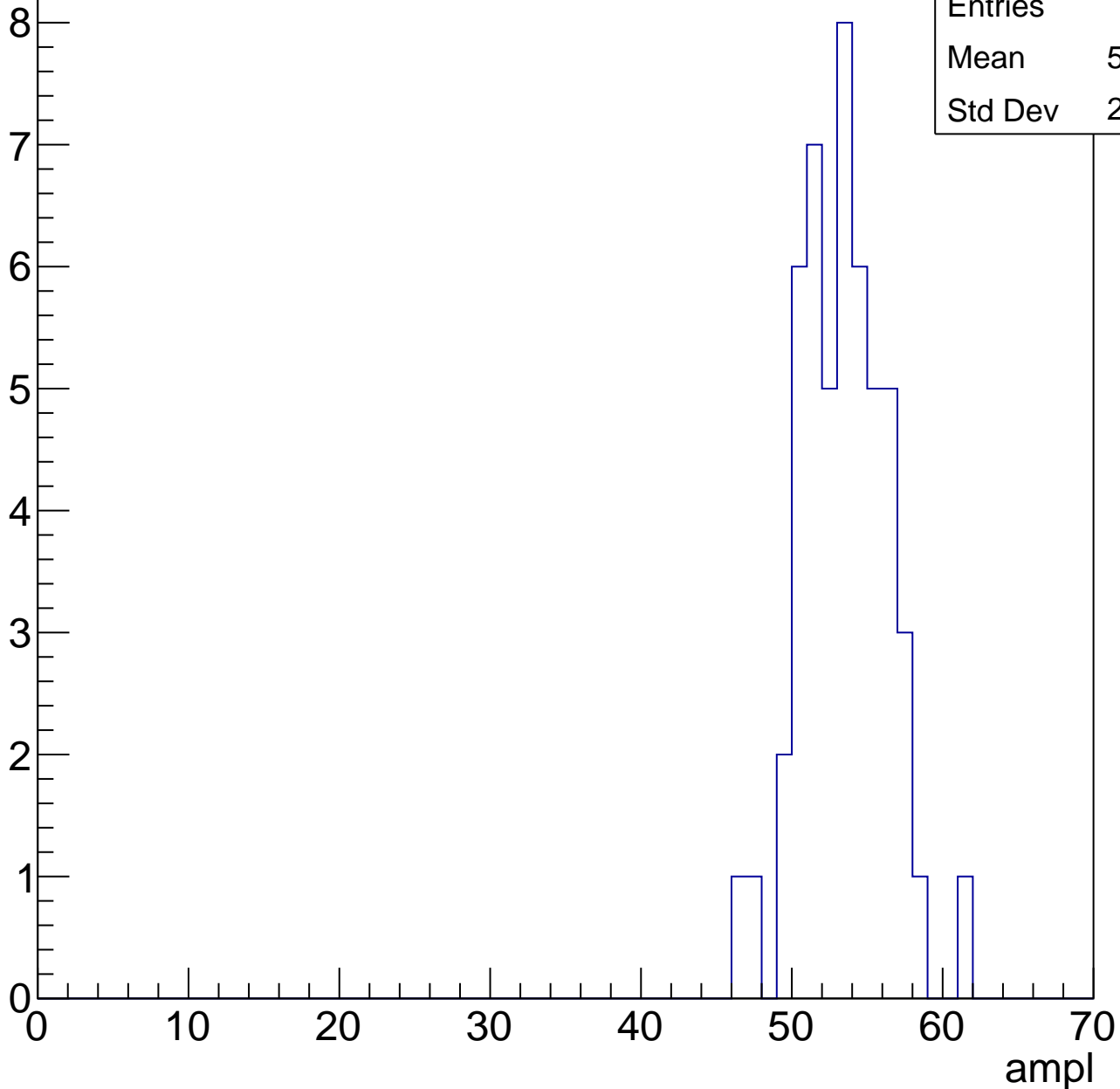


B1L100S, U25-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	52.96
Std Dev	2.842

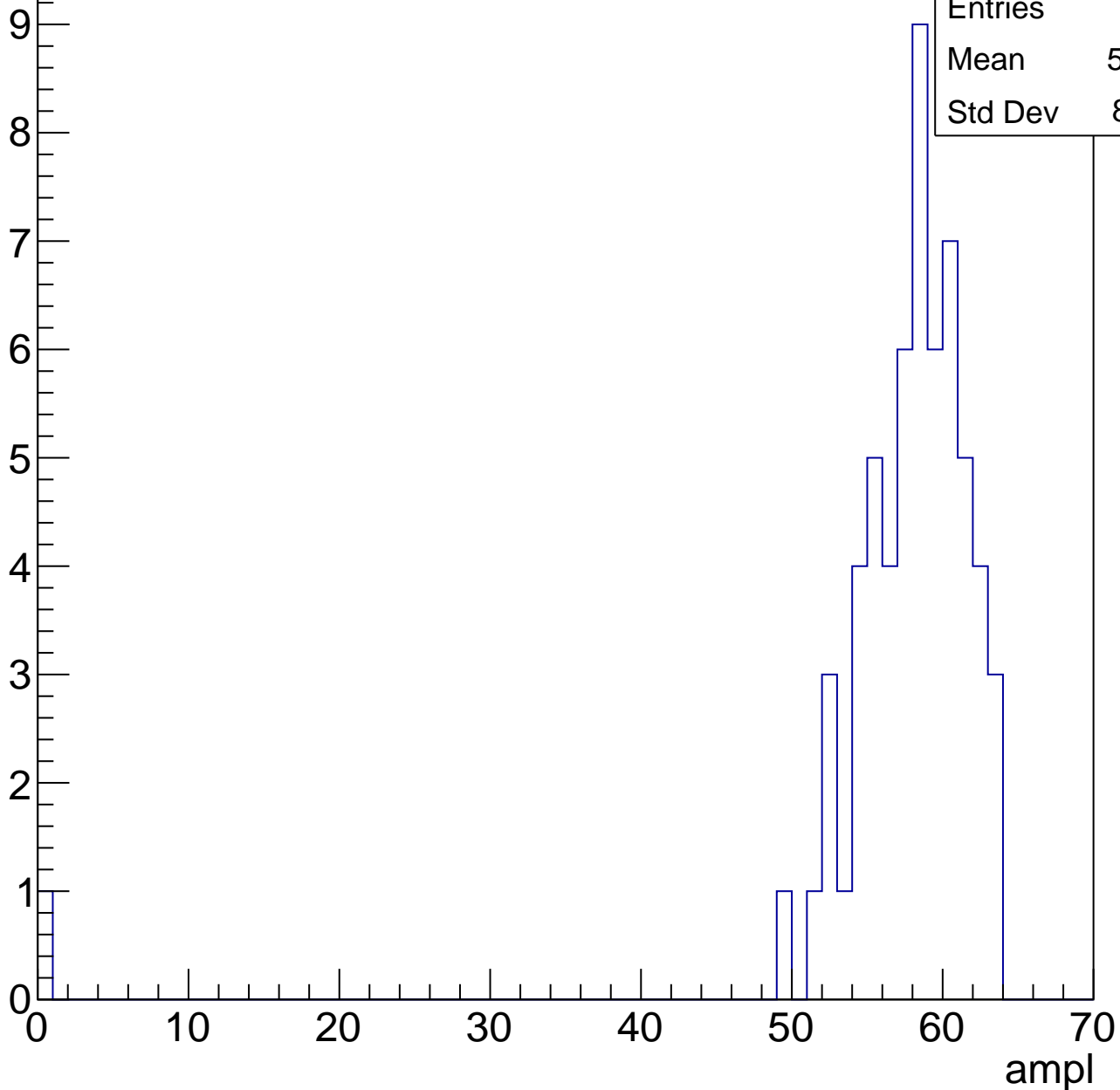


B1L100S, U25-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.73
Std Dev	8.041

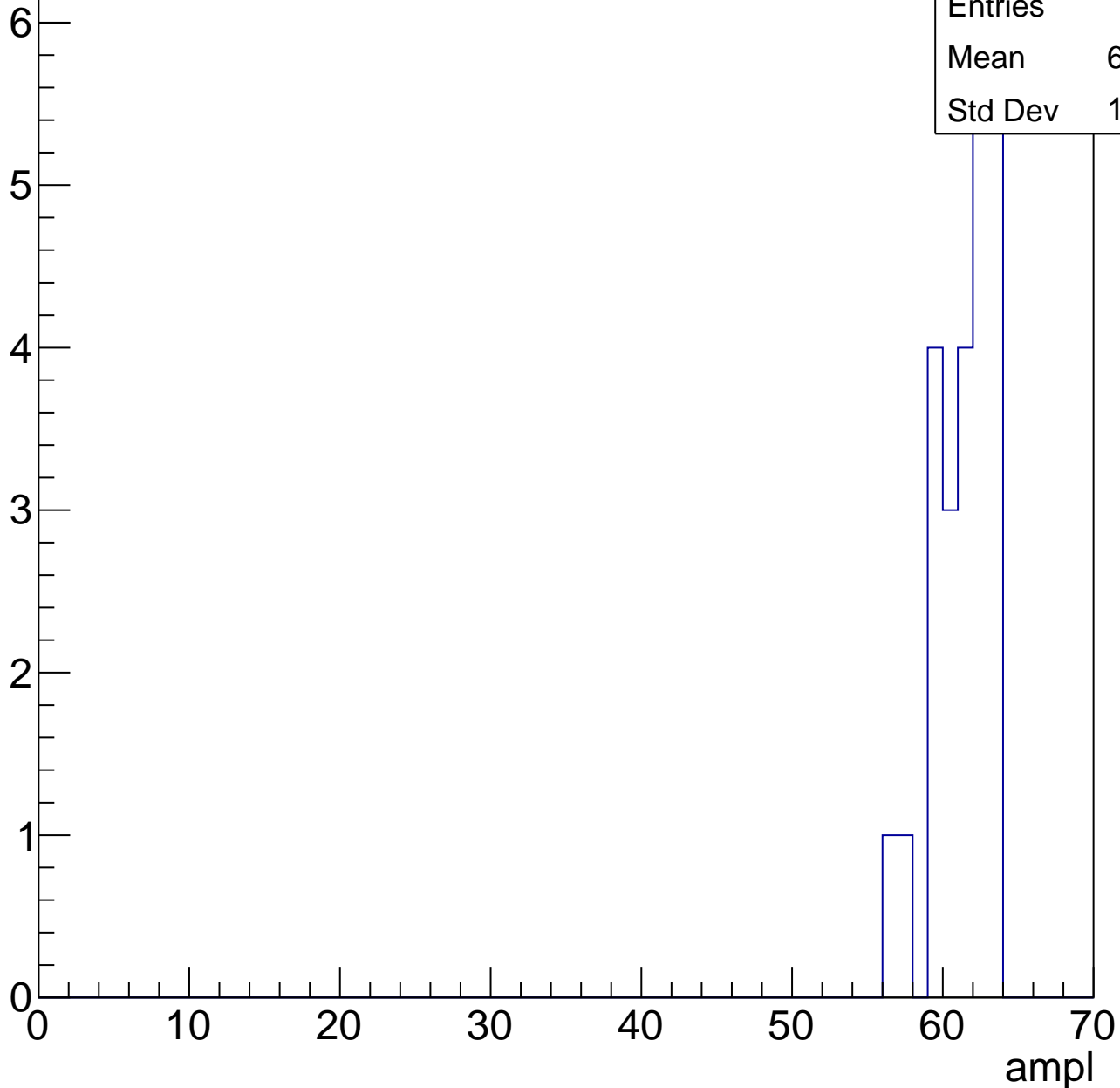


B1L100S, U25-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	60.92
Std Dev	1.896



B1L100S, U25-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch102, adc0

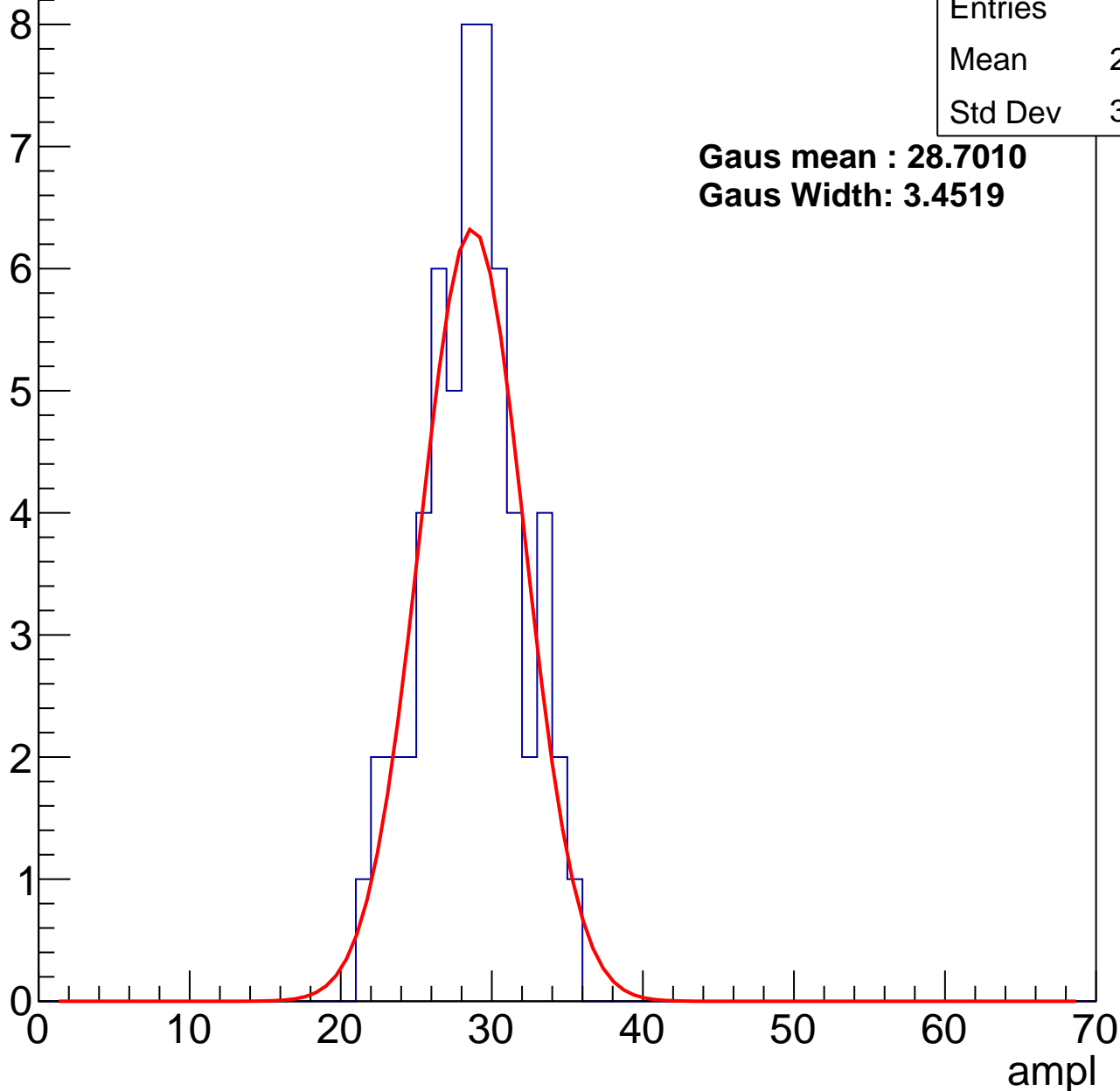
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	28.23
Std Dev	3.206

Gaus mean : 28.7010

Gaus Width: 3.4519



B1L100S, U25-ch102, adc1

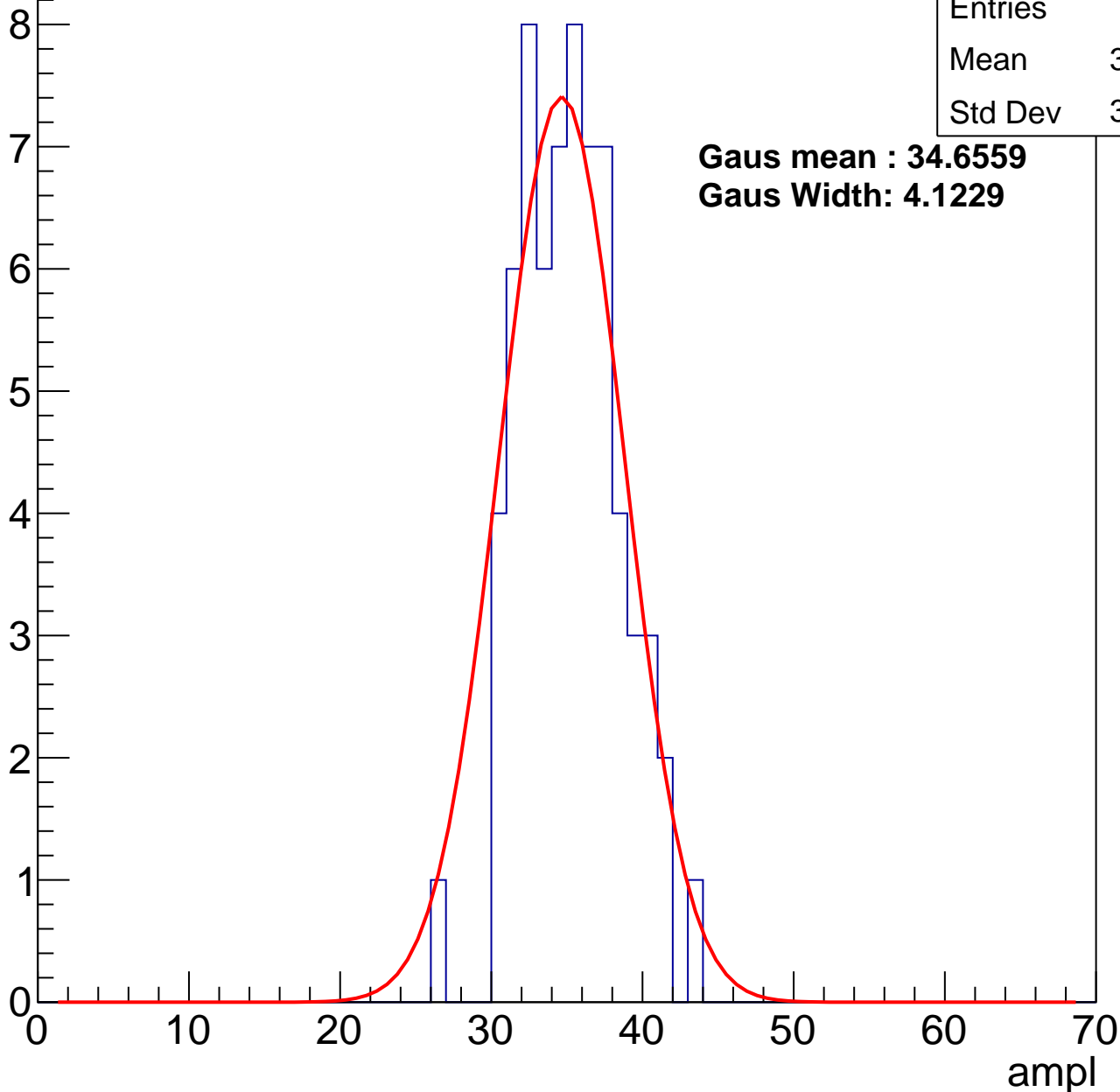
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	34.76
Std Dev	3.247

Gaus mean : 34.6559

Gaus Width: 4.1229



B1L100S, U25-ch102, adc2

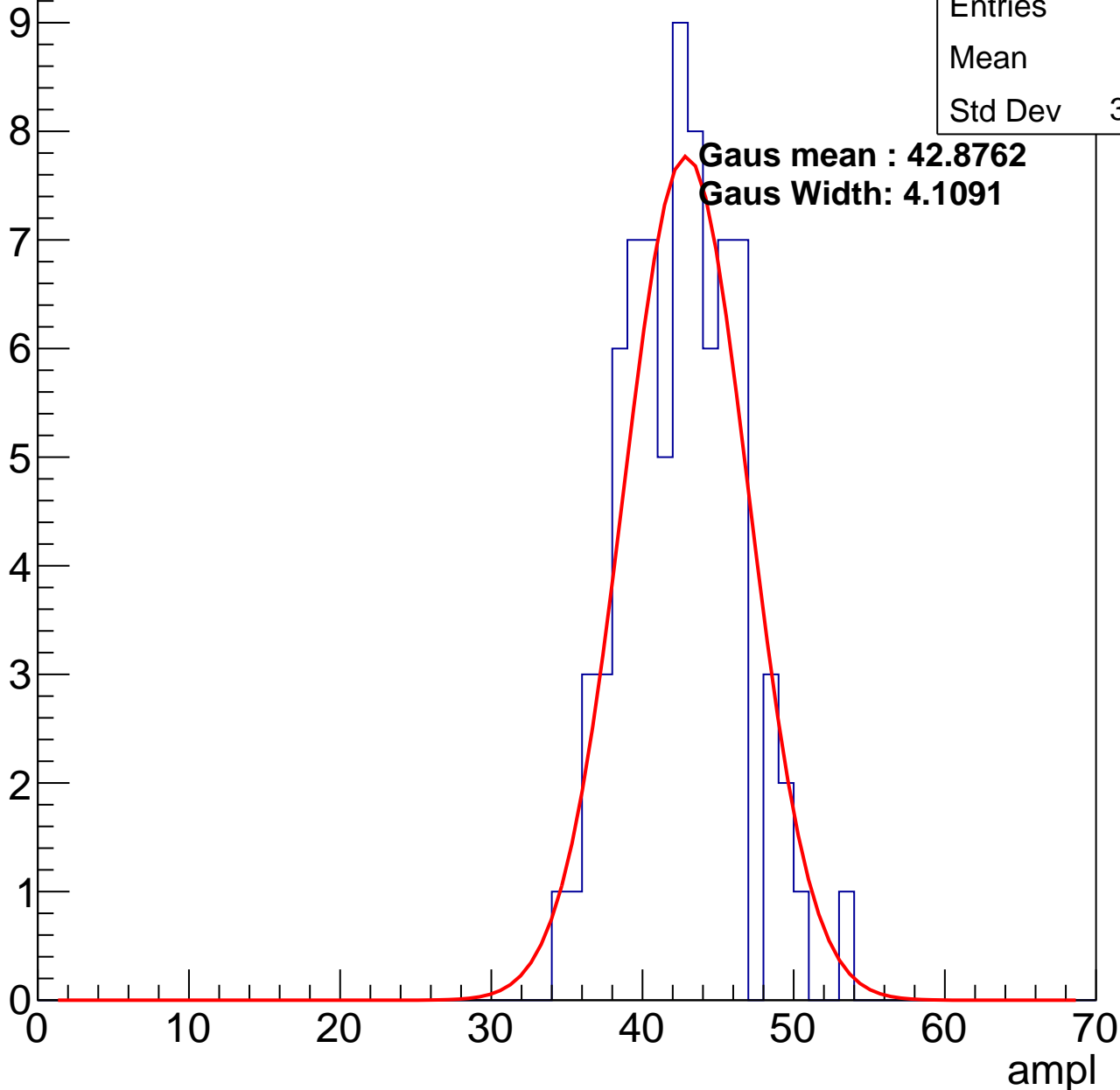
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.1
Std Dev	3.754

Gaus mean : 42.8762

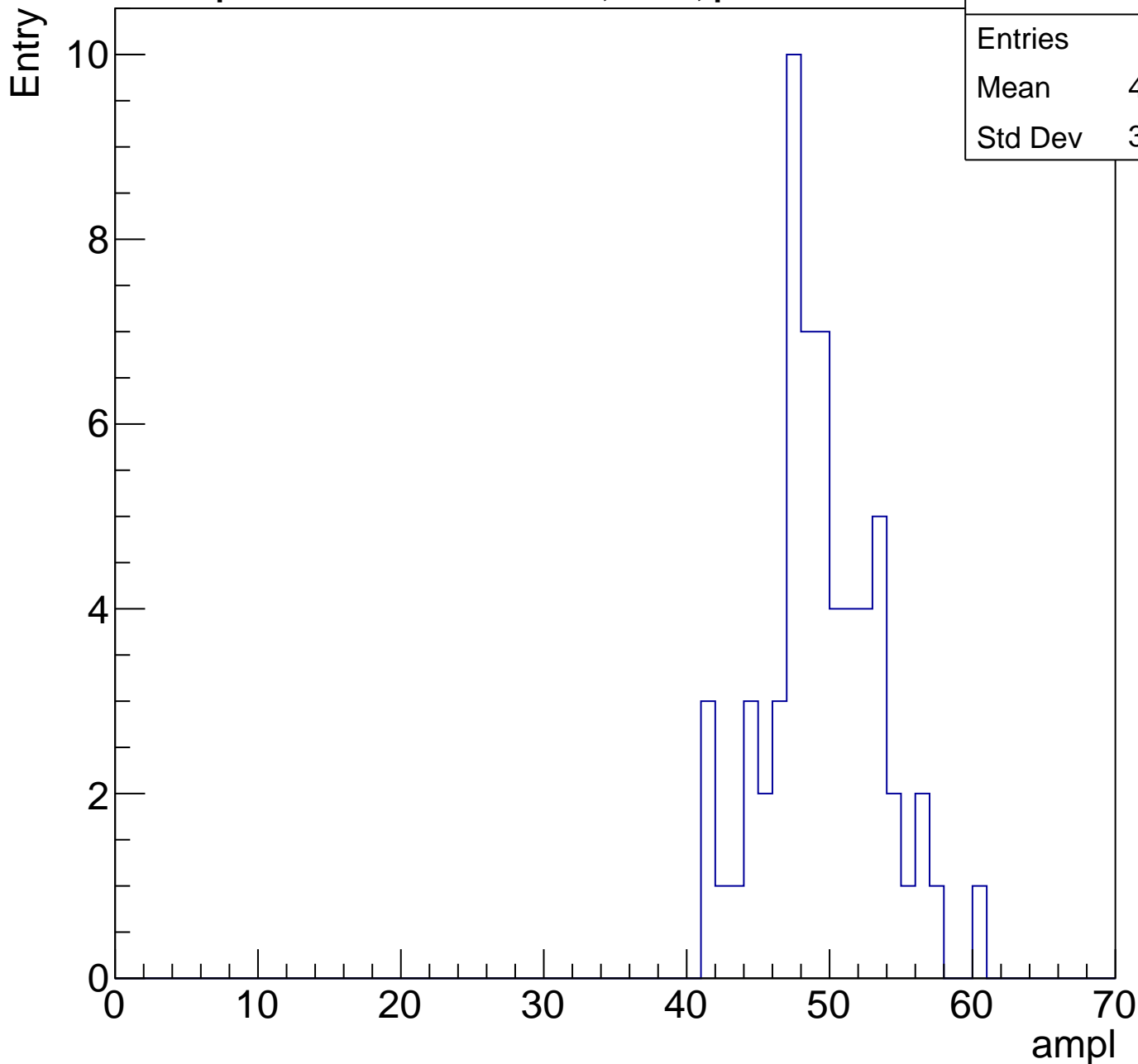
Gaus Width: 4.1091



B1L100S, U25-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	48.95
Std Dev	3.973

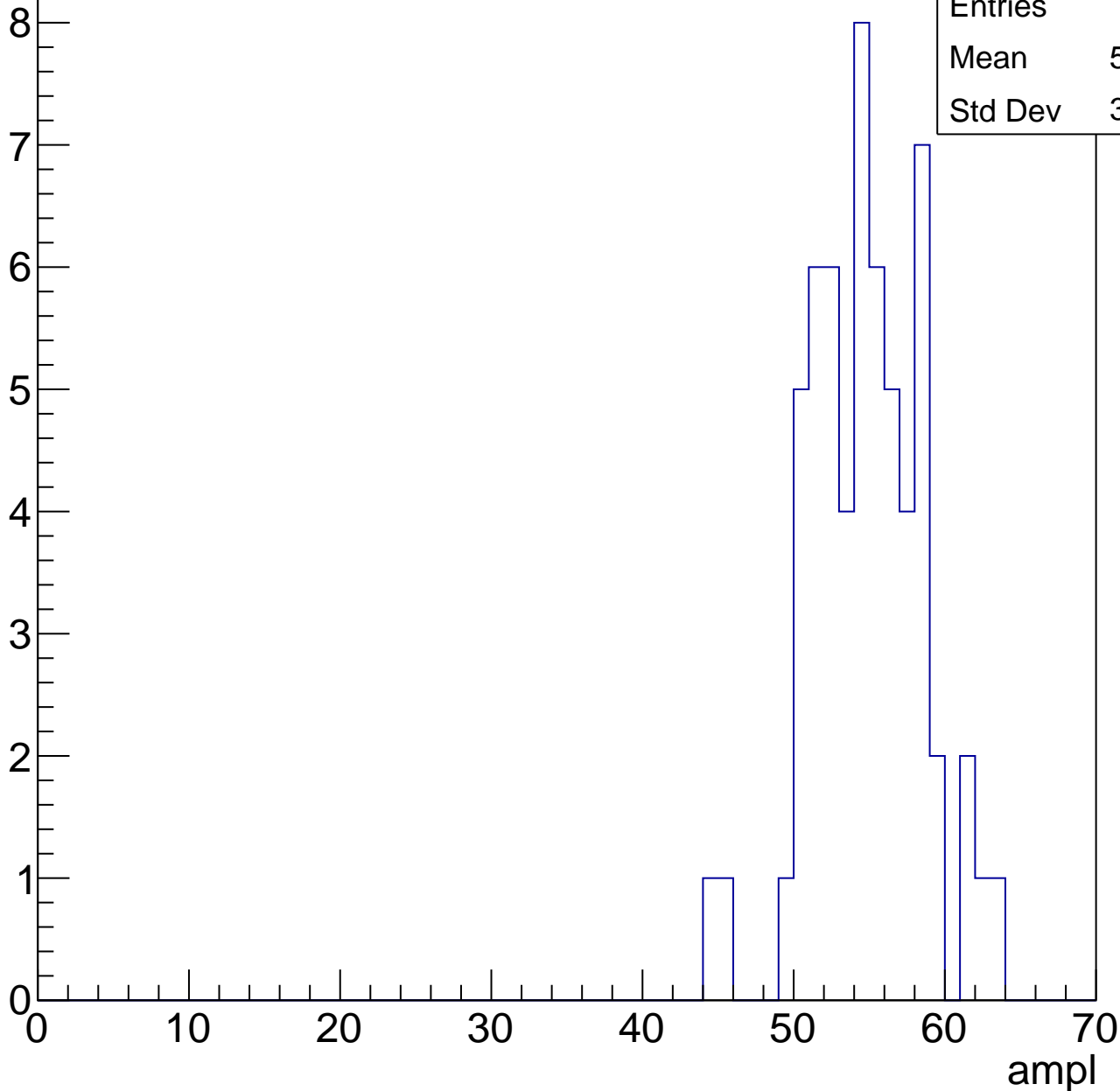


B1L100S, U25-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

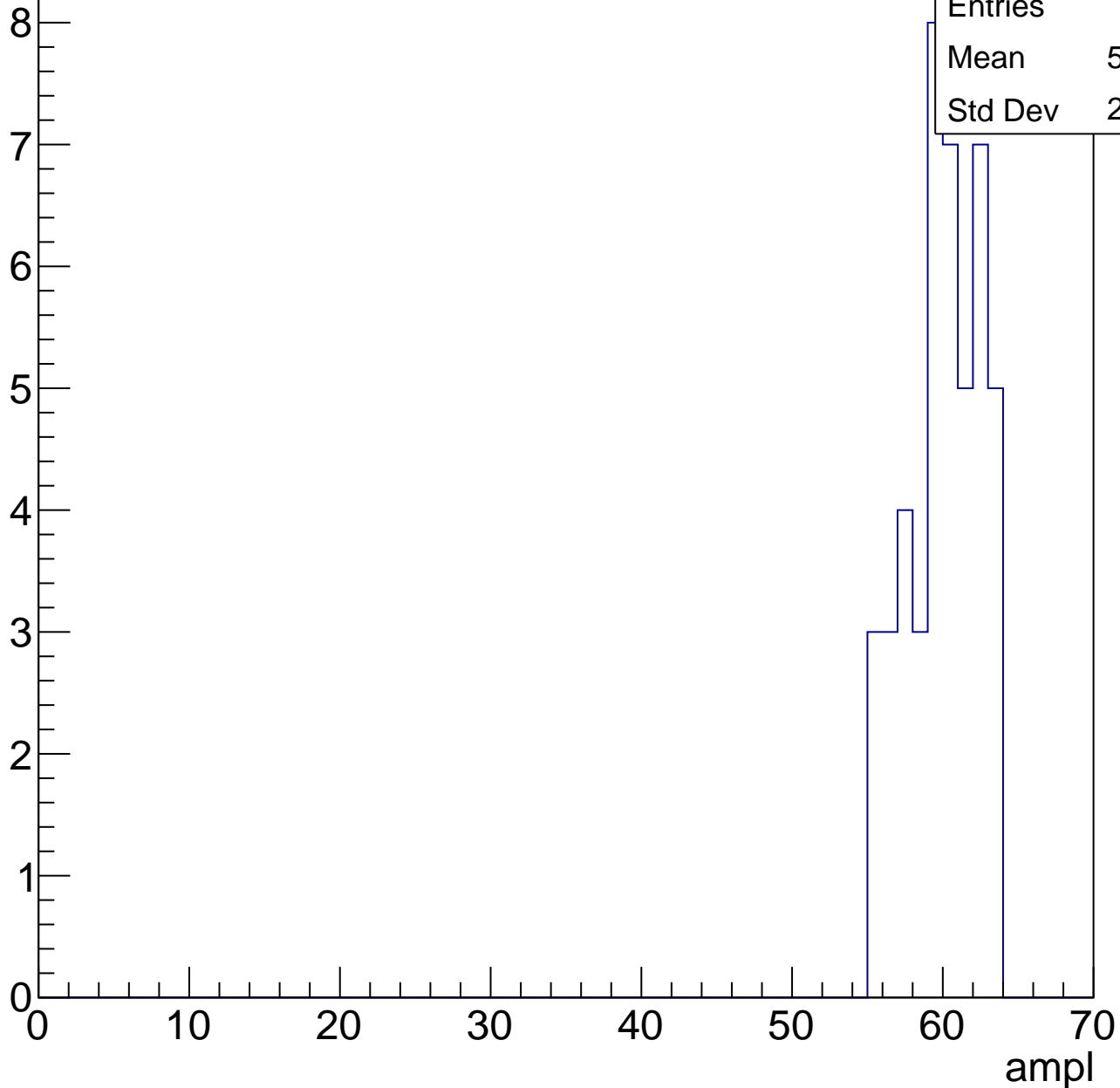
Entries	60
Mean	54.32
Std Dev	3.717



B1L100S, U25-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

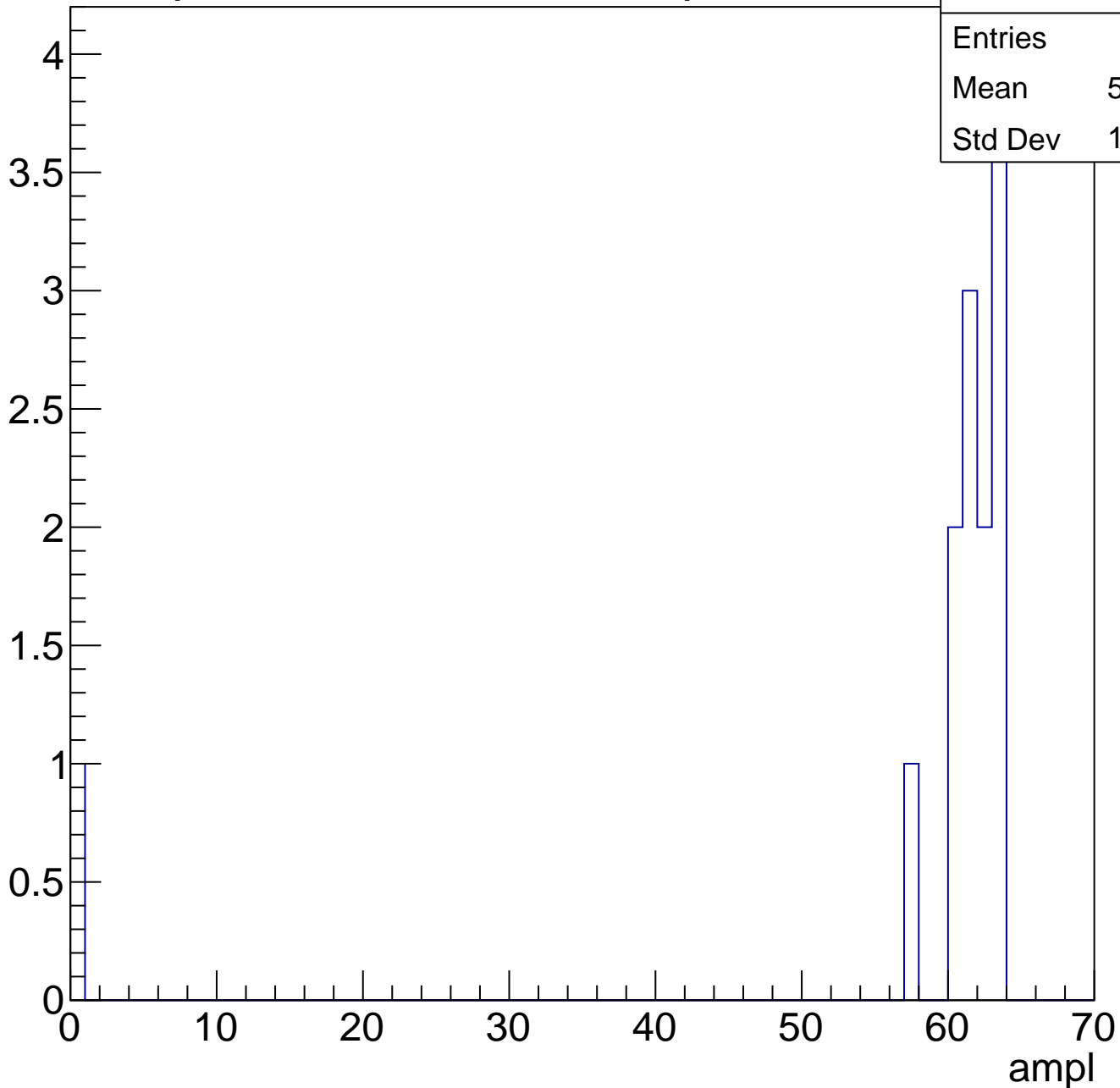
Entry



B1L100S, U25-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch103, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	28.75
Std Dev	3.595

Gaus mean : 29.3953

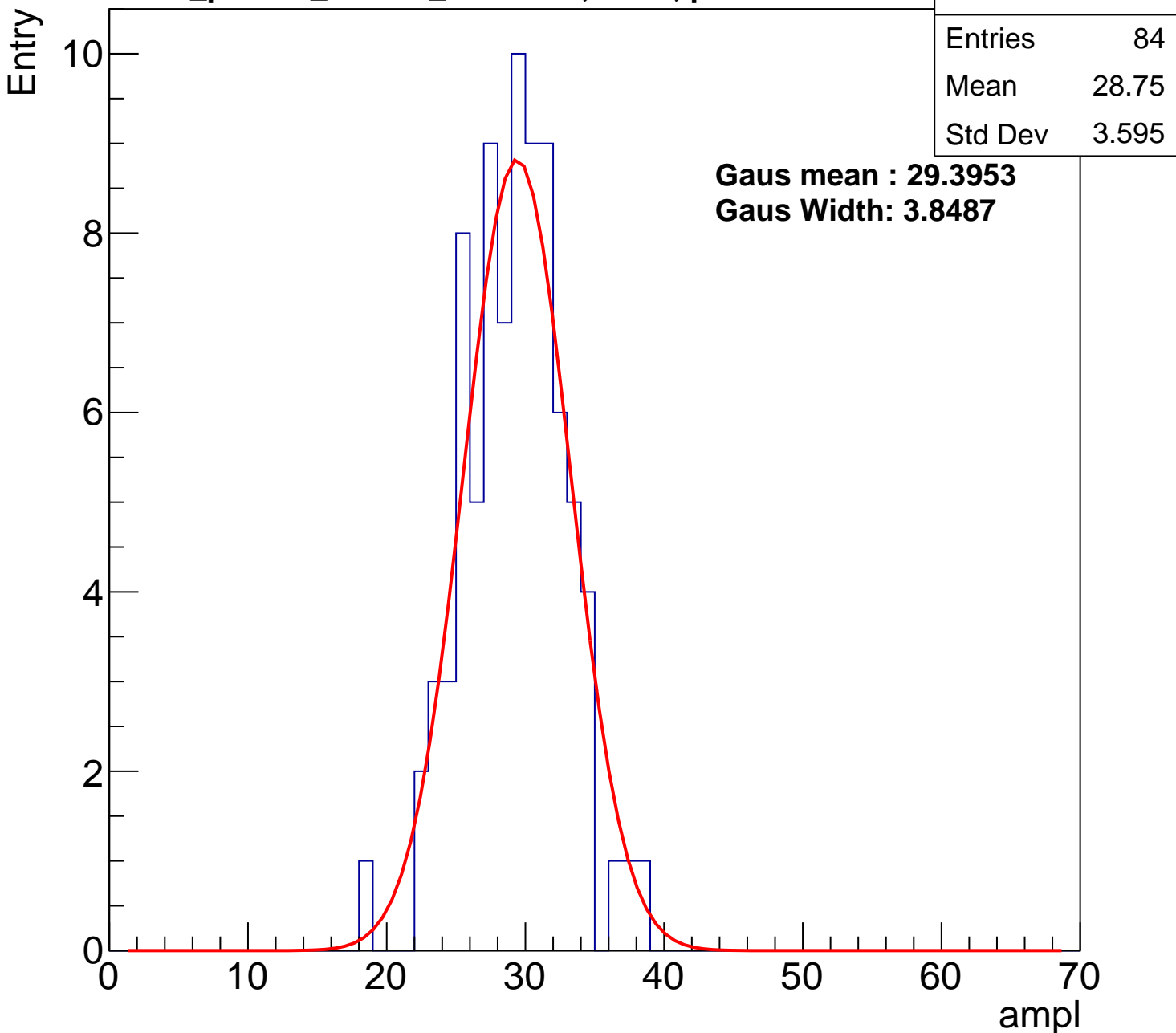
Gaus Width: 3.8487

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

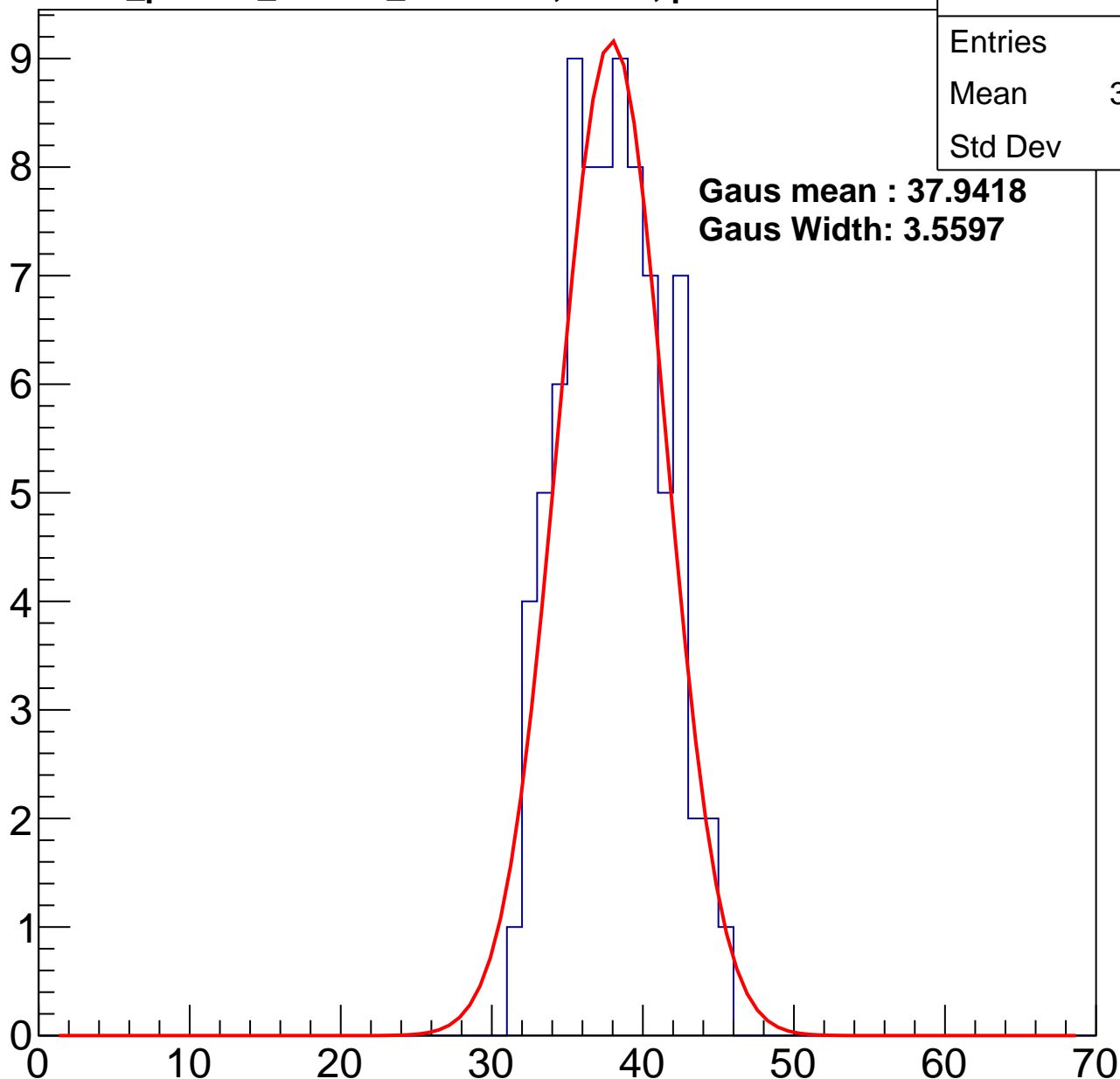
9
8
7
6
5
4
3
2
1
0

Entries	82
Mean	37.55
Std Dev	3.28

Gaus mean : 37.9418

Gaus Width: 3.5597

ampl



B1L100S, U25-ch103, adc2

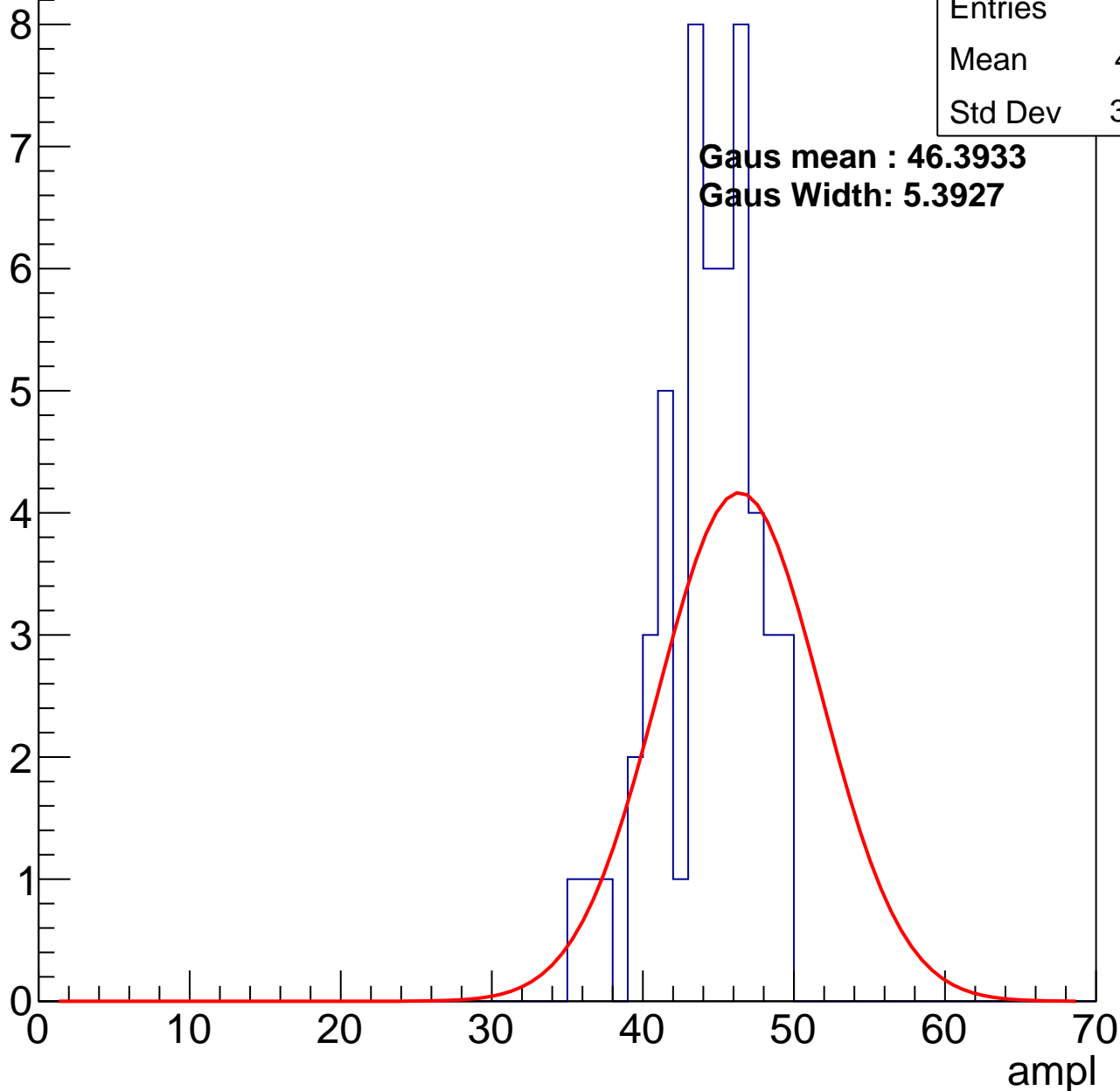
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.81
Std Dev	3.235

Gaus mean : 46.3933

Gaus Width: 5.3927



B1L100S, U25-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	50.5
Std Dev	3.604

Entry

10

8

6

4

2

0

0

10

20

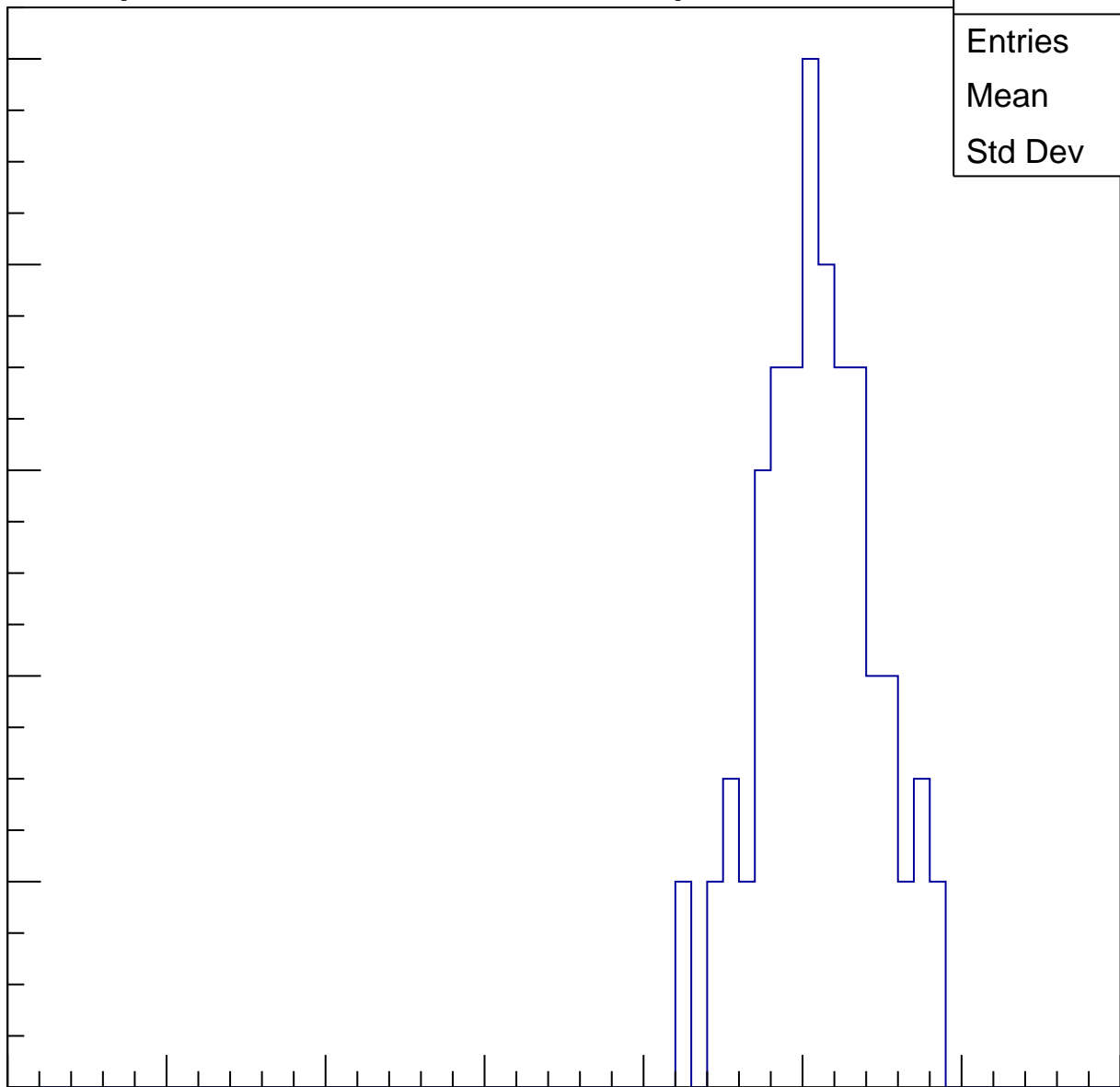
30

40

50

60

ampl

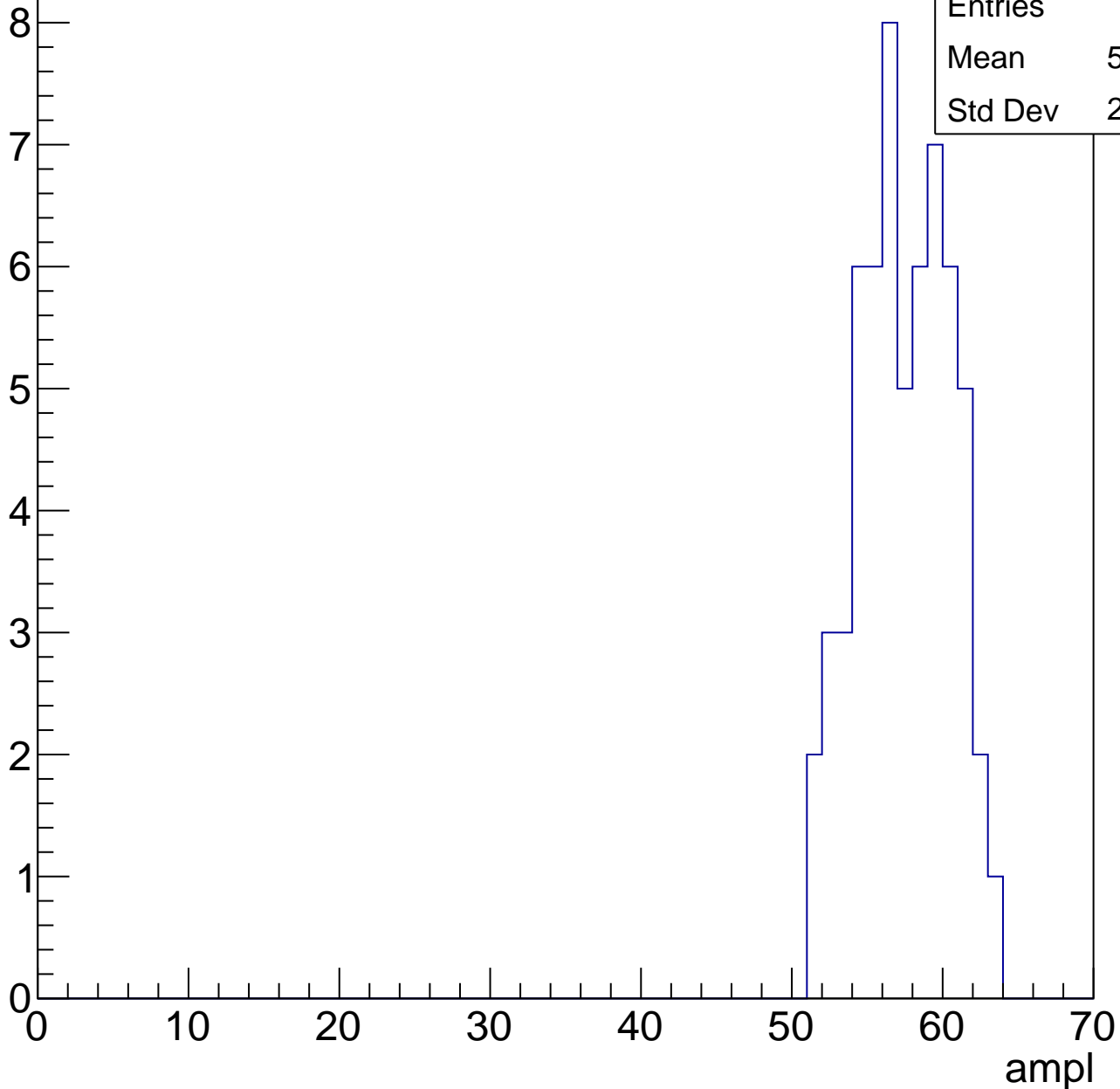


B1L100S, U25-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.95
Std Dev	2.986

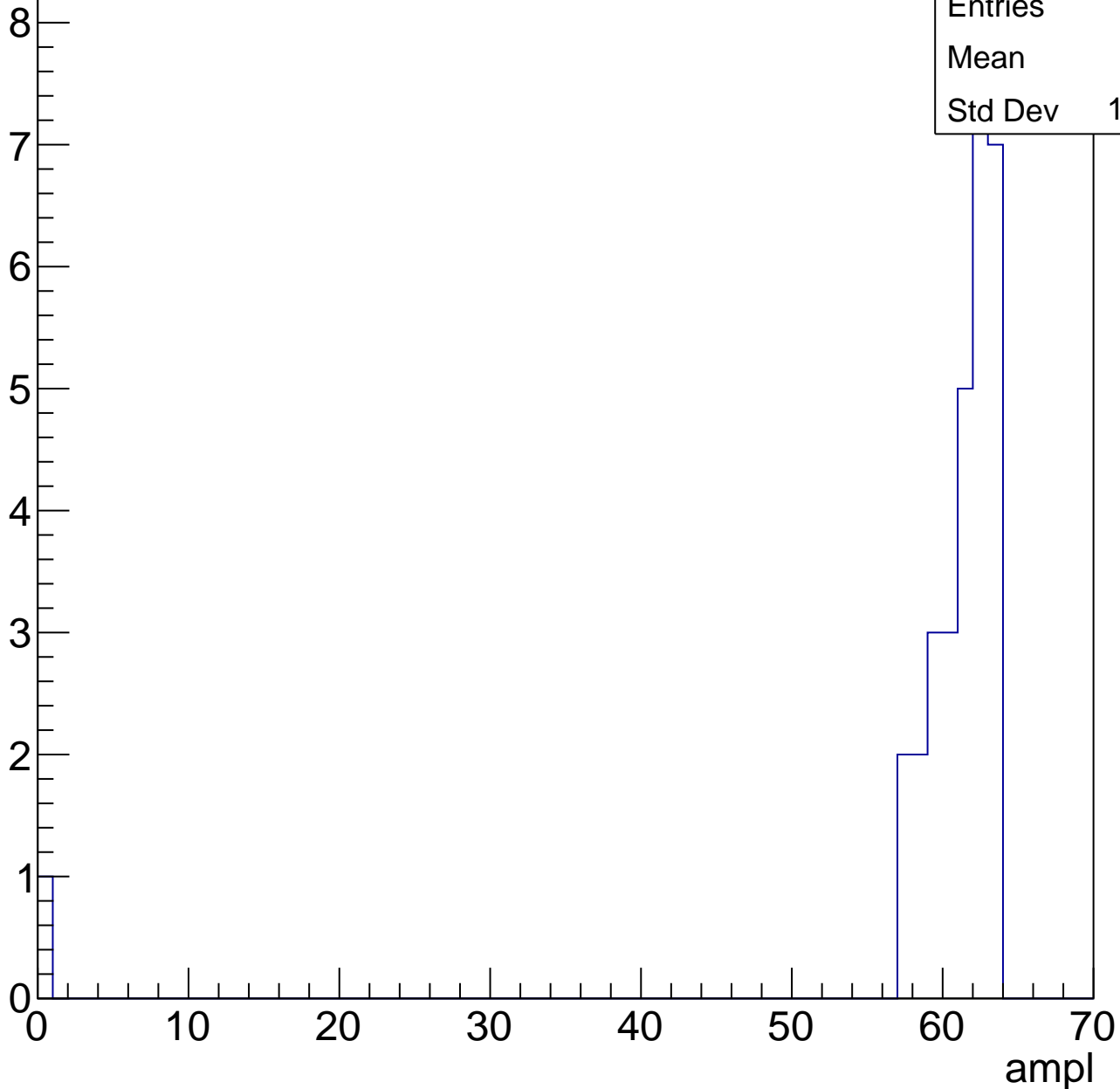


B1L100S, U25-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	59
Std Dev	10.92



B1L100S, U25-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L100S, U25-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch104, adc0

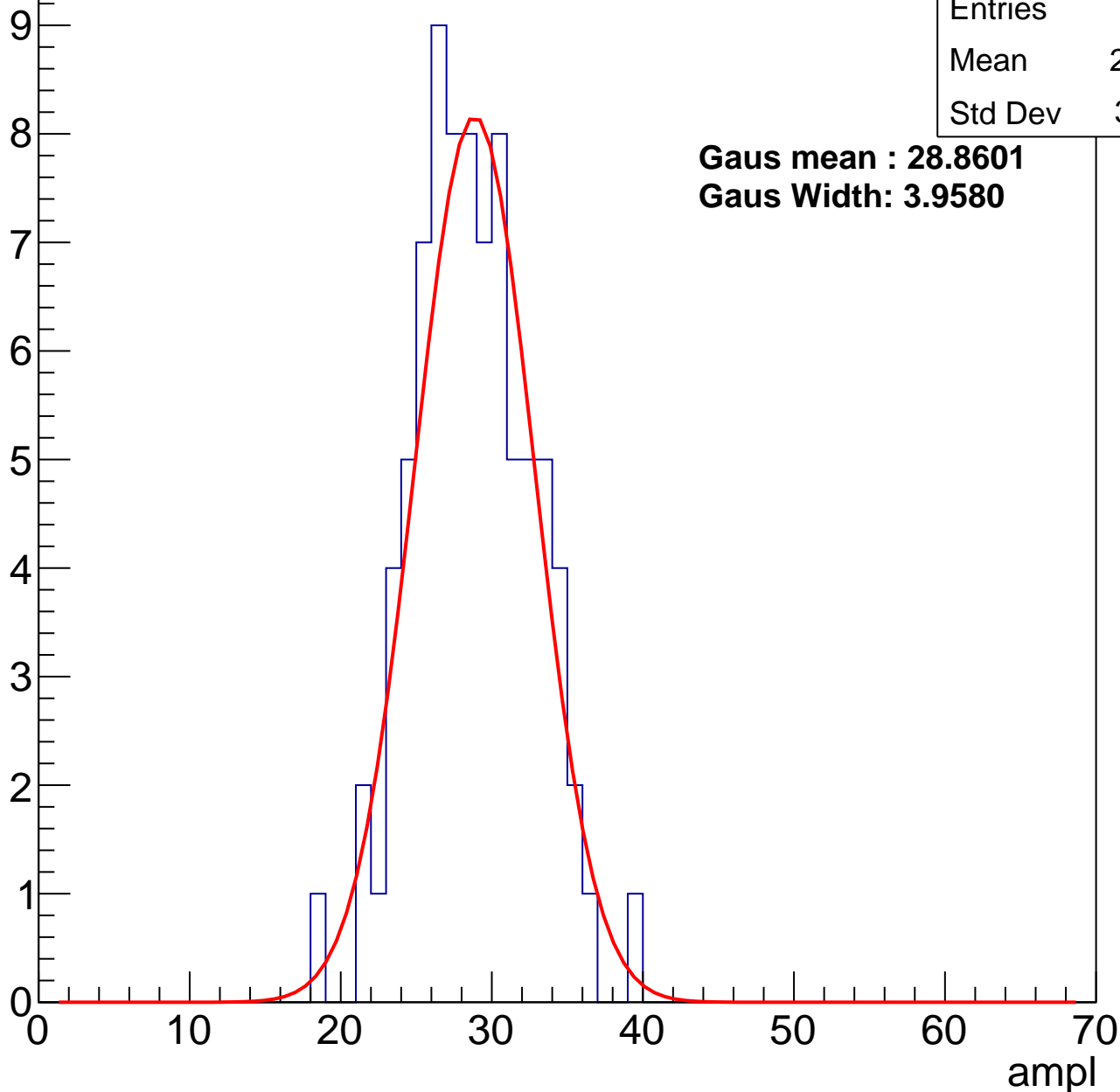
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	28.28
Std Dev	3.851

Gaus mean : 28.8601

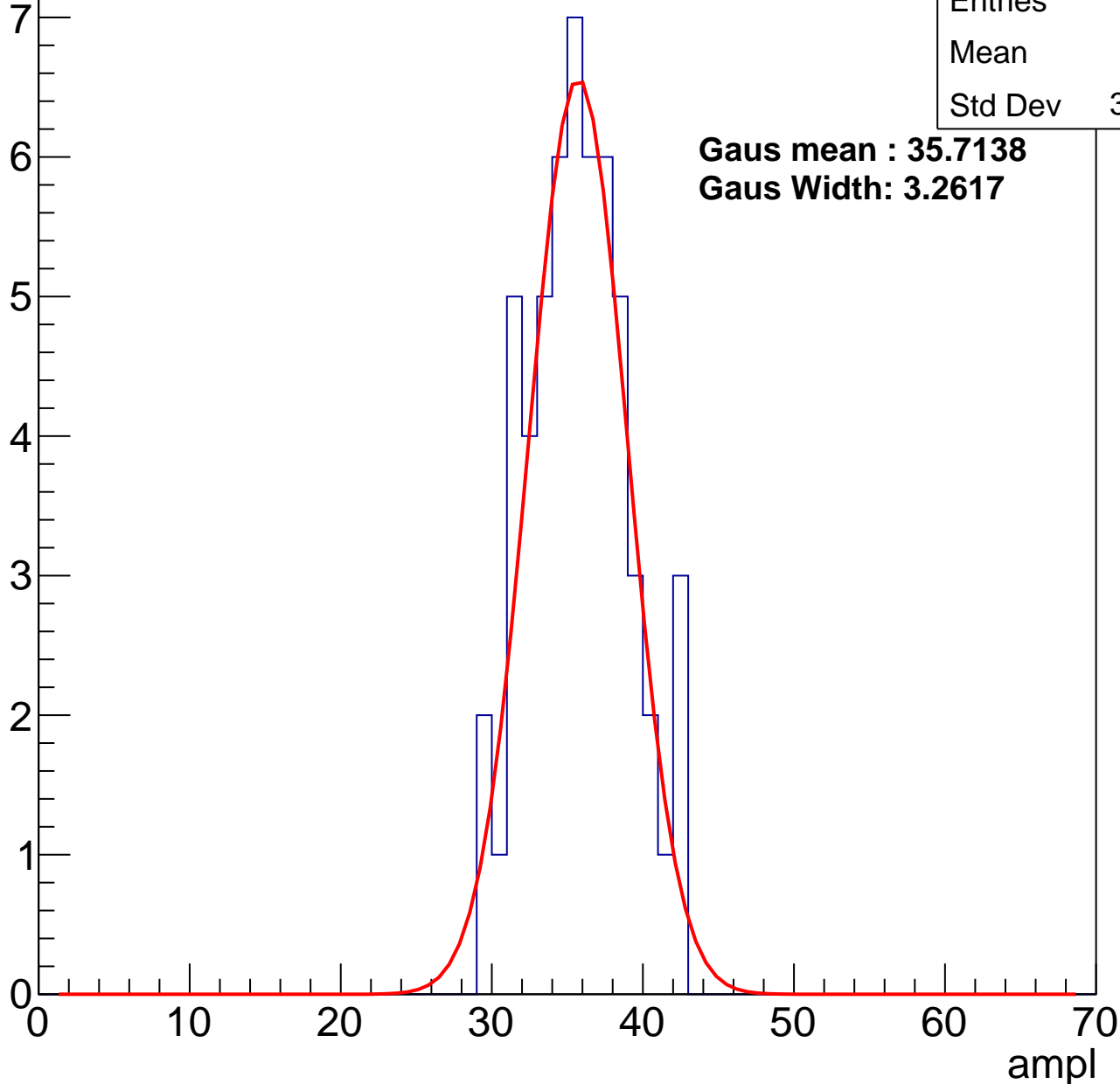
Gaus Width: 3.9580



B1L100S, U25-ch104, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch104, adc2

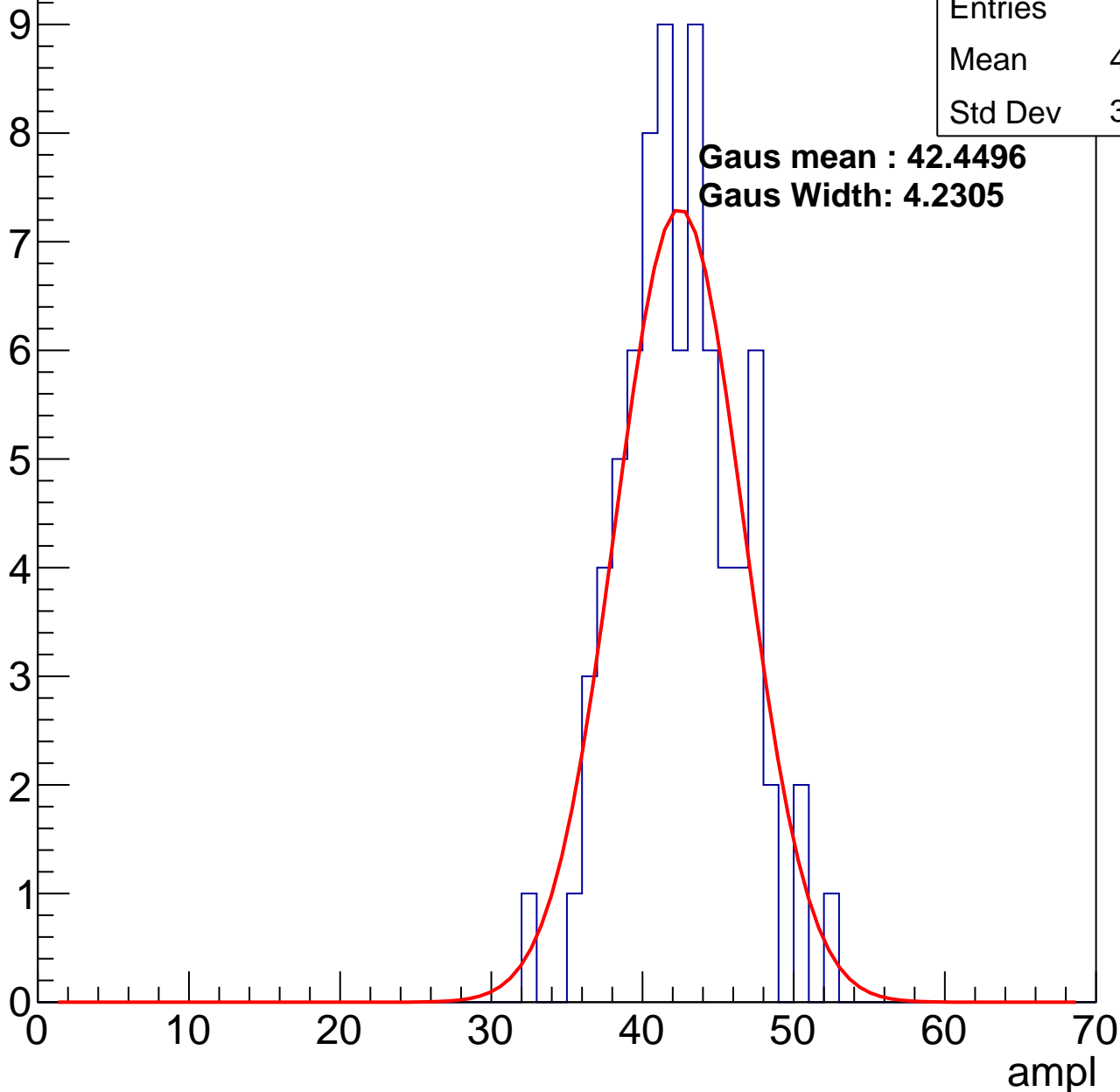
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	41.99
Std Dev	3.805

Gaus mean : 42.4496

Gaus Width: 4.2305



B1L100S, U25-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	49.38
Std Dev	3.933

Entry

10

8

6

4

2

0

0

10

20

30

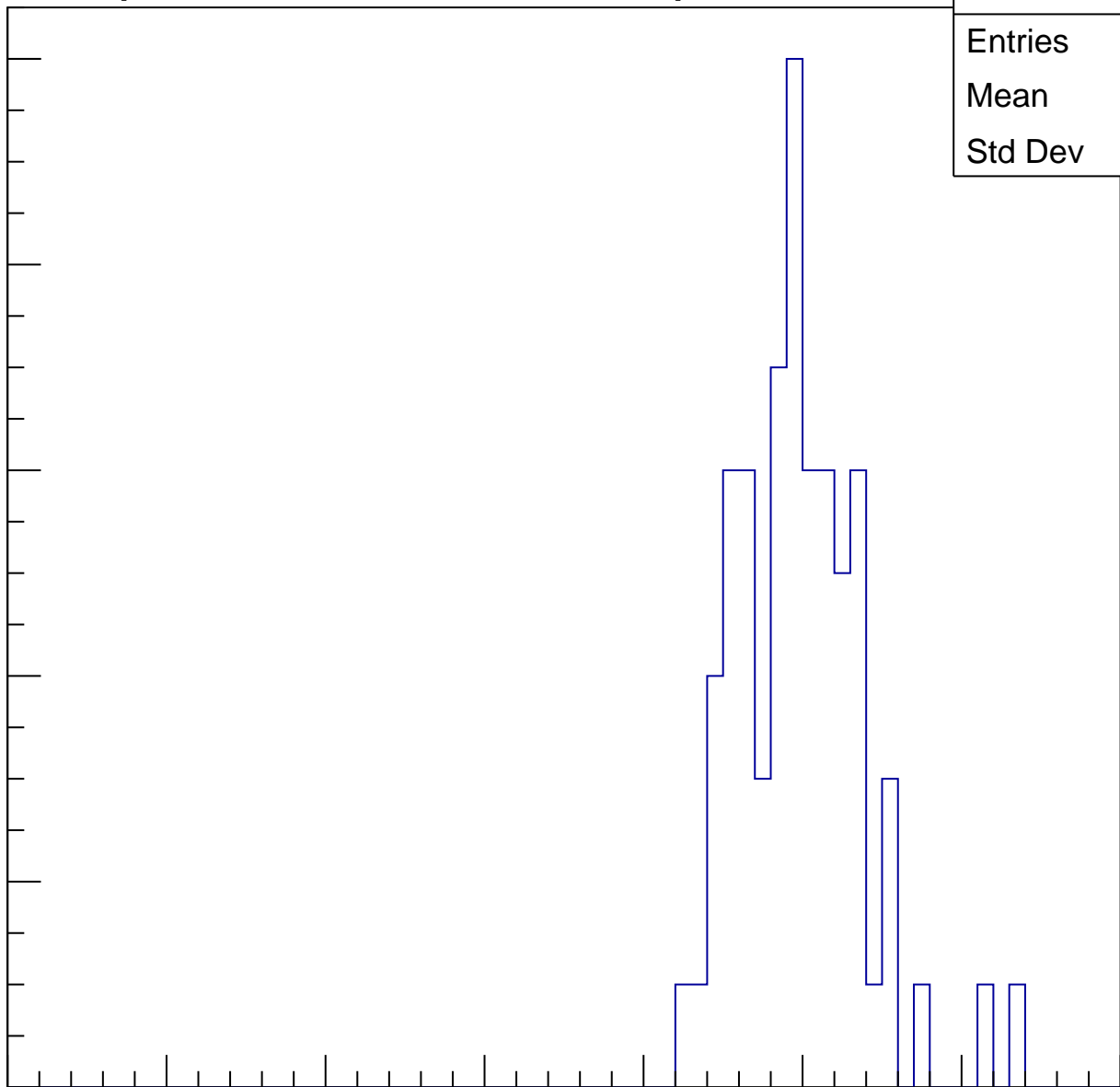
40

50

60

70

ampl

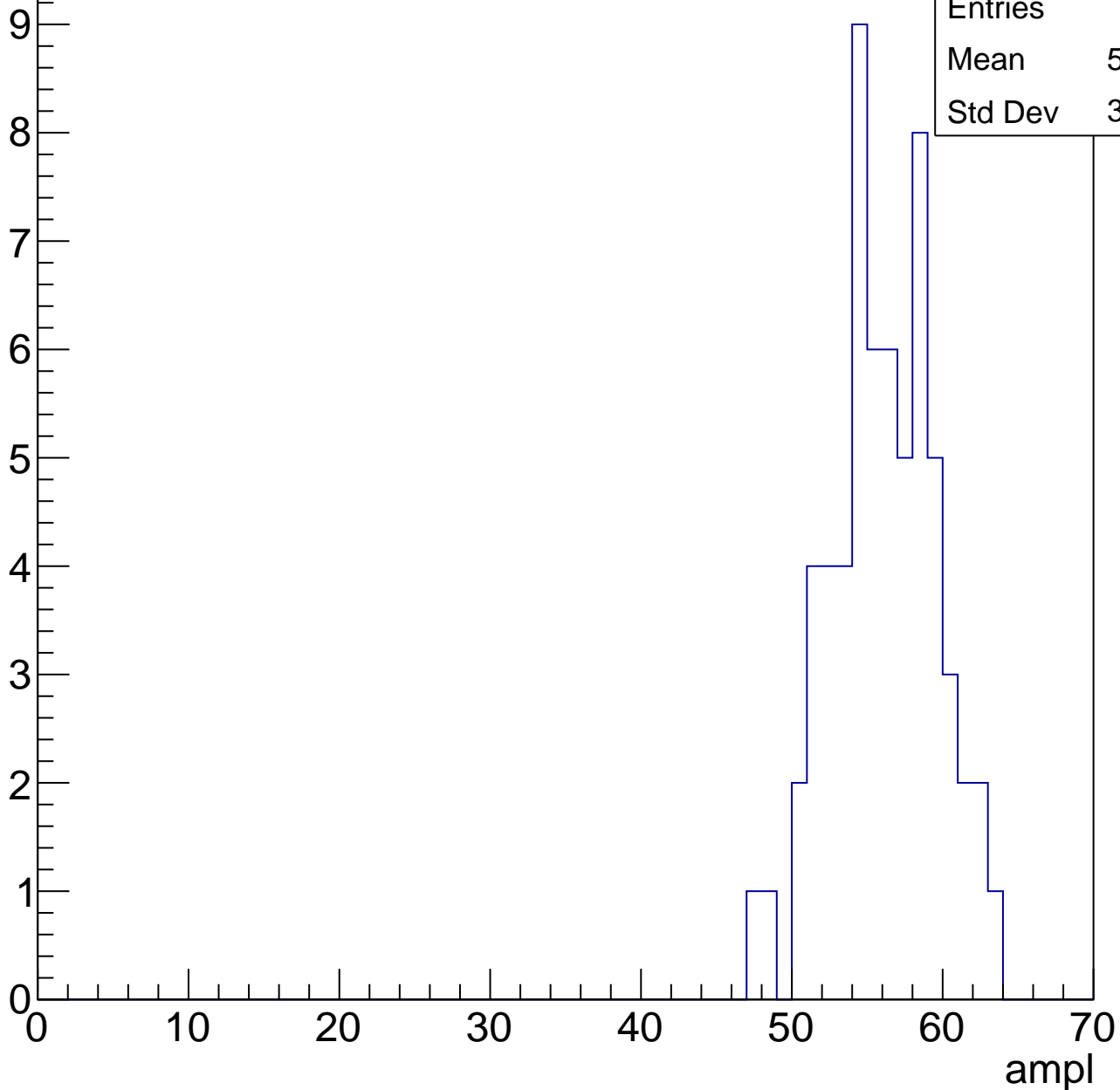


B1L100S, U25-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.62
Std Dev	3.452

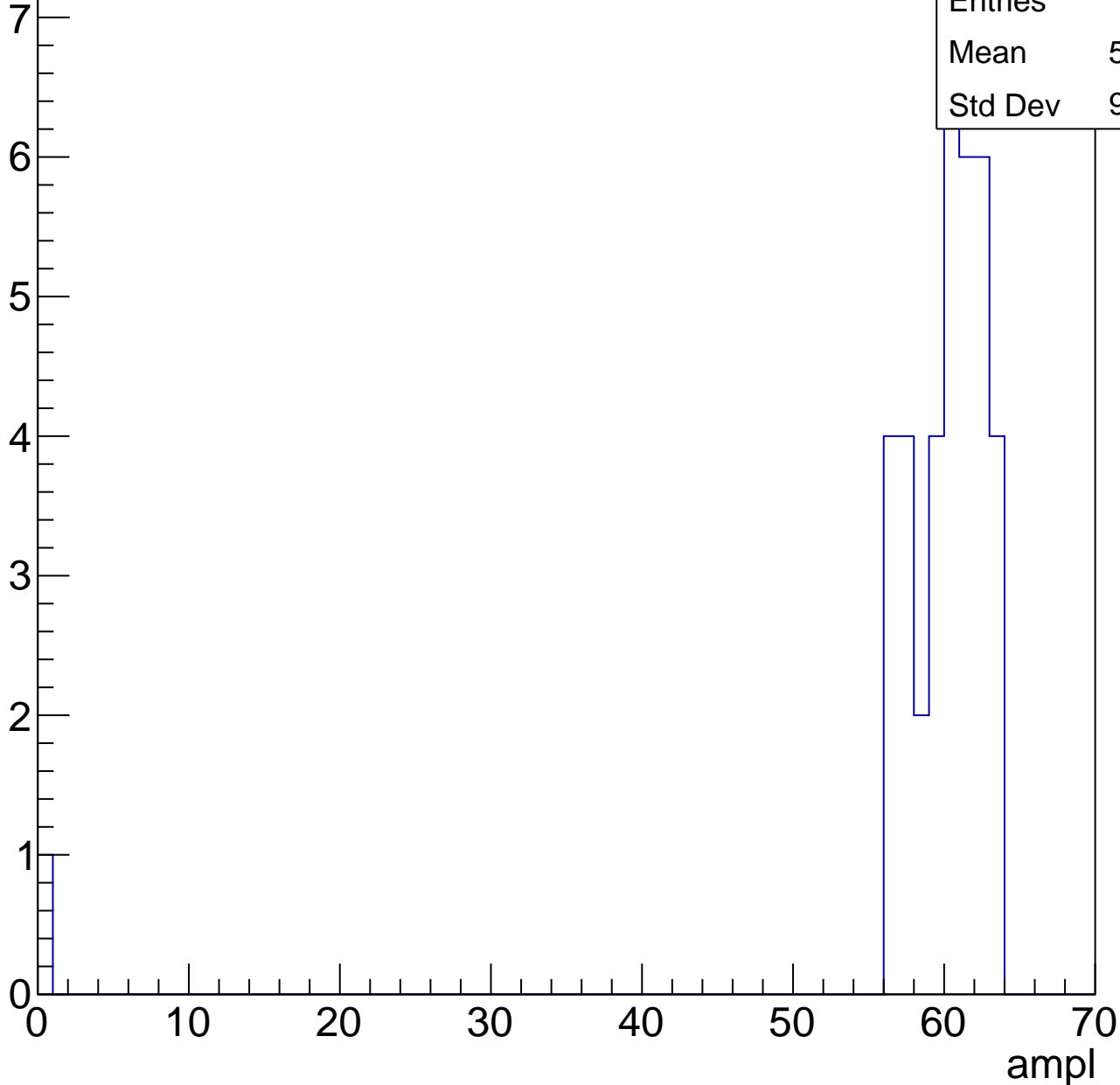


B1L100S, U25-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.26
Std Dev	9.819



B1L100S, U25-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.22
Std Dev	2.529

ampl

0 10 20 30 40 50 60 70

0 0.5 1 1.5 2 2.5 3 3.5 4

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

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0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

0 10 20 30 40 50 60 70

B1L100S, U25-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch105, adc0

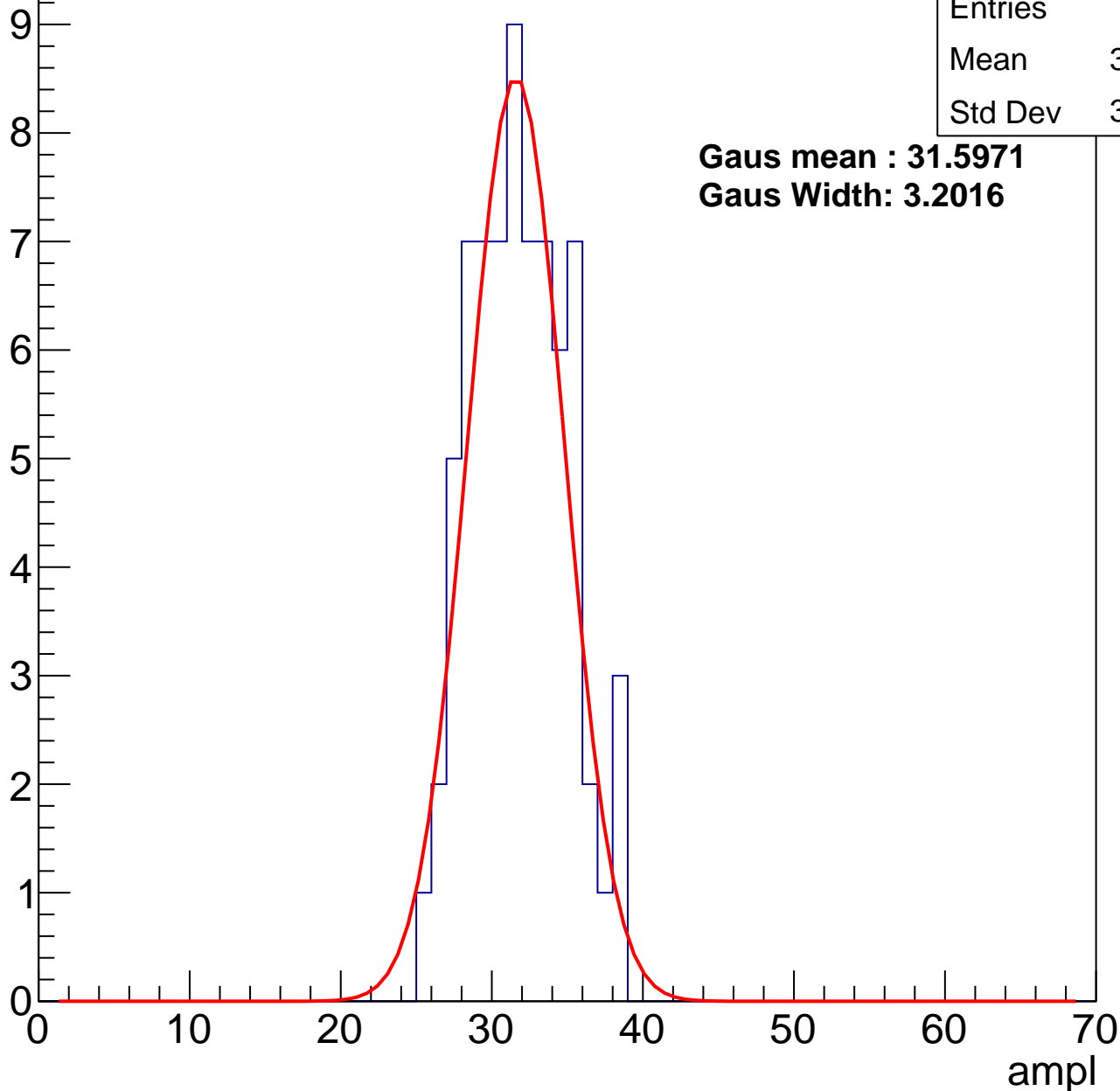
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	31.37
Std Dev	3.114

Gaus mean : 31.5971

Gaus Width: 3.2016



B1L100S, U25-ch105, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	39.08
Std Dev	3.809

Gaus mean : 39.5843

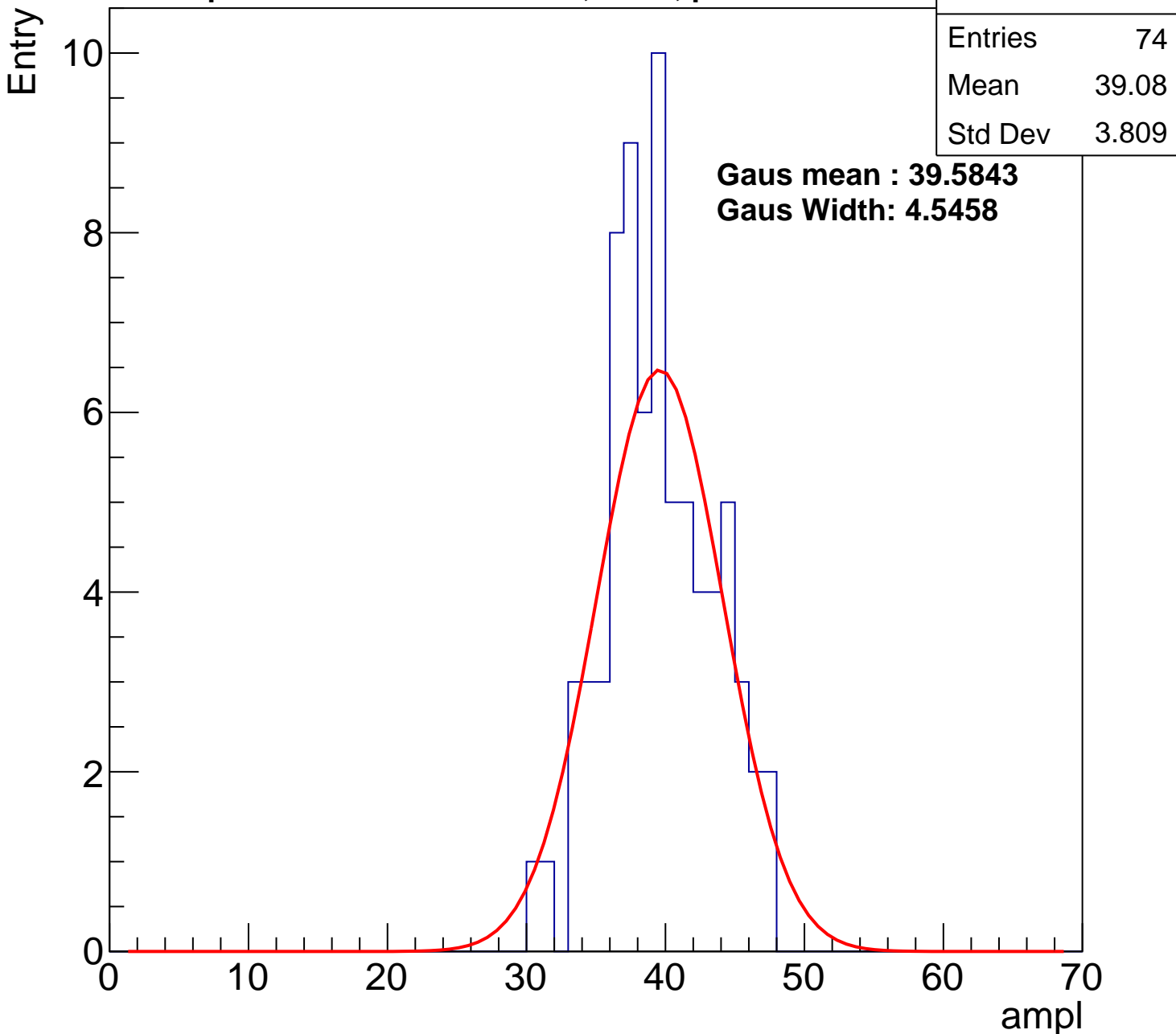
Gaus Width: 4.5458

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



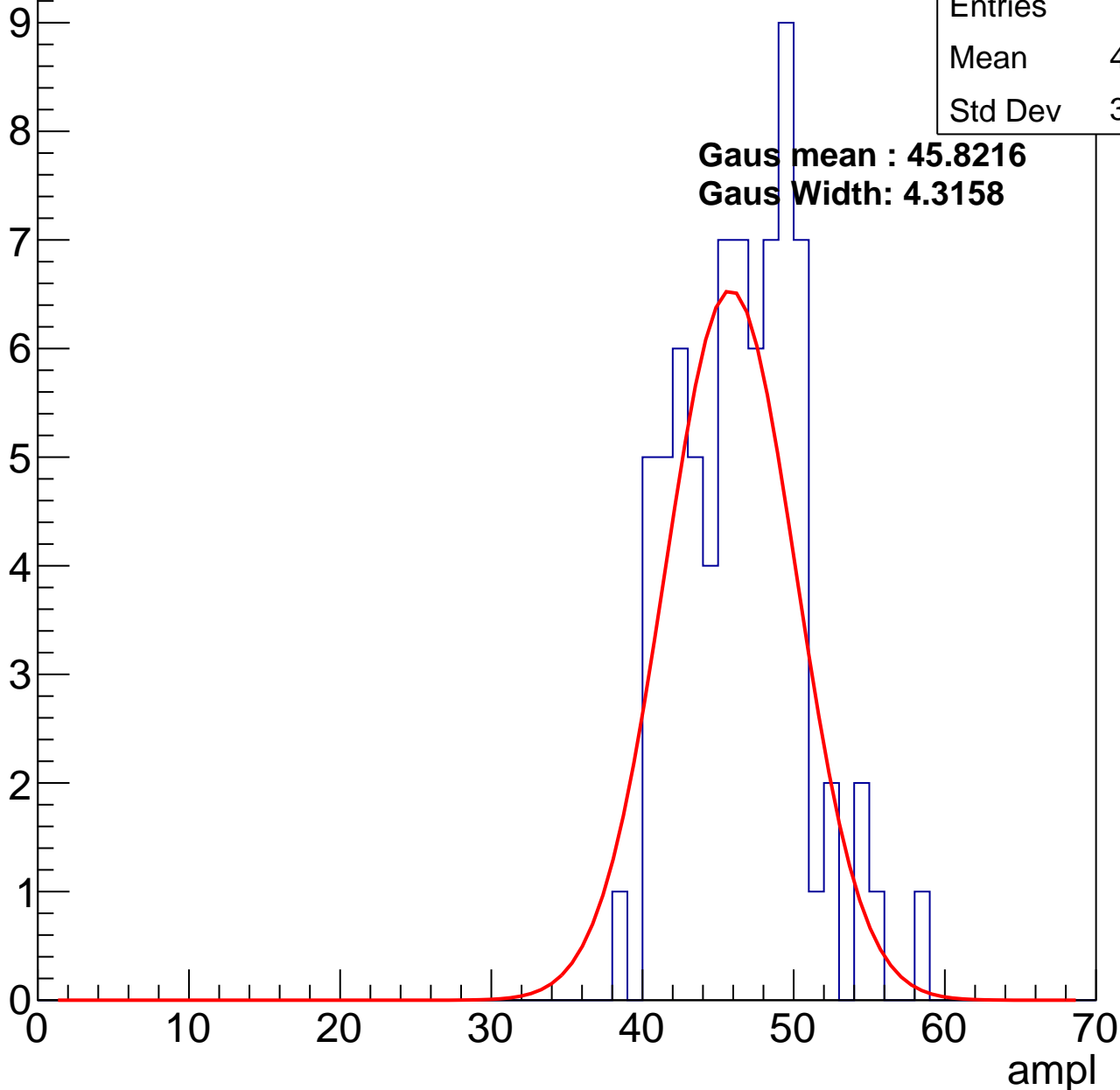
B1L100S, U25-ch105, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	46.16
Std Dev	3.987

Gaus mean : 45.8216
Gaus Width: 4.3158

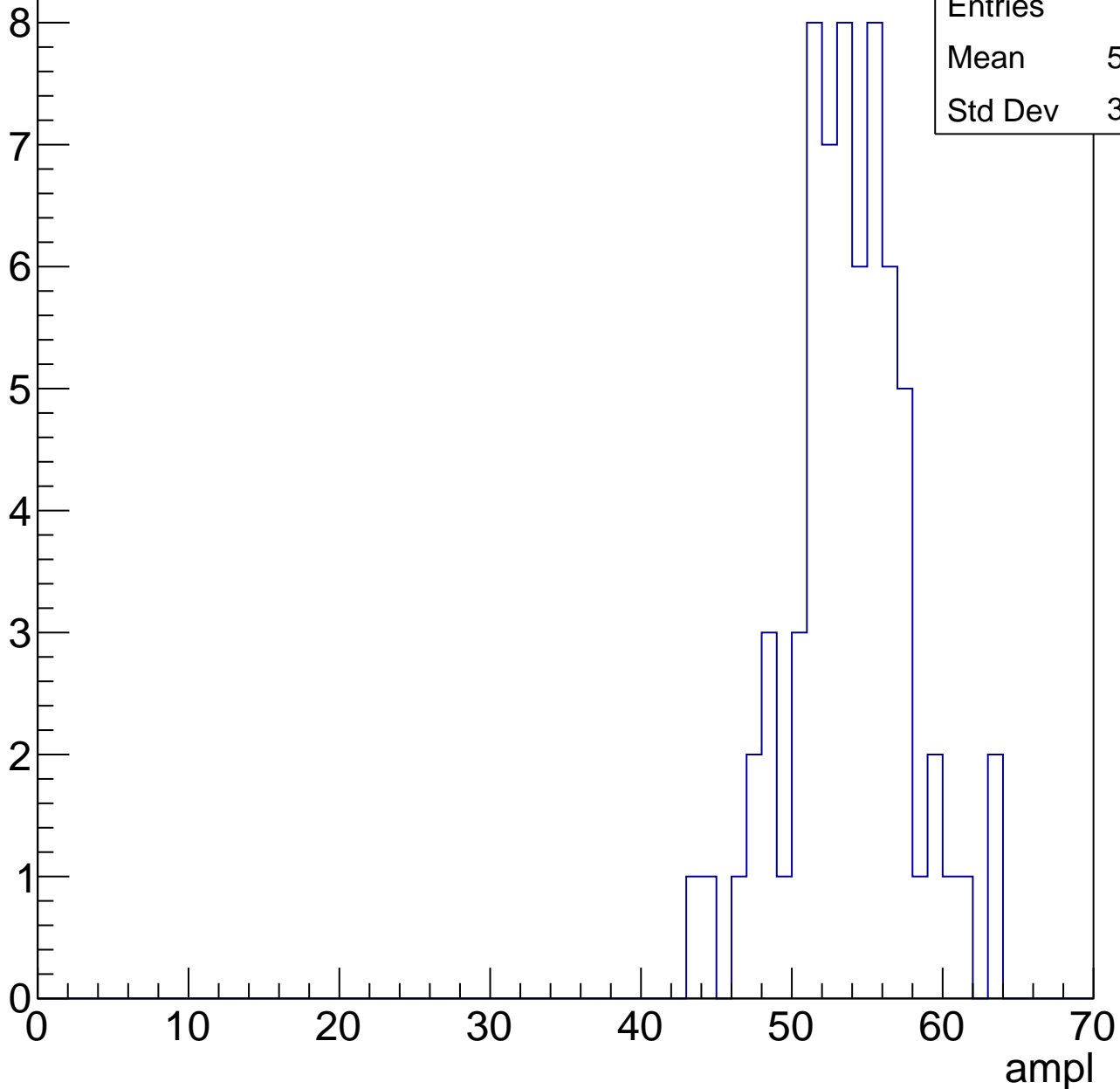


B1L100S, U25-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	53.34
Std Dev	3.927

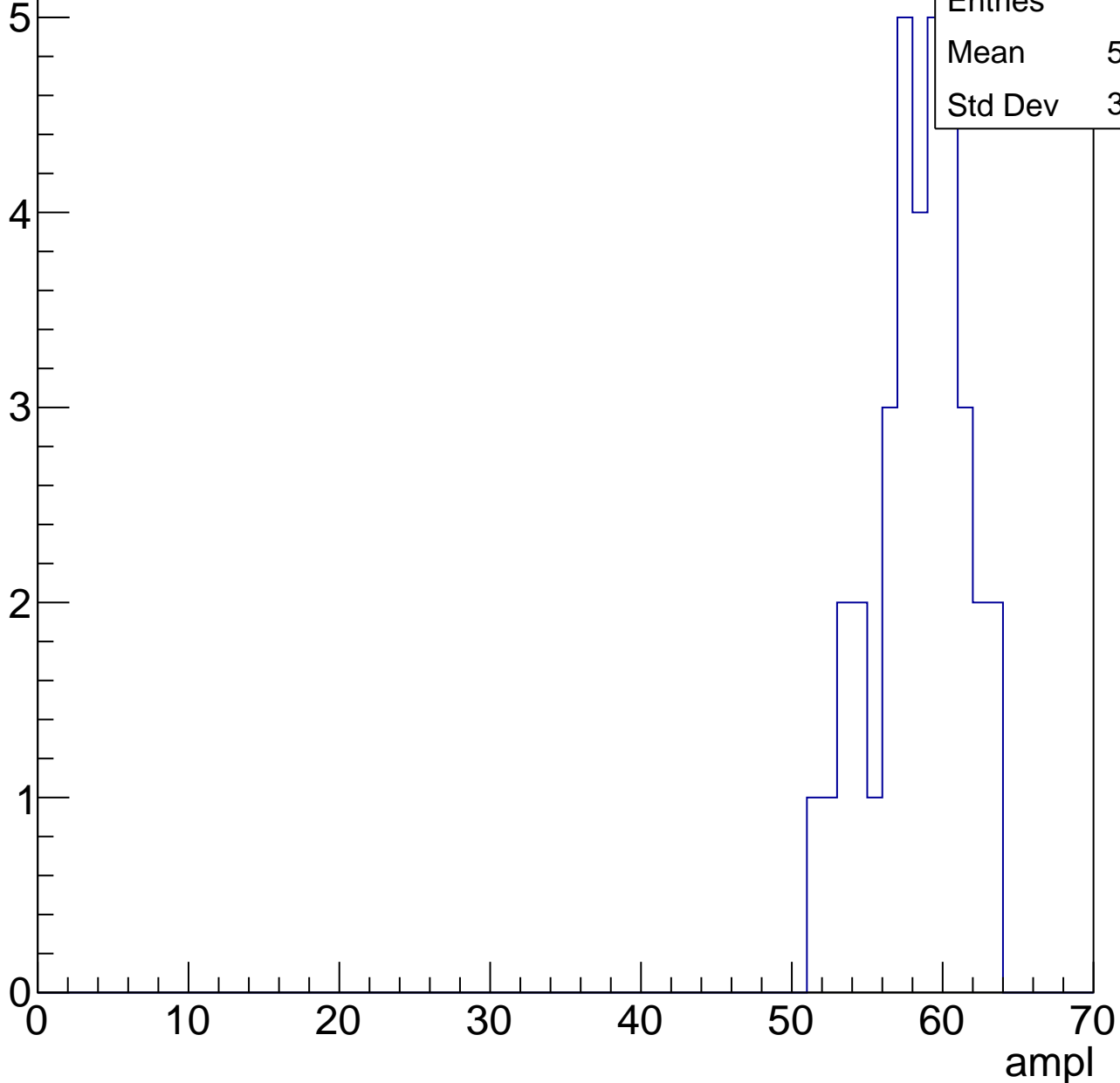


B1L100S, U25-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	57.92
Std Dev	3.013

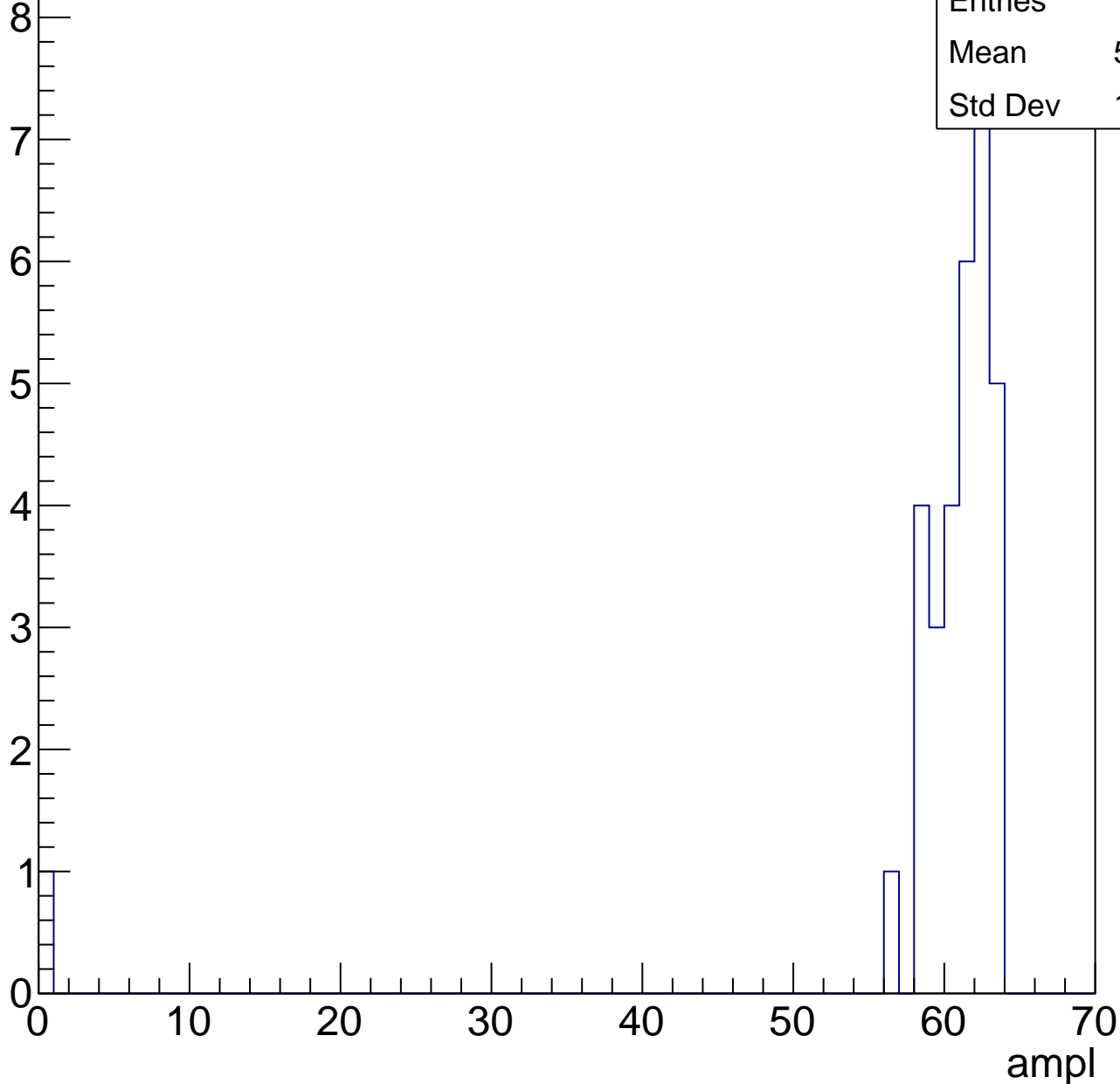


B1L100S, U25-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.81
Std Dev	10.71



B1L100S, U25-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U25-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U25-ch106, adc0

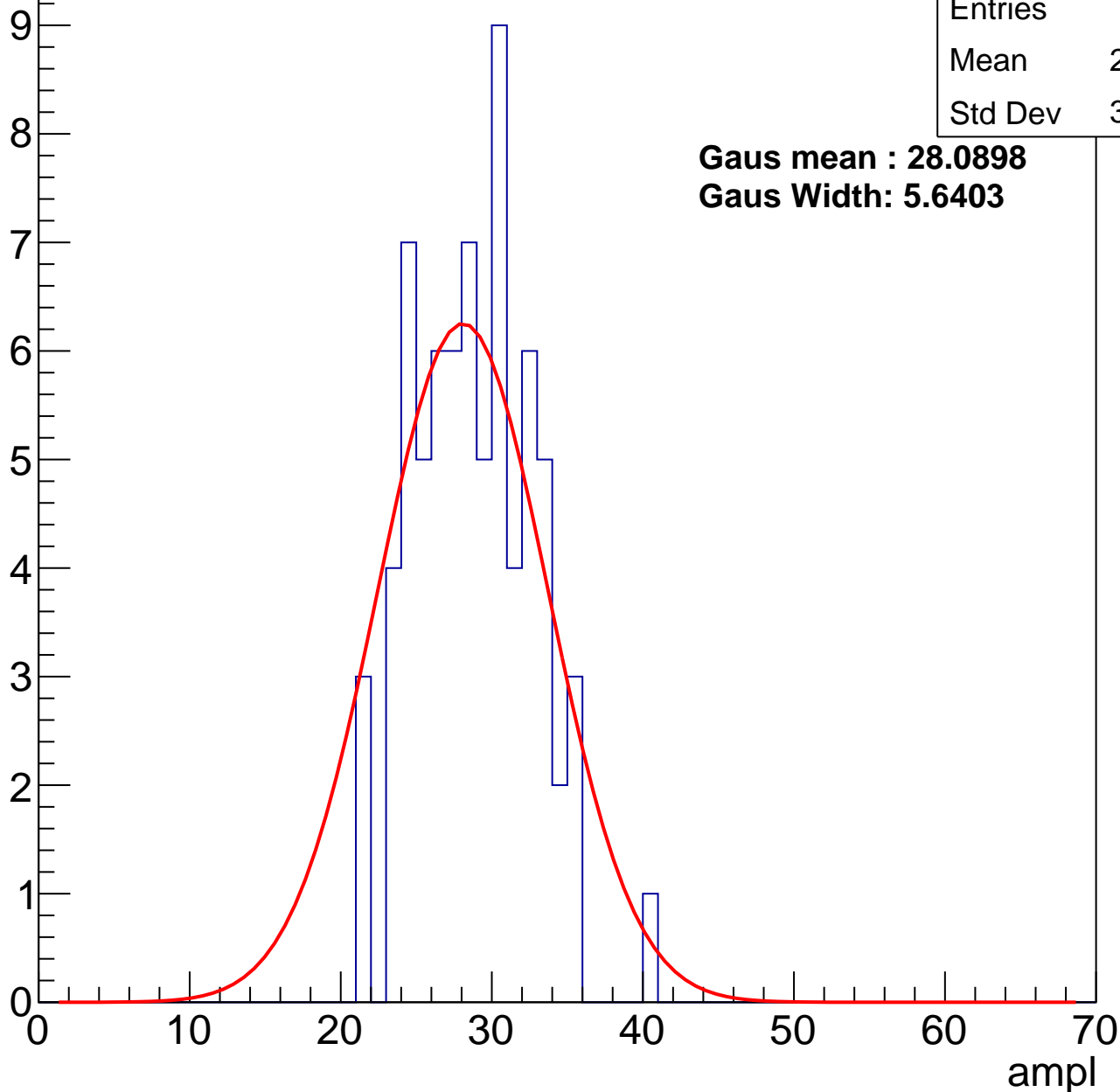
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.37
Std Dev	3.855

Gaus mean : 28.0898

Gaus Width: 5.6403



B1L100S, U25-ch106, adc1

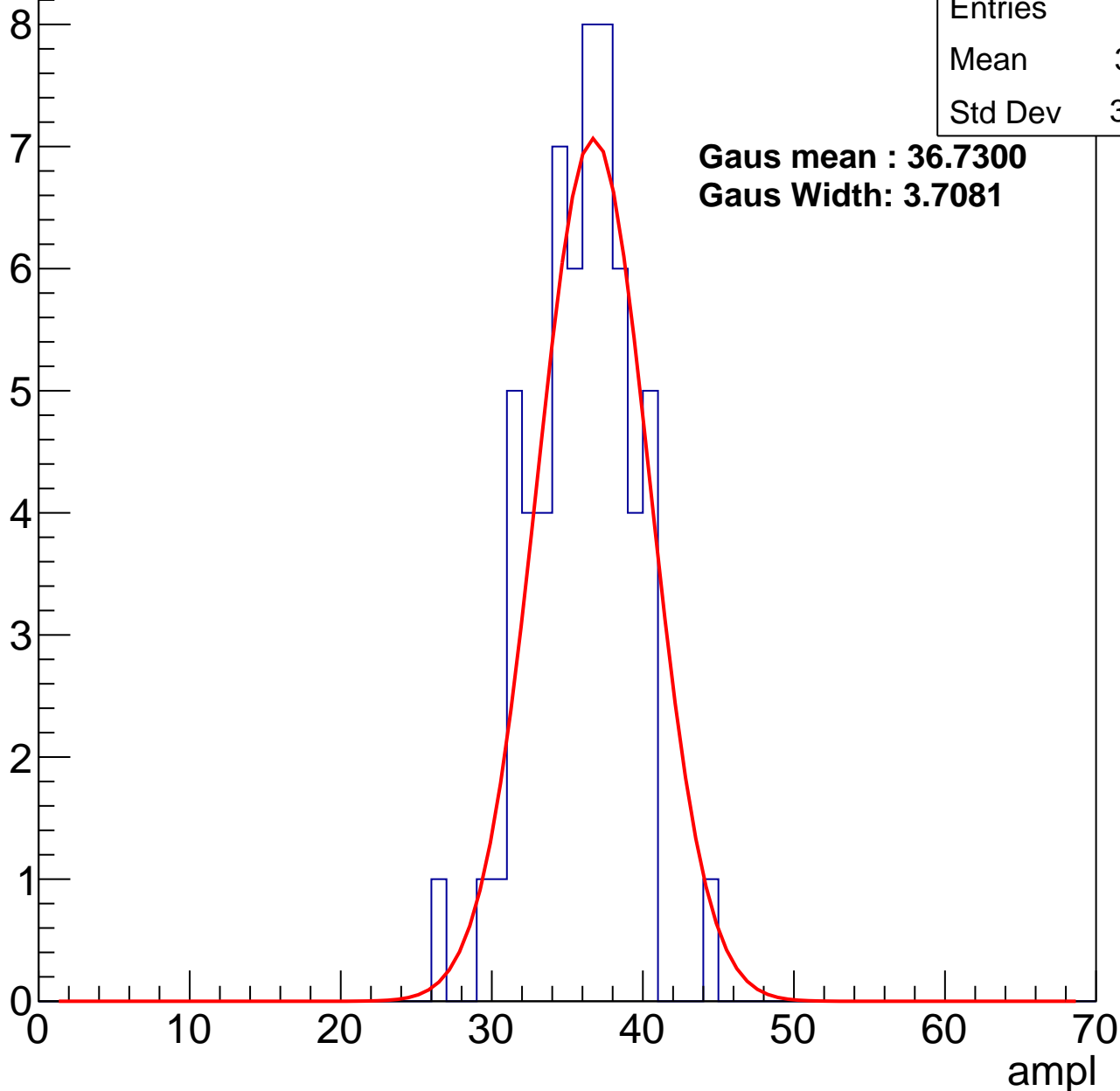
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.41
Std Dev	3.226

Gaus mean : 36.7300

Gaus Width: 3.7081



B1L100S, U25-ch106, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.42
Std Dev	3.809

Gaus mean : 42.8276

Gaus Width: 3.6894

10

8

6

4

2

0

0

10

20

30

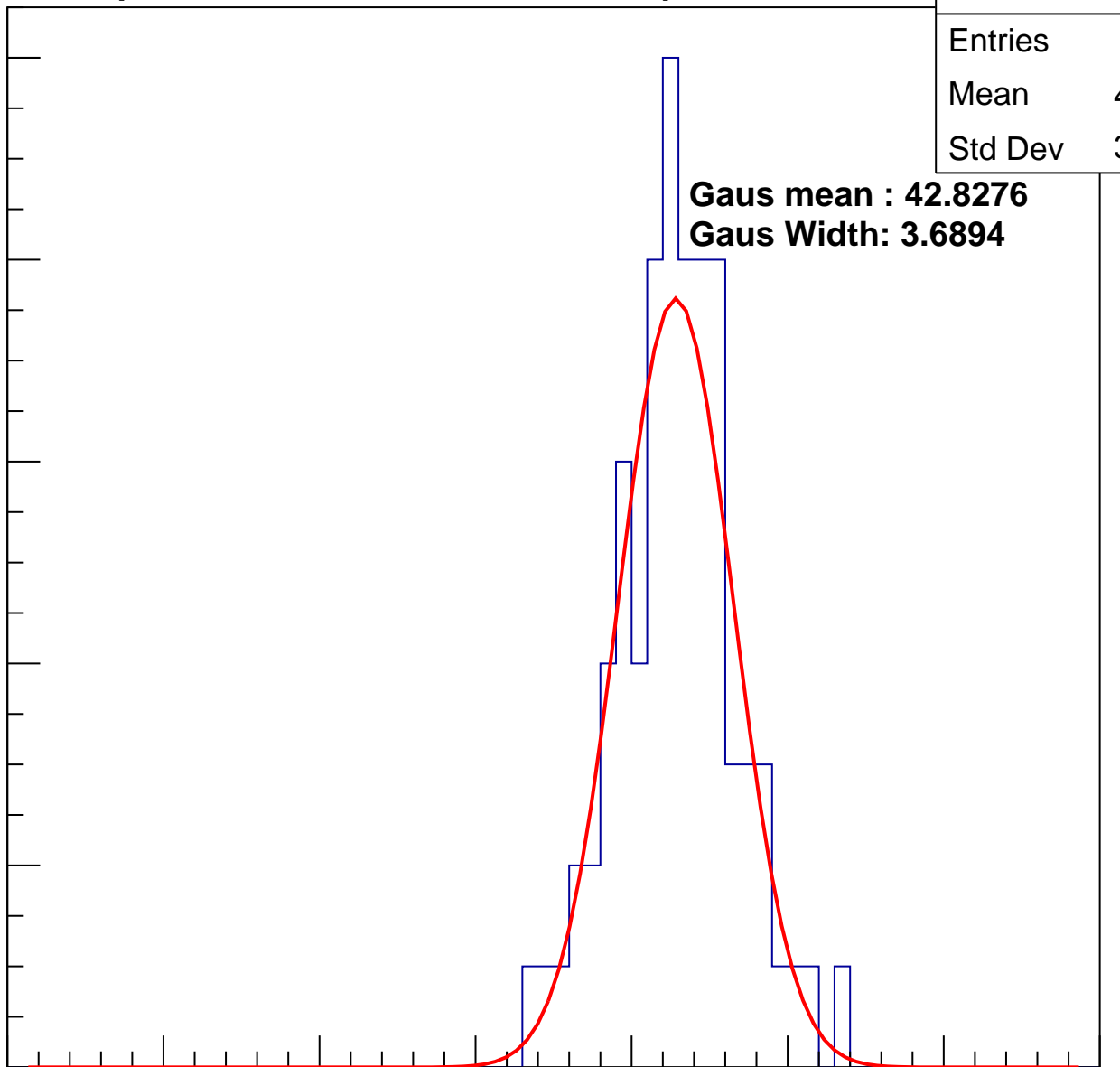
40

50

60

70

ampl

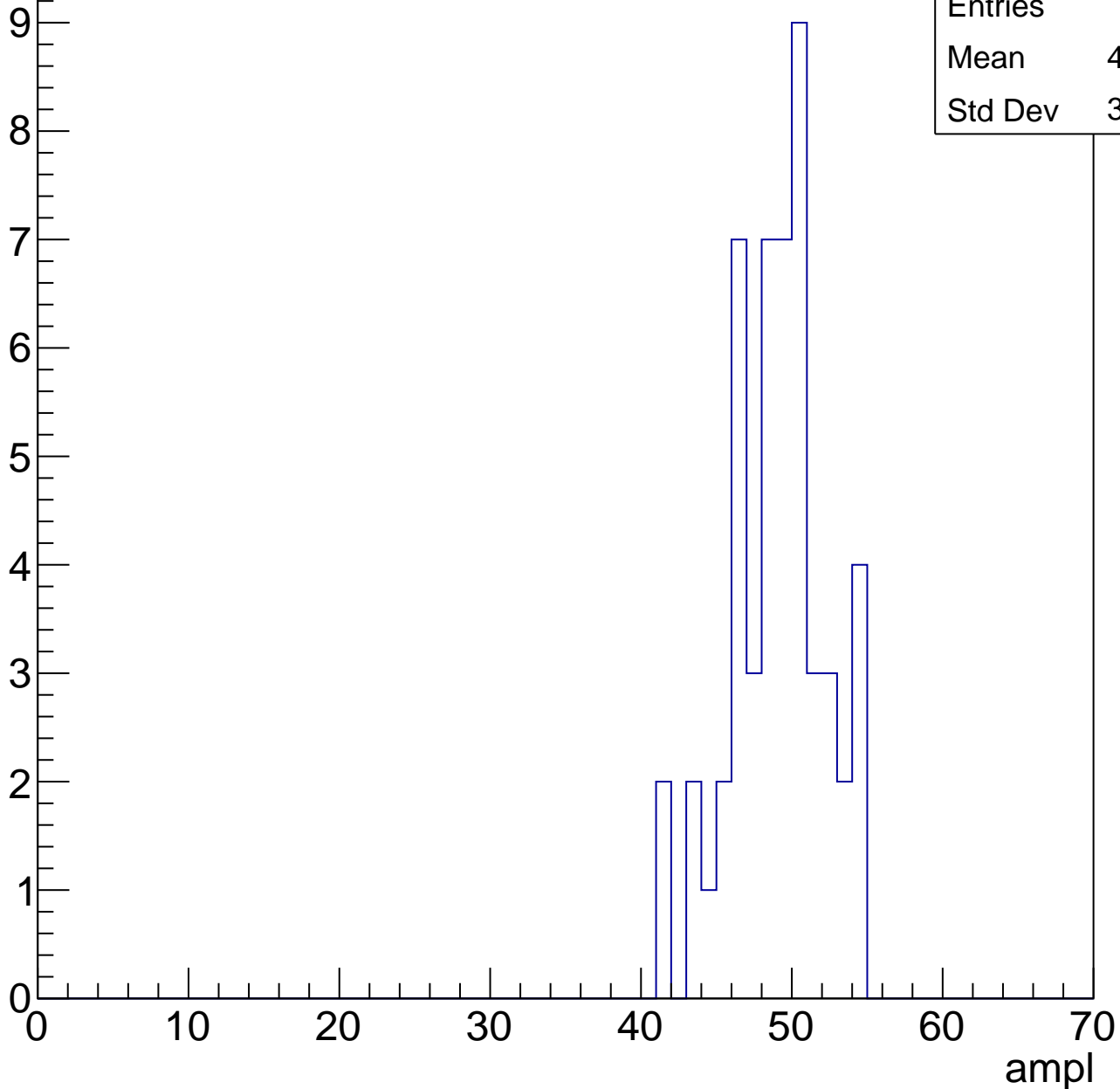


B1L100S, U25-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	48.56
Std Dev	3.128

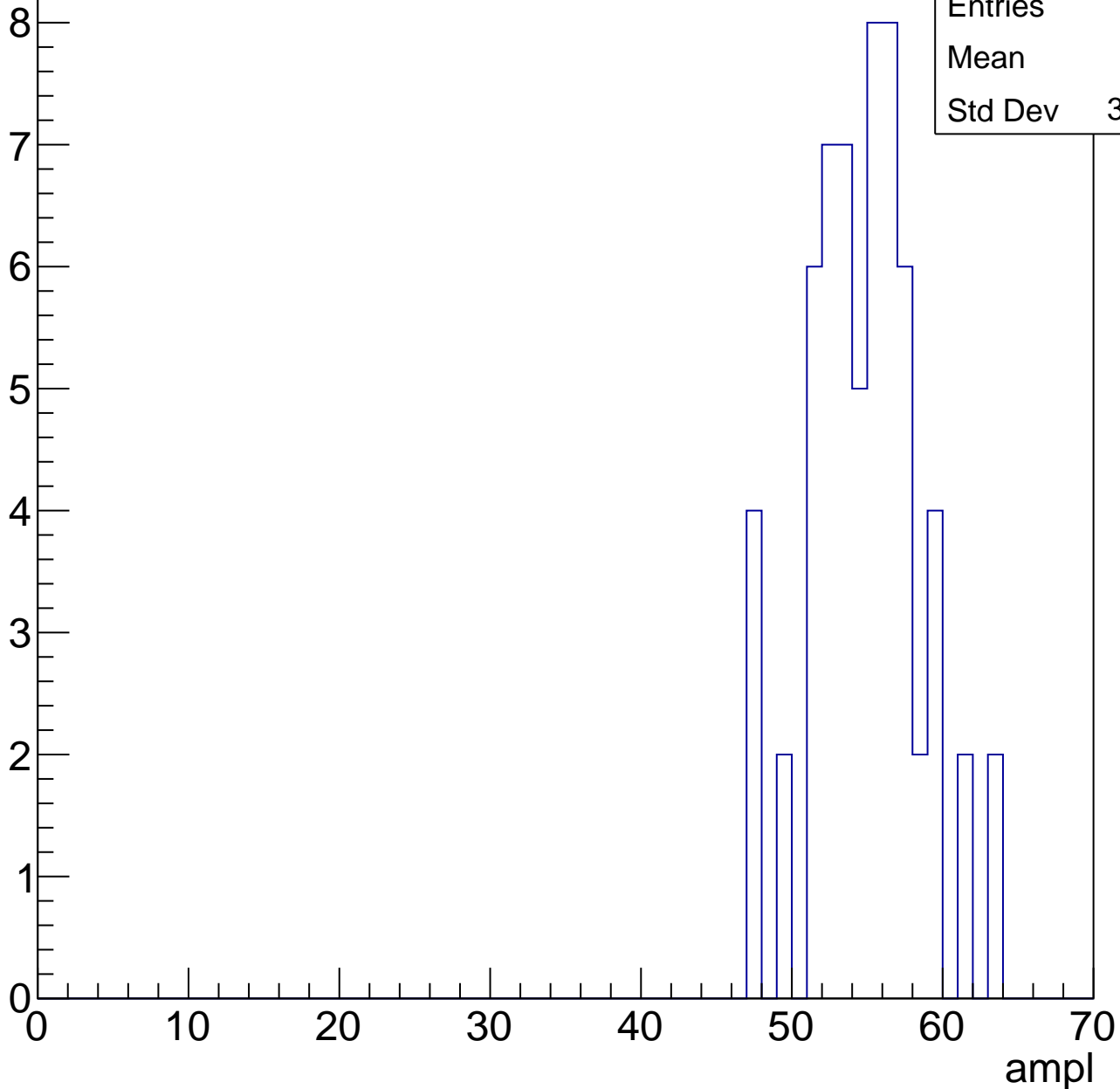


B1L100S, U25-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	54.4
Std Dev	3.588

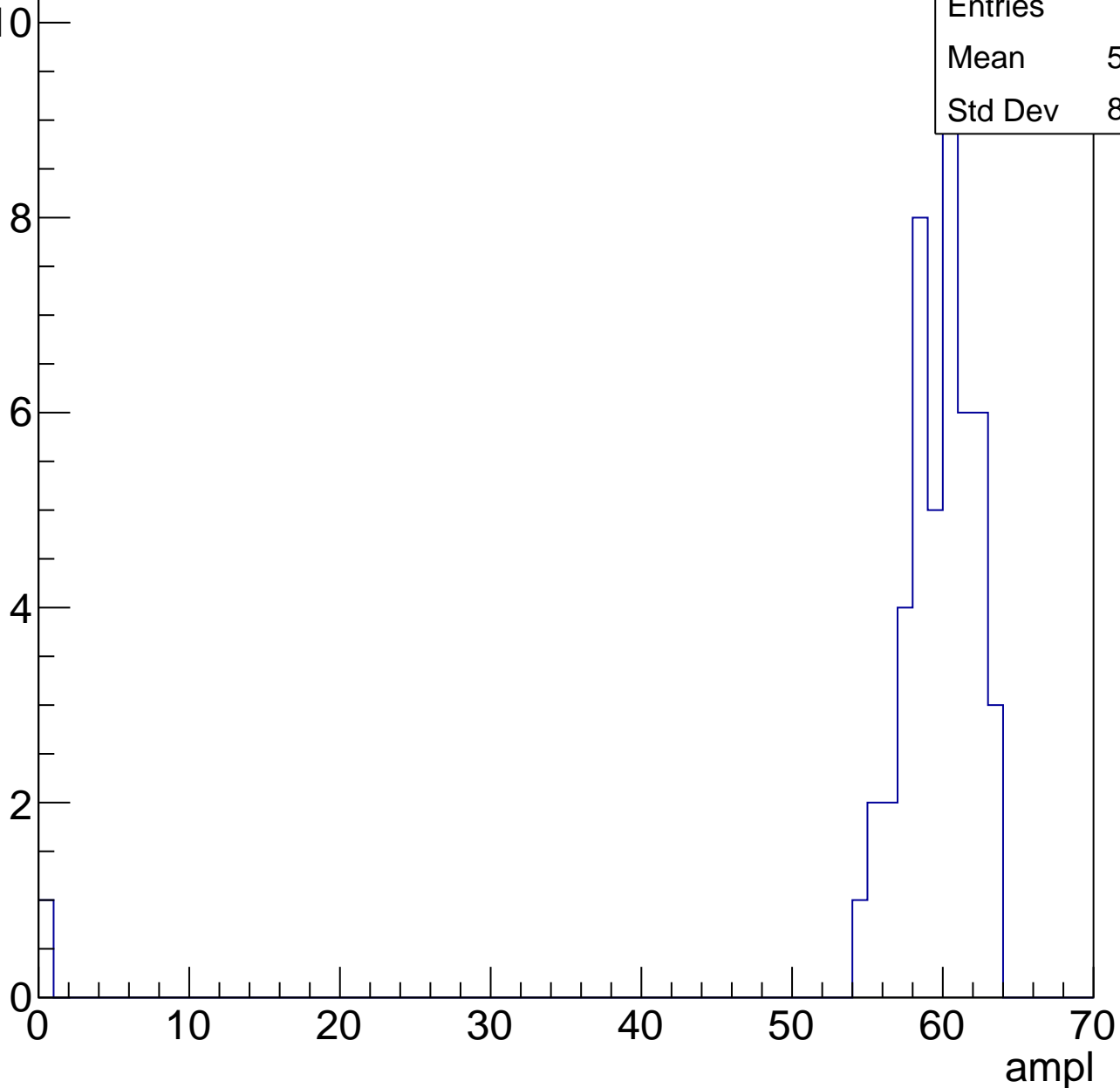


B1L100S, U25-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.12
Std Dev	8.755

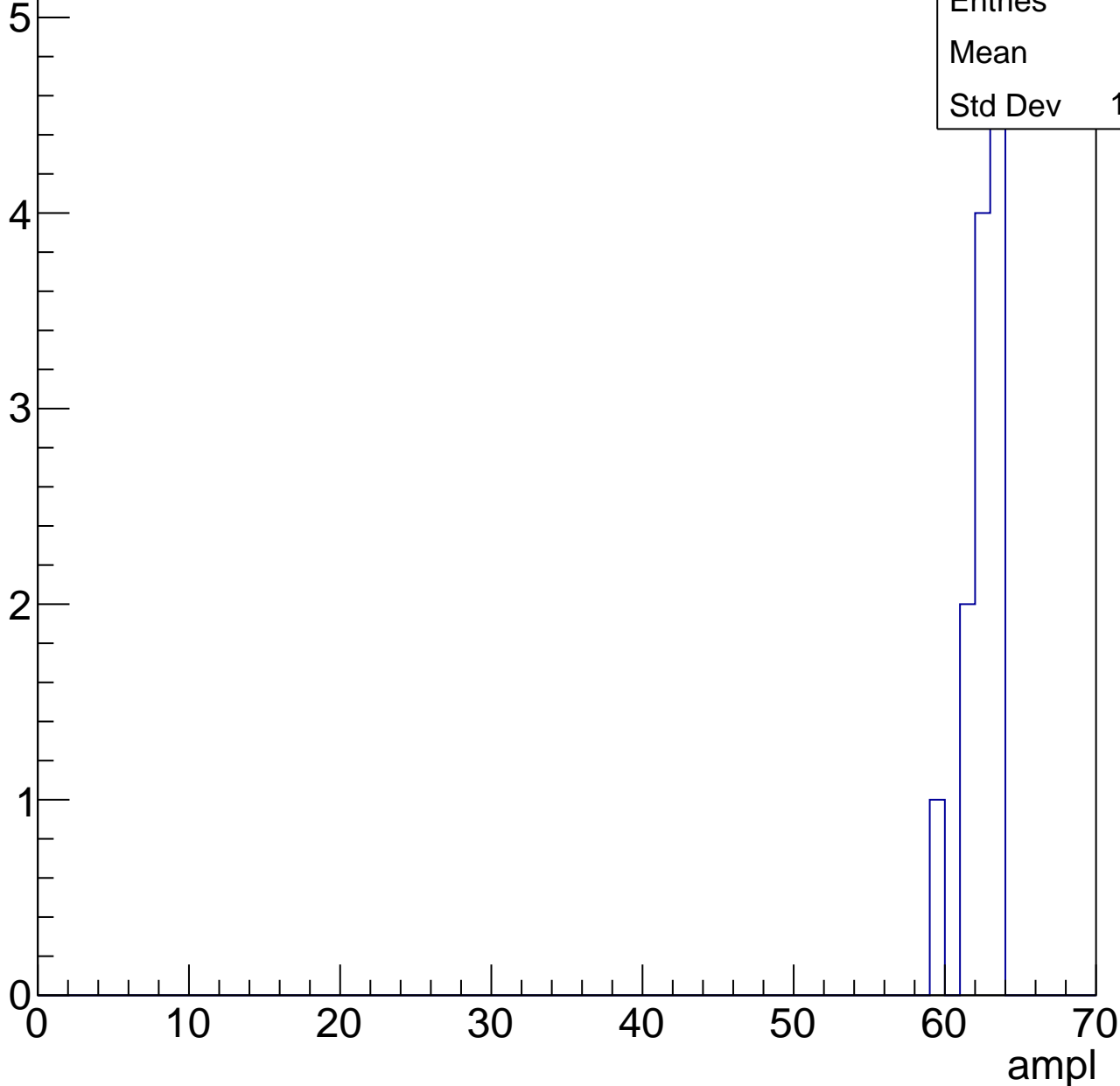


B1L100S, U25-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	62
Std Dev	1.155



B1L100S, U25-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch107, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	30.15
Std Dev	3.248

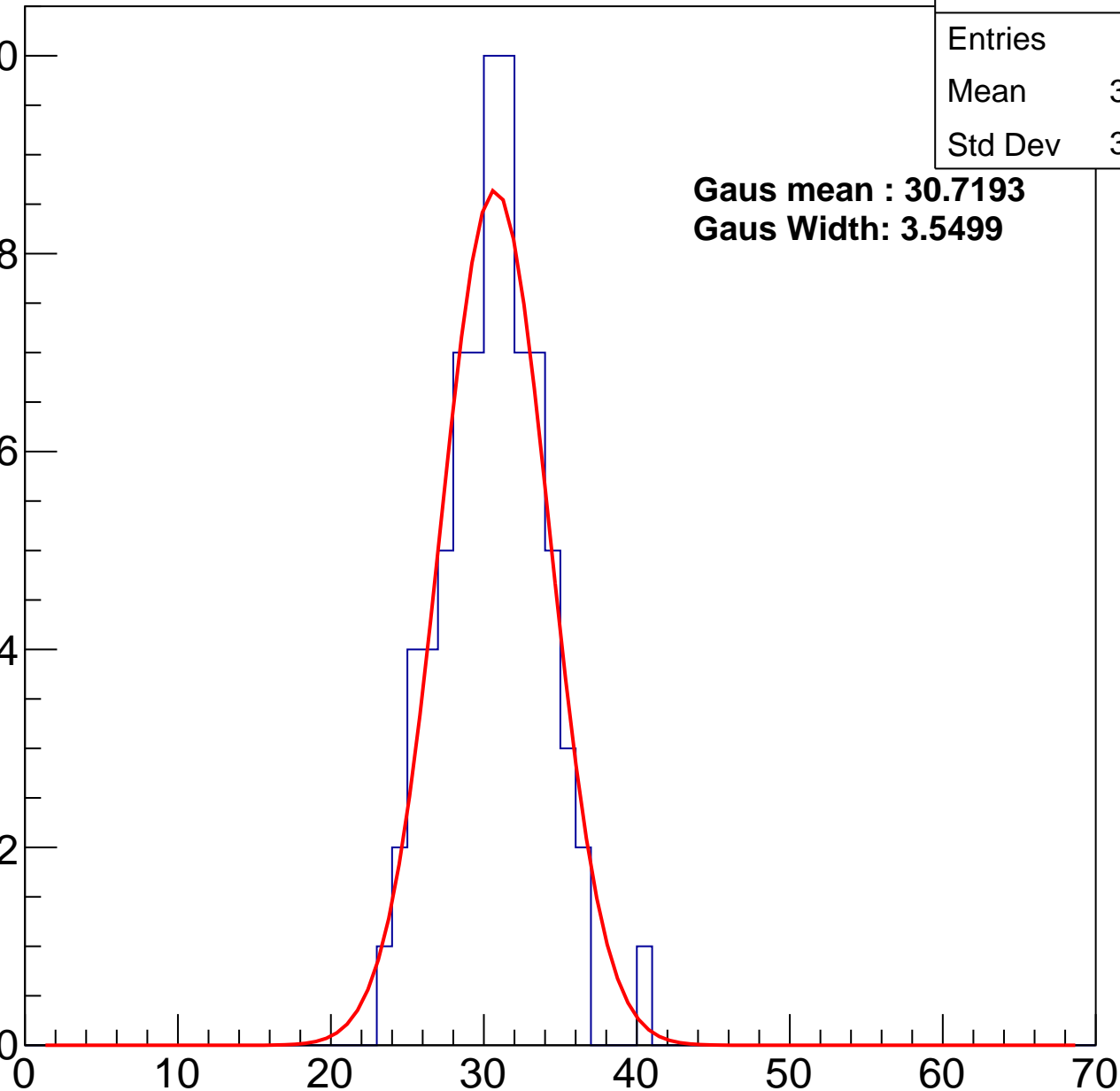
Gaus mean : 30.7193

Gaus Width: 3.5499

Entry

10
8
6
4
2
0

ampl



B1L100S, U25-ch107, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	37.36
Std Dev	2.843

Gaus mean : 37.5882

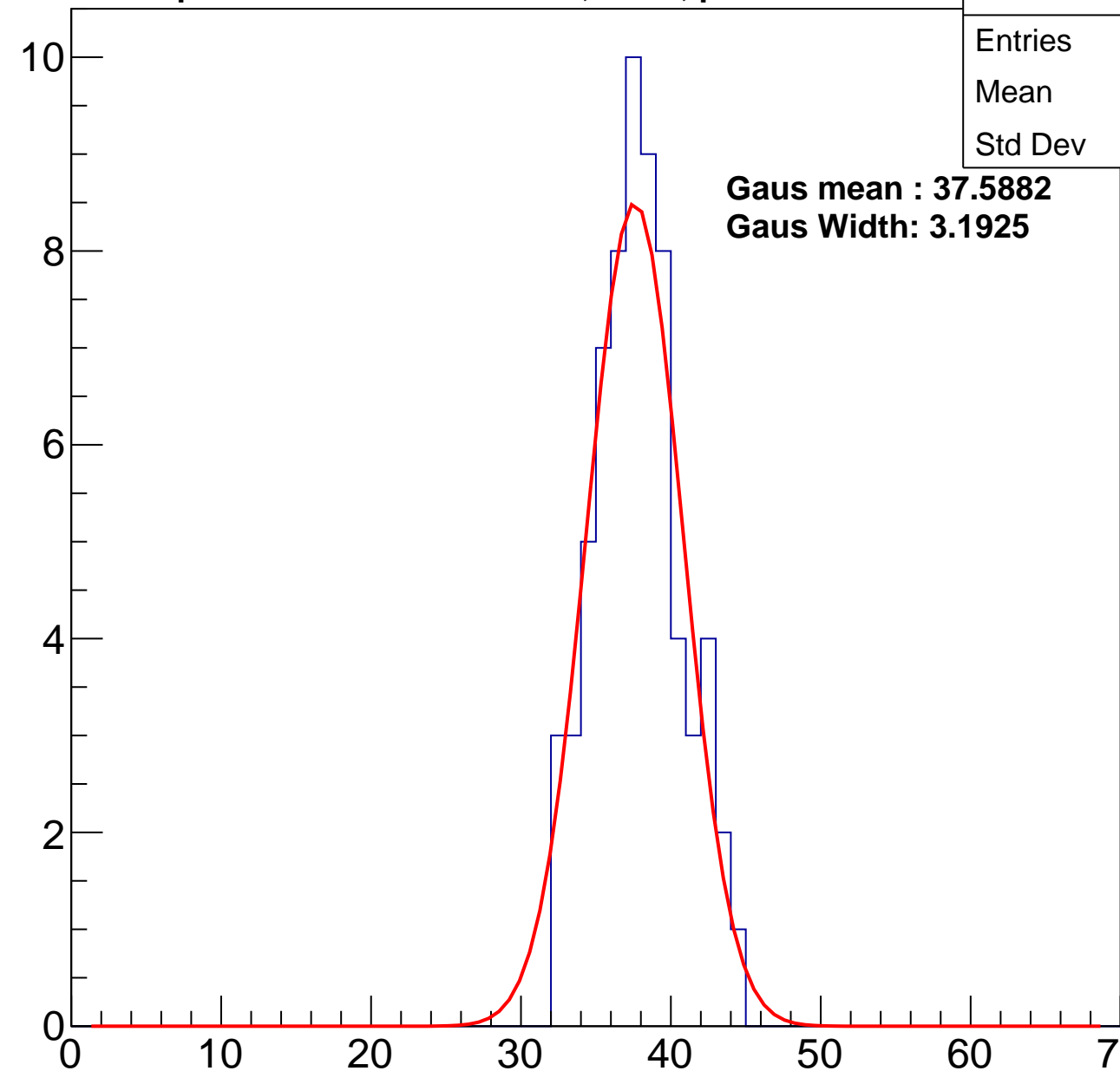
Gaus Width: 3.1925

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch107, adc2

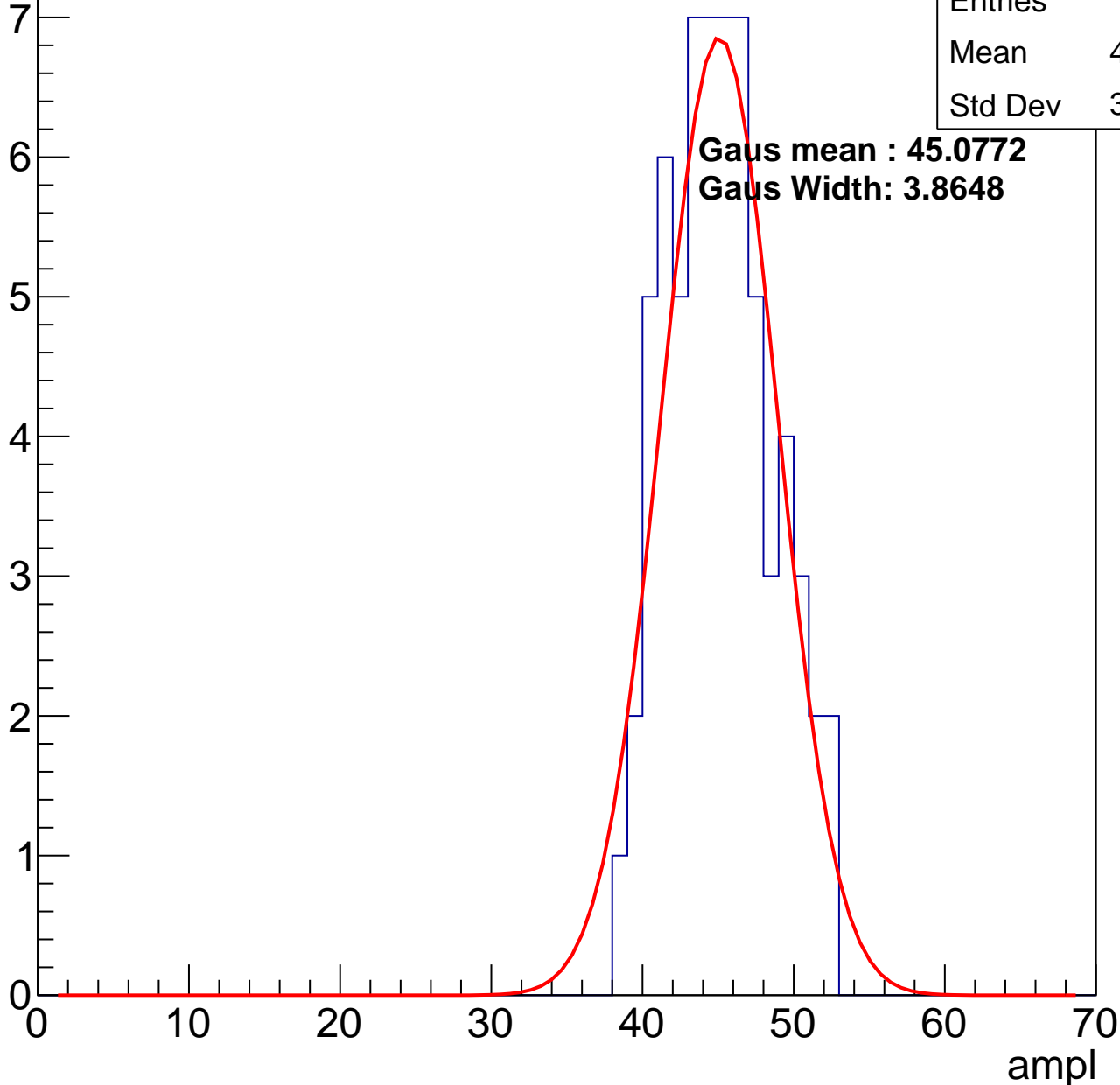
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.68
Std Dev	3.434

Gaus mean : 45.0772

Gaus Width: 3.8648

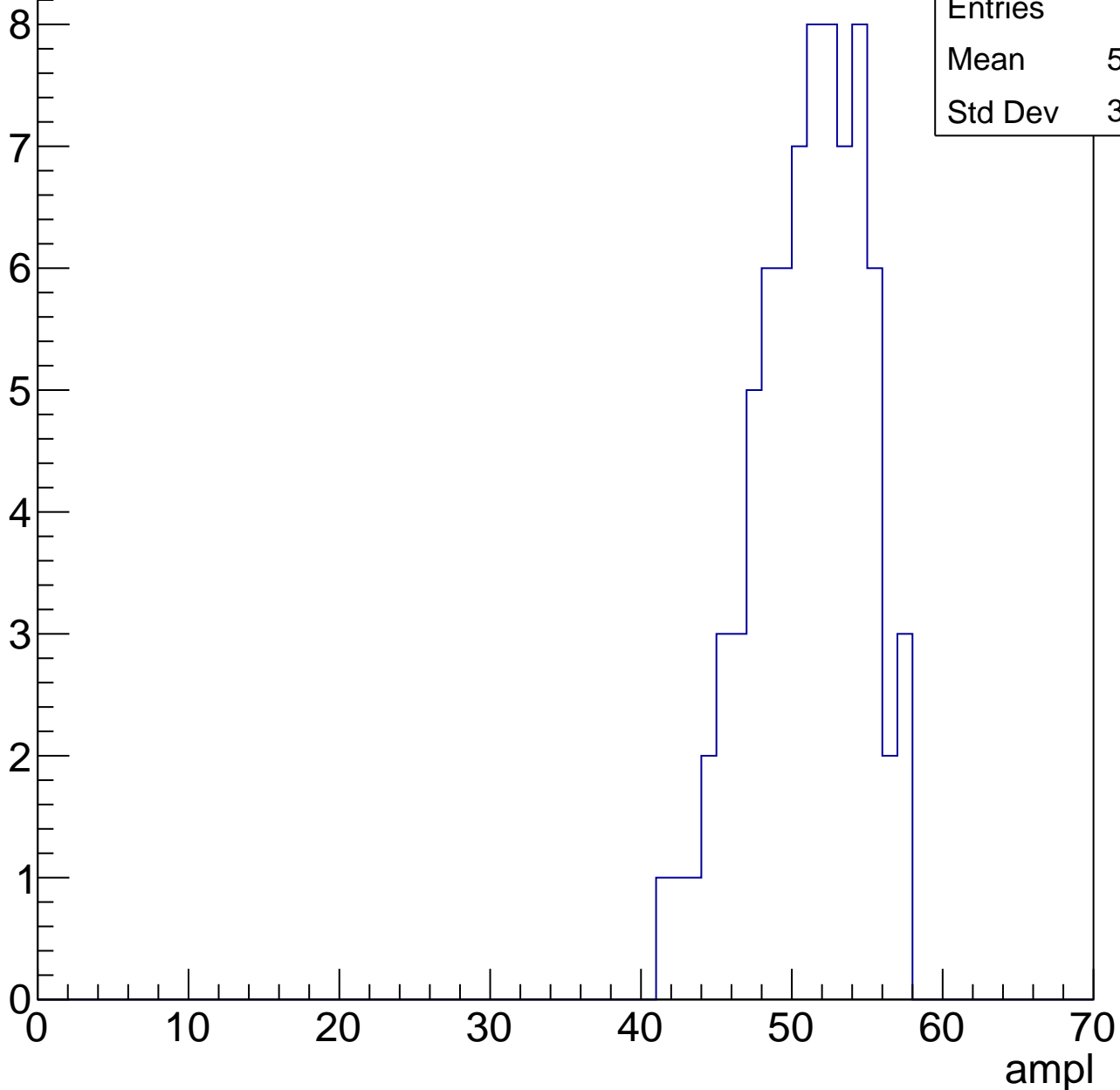


B1L100S, U25-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	50.57
Std Dev	3.673

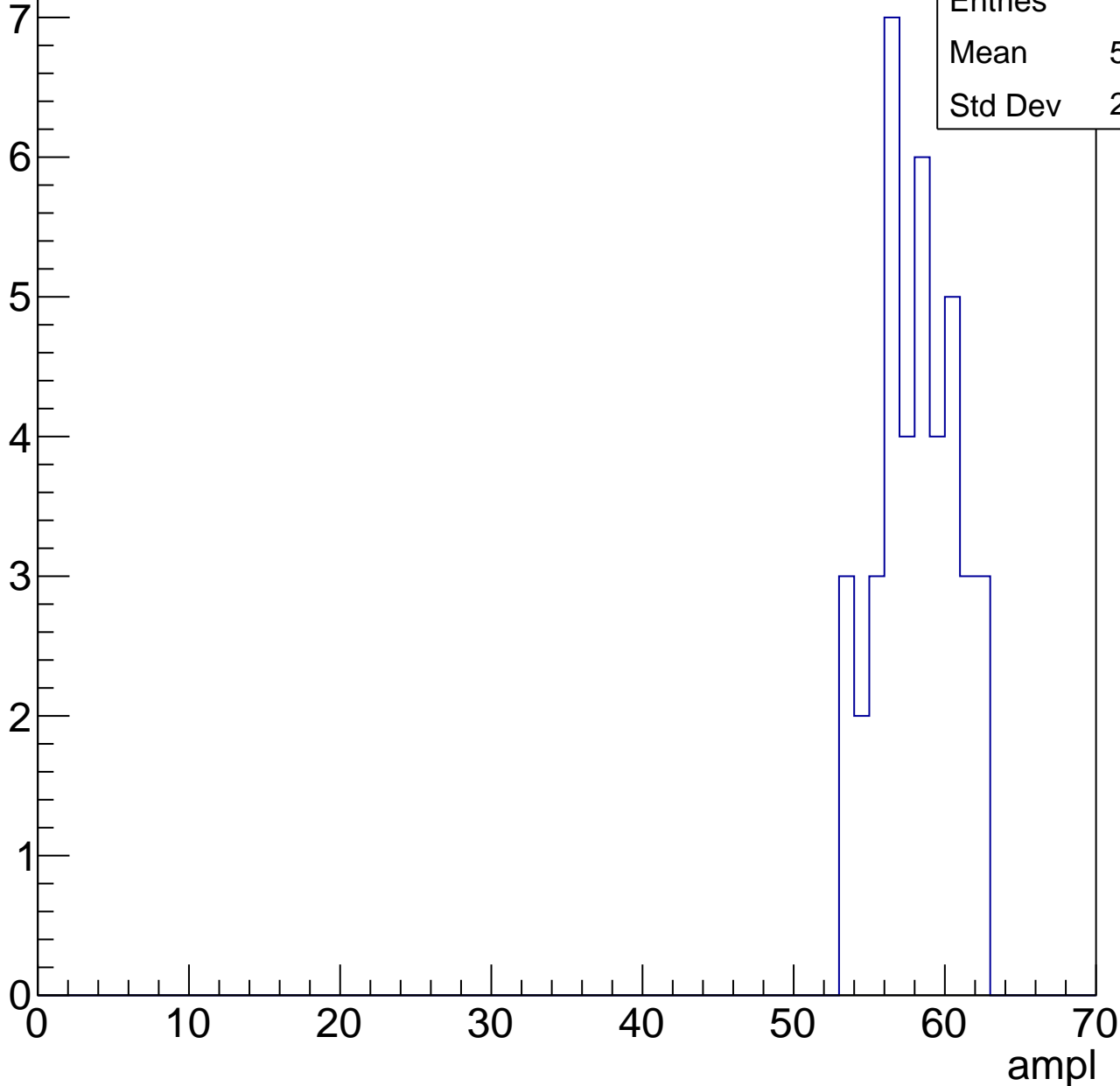


B1L100S, U25-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	57.62
Std Dev	2.546

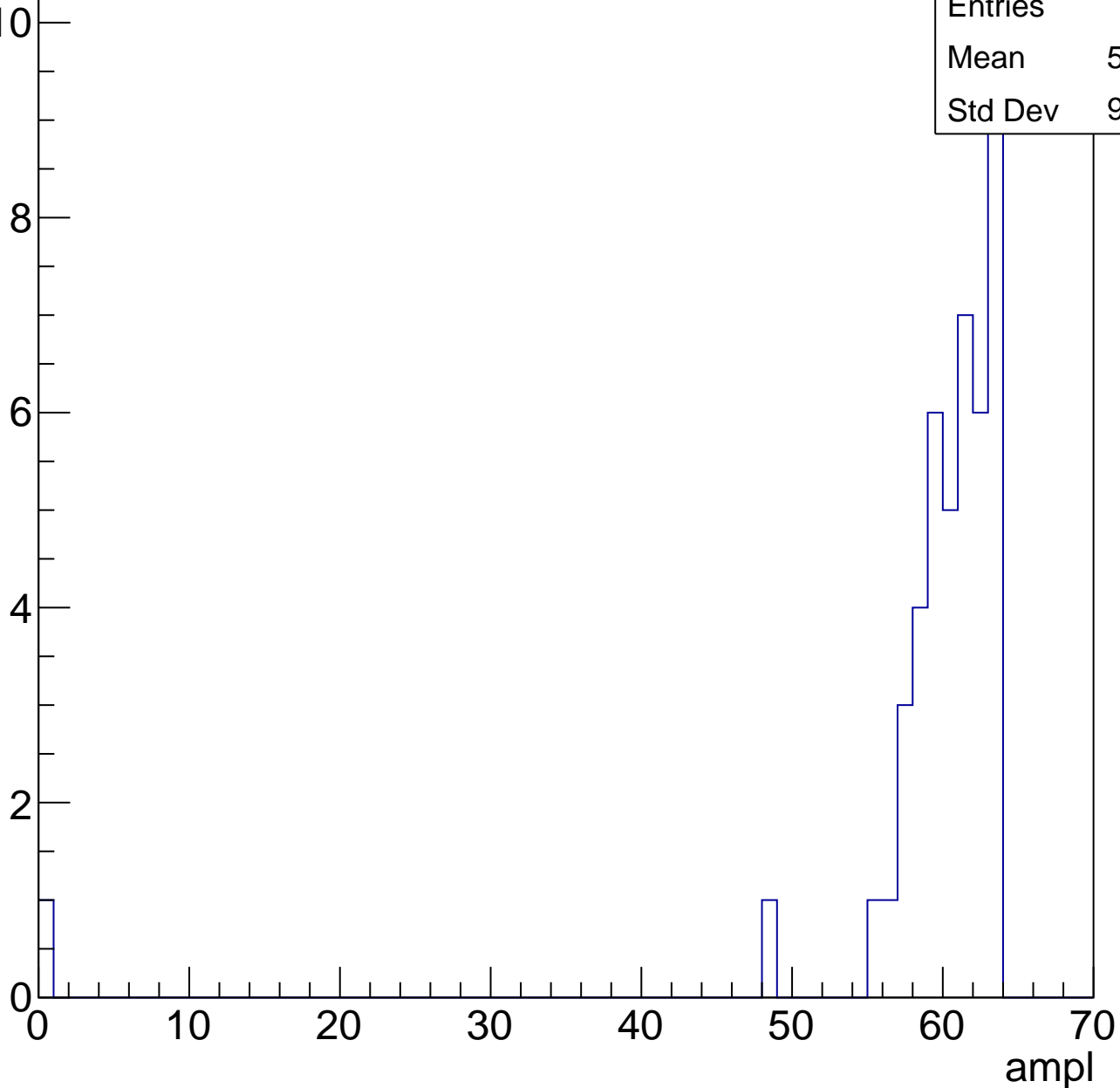


B1L100S, U25-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	58.78
Std Dev	9.295



B1L100S, U25-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch108, adc0

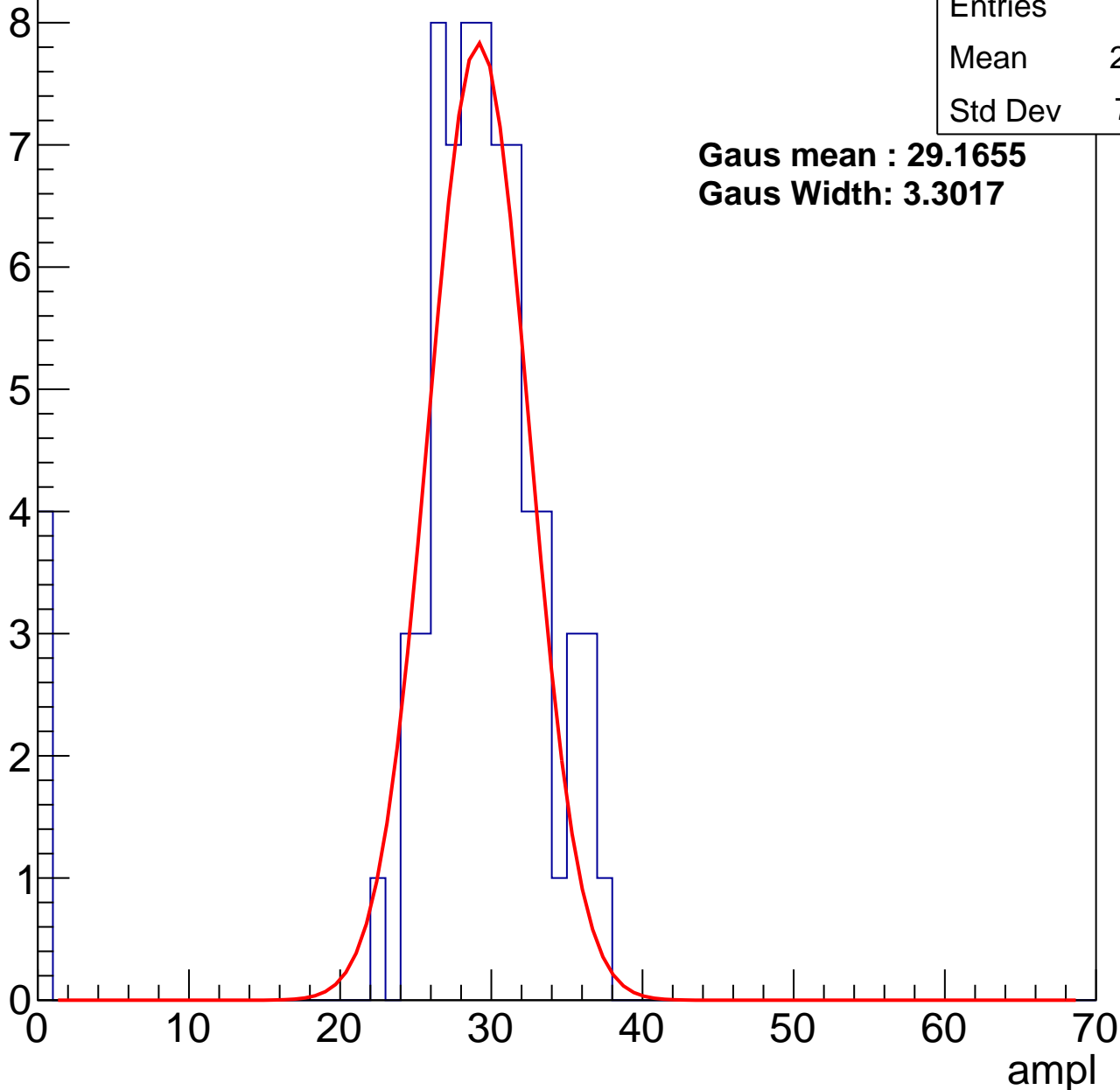
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	27.68
Std Dev	7.451

Gaus mean : 29.1655

Gaus Width: 3.3017



B1L100S, U25-ch108, adc1

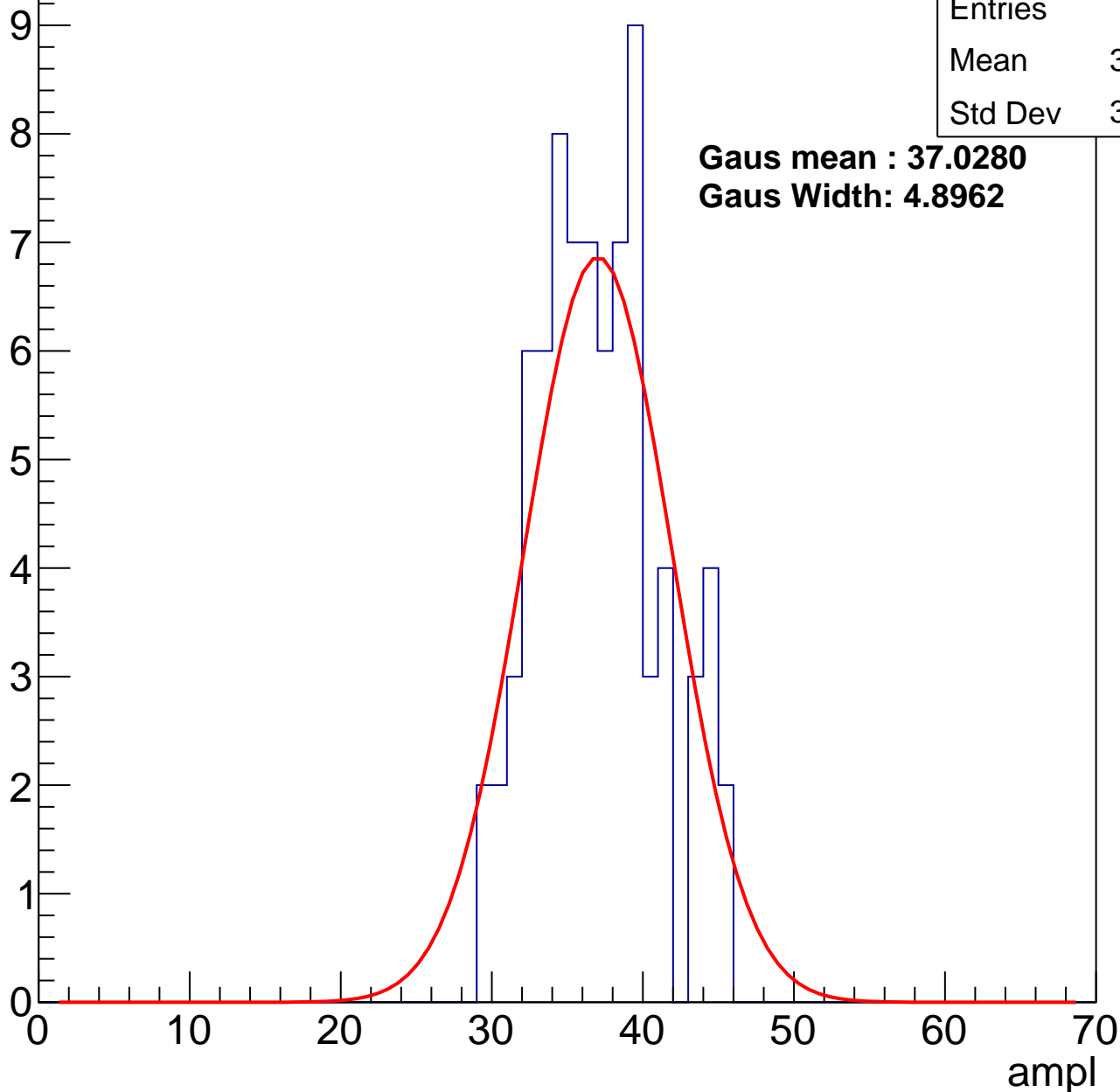
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	36.56
Std Dev	3.958

Gaus mean : 37.0280

Gaus Width: 4.8962



B1L100S, U25-ch108, adc2

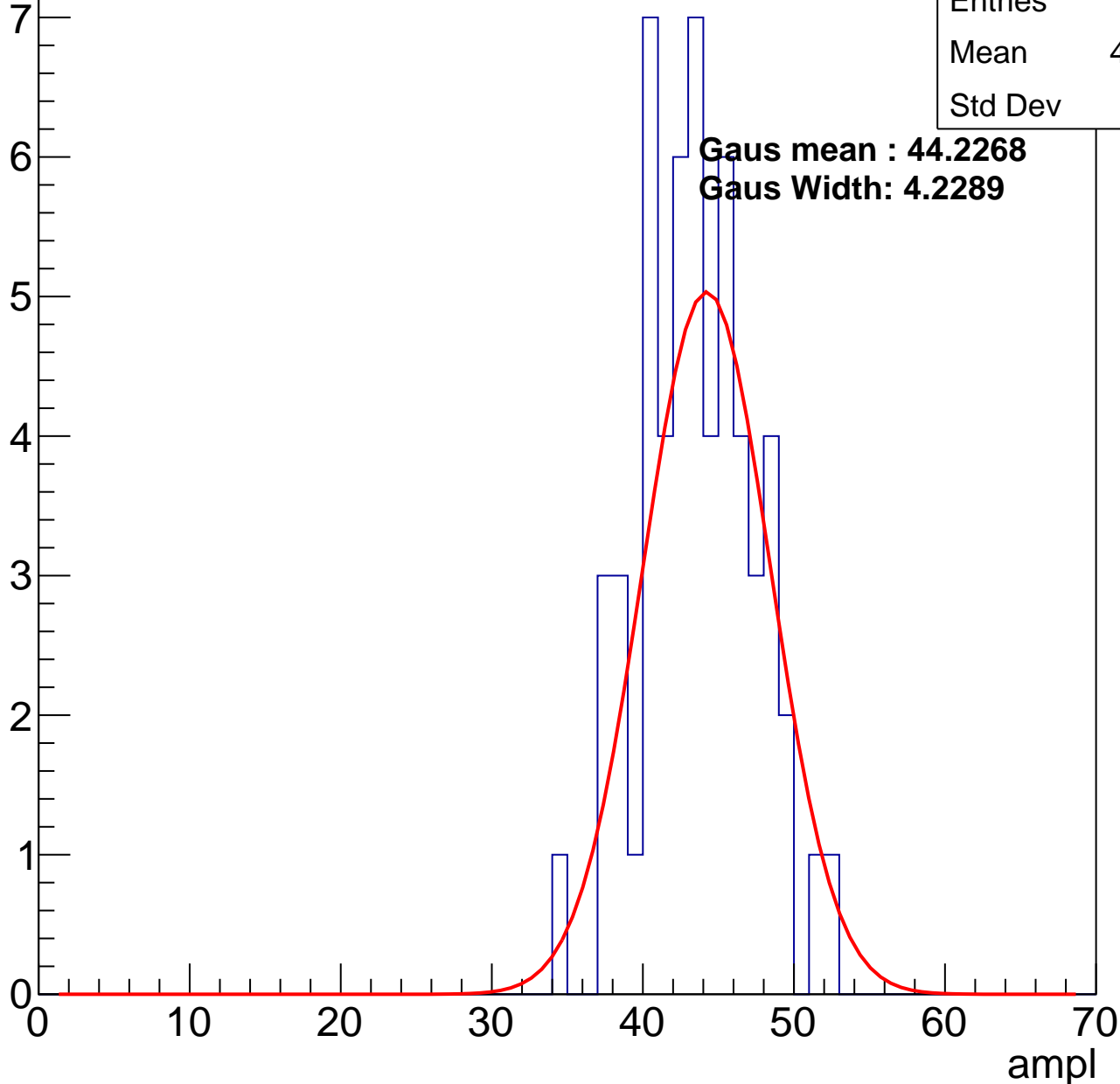
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	43.14
Std Dev	3.73

Gaus mean : 44.2268

Gaus Width: 4.2289

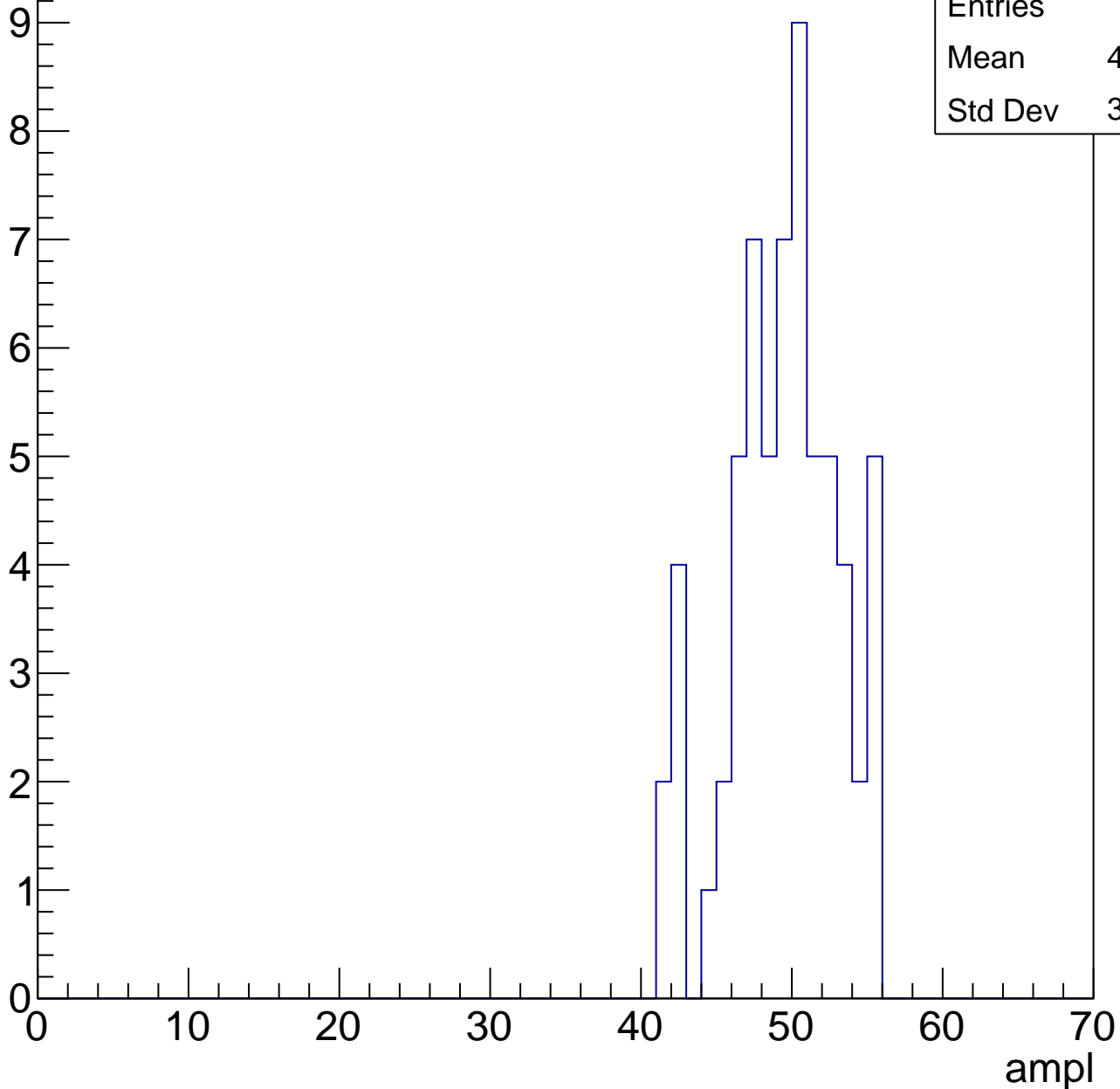


B1L100S, U25-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.98
Std Dev	3.623

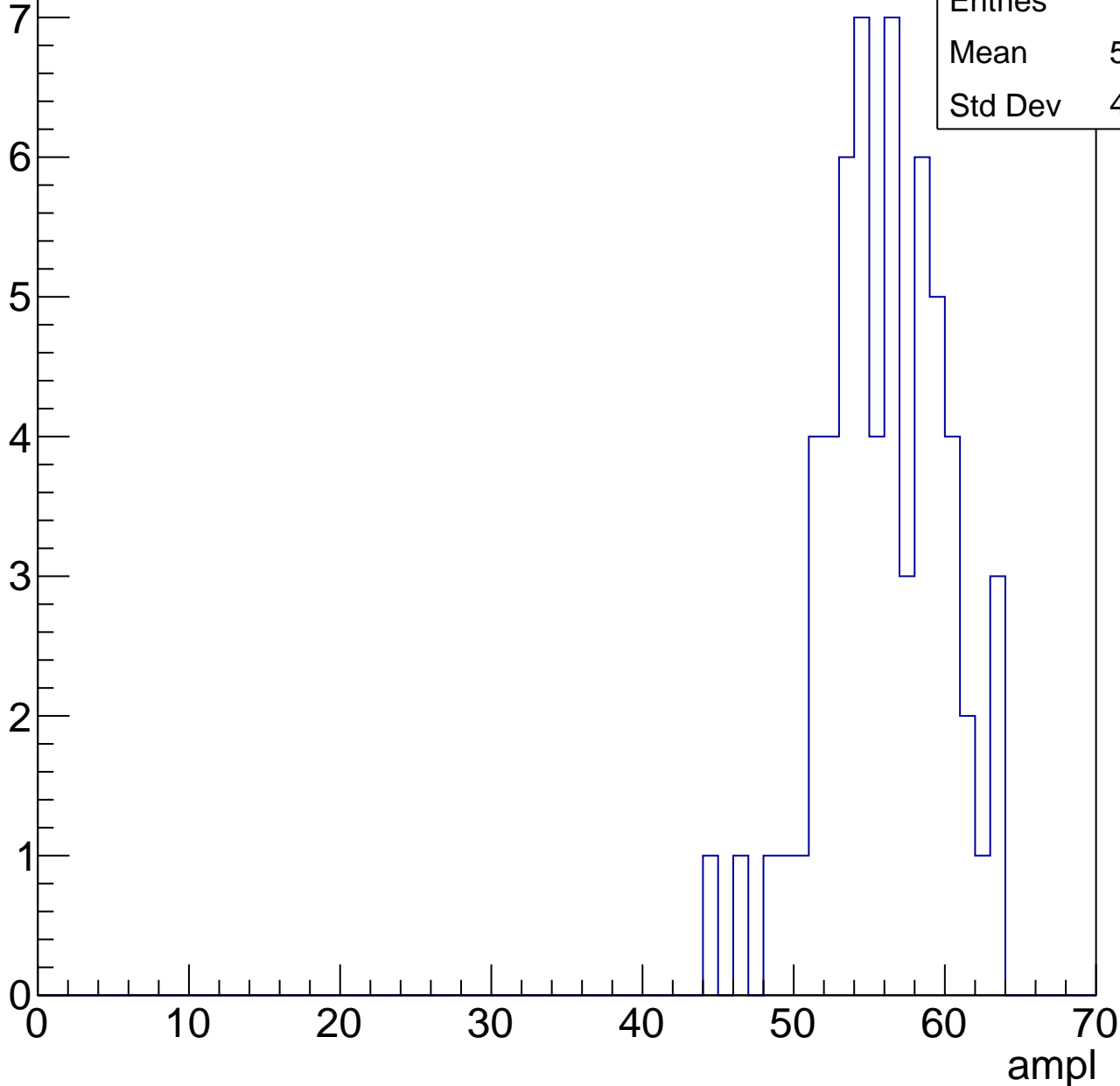


B1L100S, U25-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

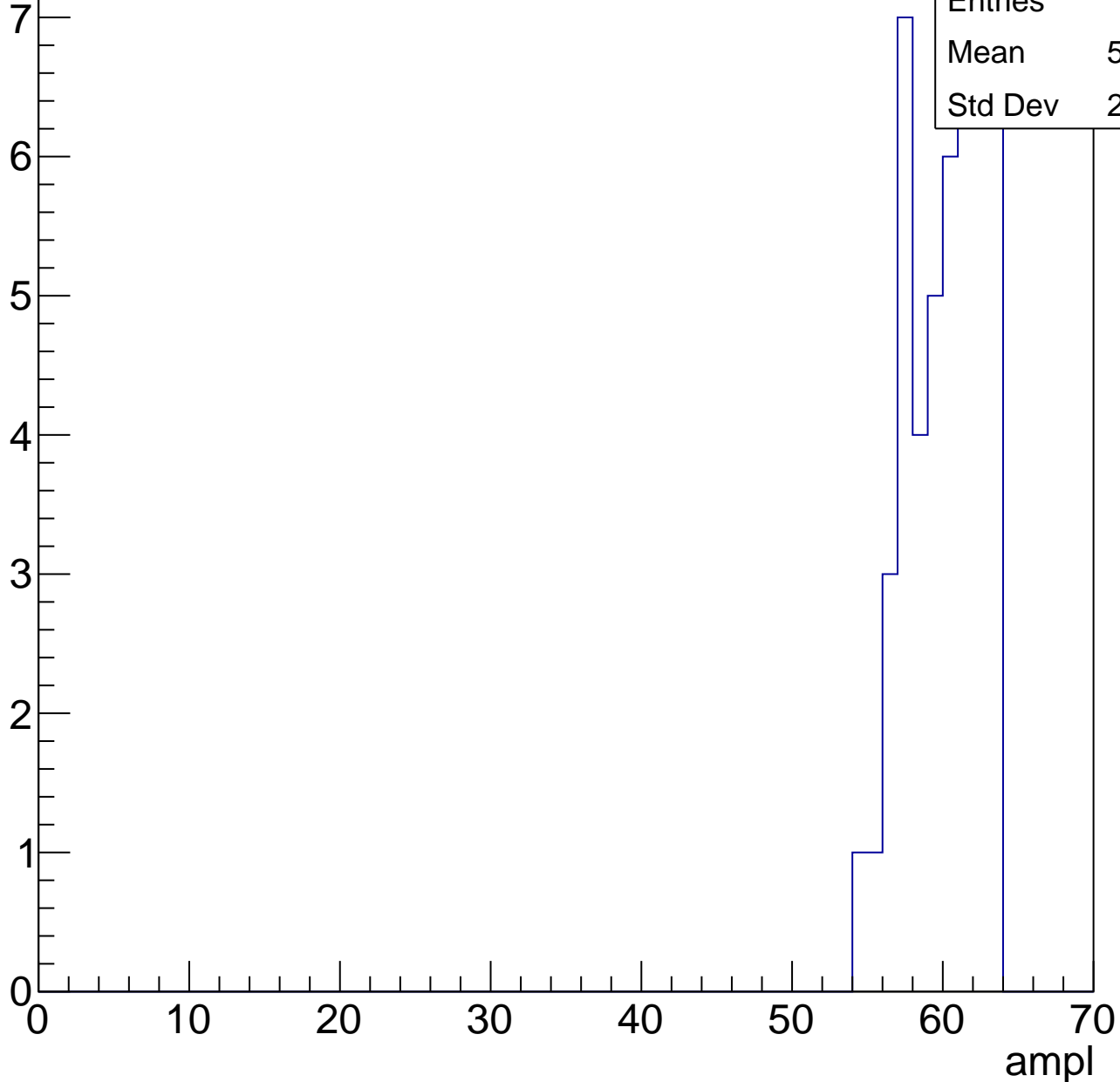
Entries	61
Mean	55.48
Std Dev	4.039



B1L100S, U25-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

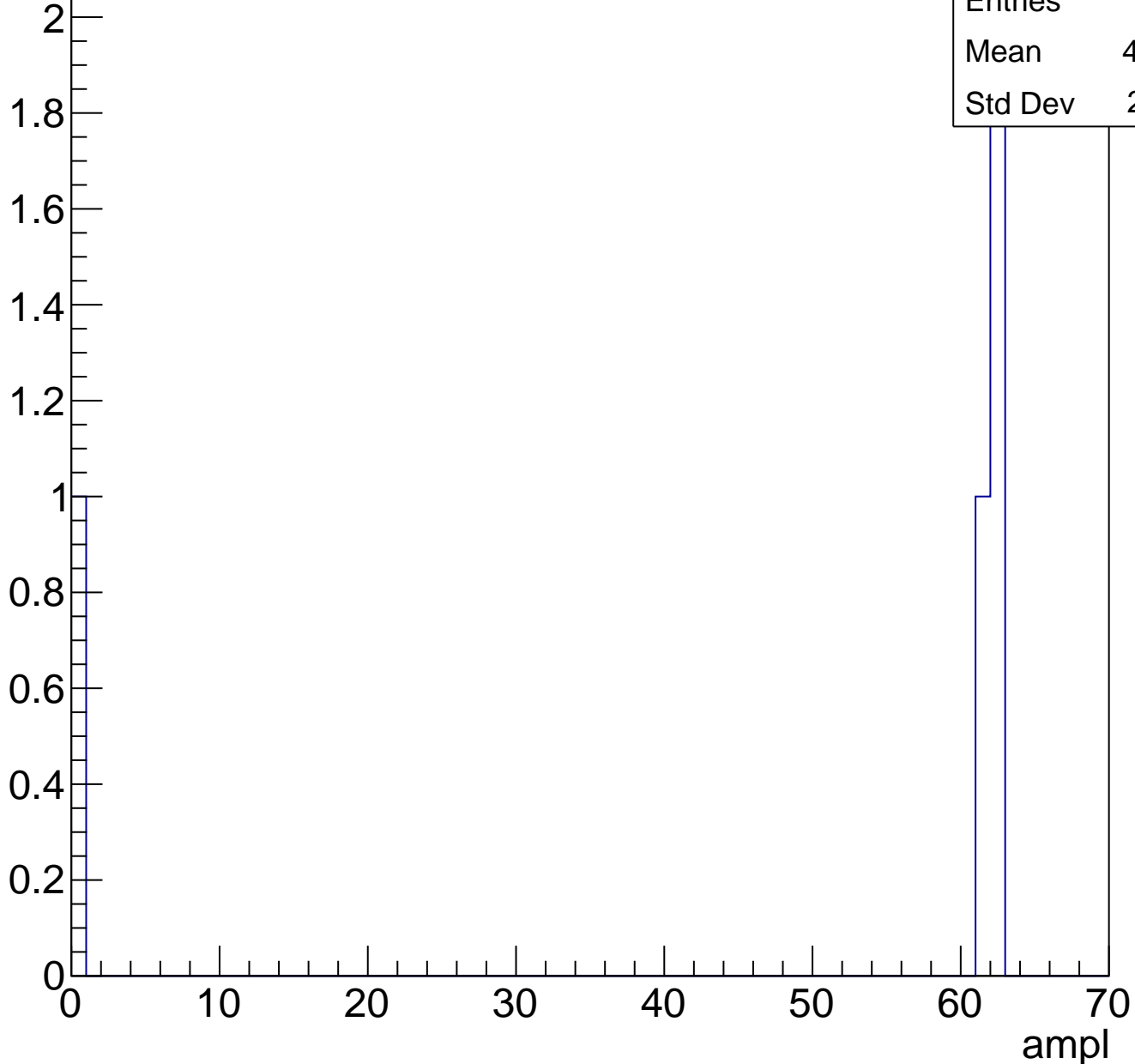
Entry



B1L100S, U25-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch109, adc0

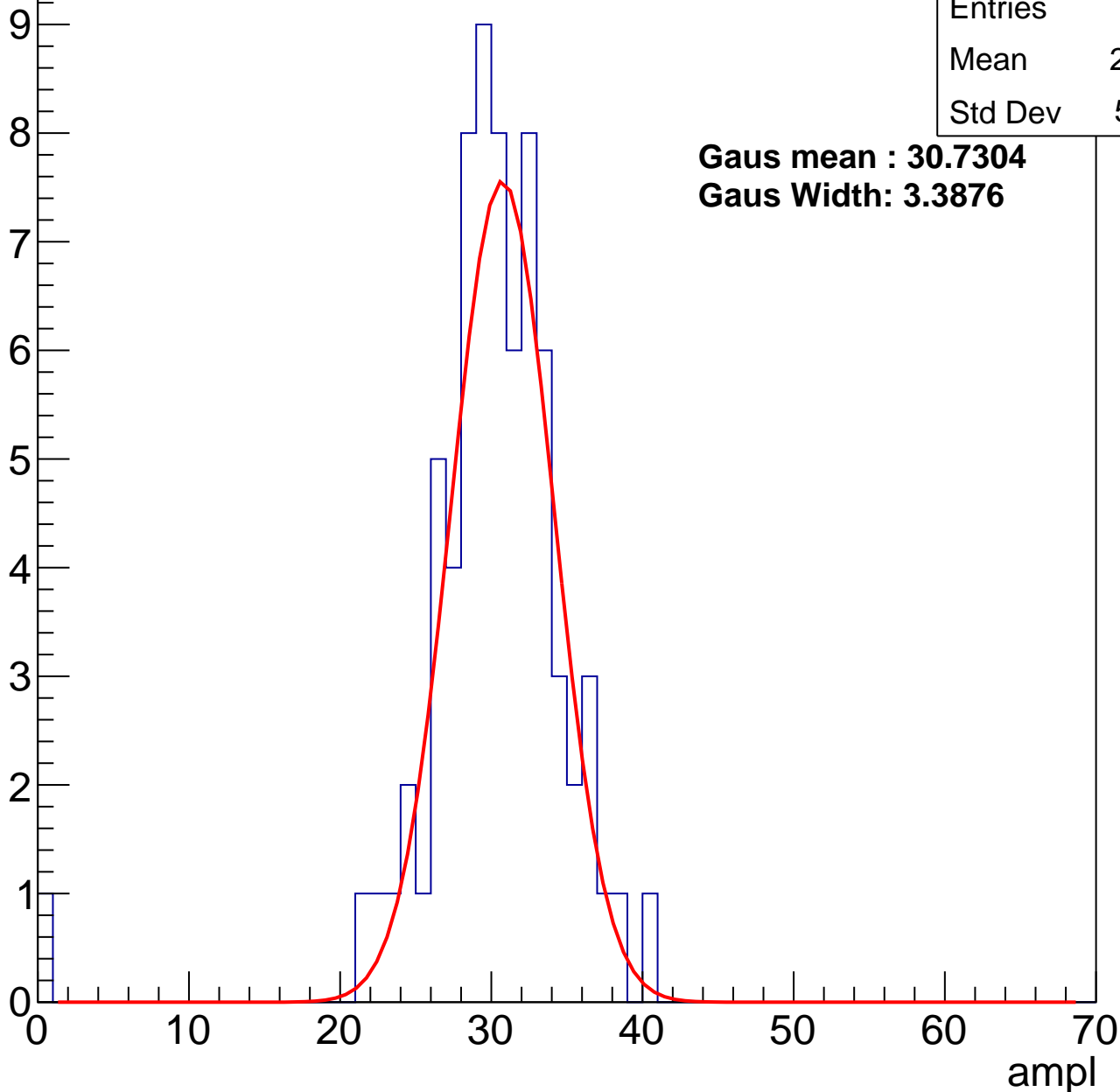
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.68
Std Dev	5.071

Gaus mean : 30.7304

Gaus Width: 3.3876



B1L100S, U25-ch109, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

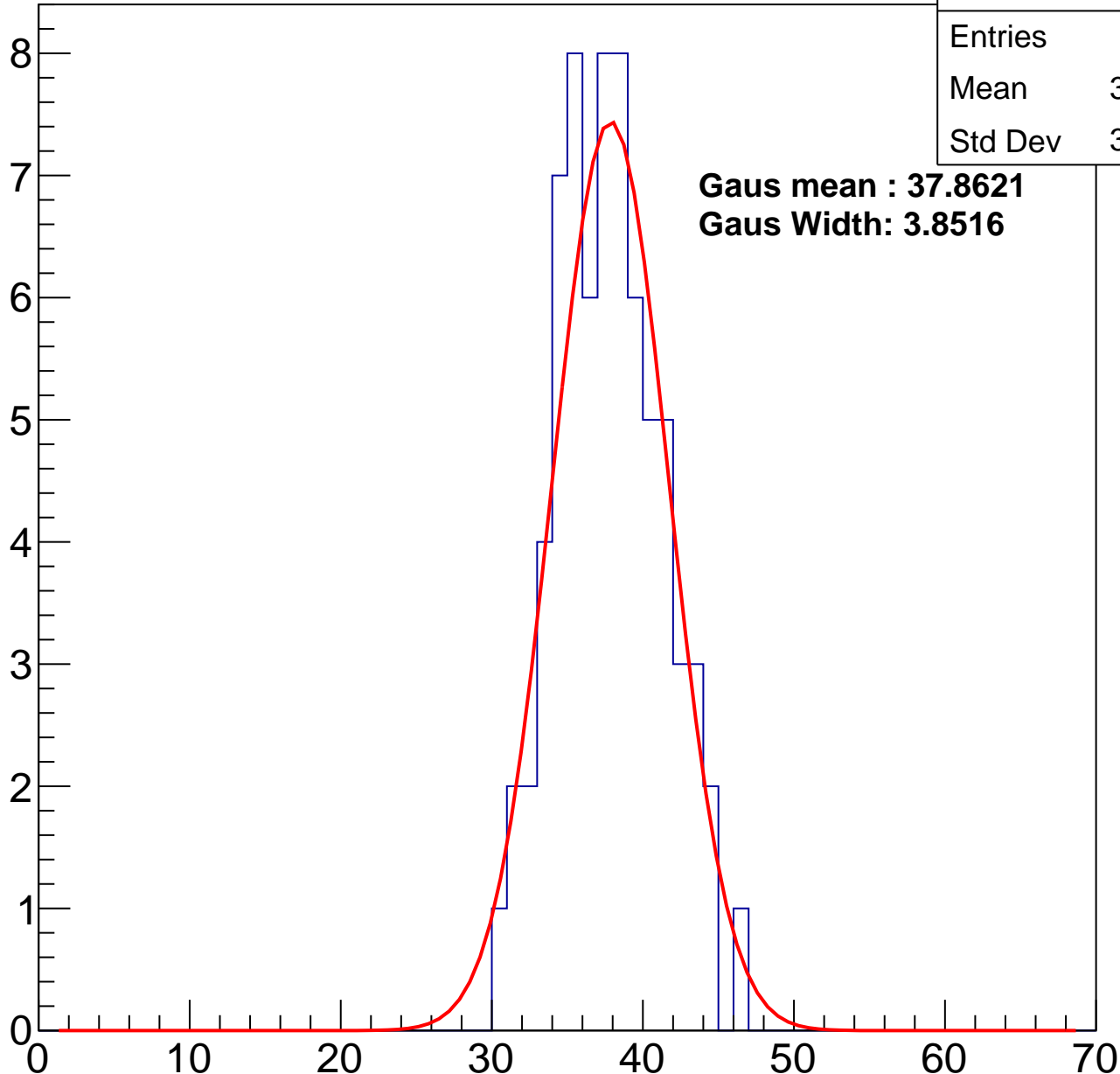
8
7
6
5
4
3
2
1
0

Entries	71
Mean	37.32
Std Dev	3.455

Gaus mean : 37.8621

Gaus Width: 3.8516

ampl



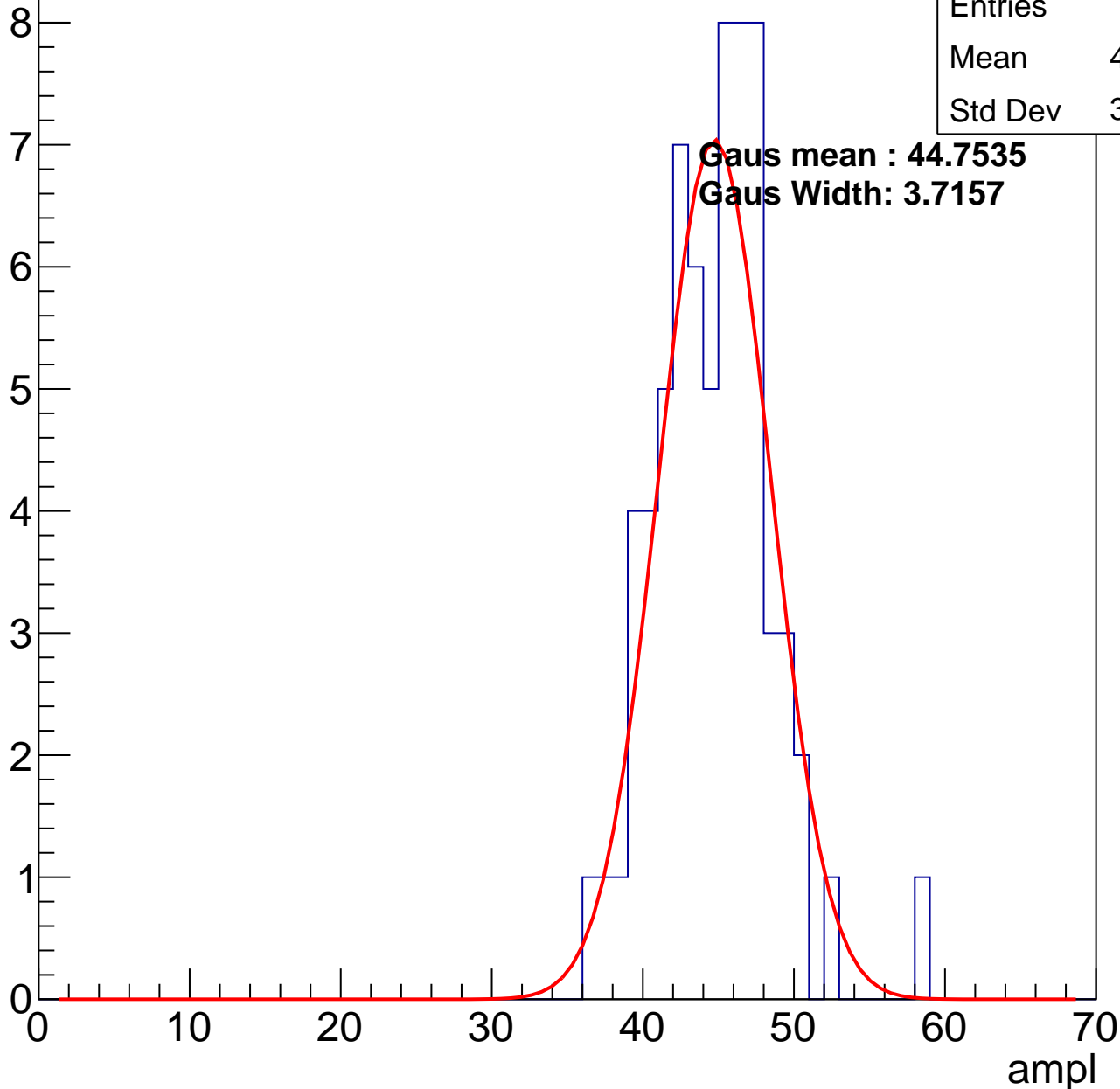
B1L100S, U25-ch109, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.25
Std Dev	3.747

Gaus mean : 44.7535
Gaus Width: 3.7157

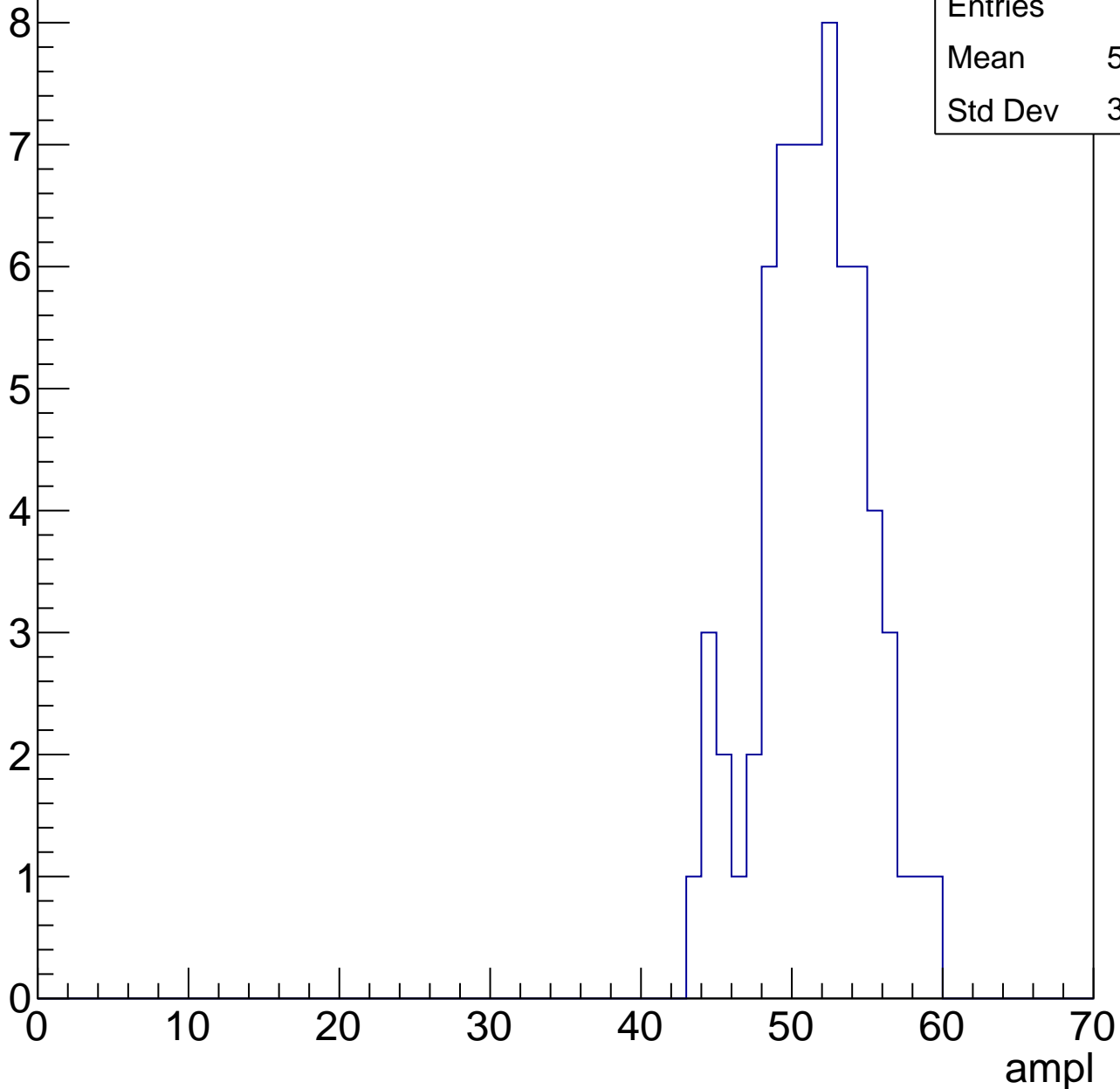


B1L100S, U25-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	50.95
Std Dev	3.488

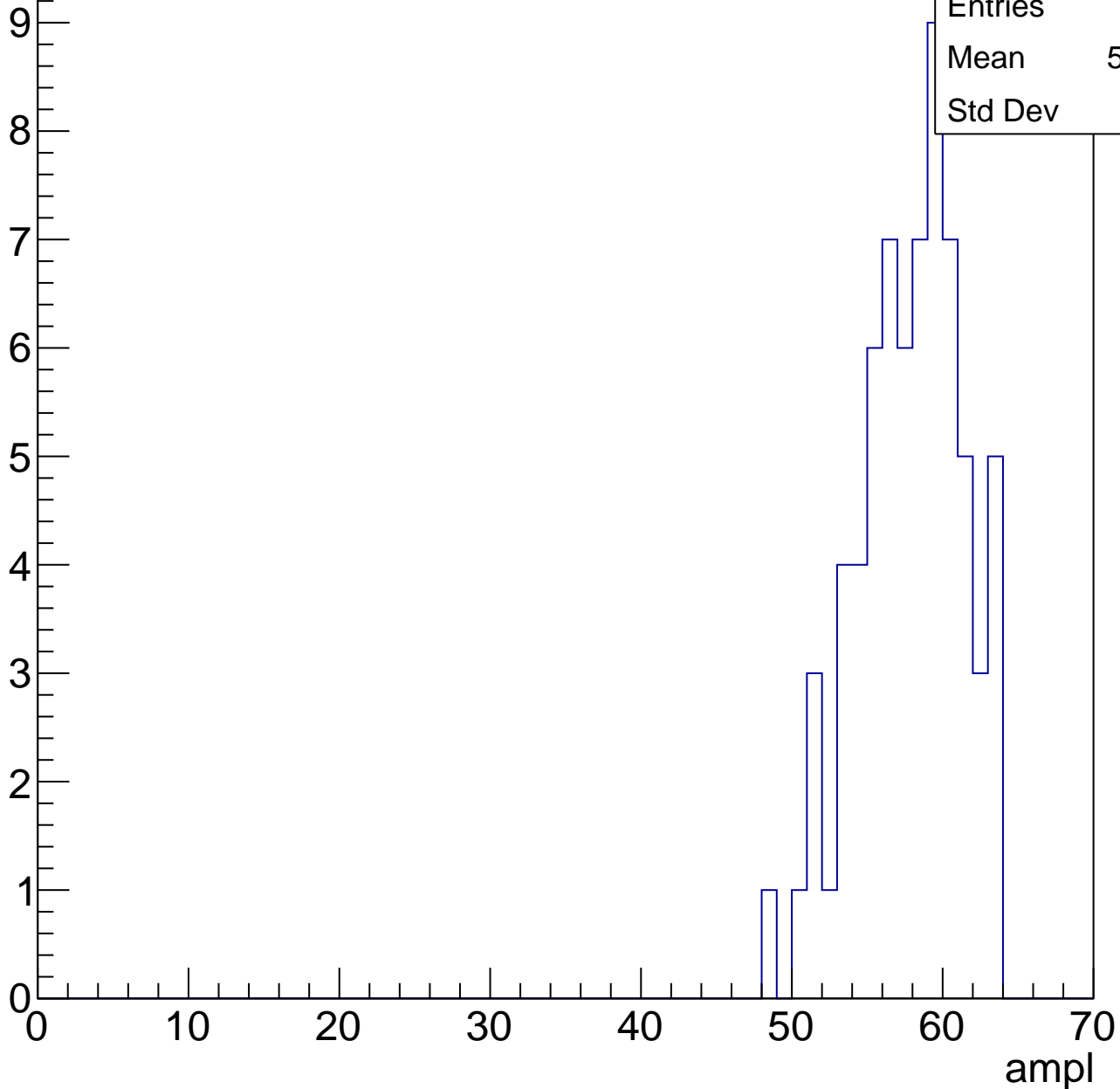


B1L100S, U25-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	57.36
Std Dev	3.46

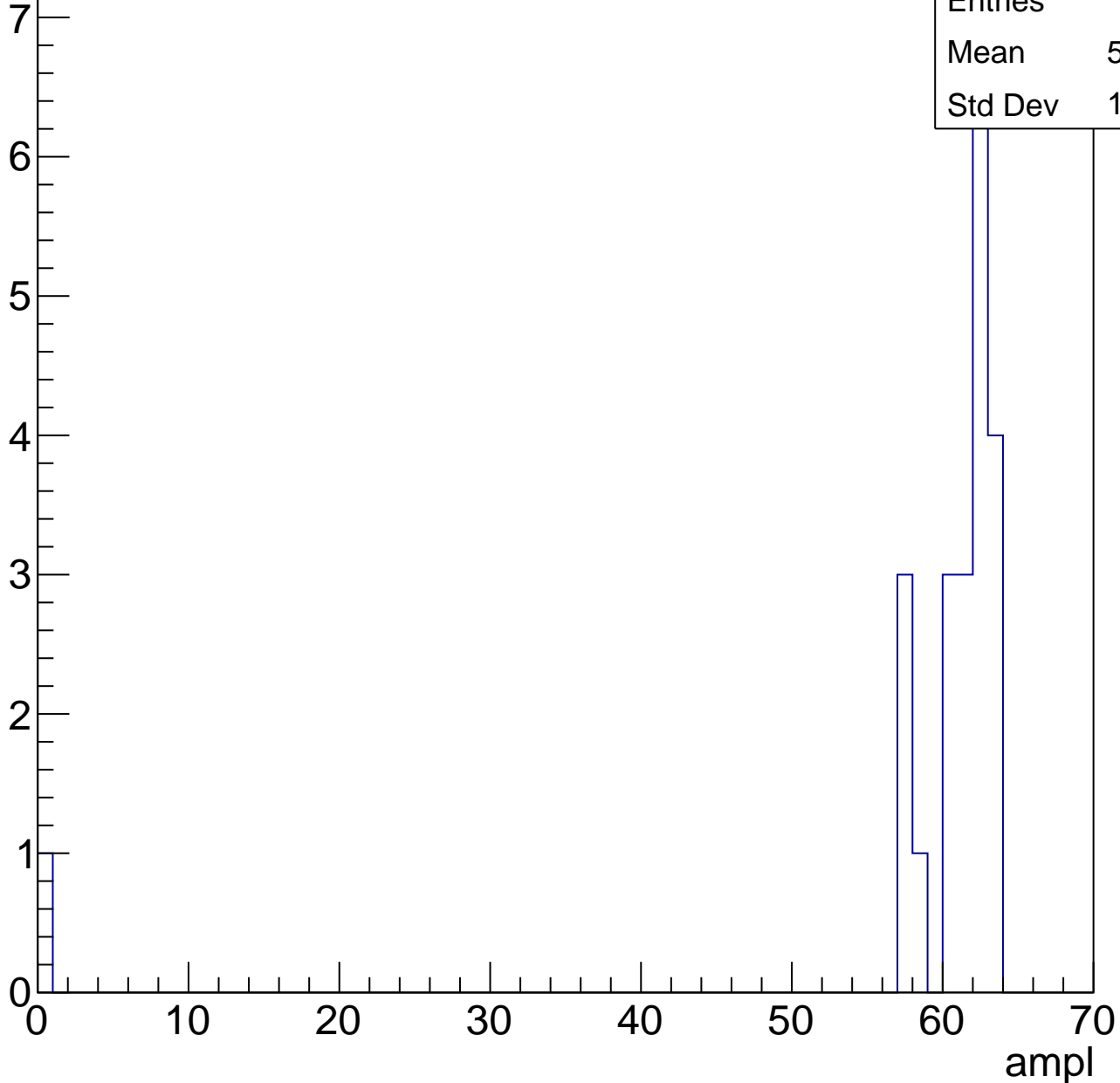


B1L100S, U25-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.09
Std Dev	12.82



B1L100S, U25-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

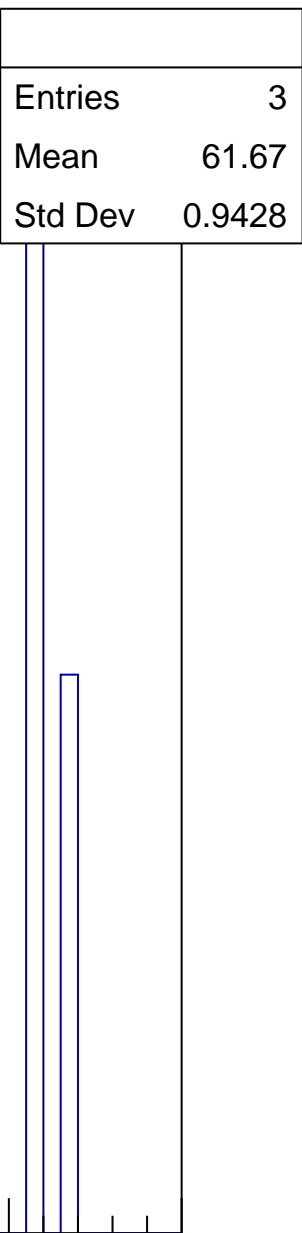
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U25-ch110, adc0

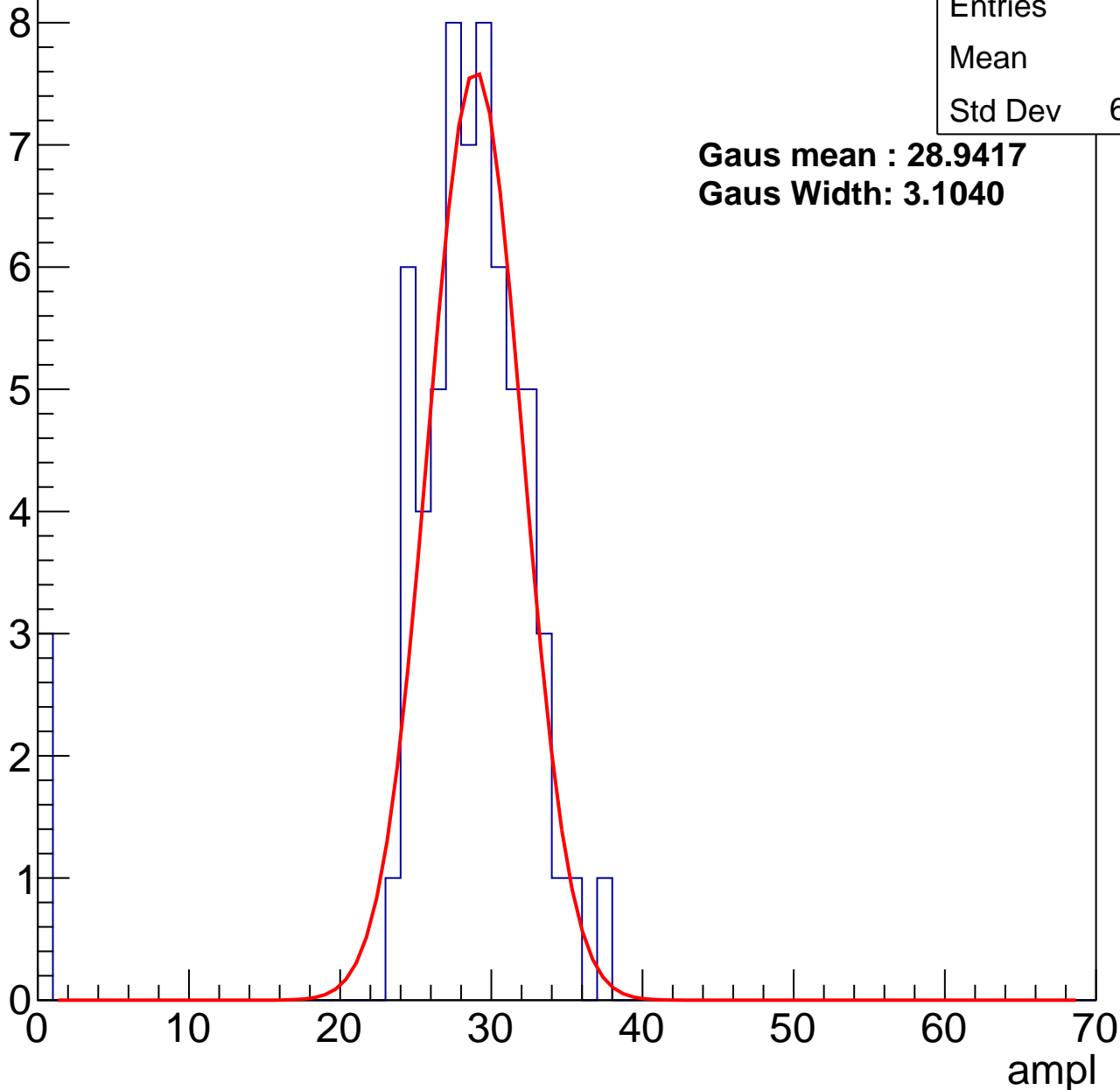
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	27.2
Std Dev	6.725

Gaus mean : 28.9417

Gaus Width: 3.1040



B1L100S, U25-ch110, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	34.78
Std Dev	3.376

Gaus mean : 35.1819

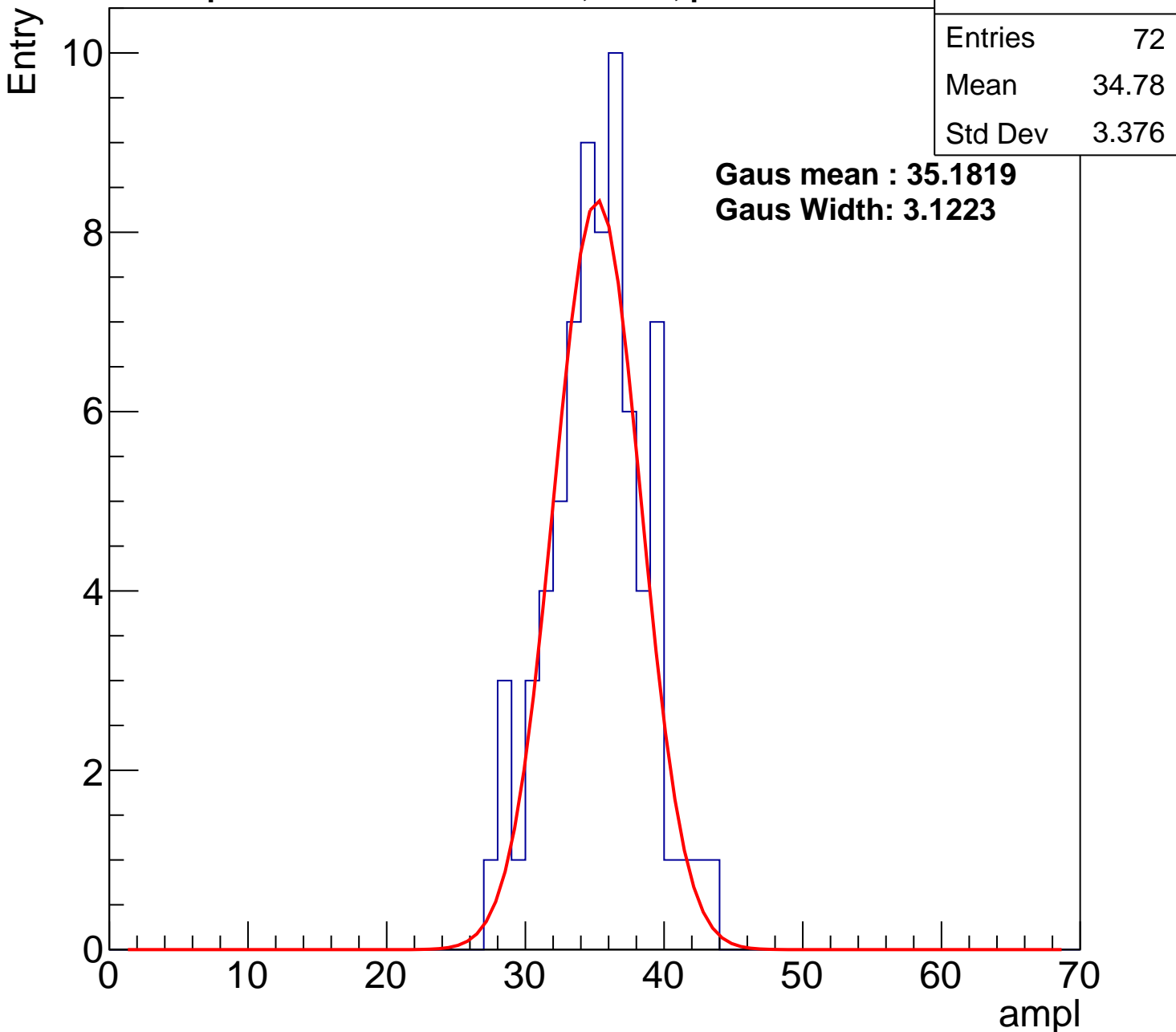
Gaus Width: 3.1223

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch110, adc2

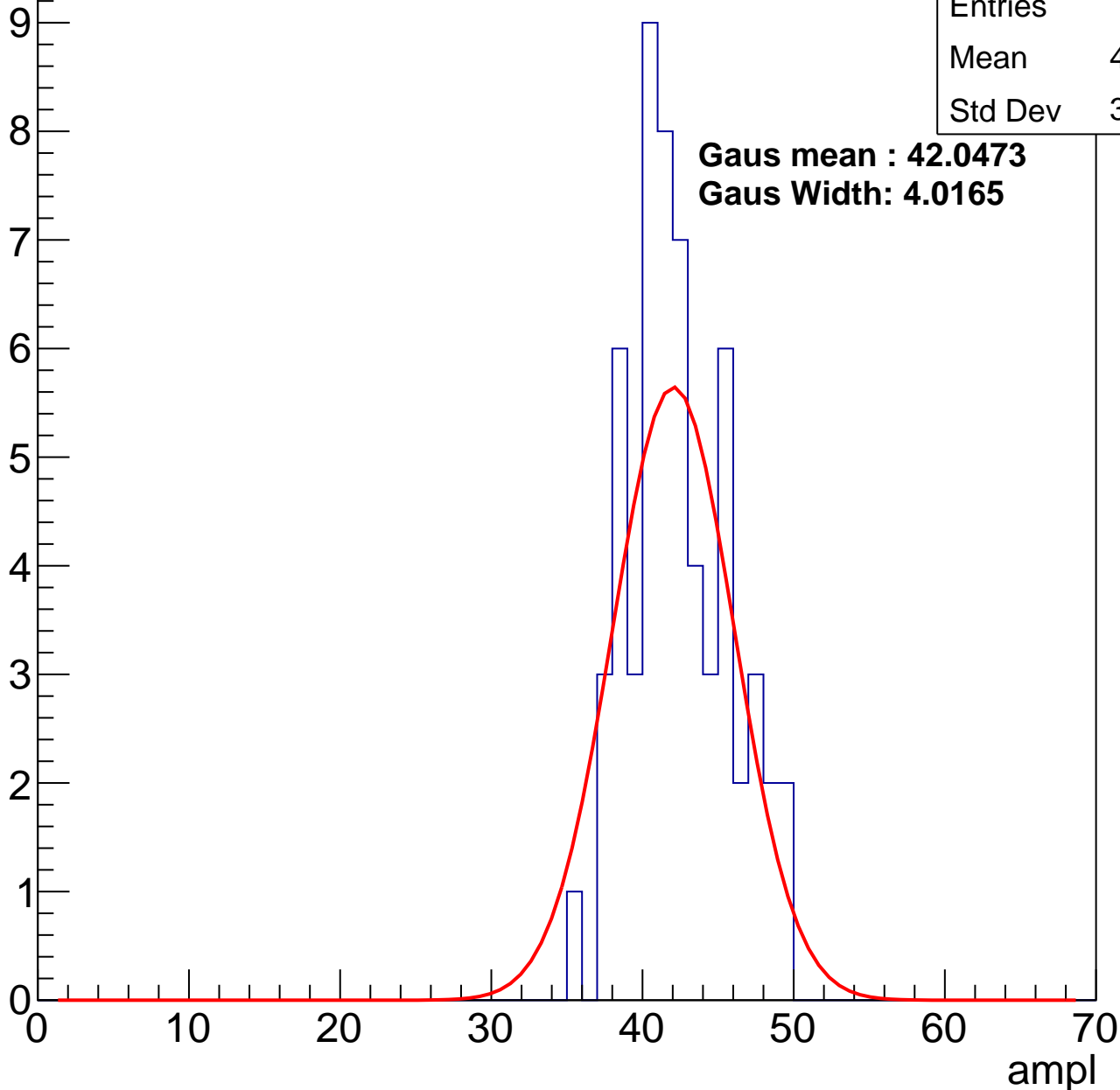
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	41.93
Std Dev	3.288

Gaus mean : 42.0473

Gaus Width: 4.0165

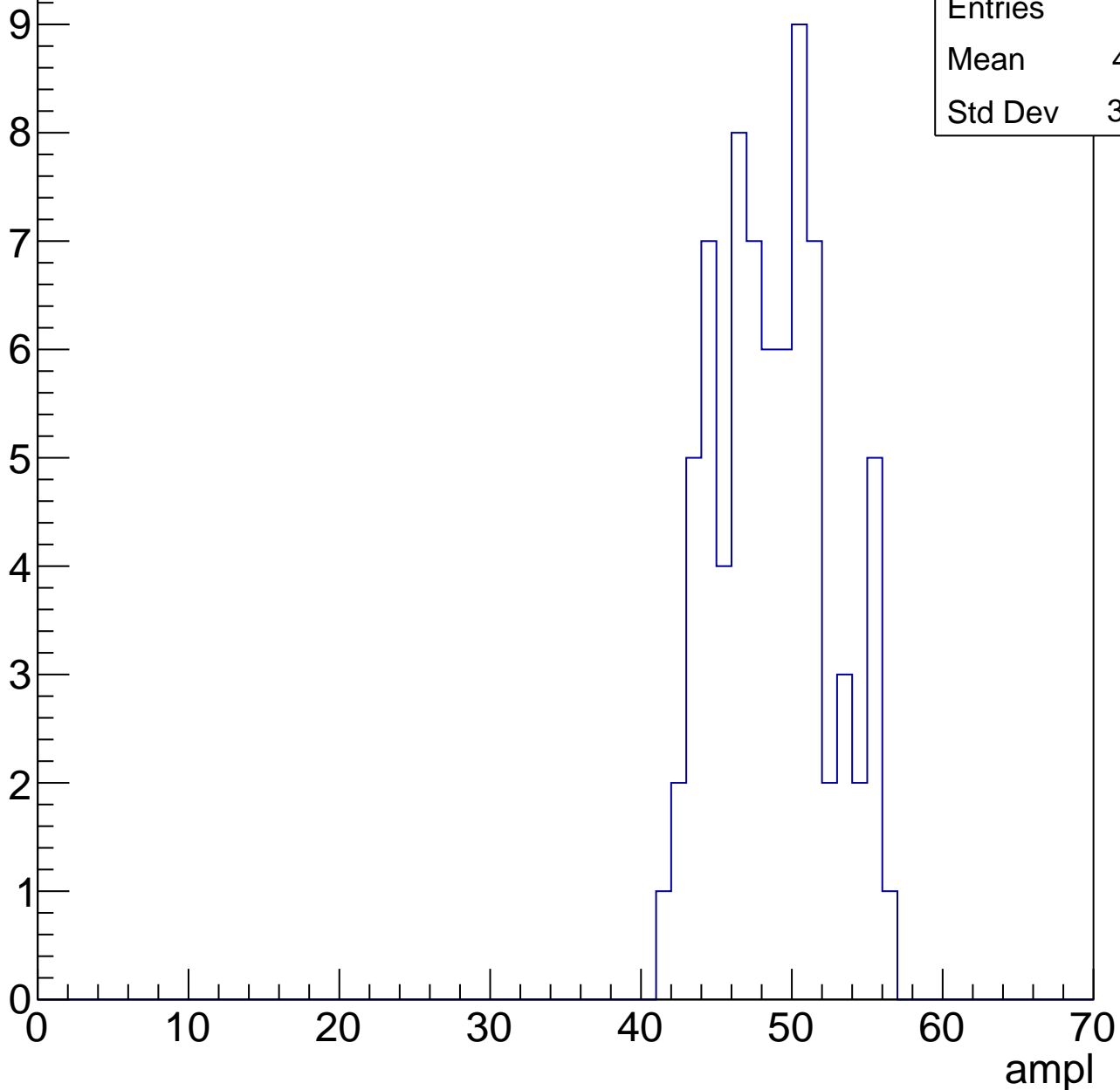


B1L100S, U25-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

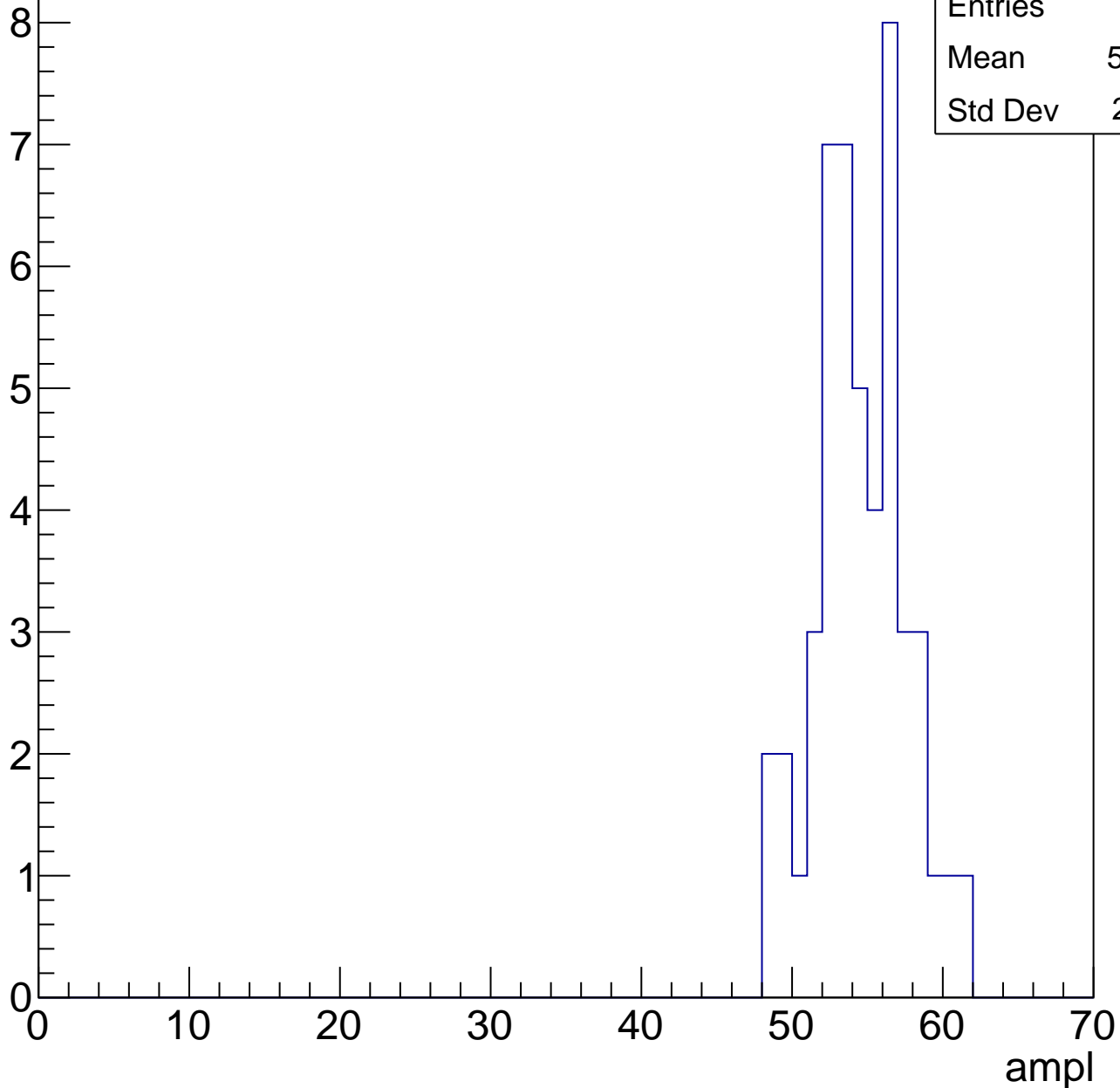
Entries	75
Mean	48.21
Std Dev	3.692



B1L100S, U25-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

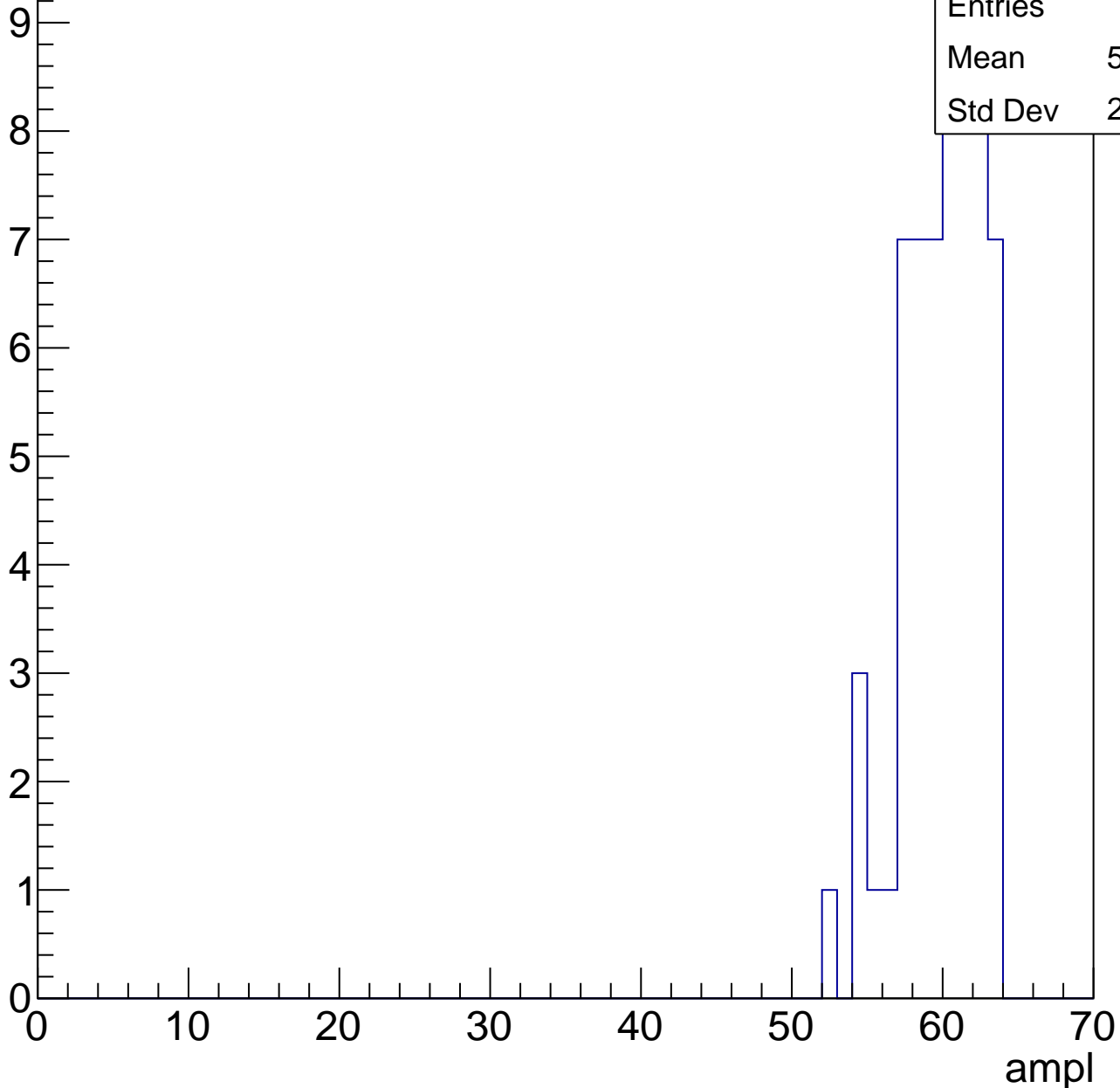


B1L100S, U25-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	59.46
Std Dev	2.593



B1L100S, U25-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	54.67
Std Dev	19.38

B1L100S, U25-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch111, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	30.07
Std Dev	4.802

Gaus mean : 30.9023

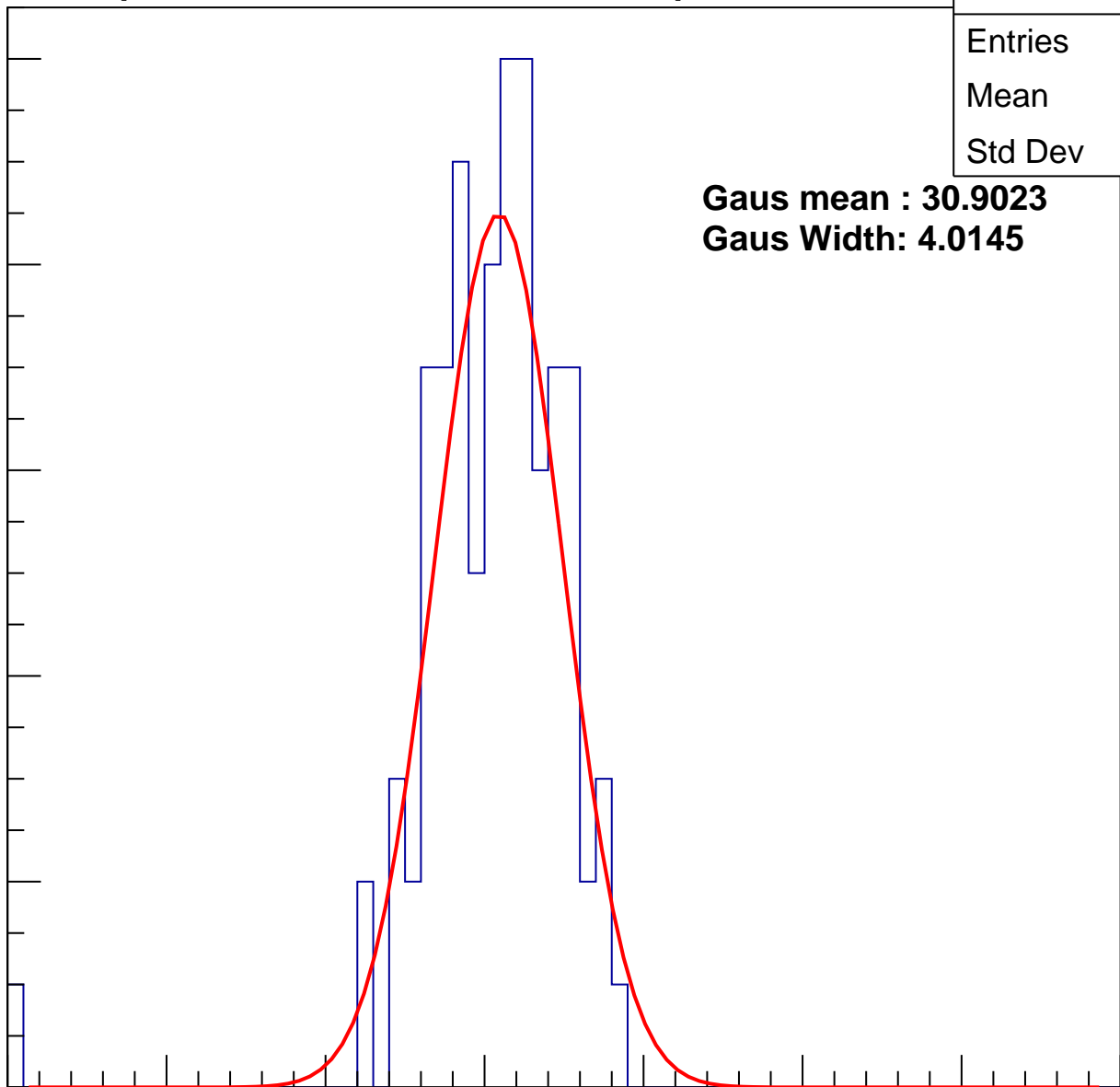
Gaus Width: 4.0145

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch111, adc1

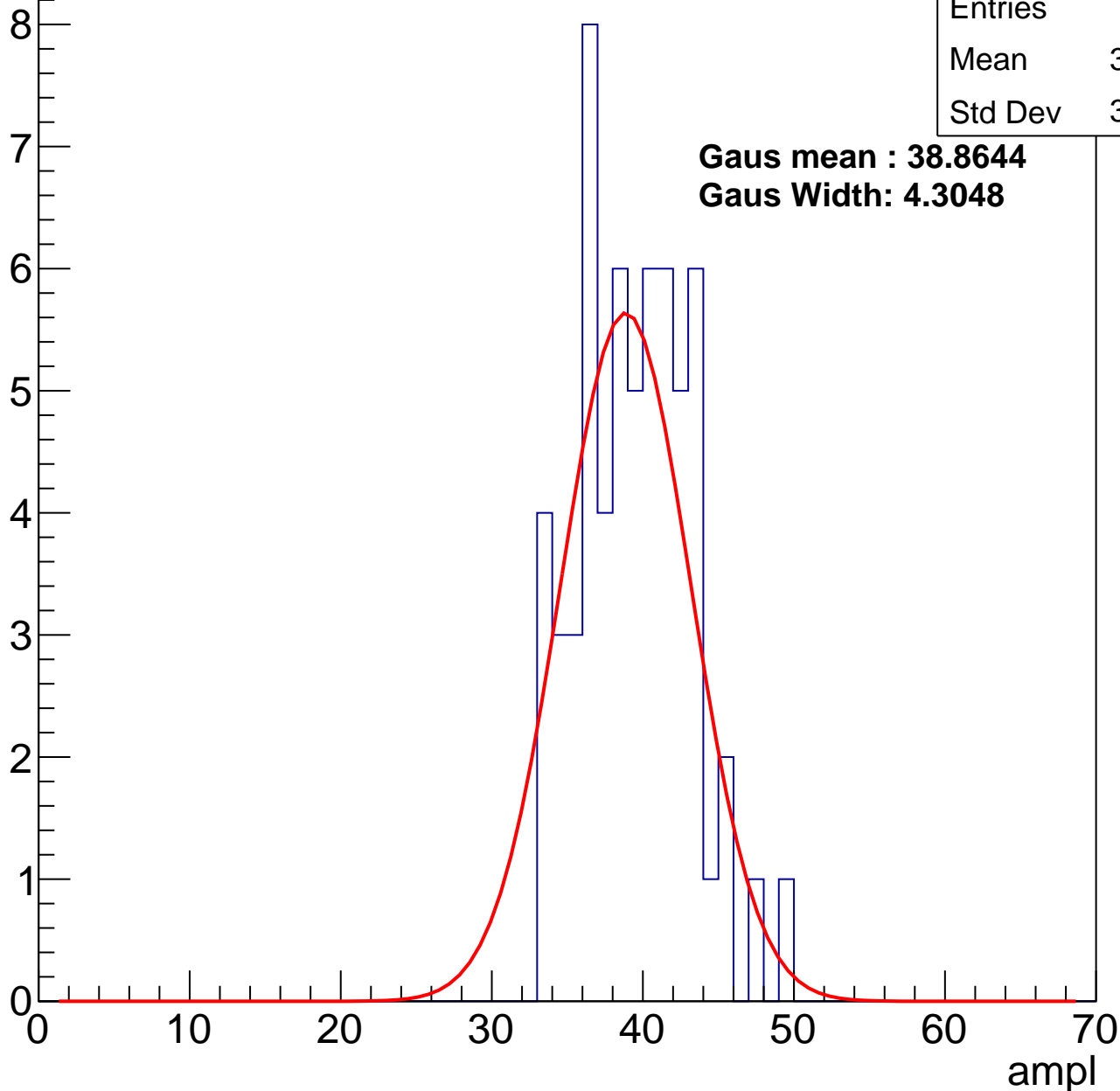
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	39.05
Std Dev	3.605

Gaus mean : 38.8644

Gaus Width: 4.3048



B1L100S, U25-ch111, adc2

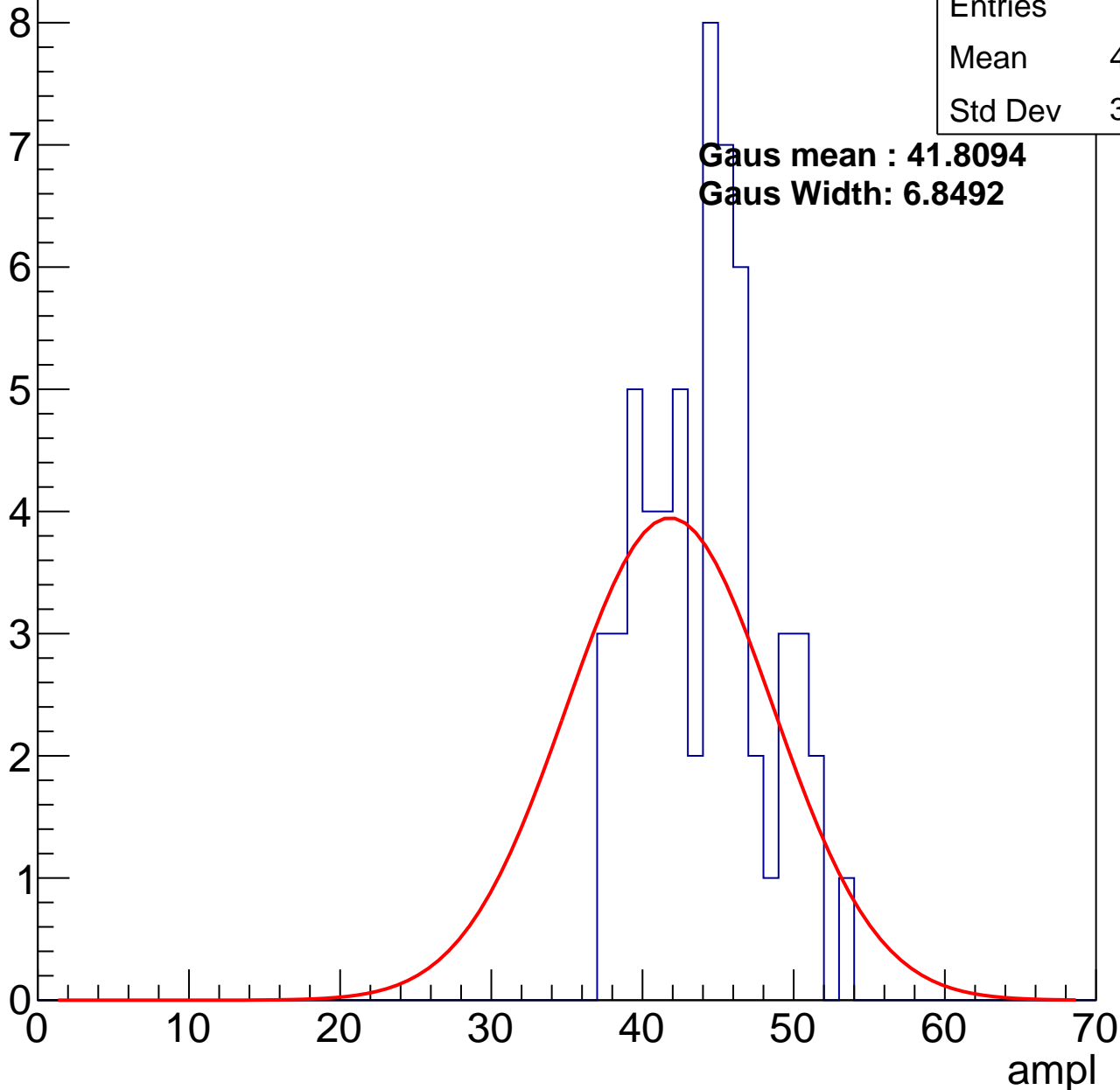
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.68
Std Dev	3.955

Gaus mean : 41.8094

Gaus Width: 6.8492

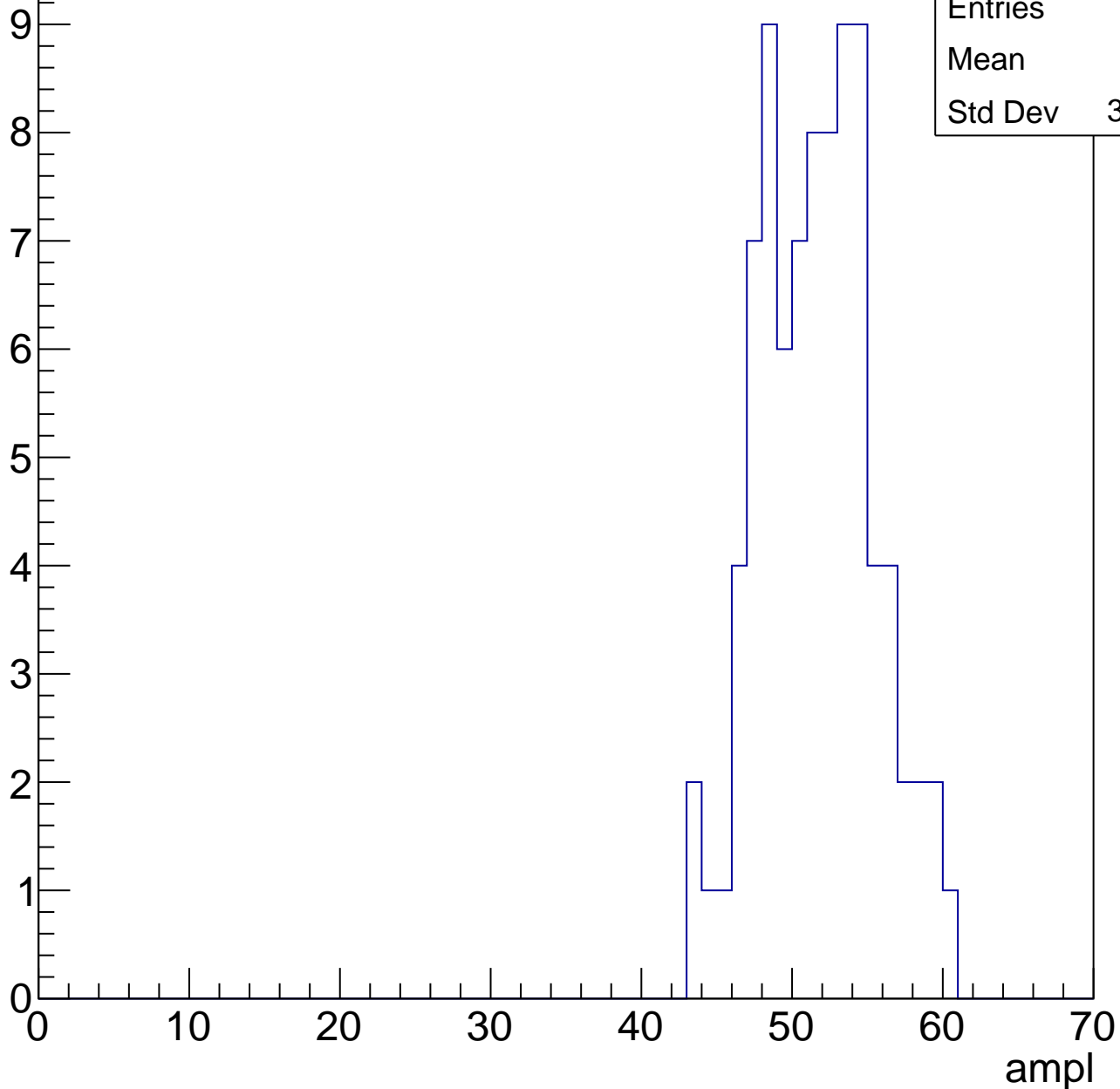


B1L100S, U25-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	51.2
Std Dev	3.738

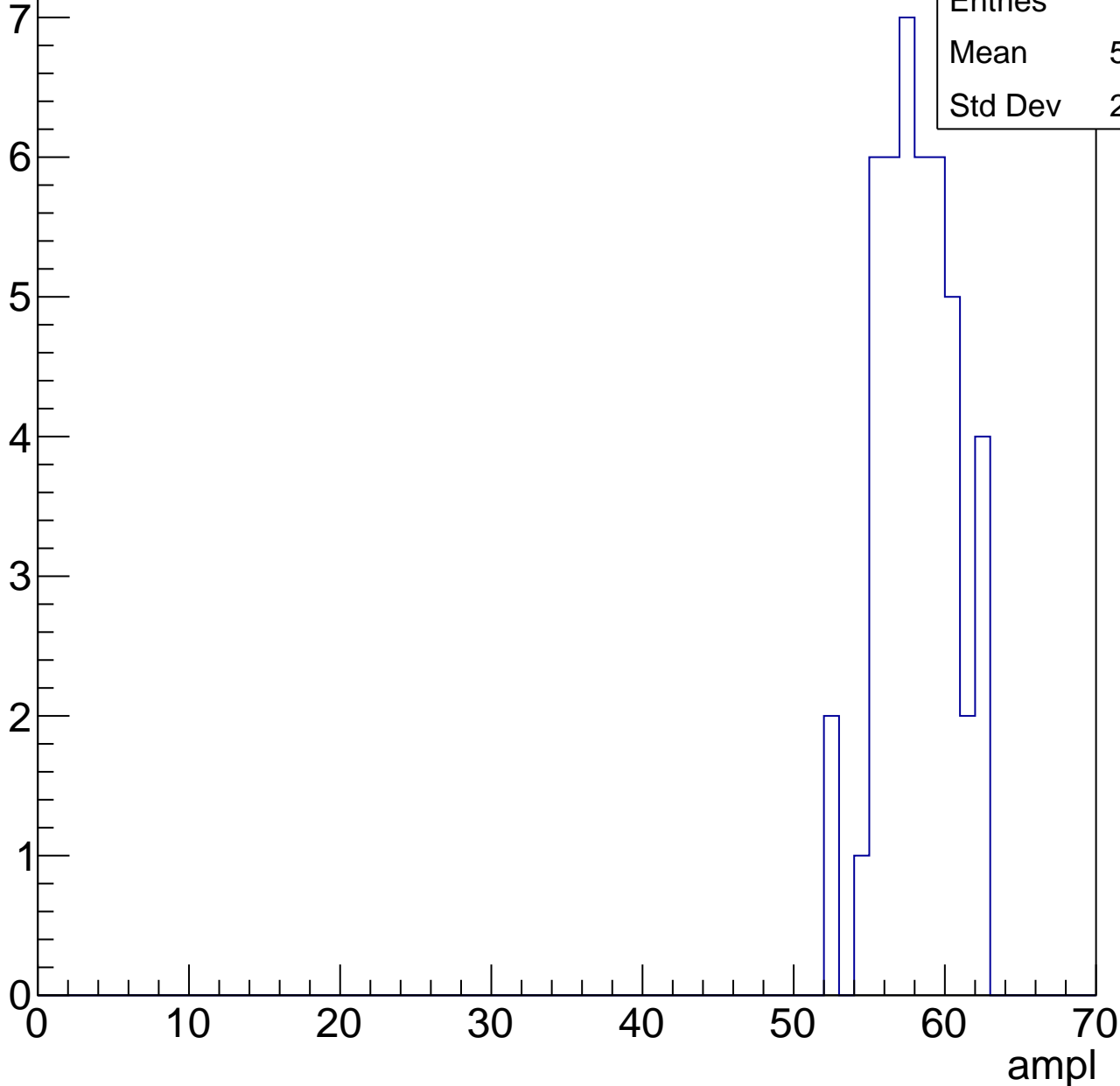


B1L100S, U25-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	57.67
Std Dev	2.477



B1L100S, U25-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	33
Mean	59.36
Std Dev	10.62

Entry

10

8

6

4

2

0

0

10

20

30

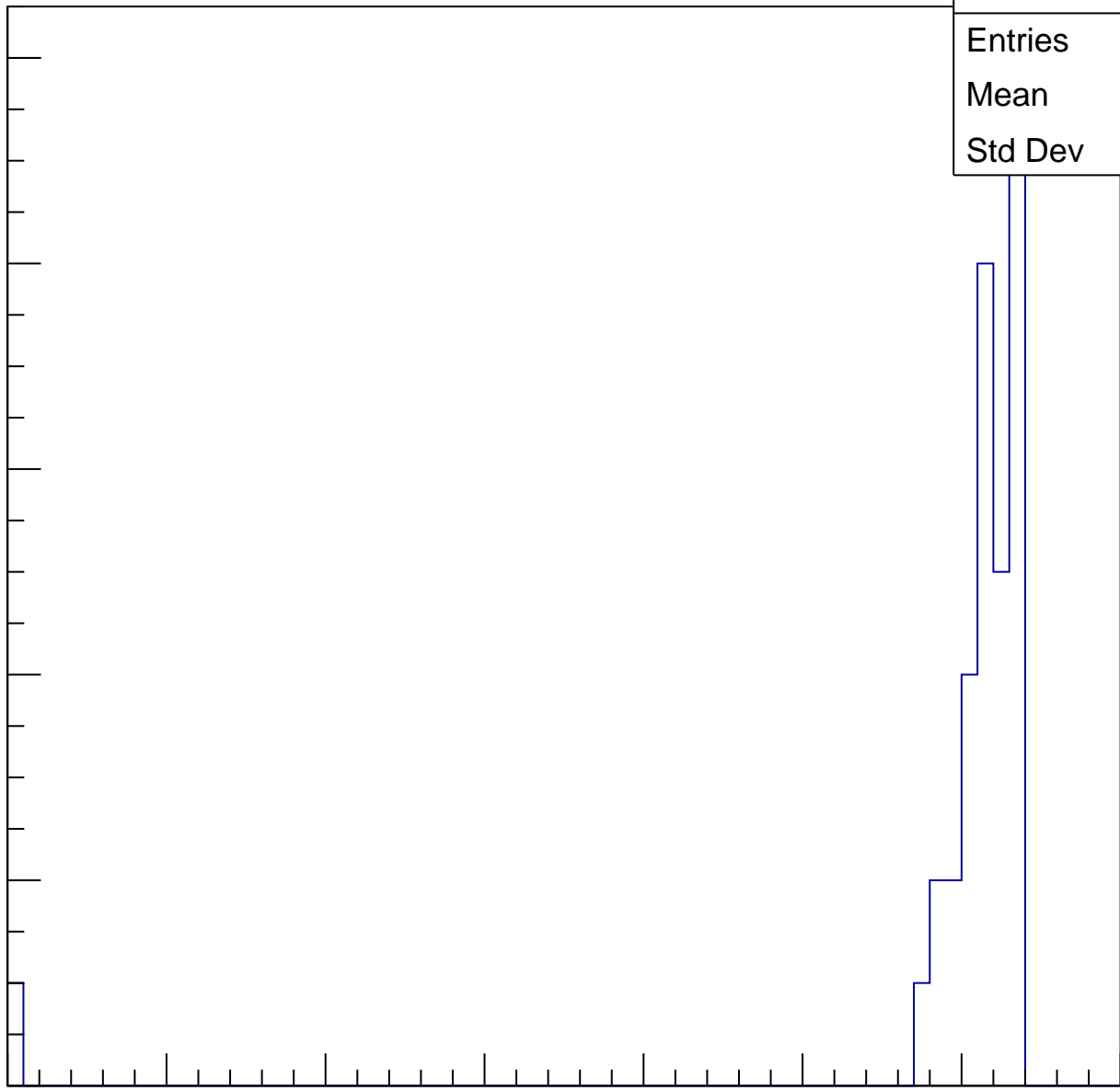
40

50

60

70

ampl



B1L100S, U25-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch112, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	29.02
Std Dev	4.798

Gaus mean : 29.7095

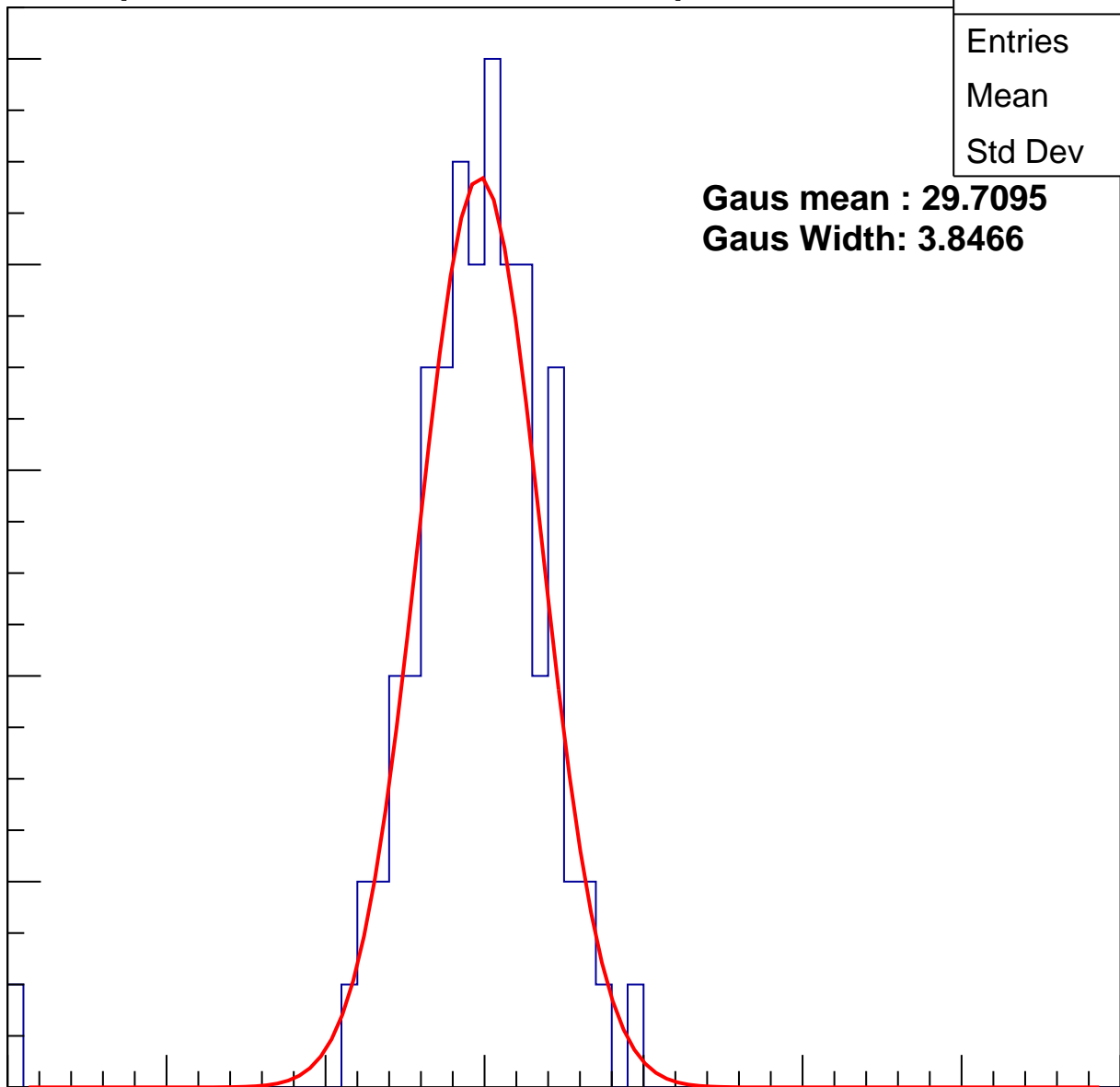
Gaus Width: 3.8466

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch112, adc1

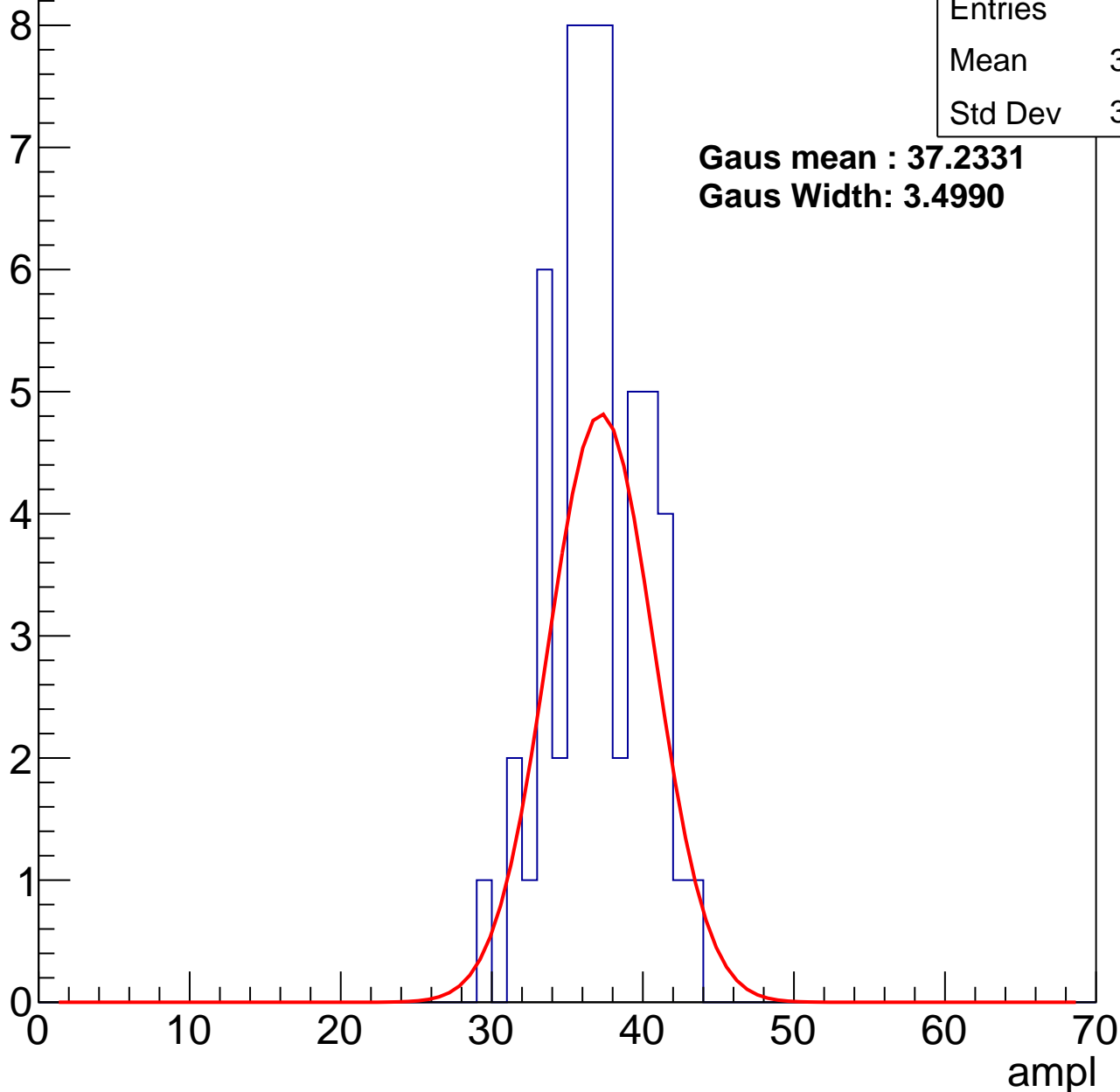
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.54
Std Dev	3.029

Gaus mean : 37.2331

Gaus Width: 3.4990



B1L100S, U25-ch112, adc2

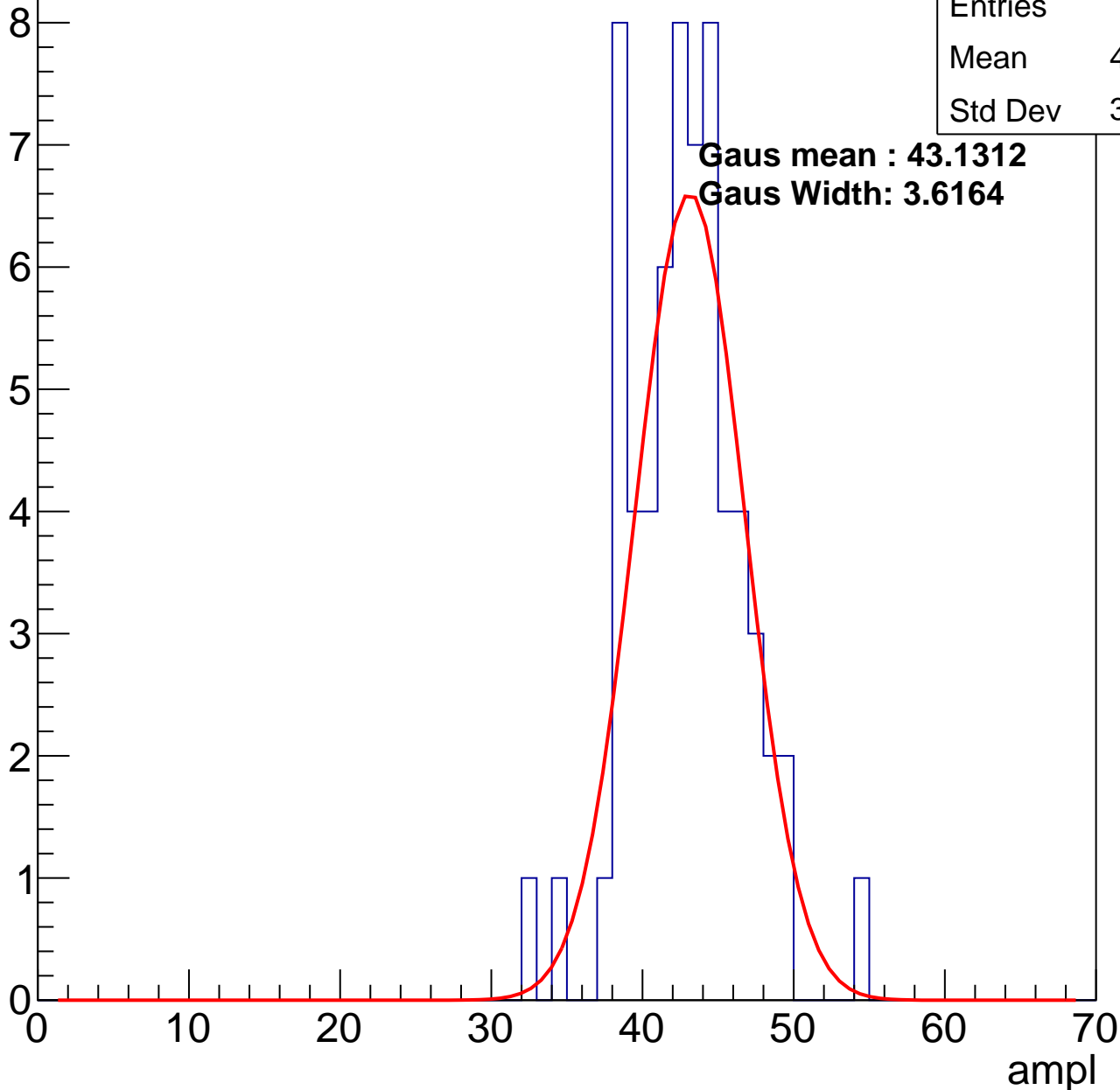
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.36
Std Dev	3.739

Gaus mean : 43.1312

Gaus Width: 3.6164

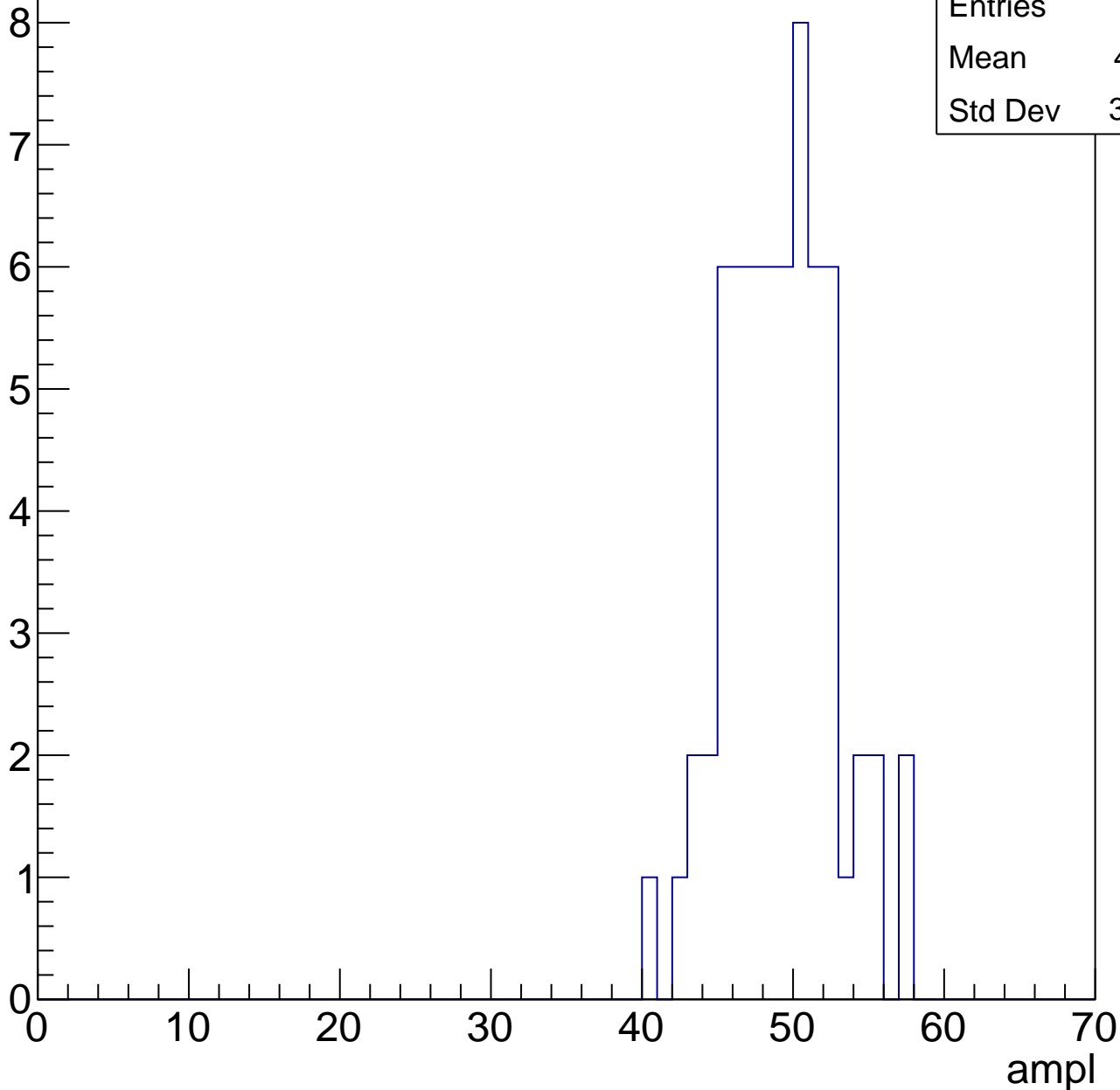


B1L100S, U25-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.71
Std Dev	3.516

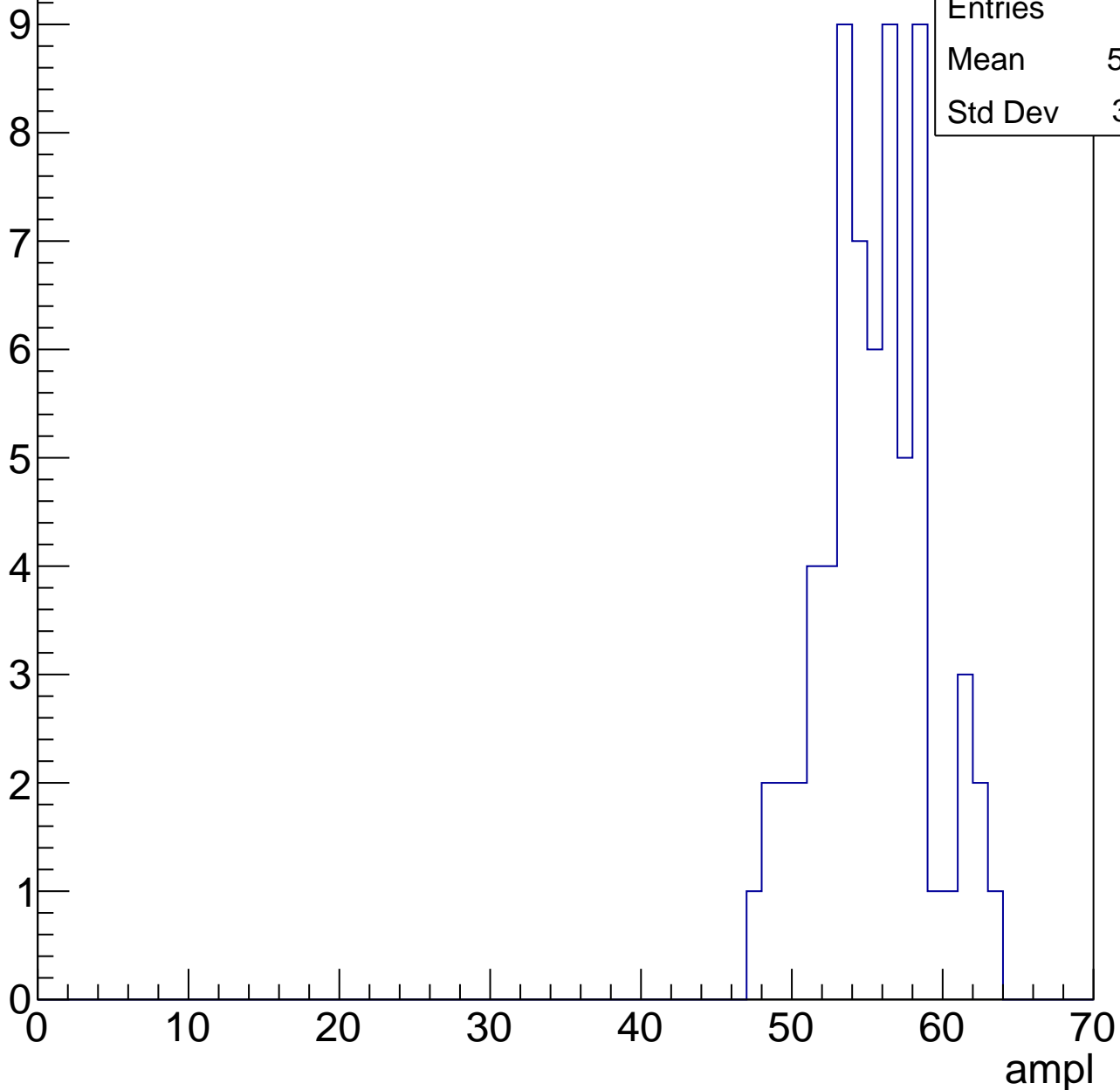


B1L100S, U25-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

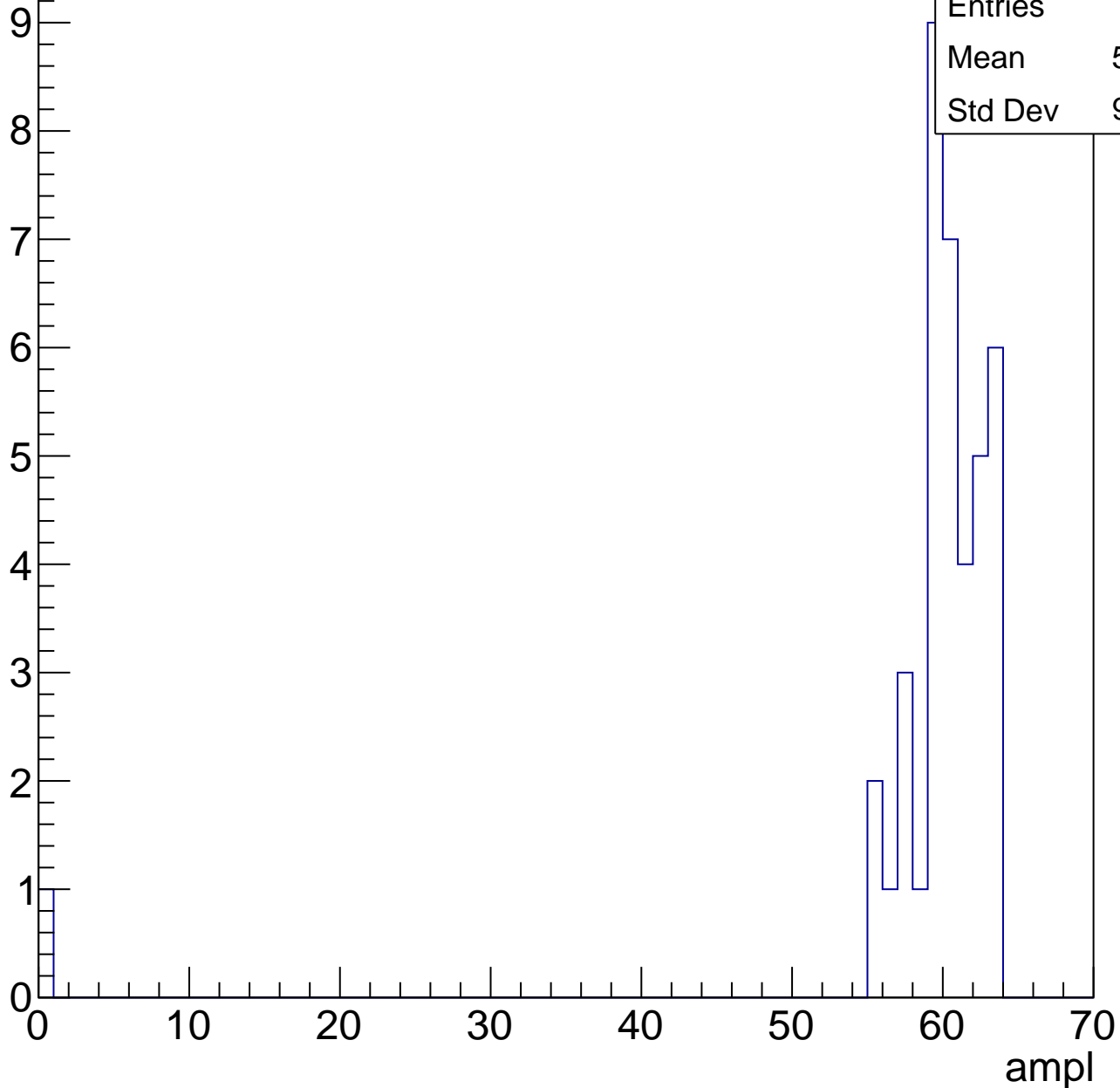
Entries	68
Mean	54.97
Std Dev	3.531



B1L100S, U25-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

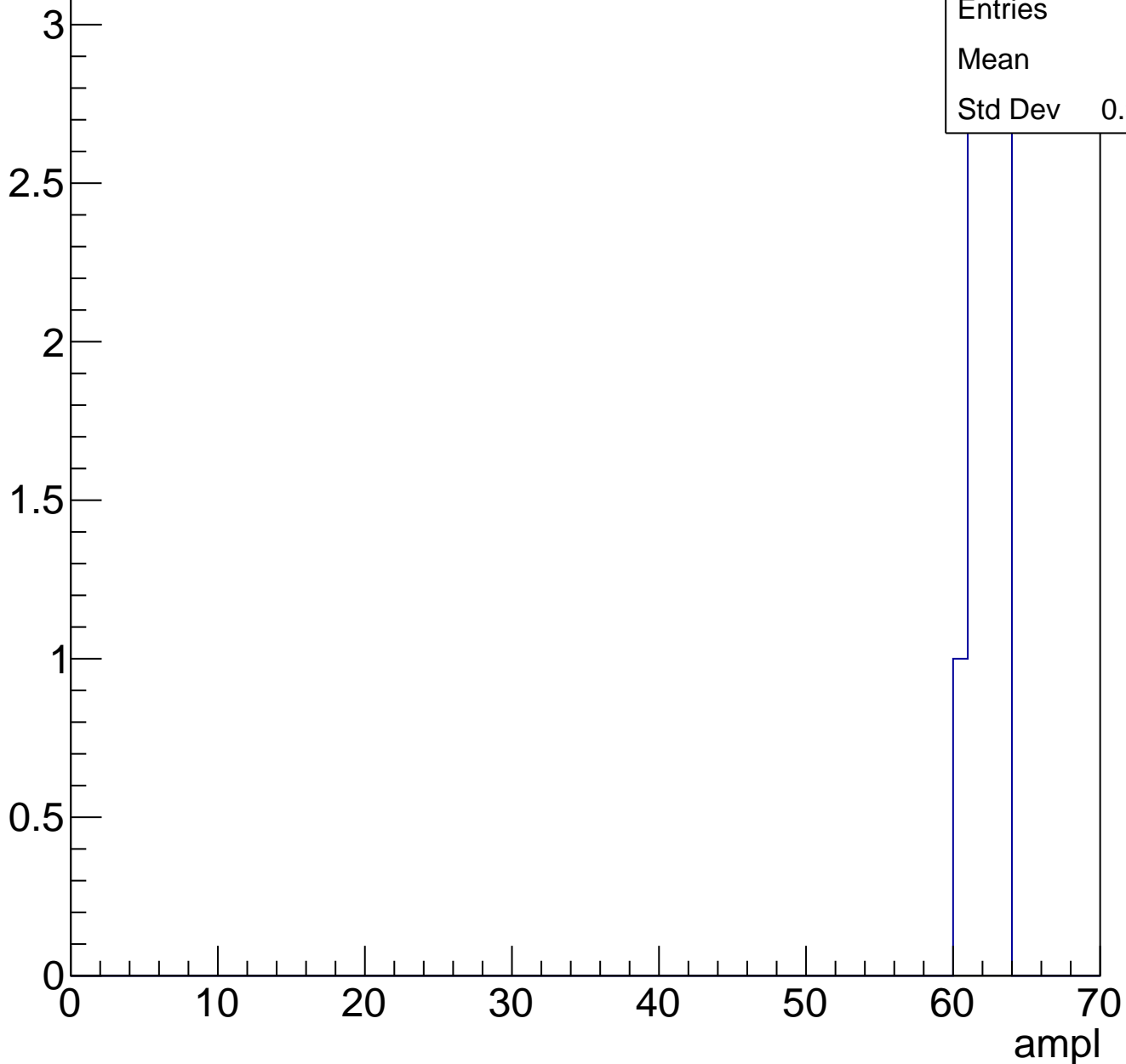
Entry



B1L100S, U25-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	30
Std Dev	30

B1L100S, U25-ch113, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	29.76
Std Dev	6.526

Gaus mean : 31.2534

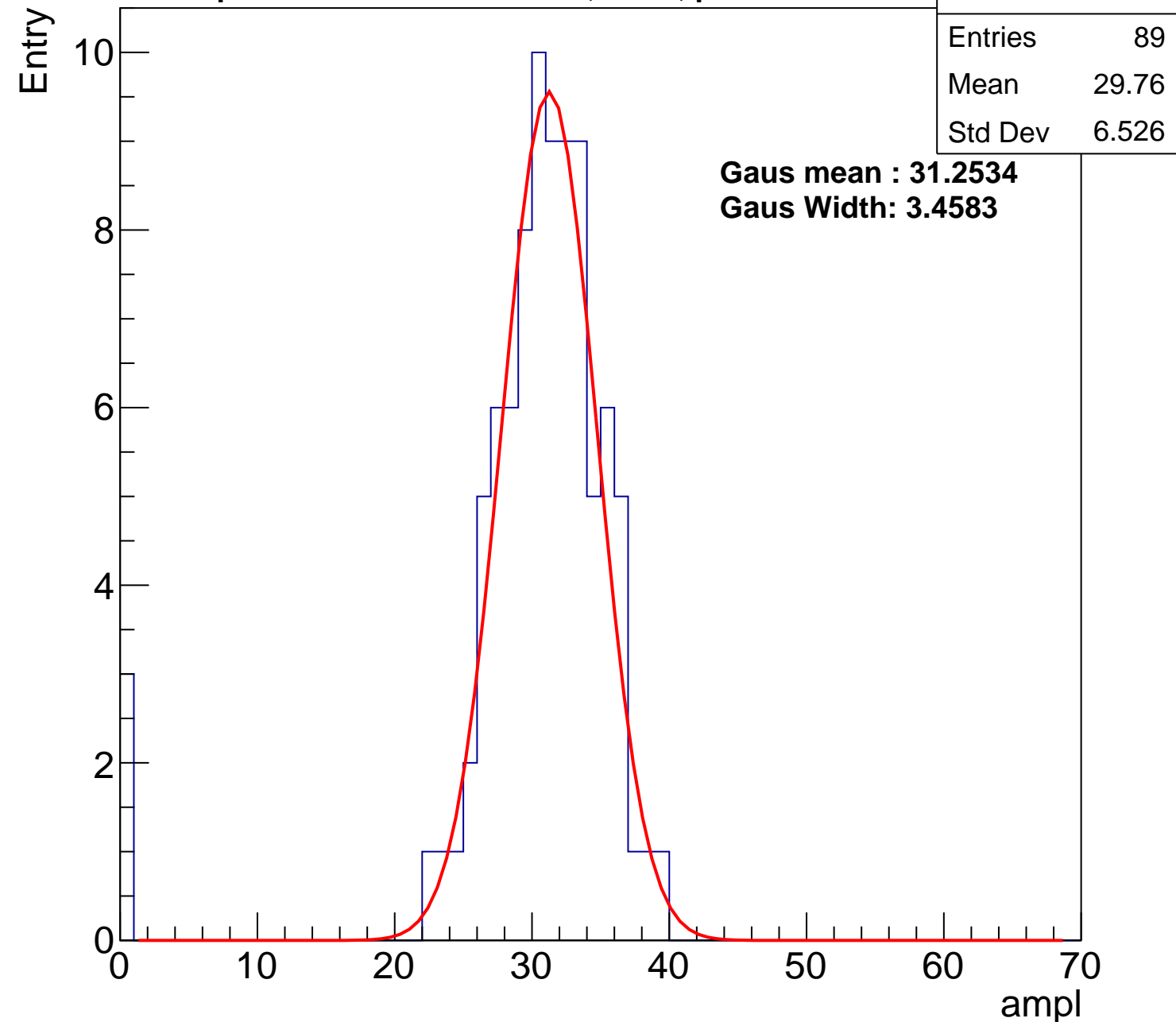
Gaus Width: 3.4583

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch113, adc1

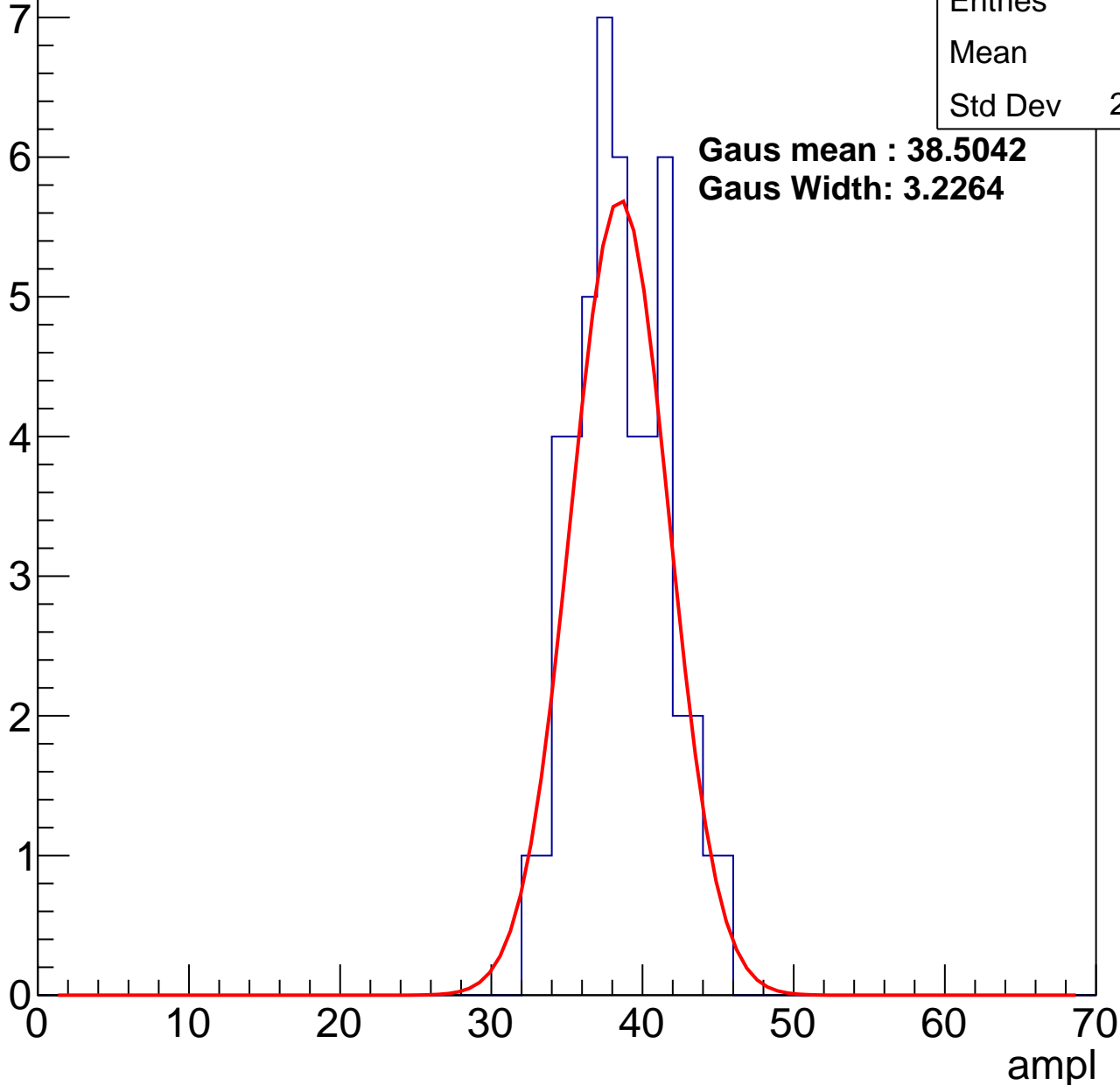
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	38.1
Std Dev	2.988

Gaus mean : 38.5042

Gaus Width: 3.2264



B1L100S, U25-ch113, adc2

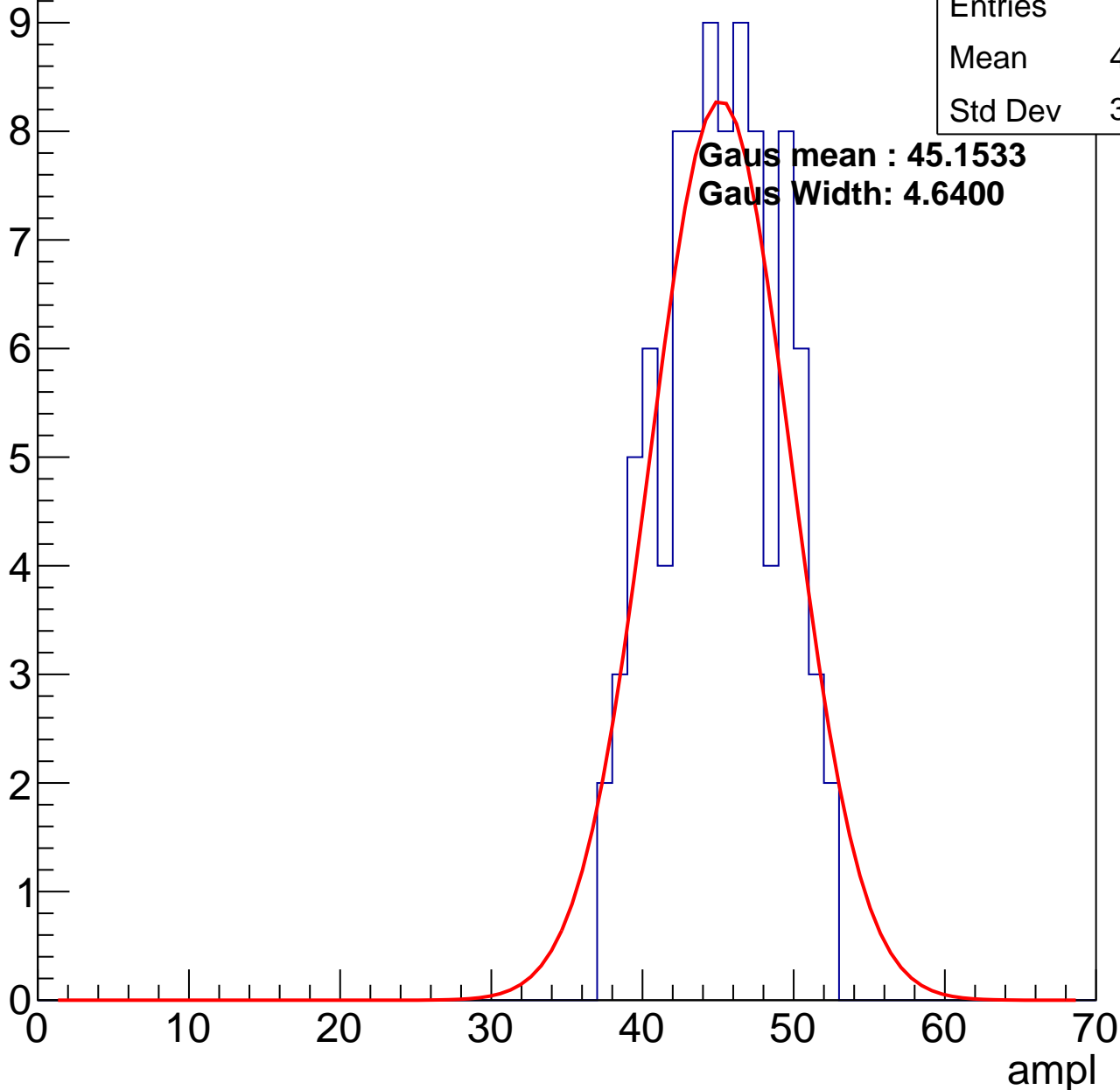
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	93
Mean	44.67
Std Dev	3.785

Gaus mean : 45.1533

Gaus Width: 4.6400

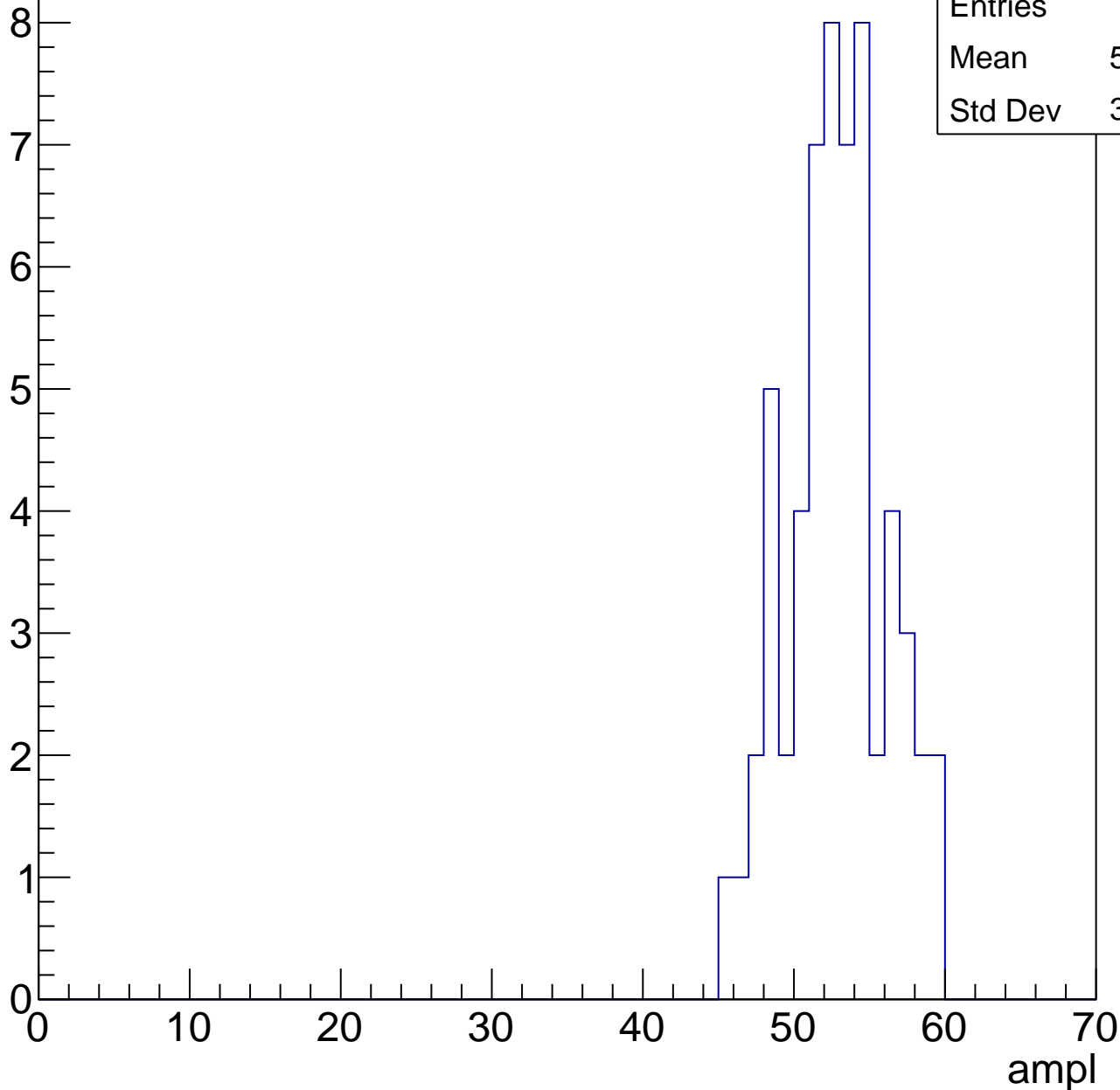


B1L100S, U25-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	52.38
Std Dev	3.253

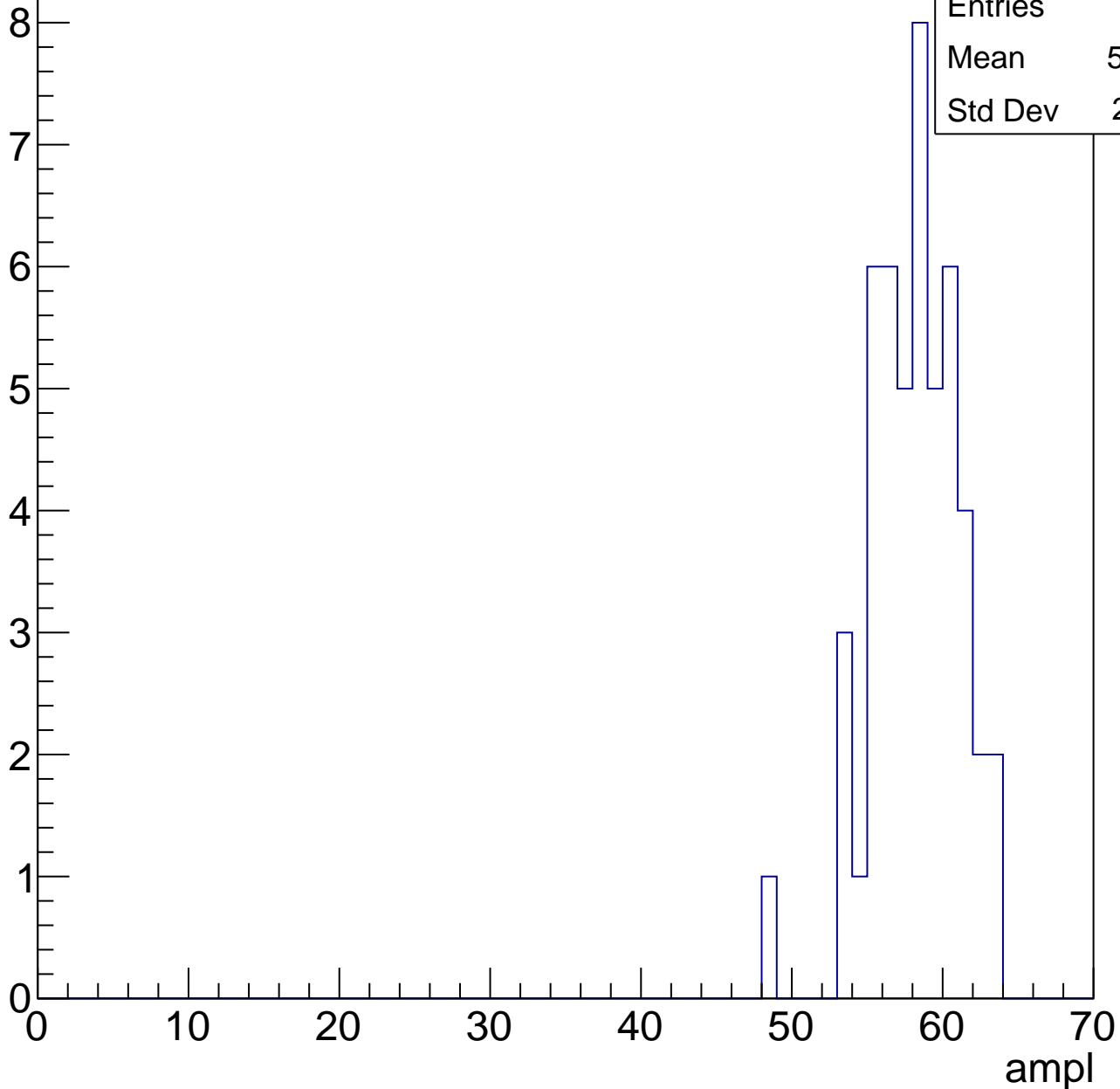


B1L100S, U25-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	57.65
Std Dev	2.911

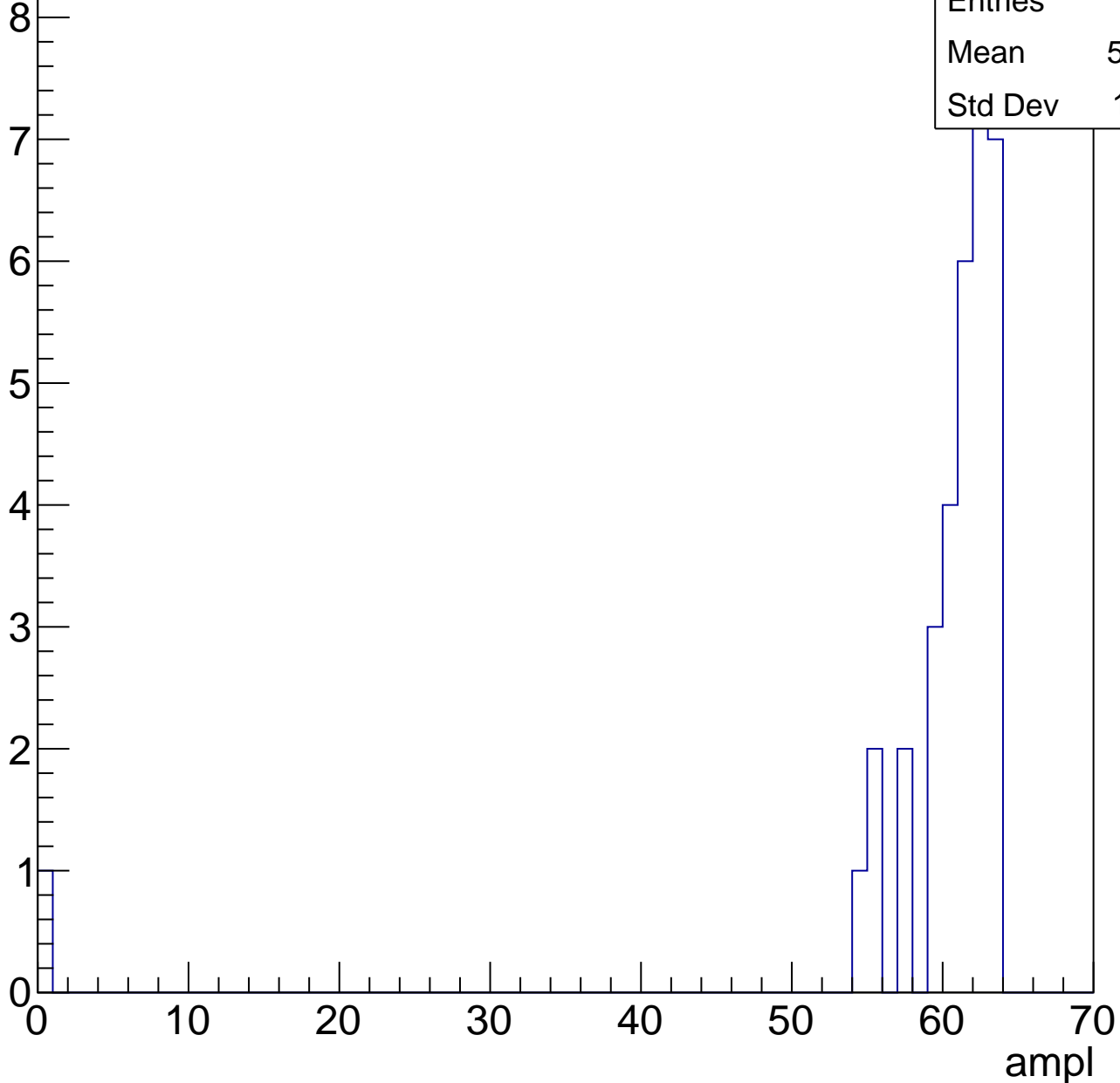


B1L100S, U25-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.76
Std Dev	10.51



B1L100S, U25-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch114, adc0

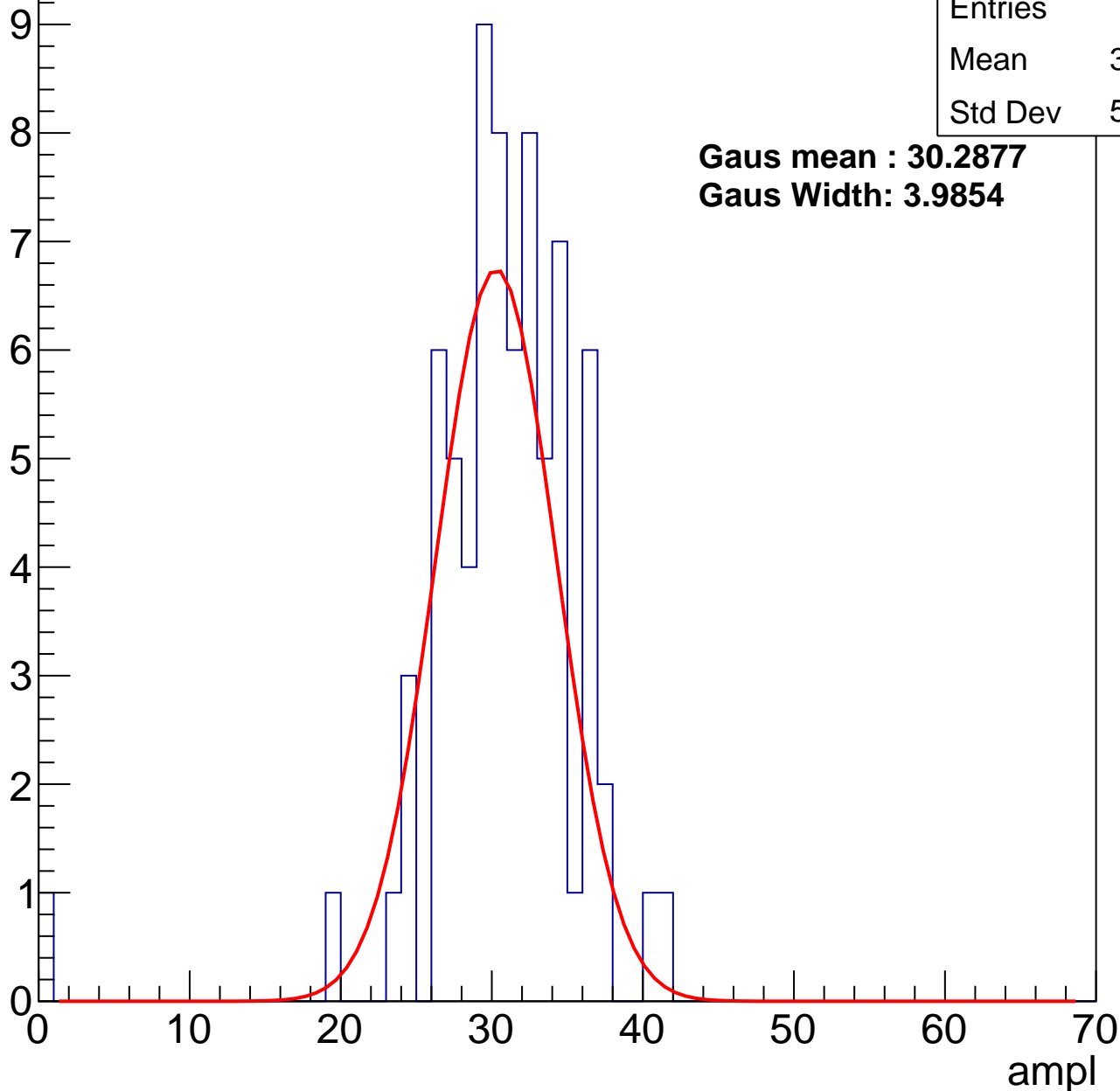
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.25
Std Dev	5.284

Gaus mean : 30.2877

Gaus Width: 3.9854



B1L100S, U25-ch114, adc1

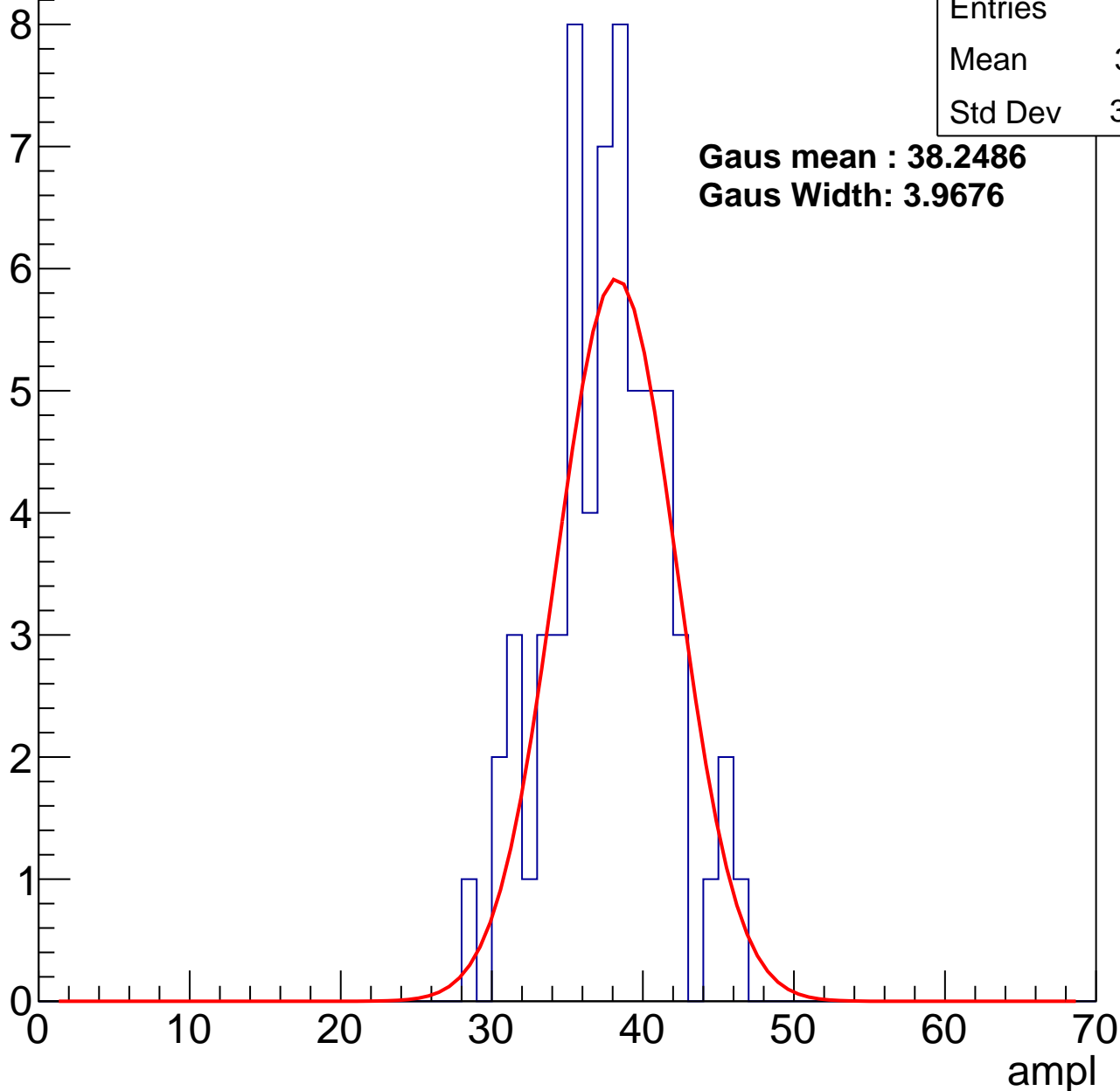
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	37.21
Std Dev	3.823

Gaus mean : 38.2486

Gaus Width: 3.9676



B1L100S, U25-ch114, adc2

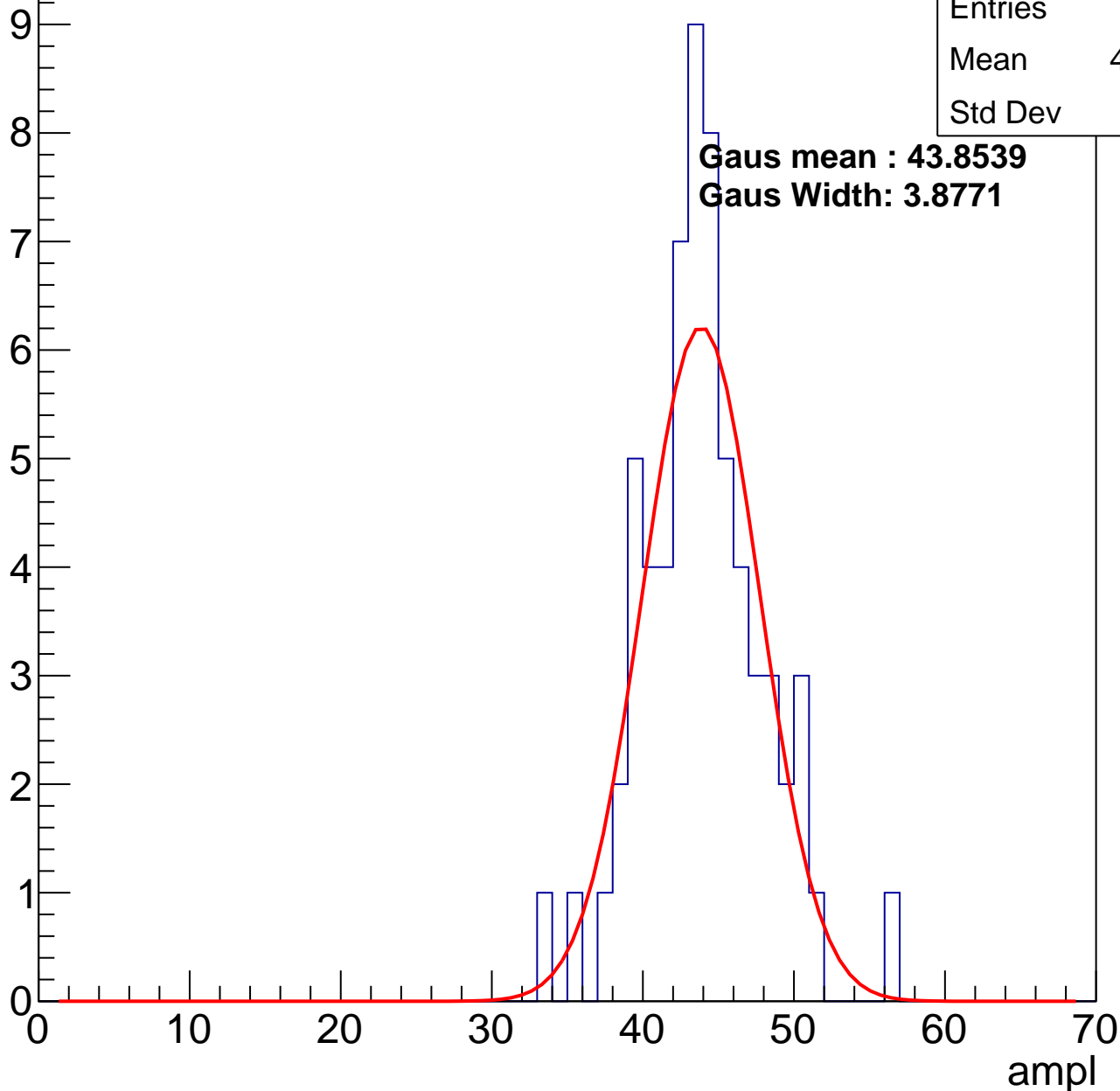
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.47
Std Dev	3.98

Gaus mean : 43.8539

Gaus Width: 3.8771

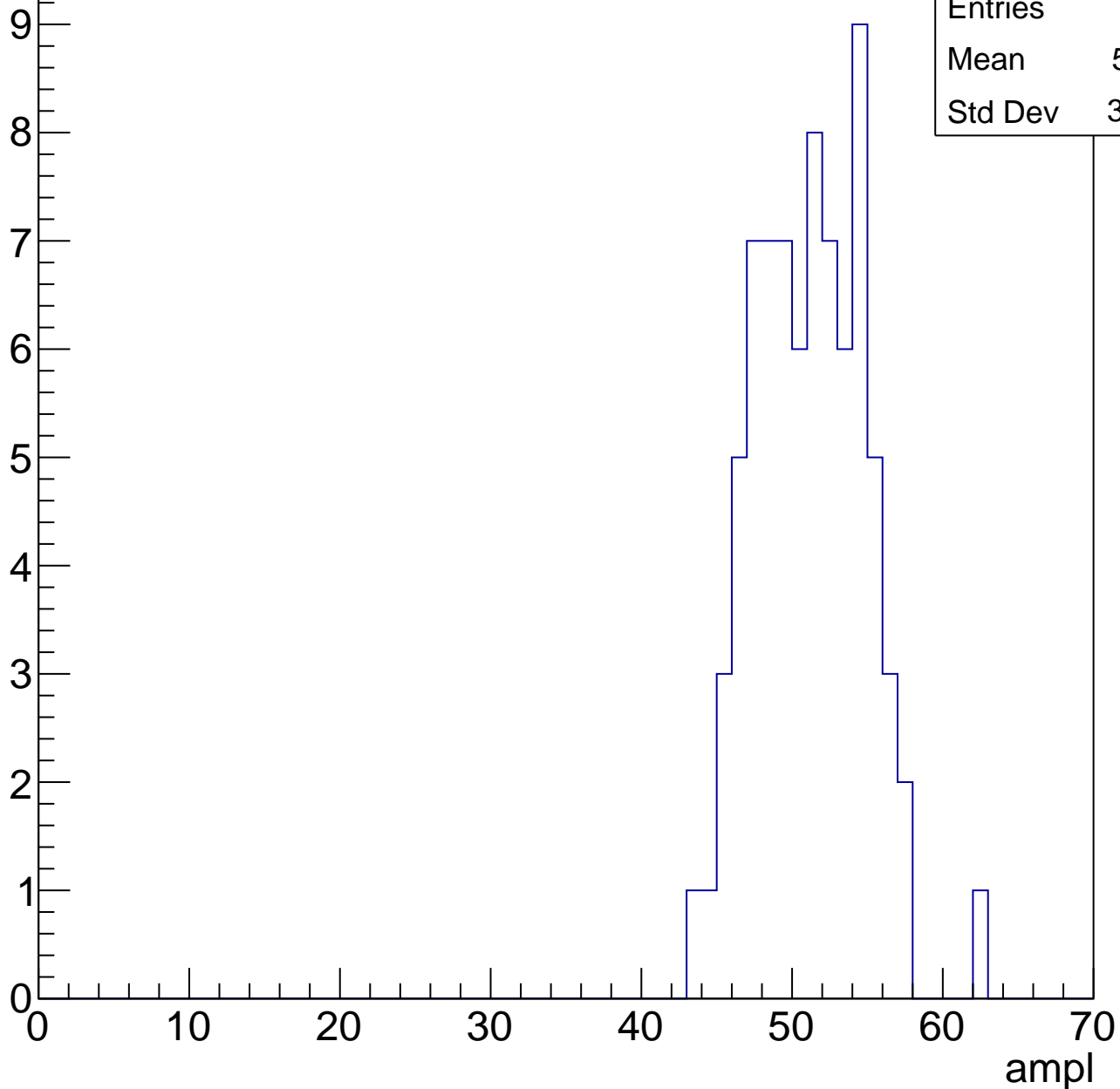


B1L100S, U25-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	50.71
Std Dev	3.595

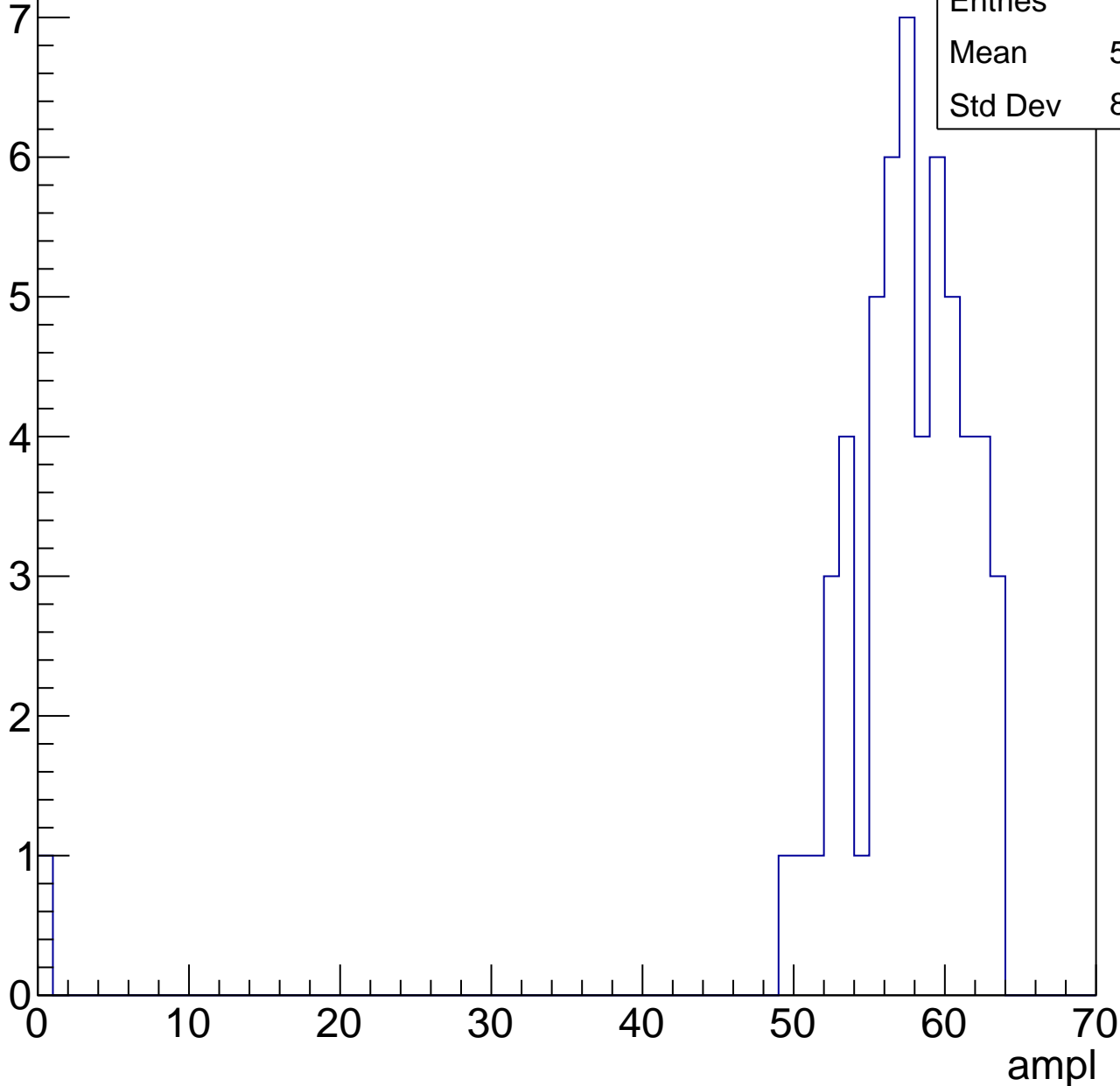


B1L100S, U25-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.23
Std Dev	8.328

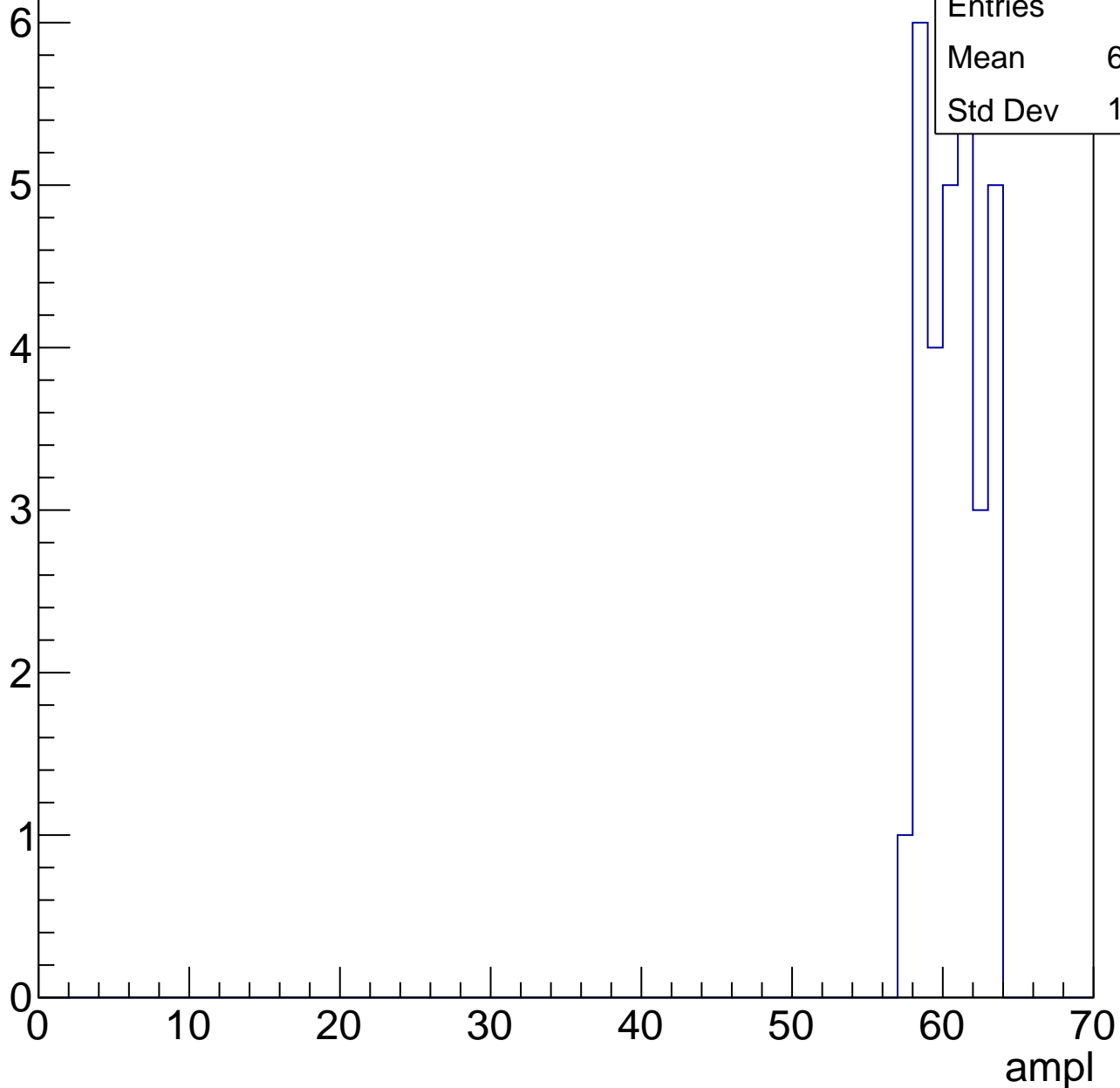


B1L100S, U25-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.27
Std Dev	1.806



B1L100S, U25-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch115, adc0

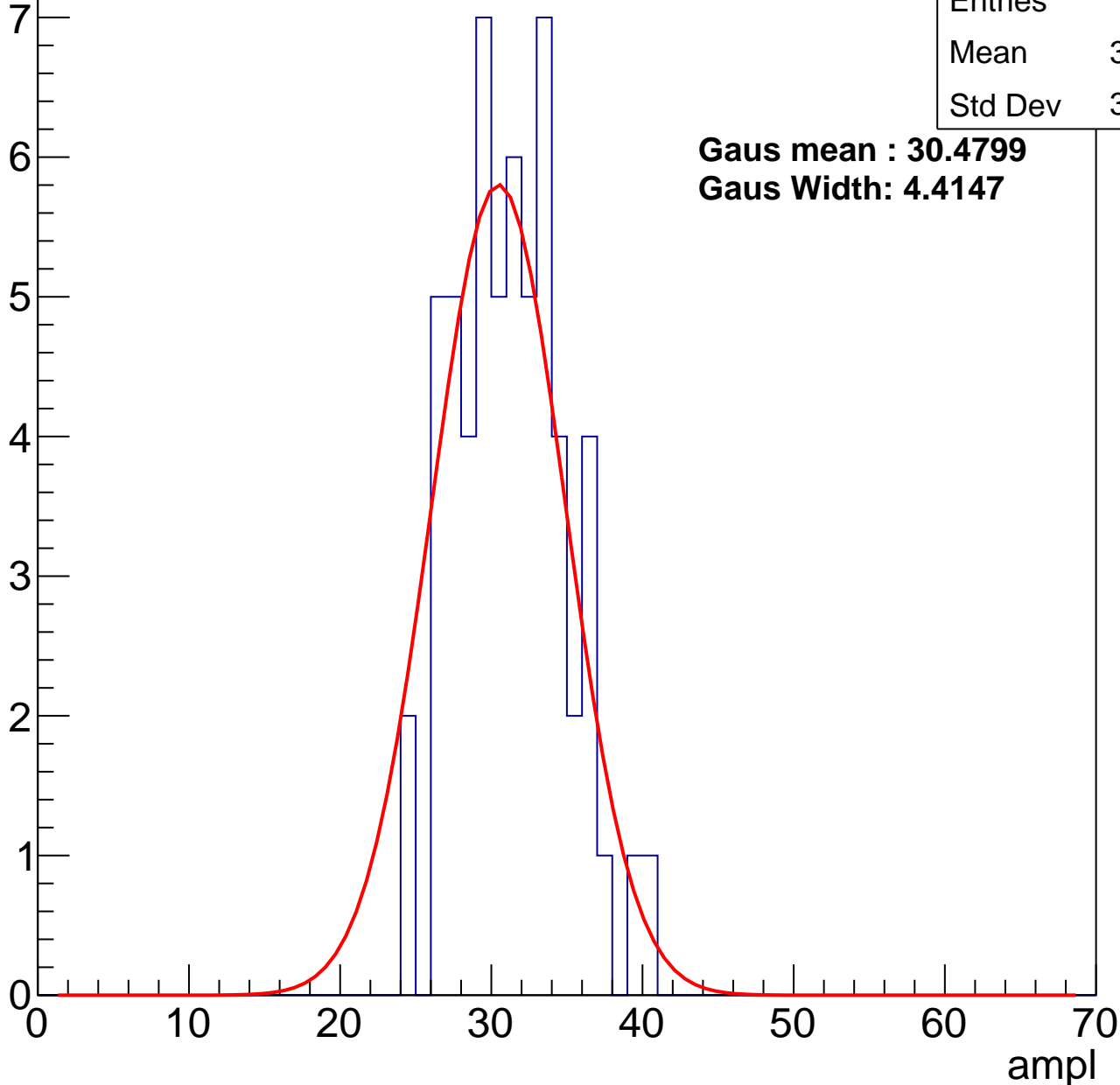
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	30.86
Std Dev	3.572

Gaus mean : 30.4799

Gaus Width: 4.4147



B1L100S, U25-ch115, adc1

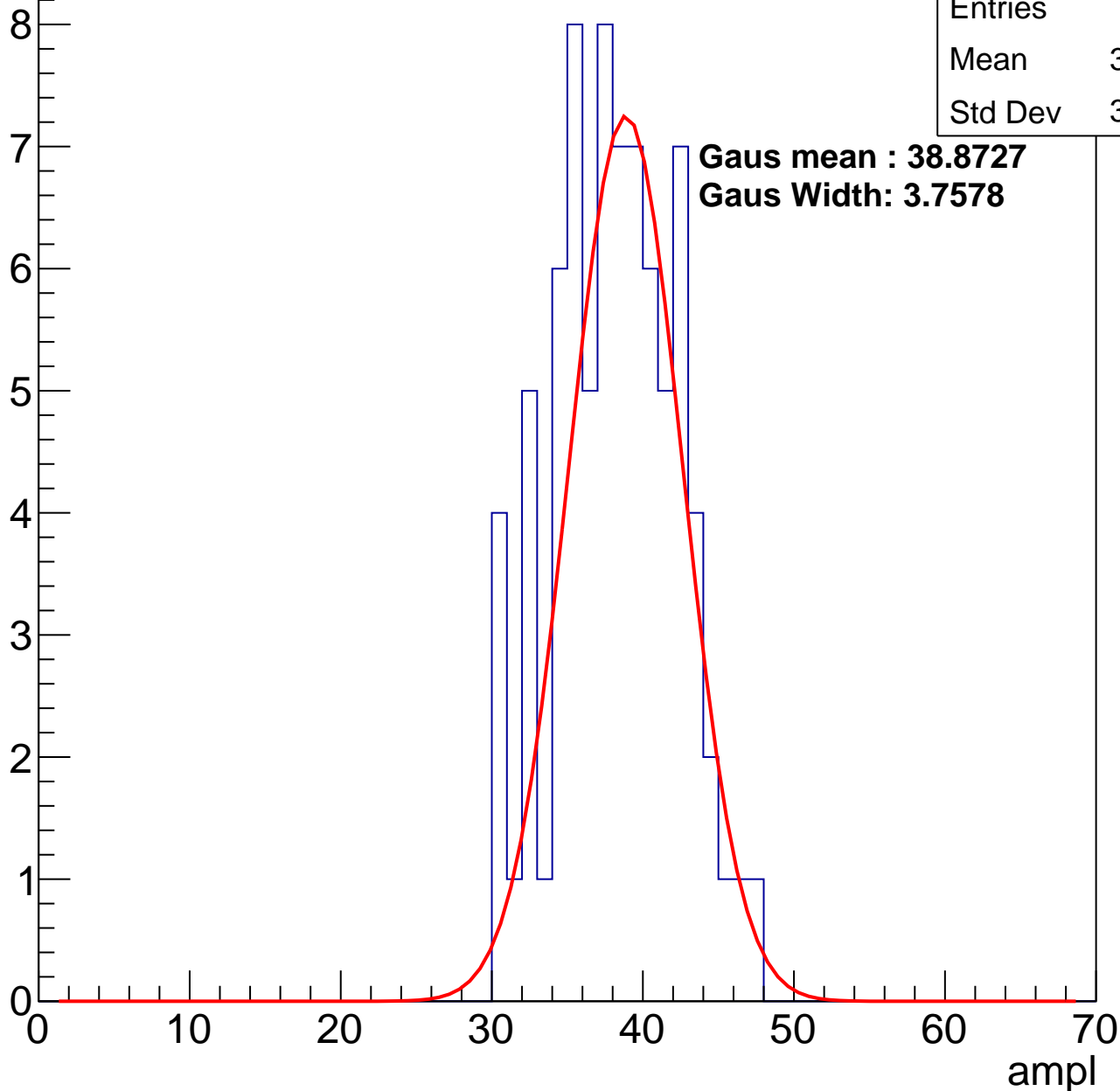
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37.72
Std Dev	3.984

Gaus mean : 38.8727

Gaus Width: 3.7578



B1L100S, U25-ch115, adc2

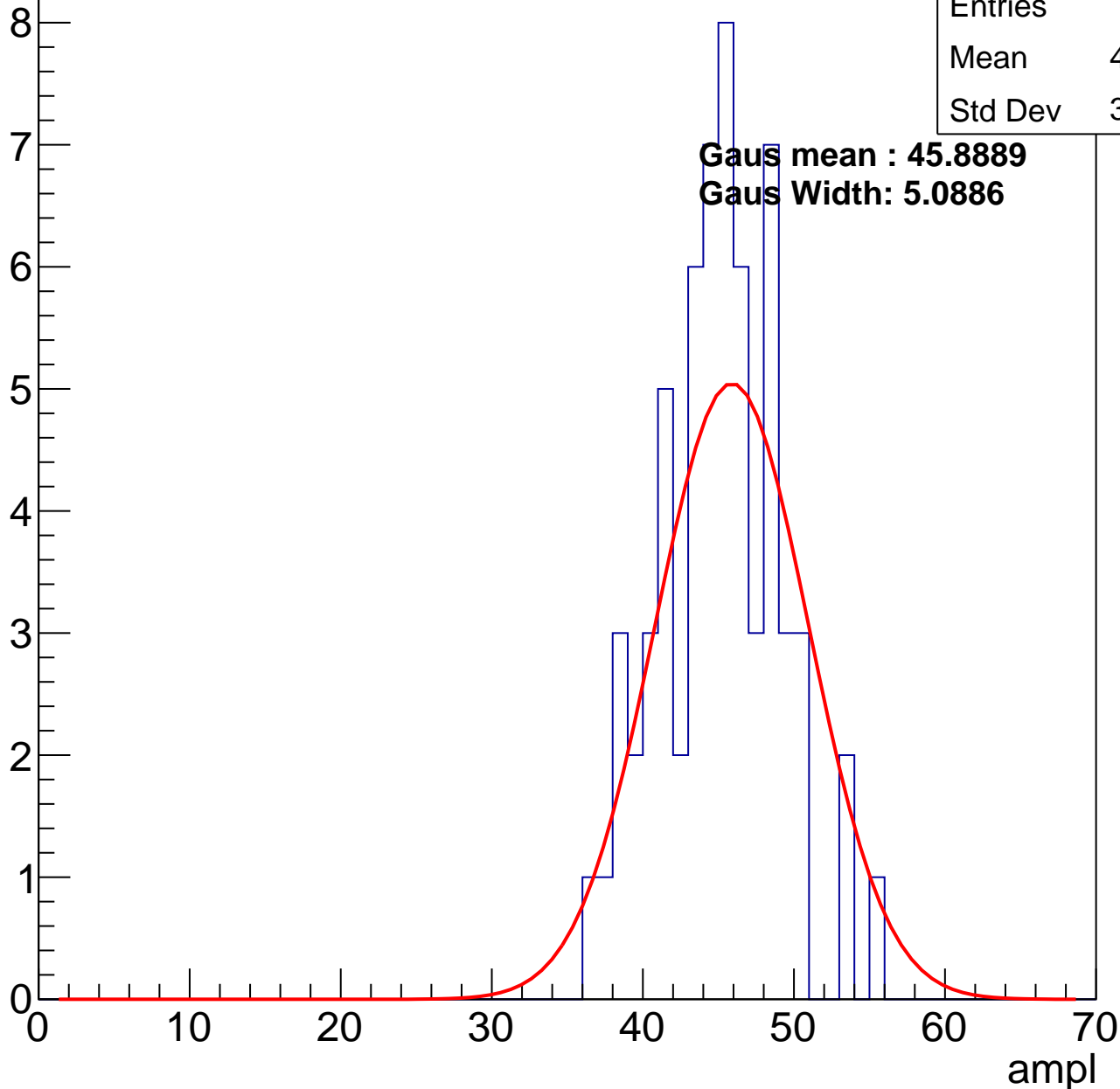
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.62
Std Dev	3.966

Gaus mean : 45.8889

Gaus Width: 5.0886

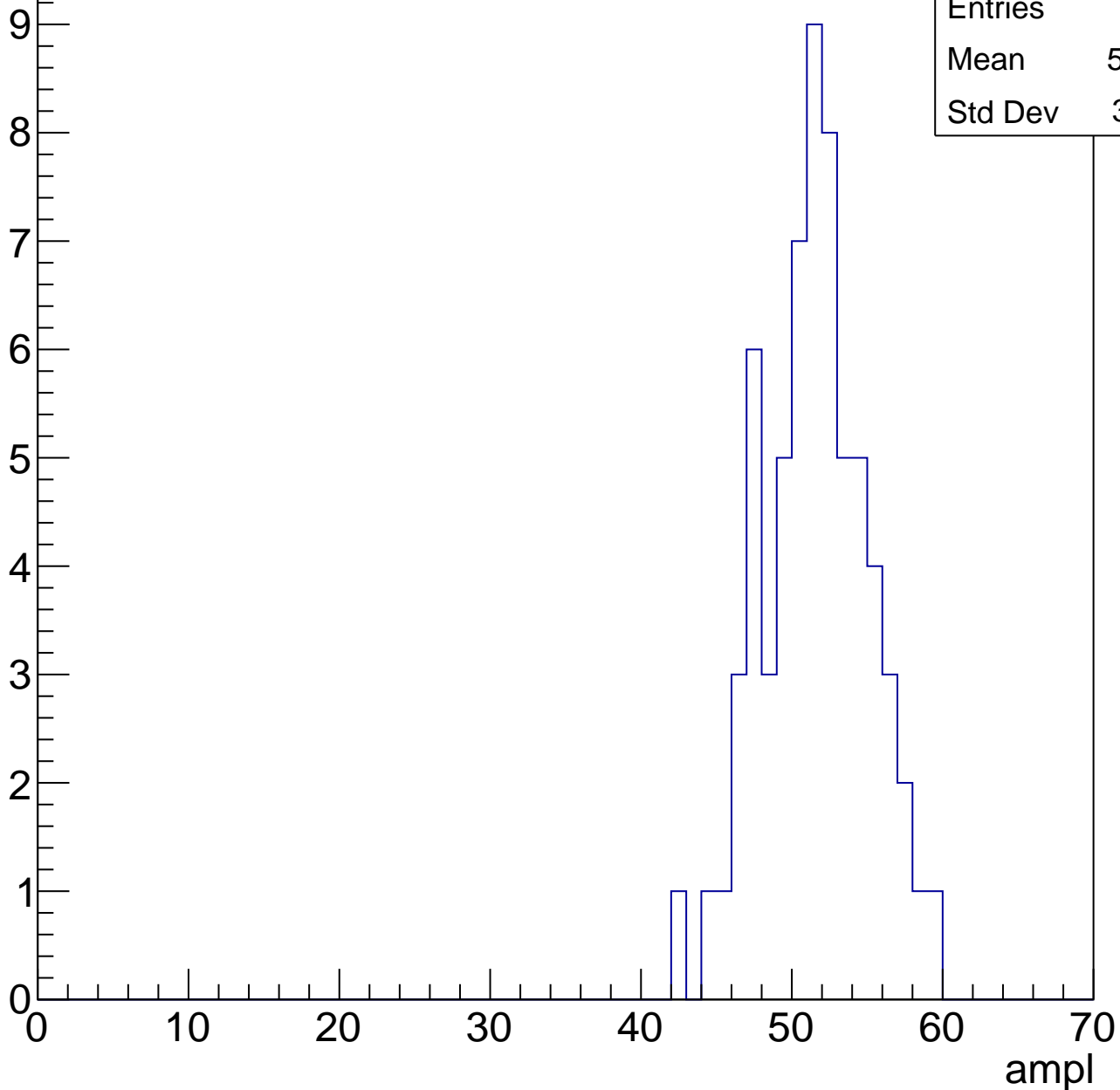


B1L100S, U25-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	51.06
Std Dev	3.481

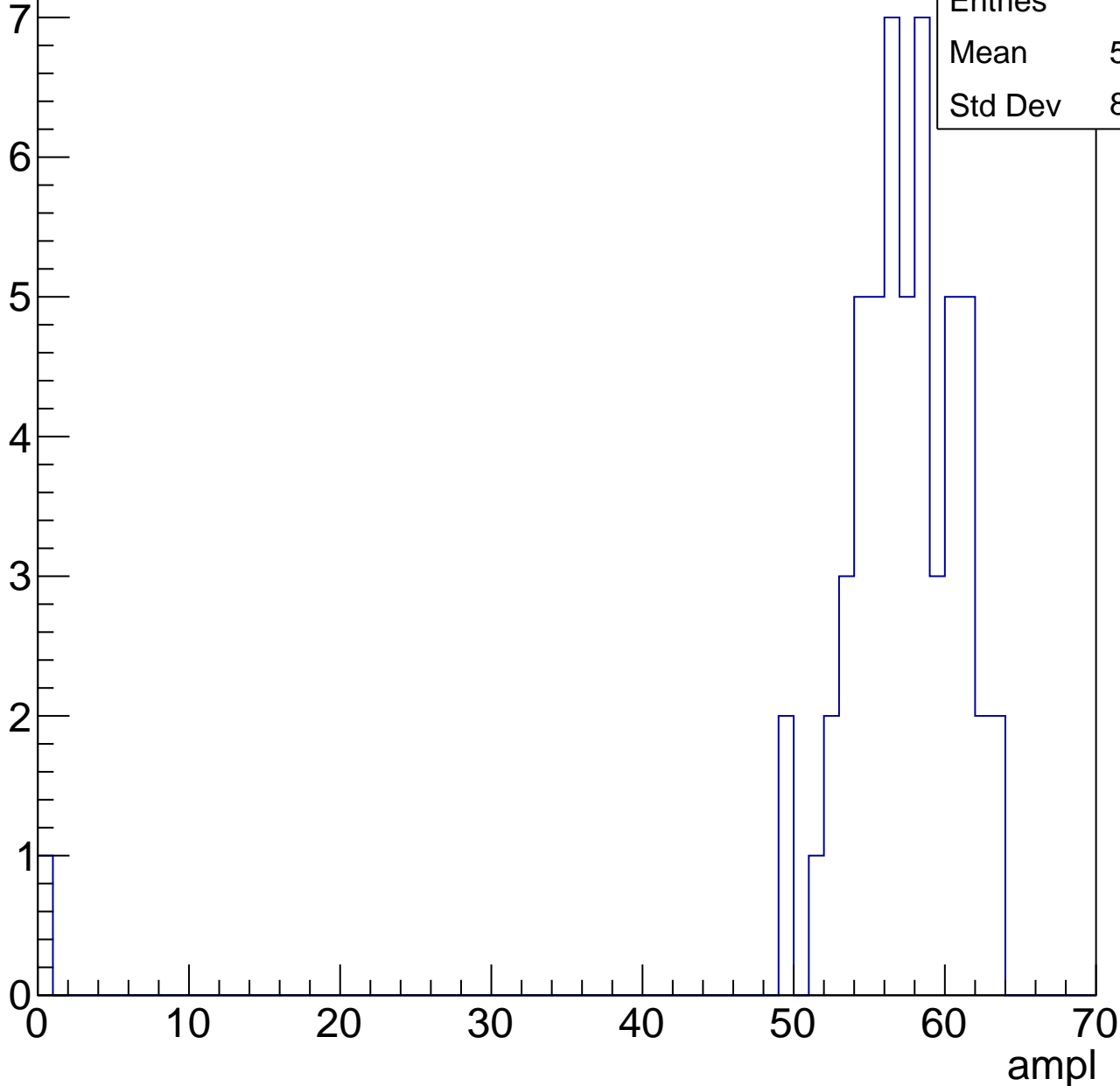


B1L100S, U25-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.85
Std Dev	8.287

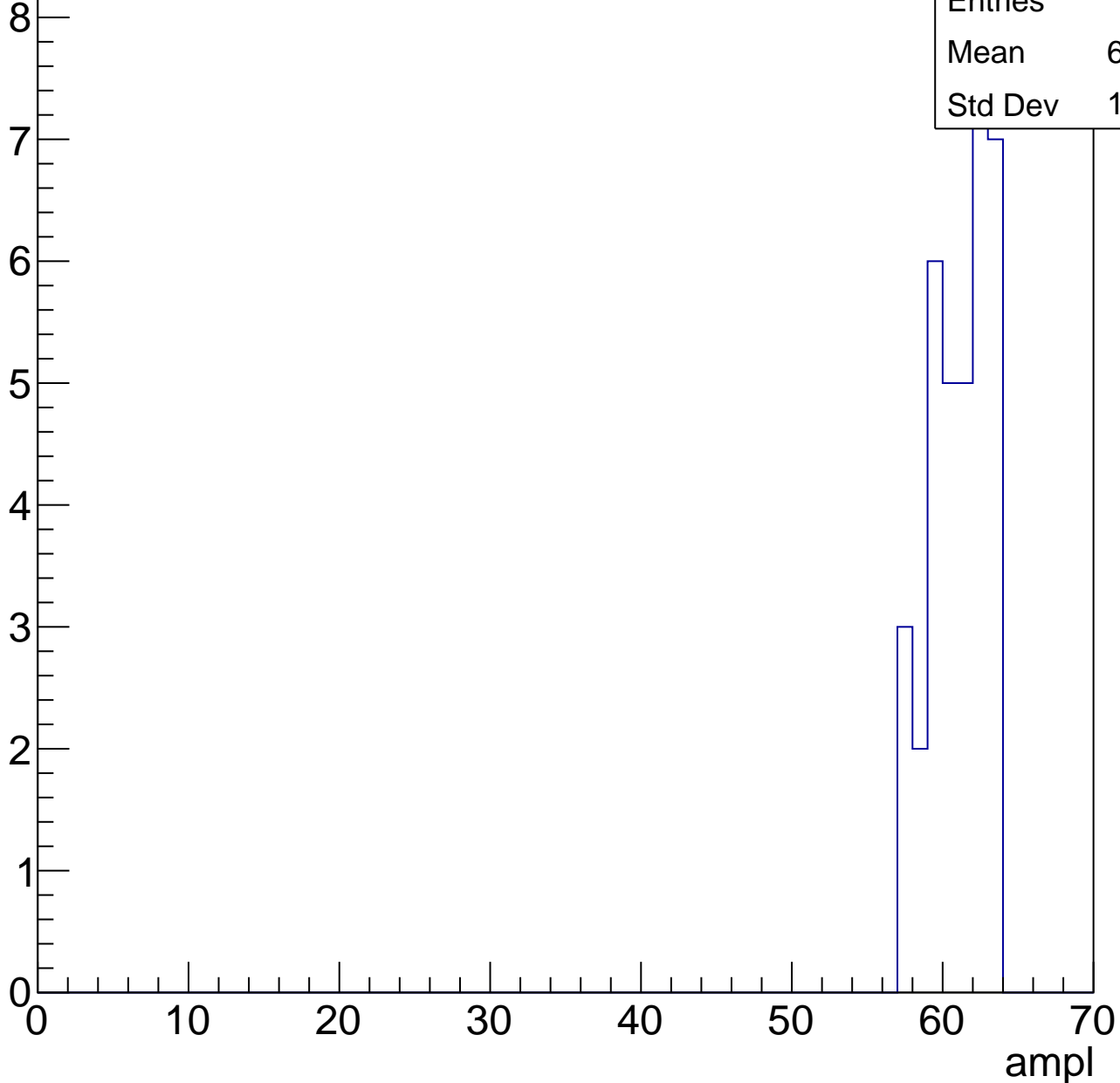


B1L100S, U25-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	60.64
Std Dev	1.873



B1L100S, U25-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U25-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch116, adc0

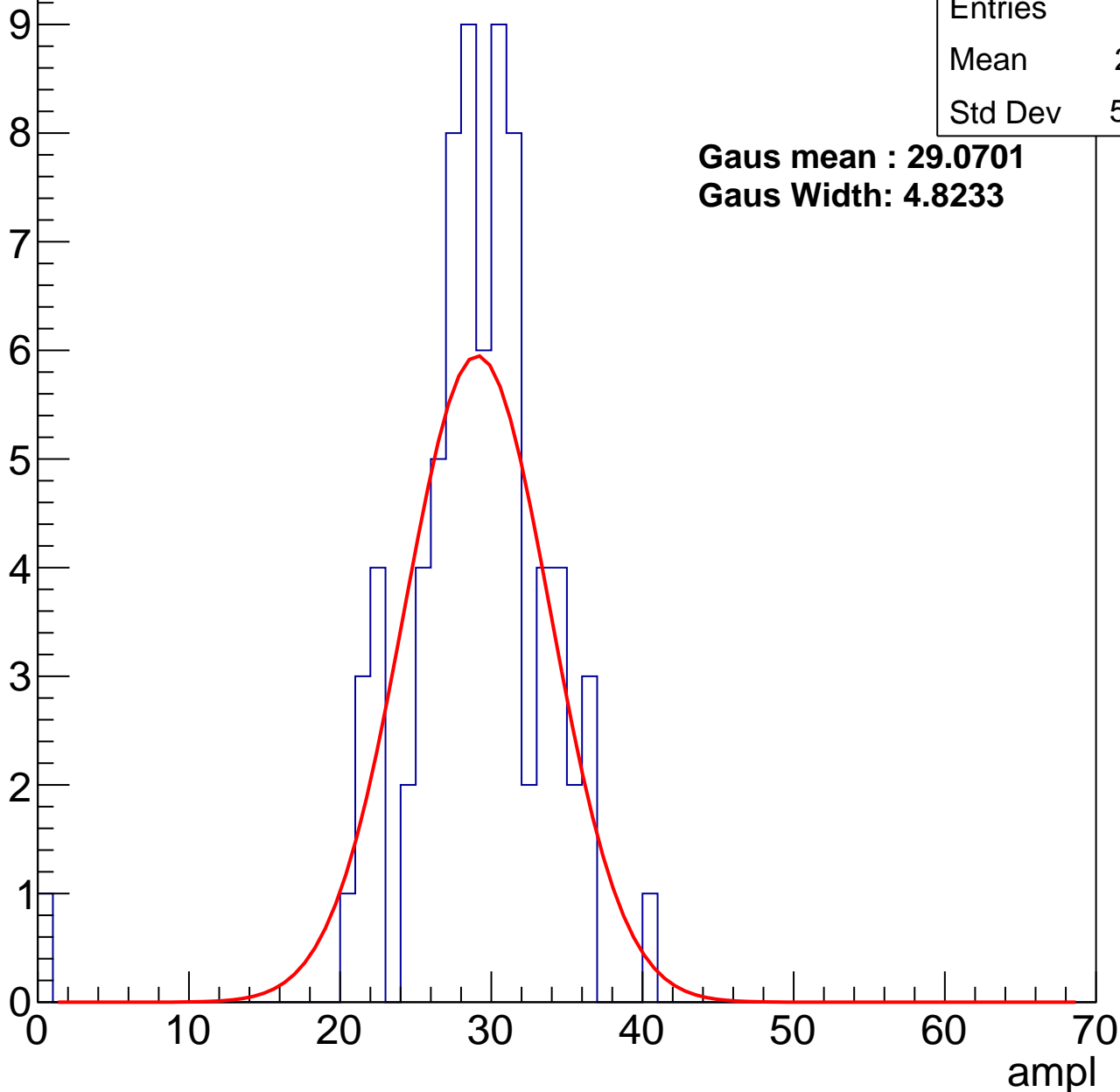
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	28.41
Std Dev	5.189

Gaus mean : 29.0701

Gaus Width: 4.8233



B1L100S, U25-ch116, adc1

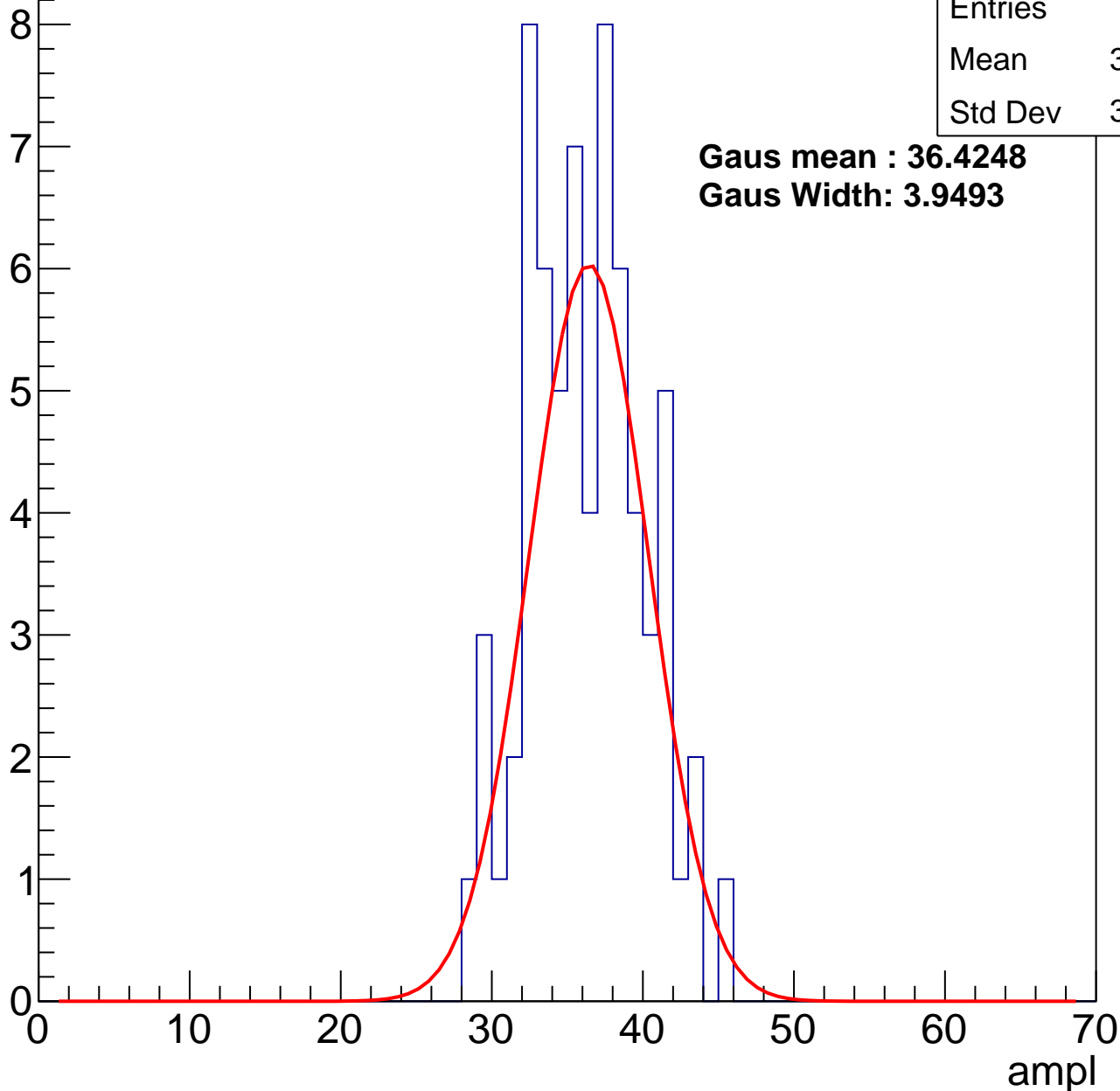
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.79
Std Dev	3.783

Gaus mean : 36.4248

Gaus Width: 3.9493



B1L100S, U25-ch116, adc2

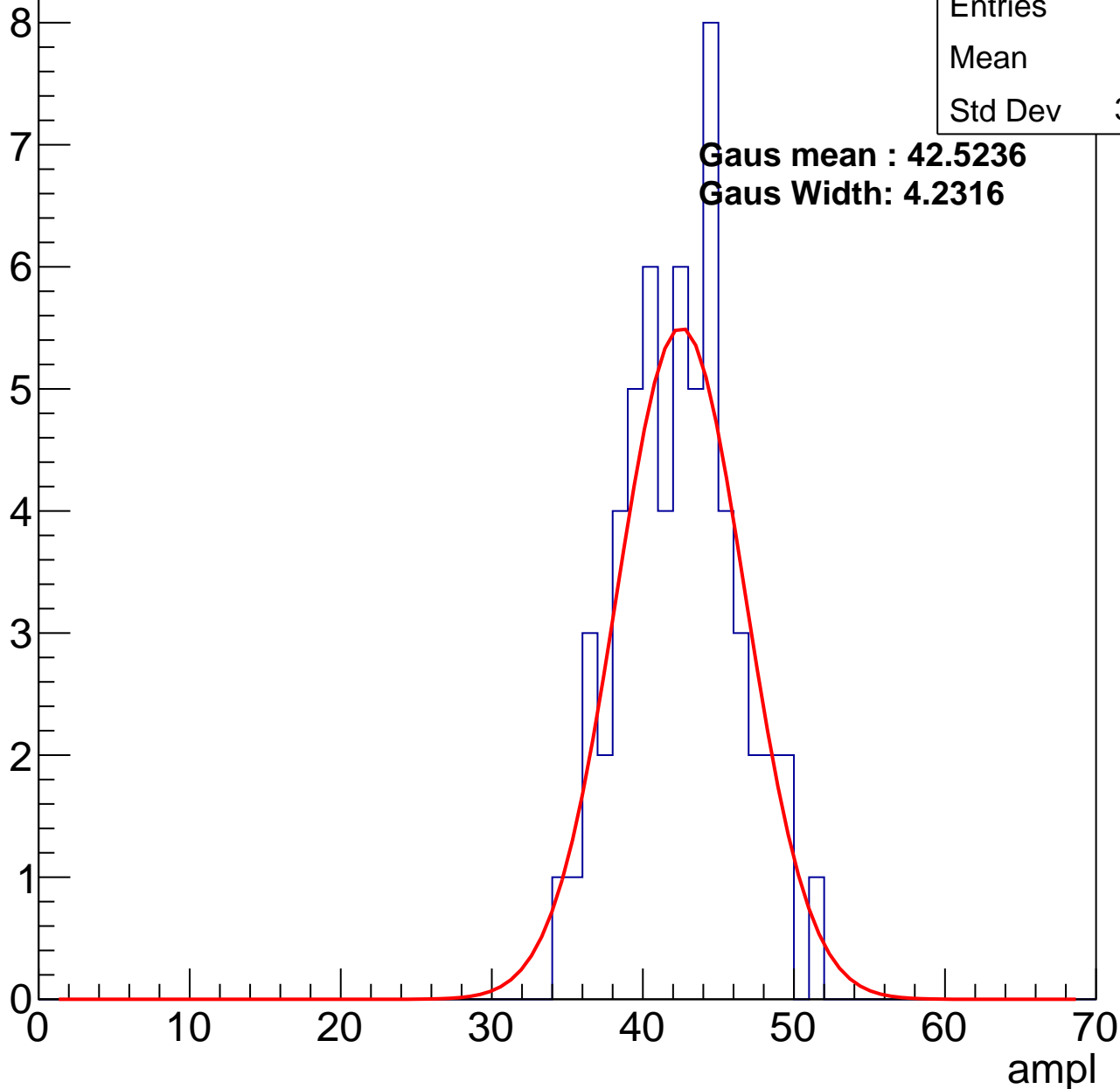
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42
Std Dev	3.751

Gaus mean : 42.5236

Gaus Width: 4.2316

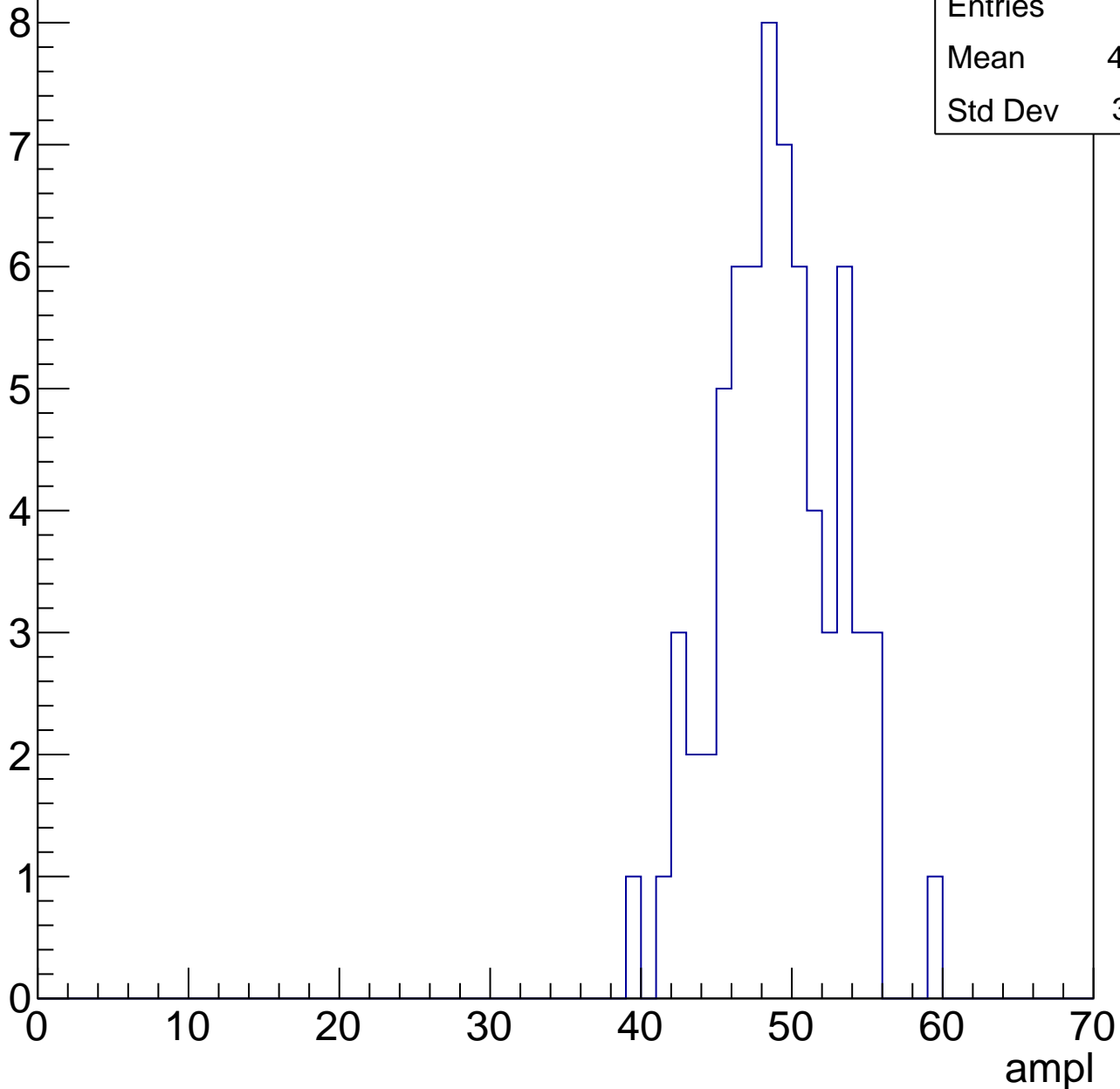


B1L100S, U25-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	48.57
Std Dev	3.891

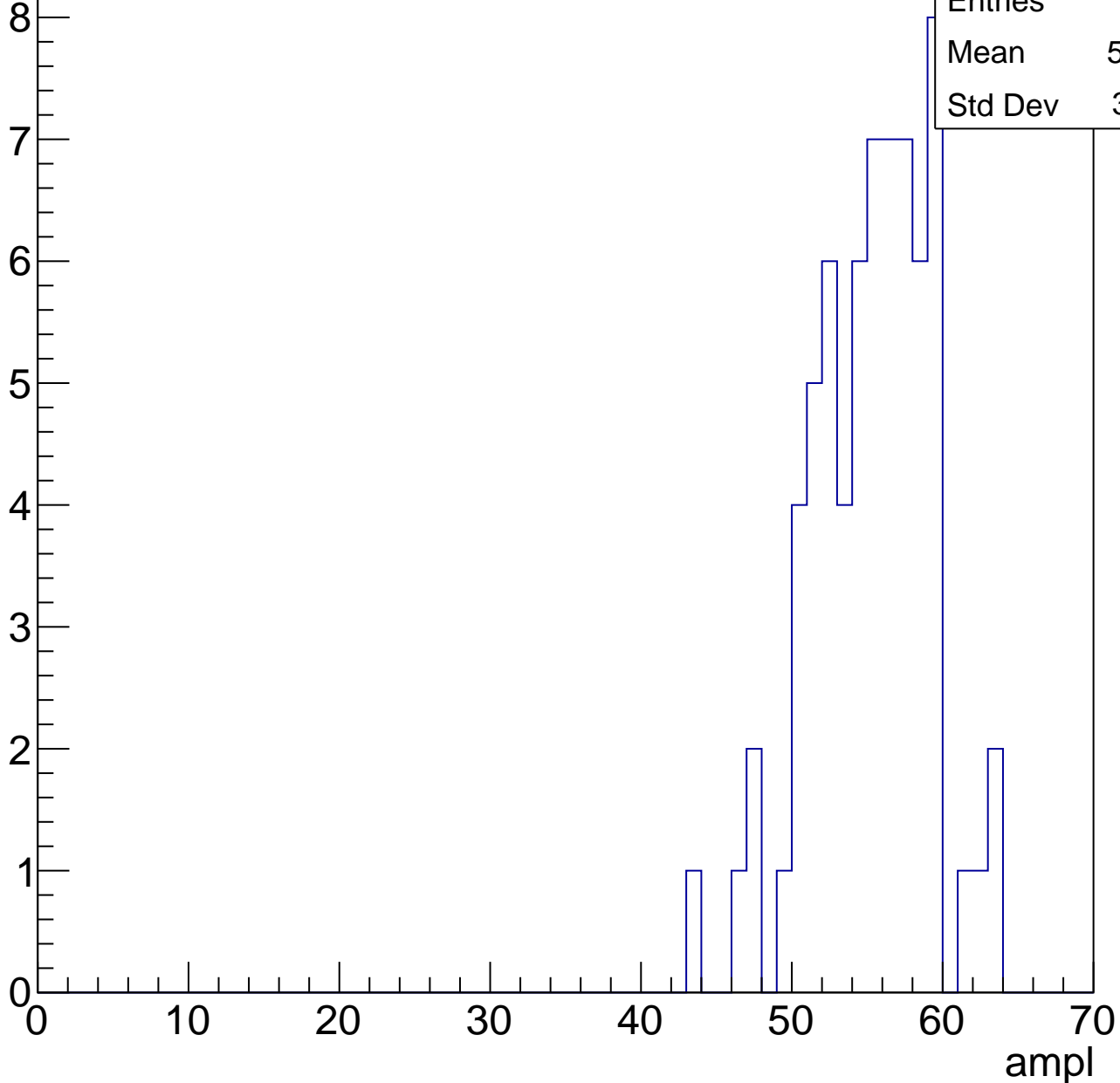


B1L100S, U25-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	54.78
Std Dev	3.941

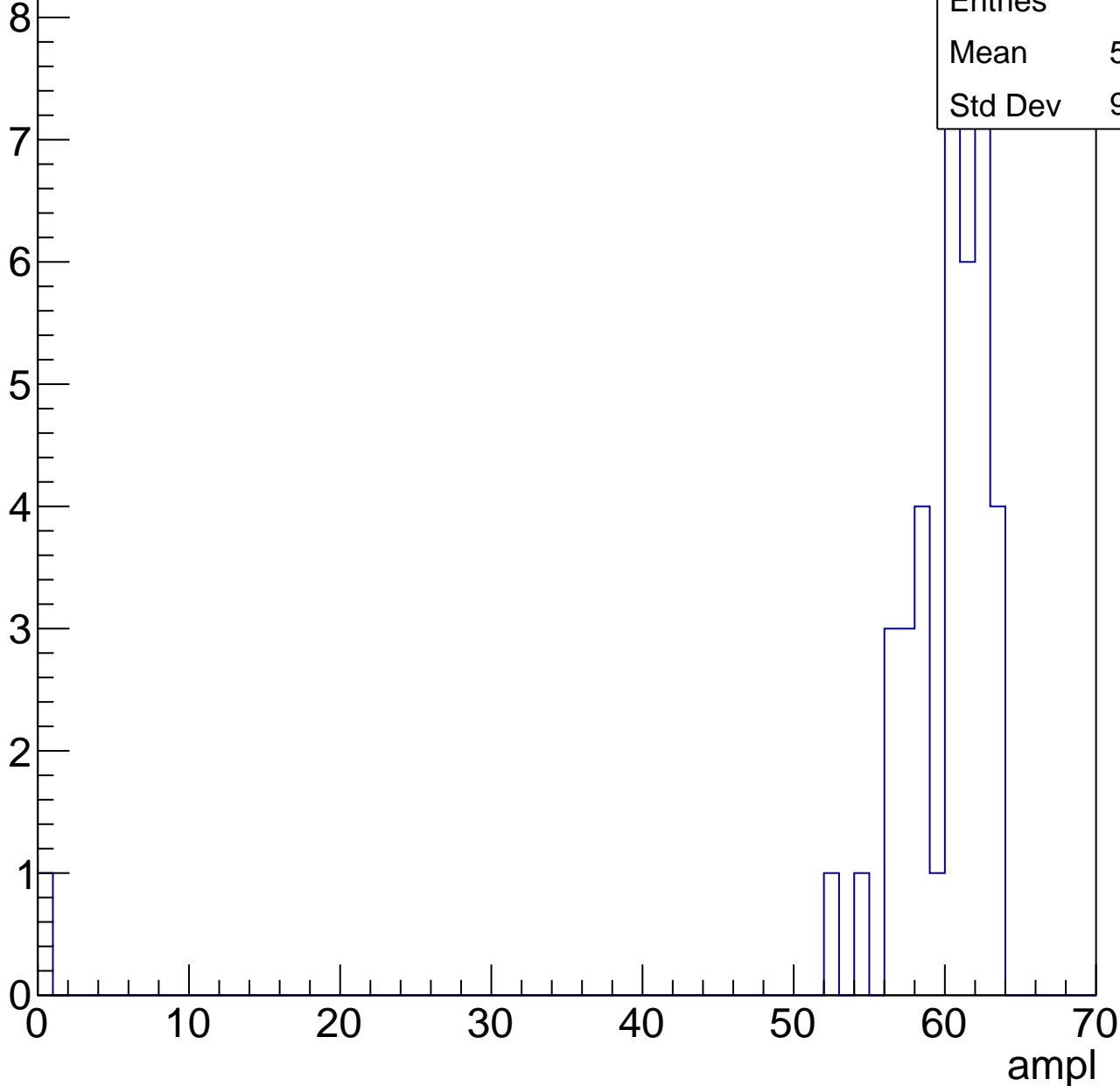


B1L100S, U25-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

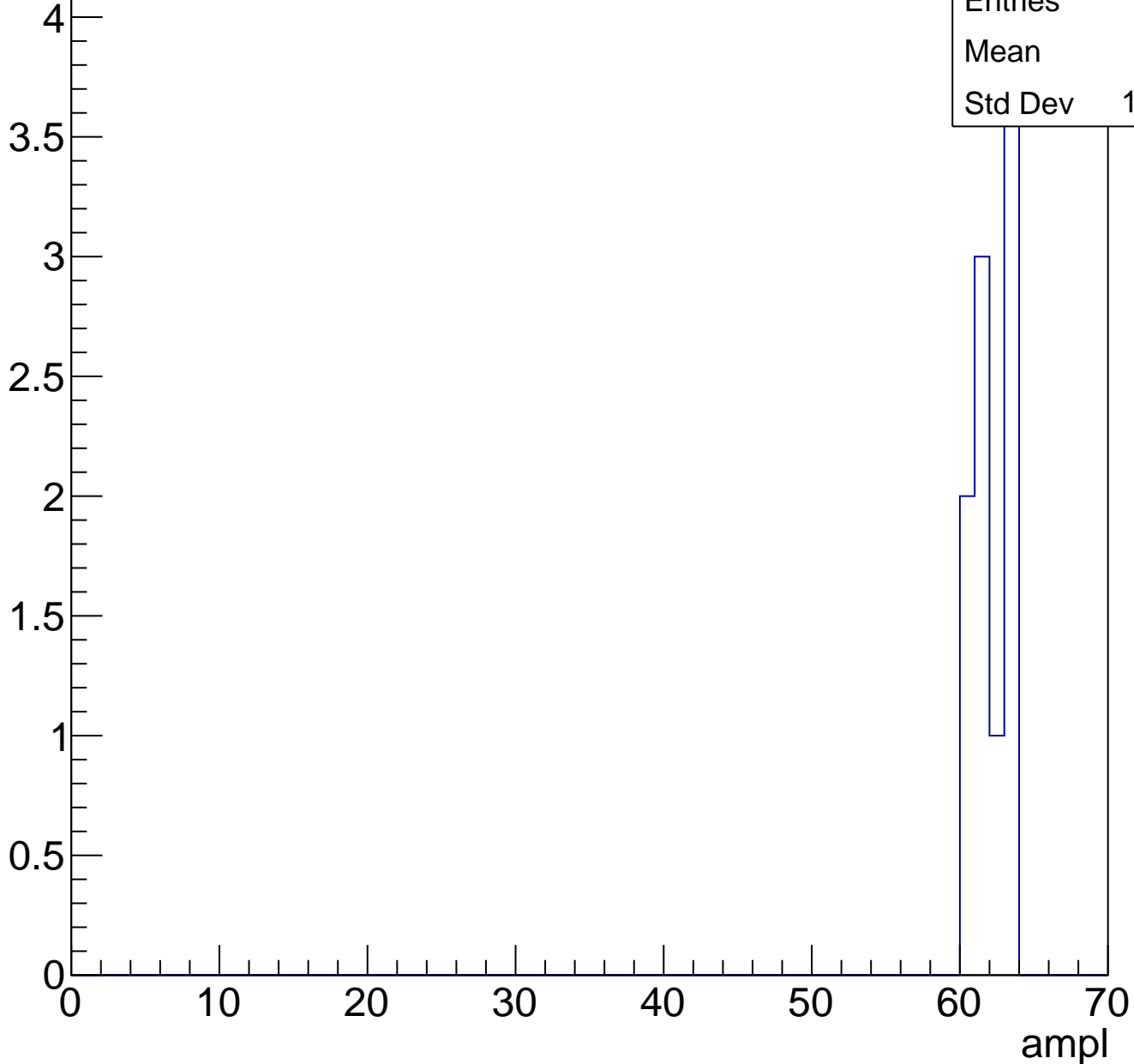
Entries	40
Mean	58.25
Std Dev	9.674



B1L100S, U25-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

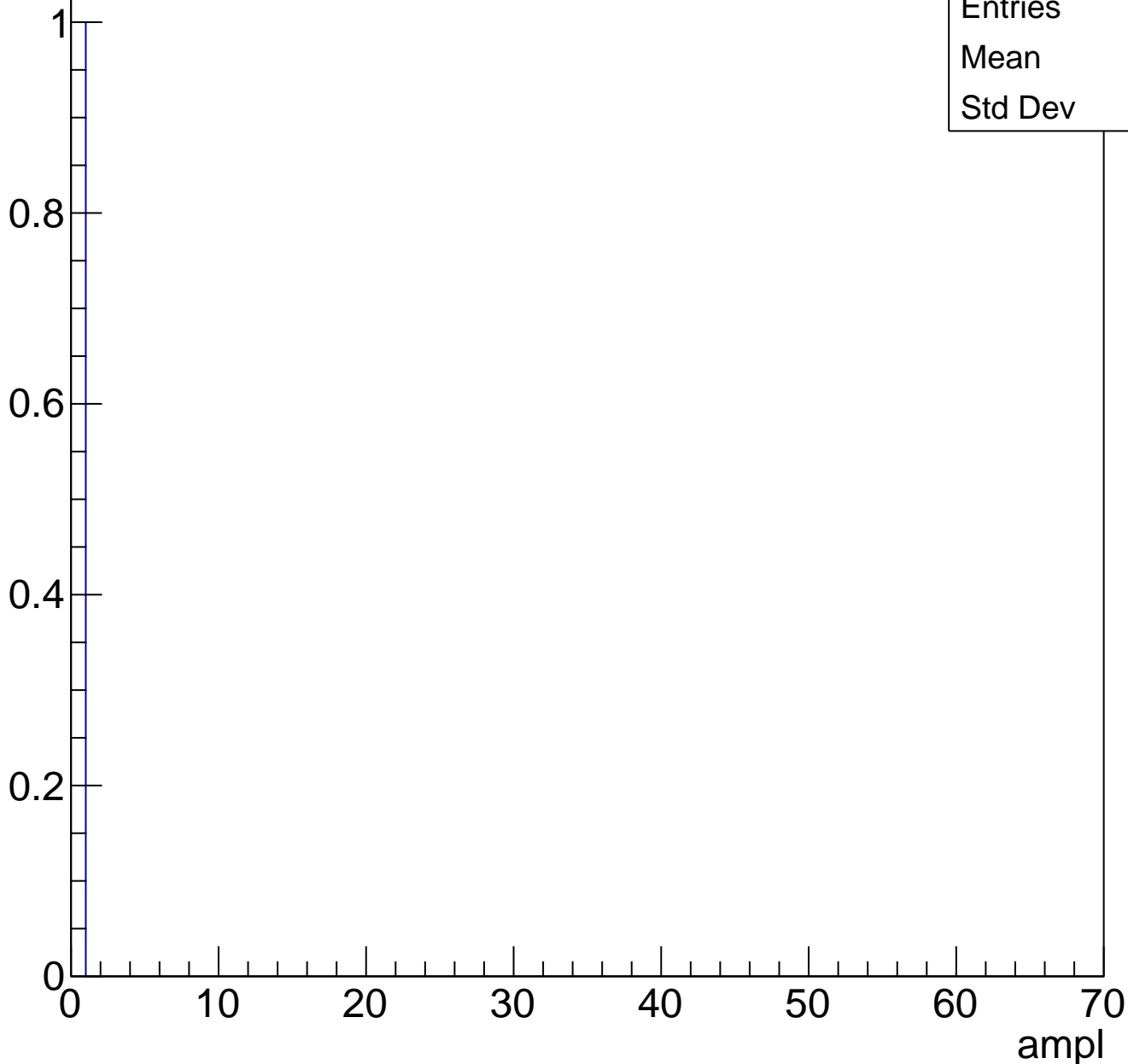
Entry



B1L100S, U25-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch117, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	28.13
Std Dev	5.66

Gaus mean : 29.4549

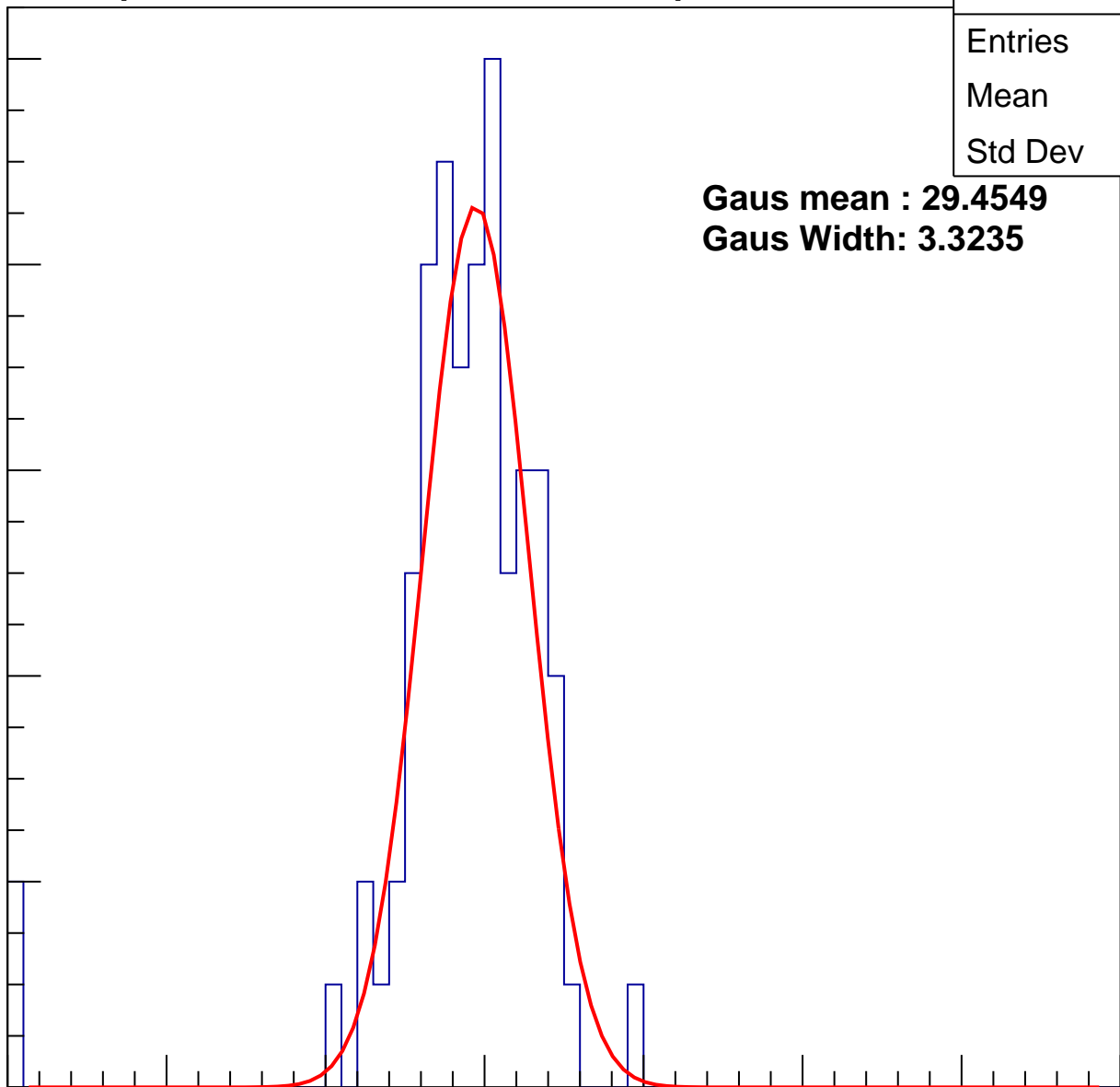
Gaus Width: 3.3235

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch117, adc1

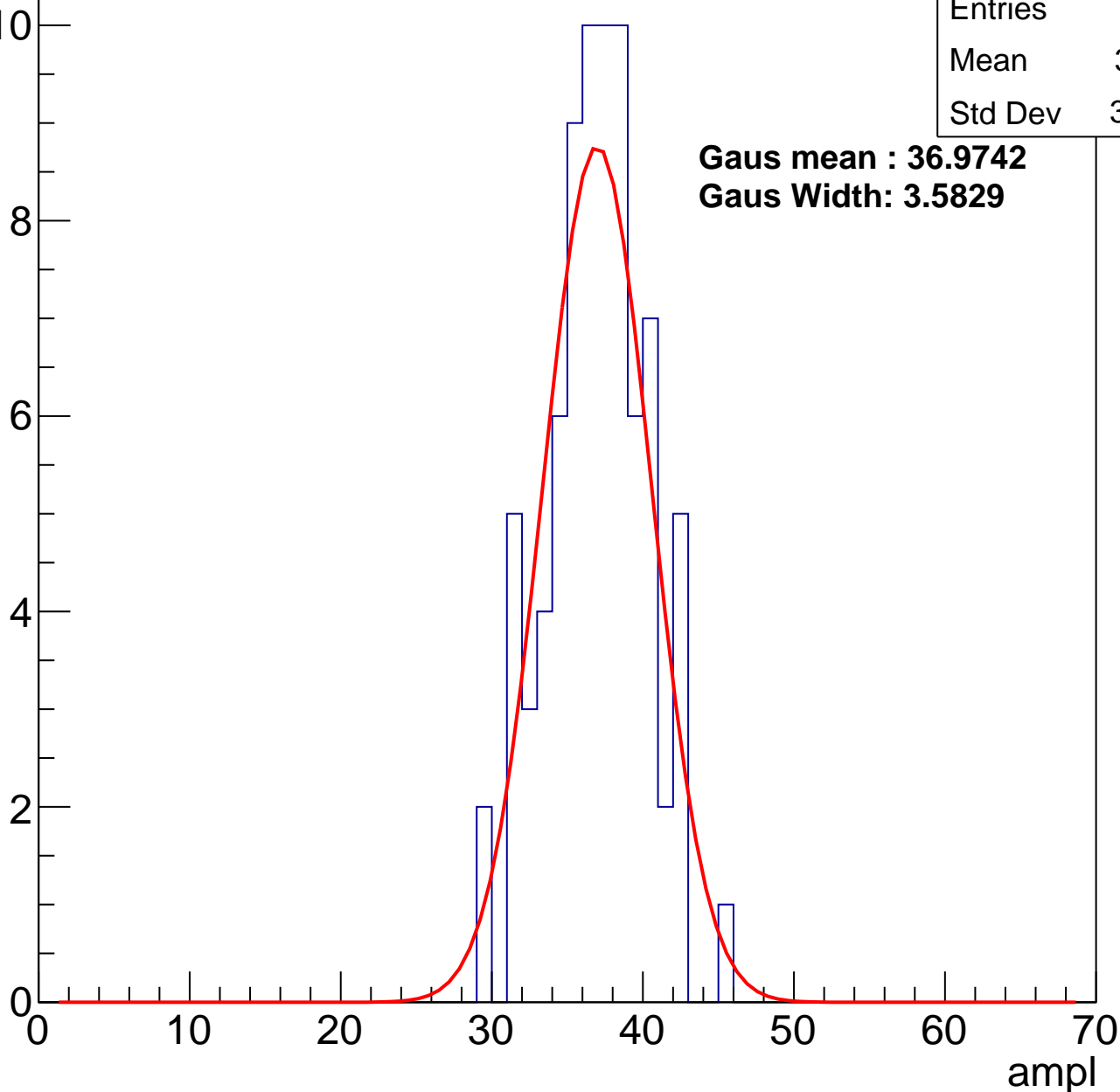
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	36.51
Std Dev	3.252

Gaus mean : 36.9742

Gaus Width: 3.5829



B1L100S, U25-ch117, adc2

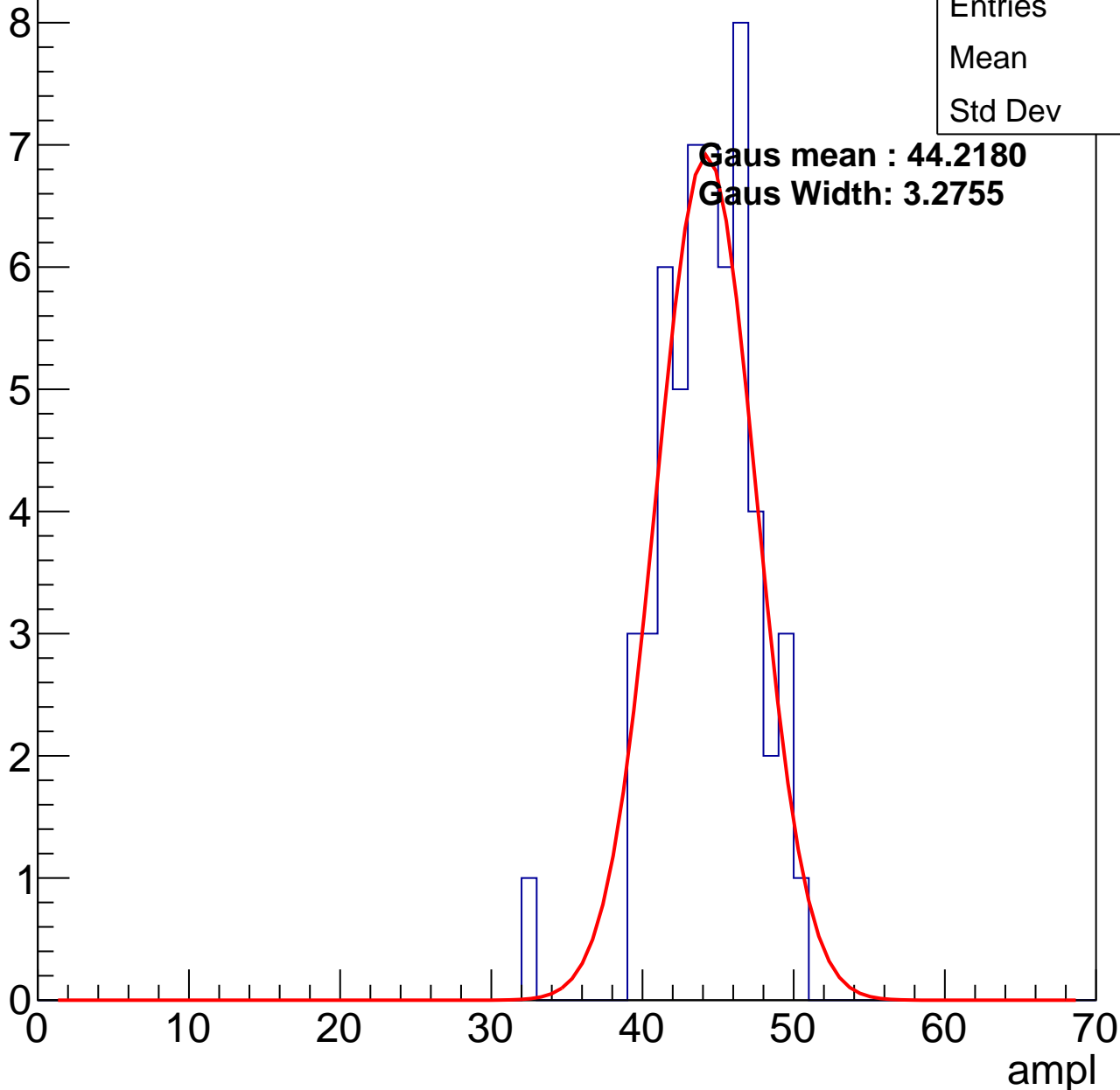
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.8
Std Dev	3.17

Gaus mean : 44.2180

Gaus Width: 3.2755

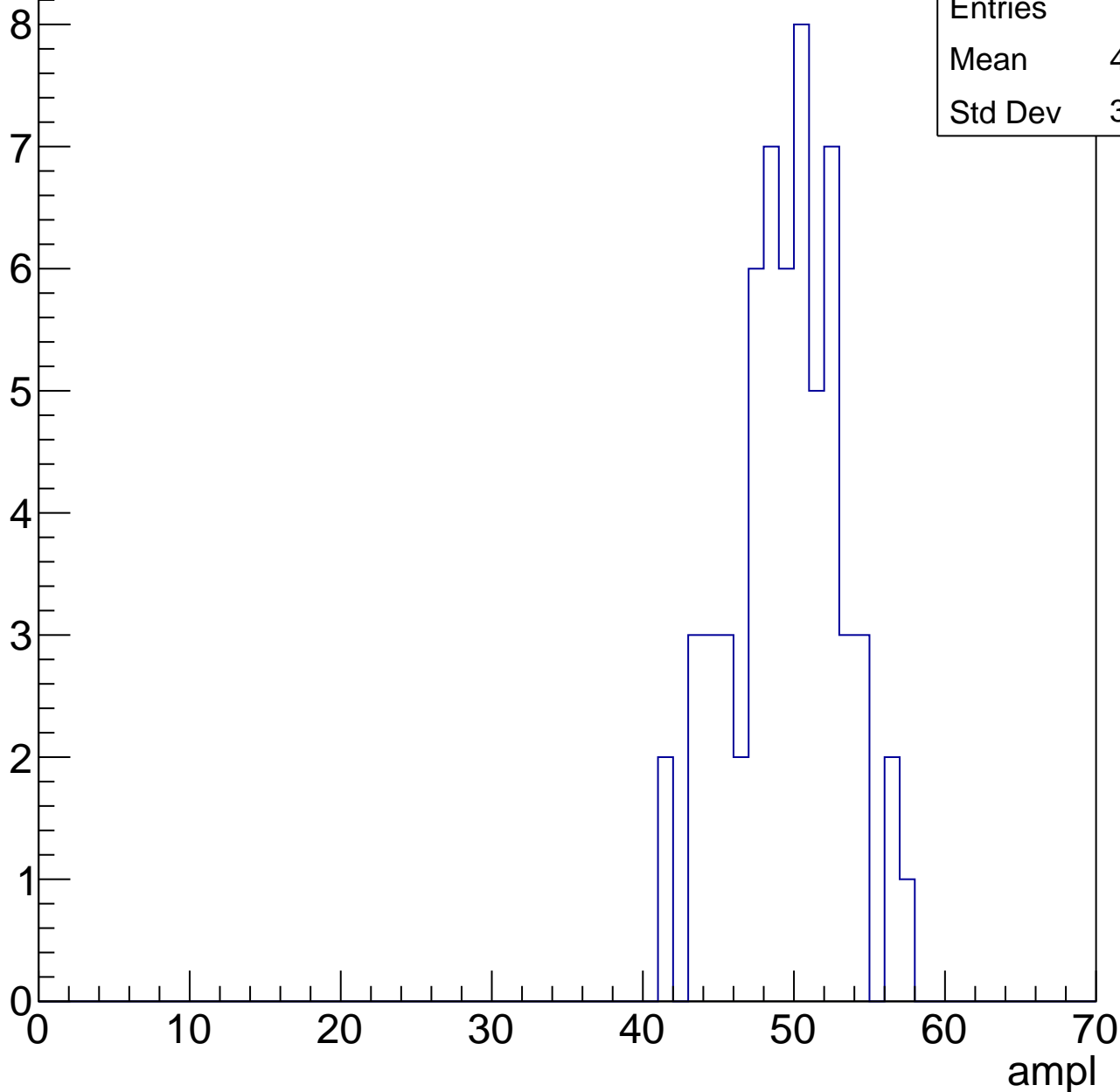


B1L100S, U25-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.03
Std Dev	3.585

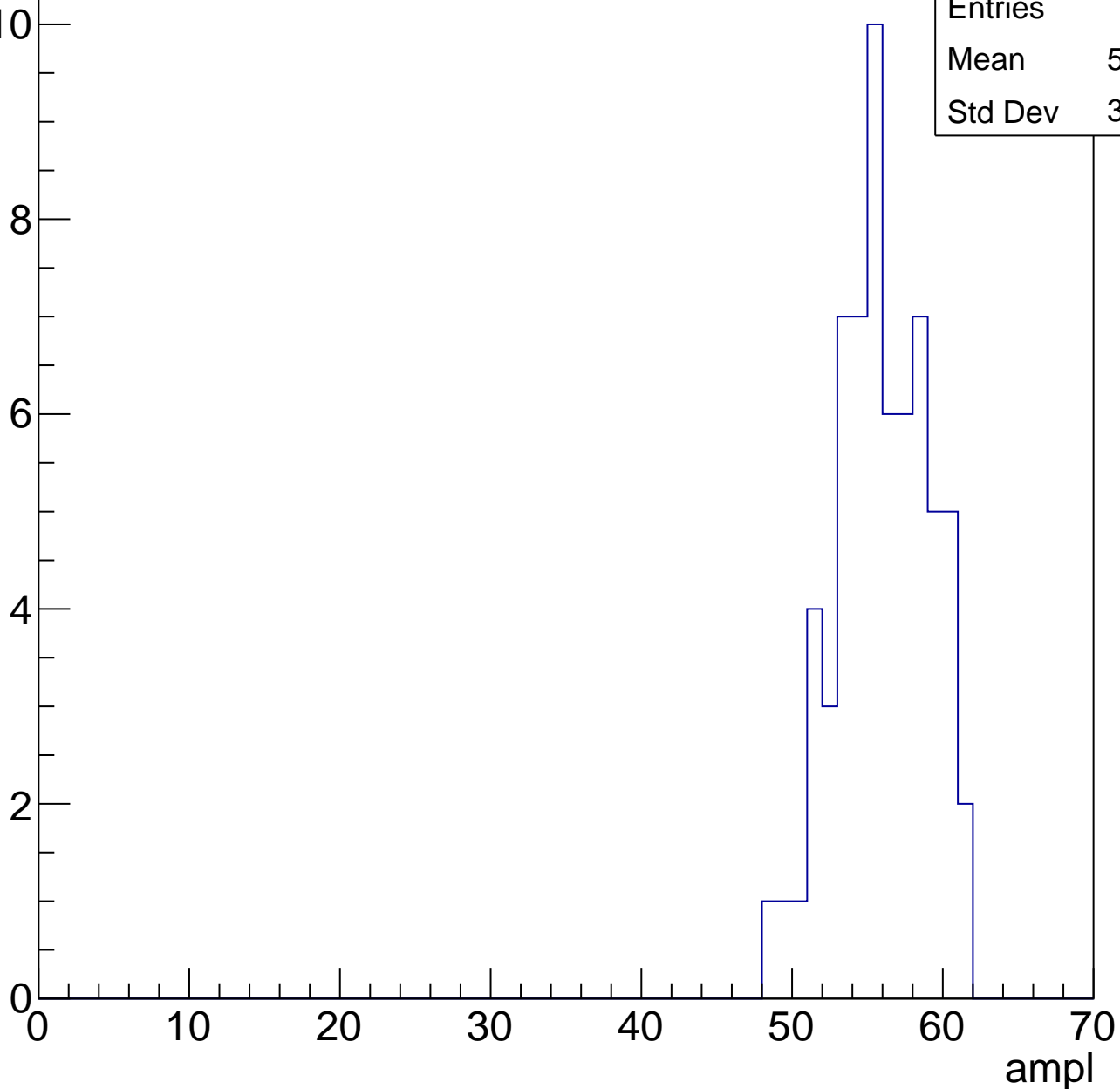


B1L100S, U25-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	55.49
Std Dev	3.013

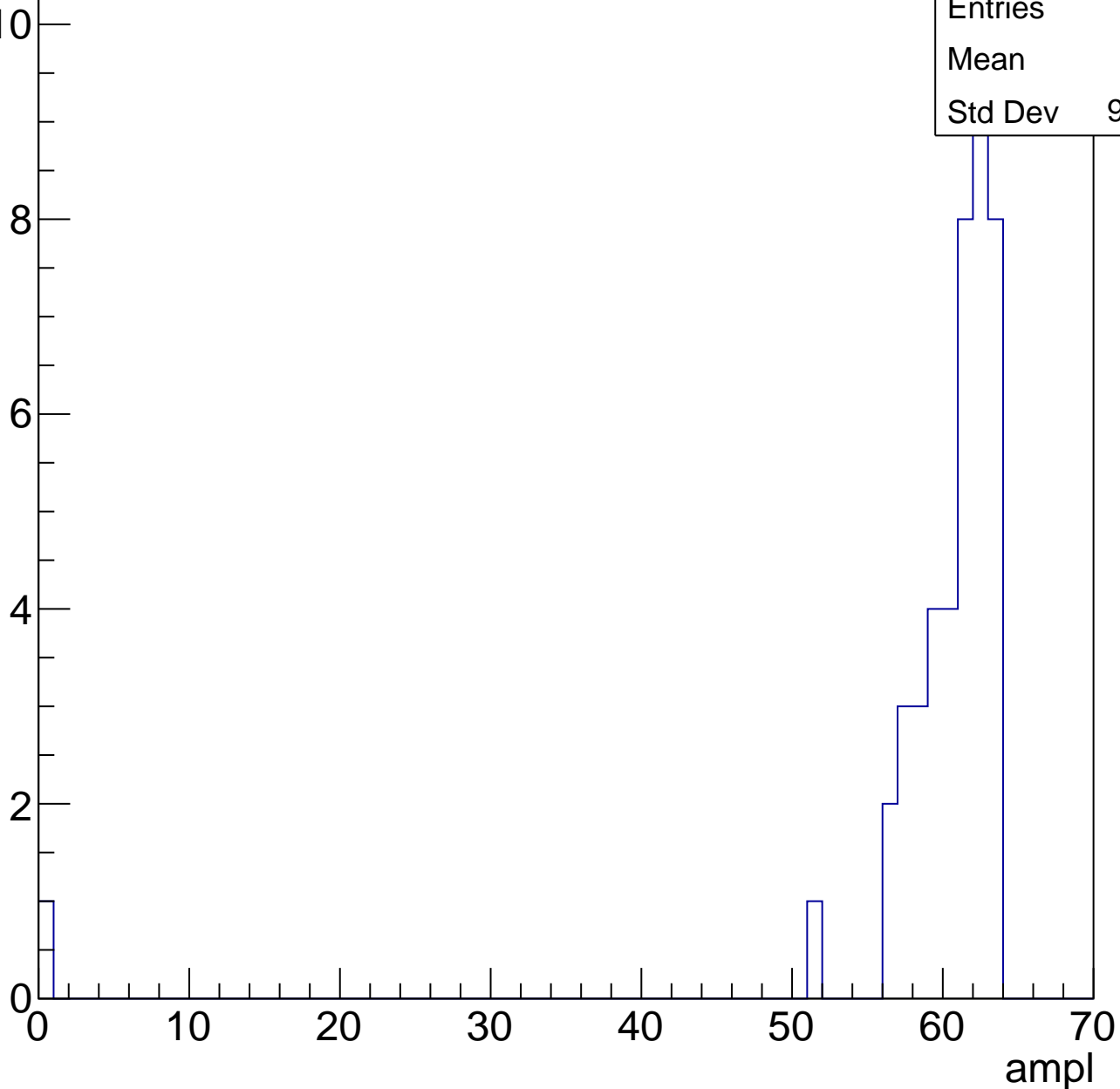


B1L100S, U25-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	59
Std Dev	9.332



B1L100S, U25-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch118, adc0

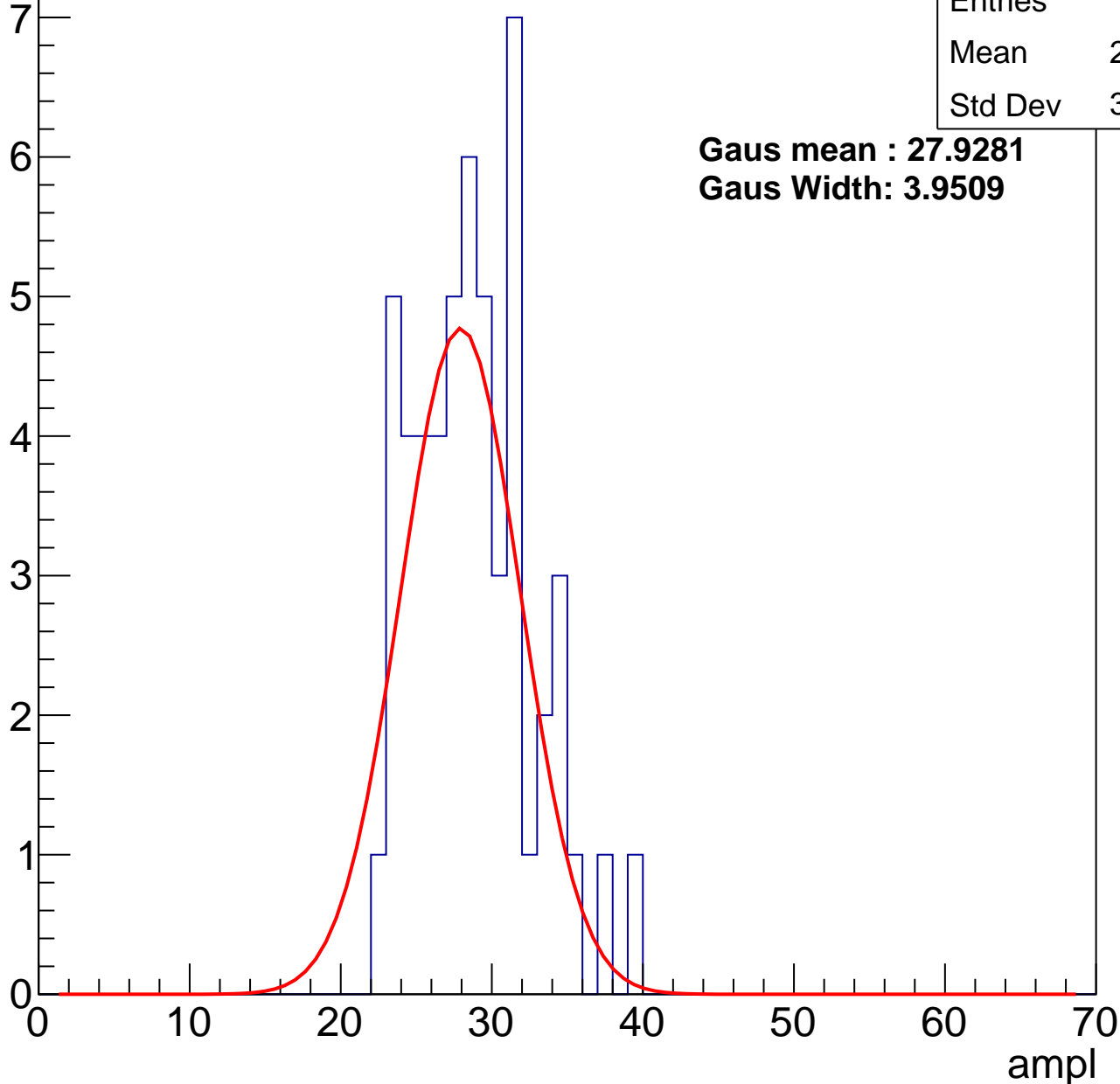
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	28.36
Std Dev	3.837

Gaus mean : 27.9281

Gaus Width: 3.9509



B1L100S, U25-ch118, adc1

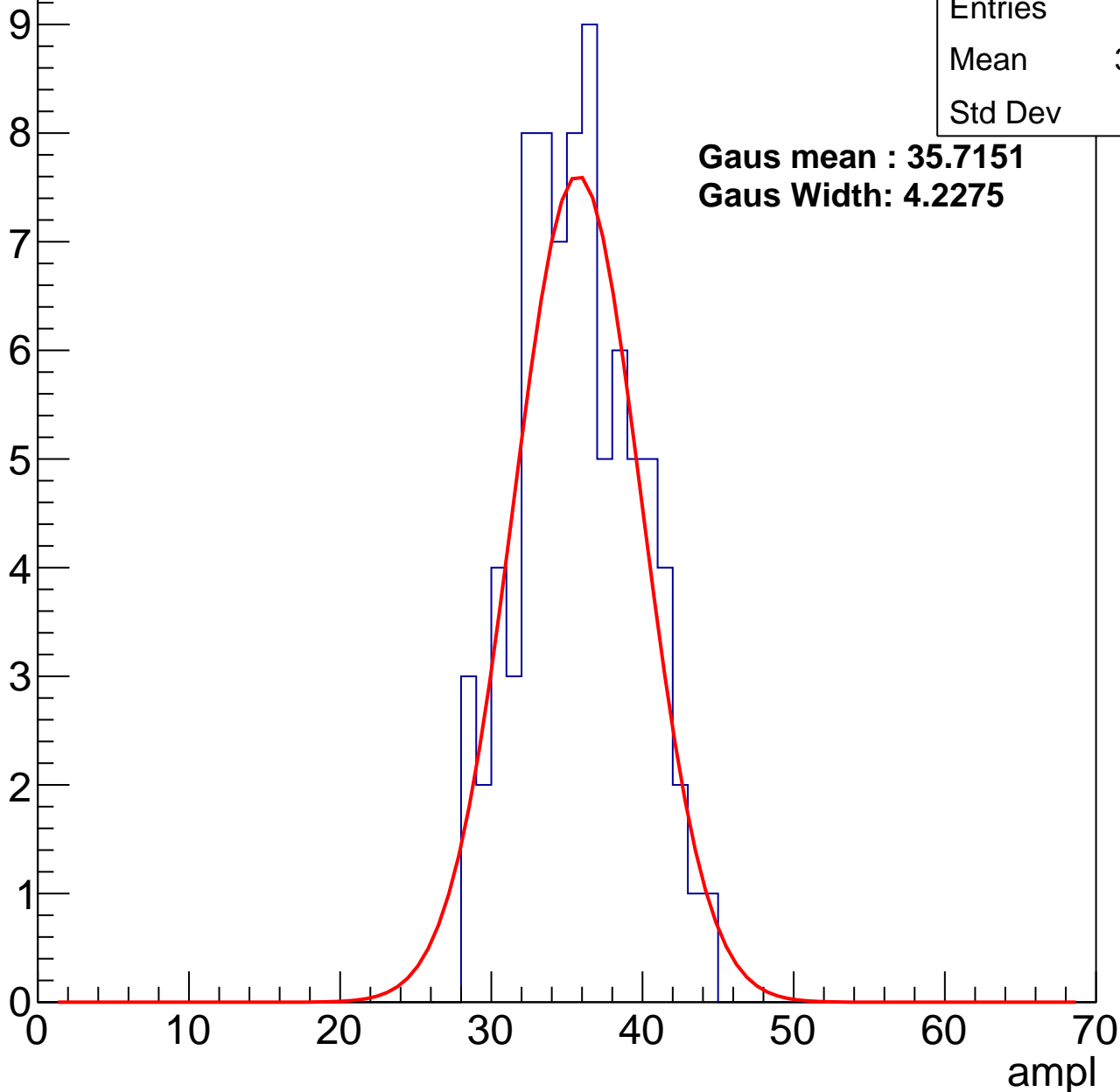
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	35.31
Std Dev	3.76

Gaus mean : 35.7151

Gaus Width: 4.2275



B1L100S, U25-ch118, adc2

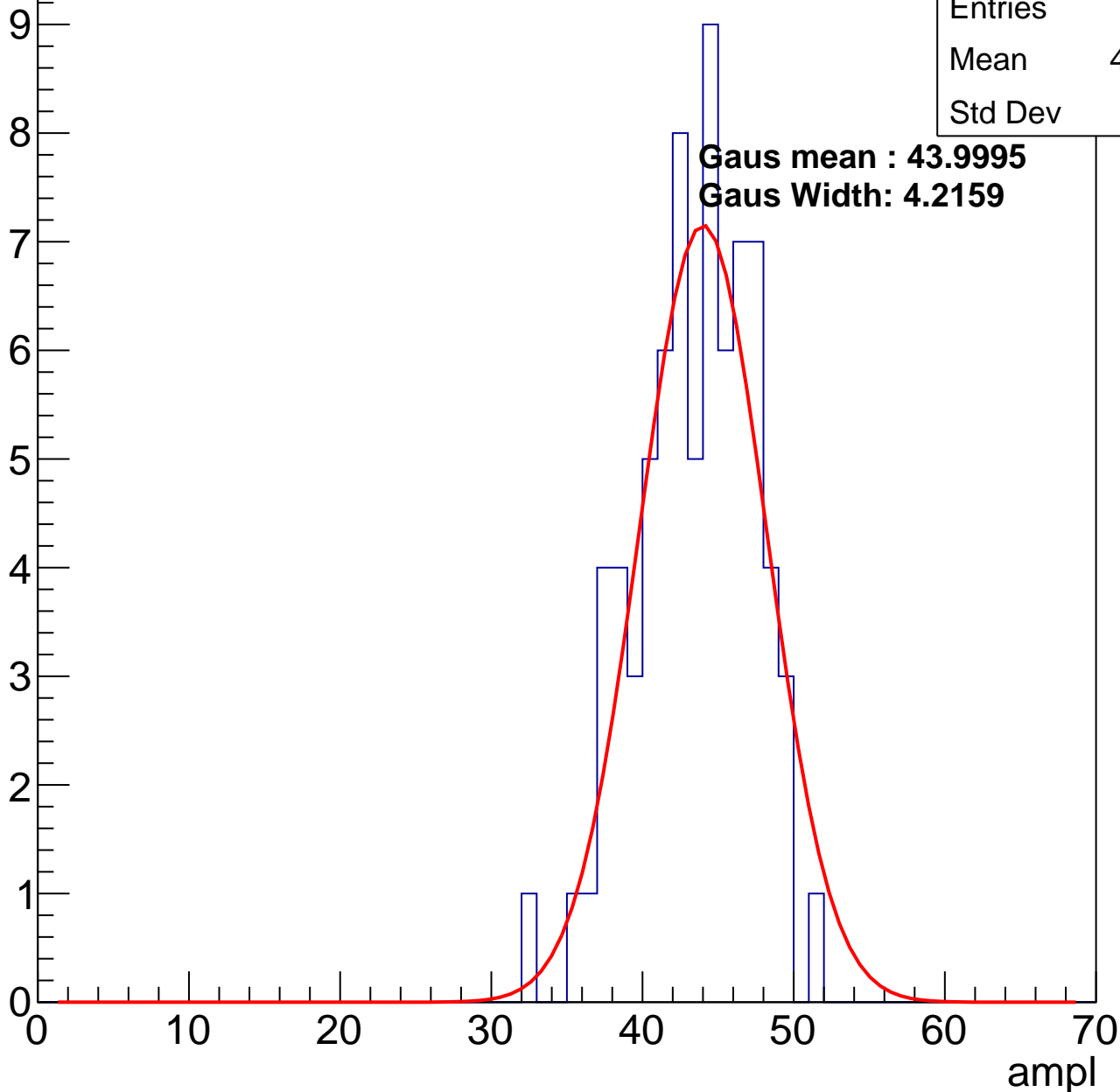
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	42.99
Std Dev	3.8

Gaus mean : 43.9995

Gaus Width: 4.2159

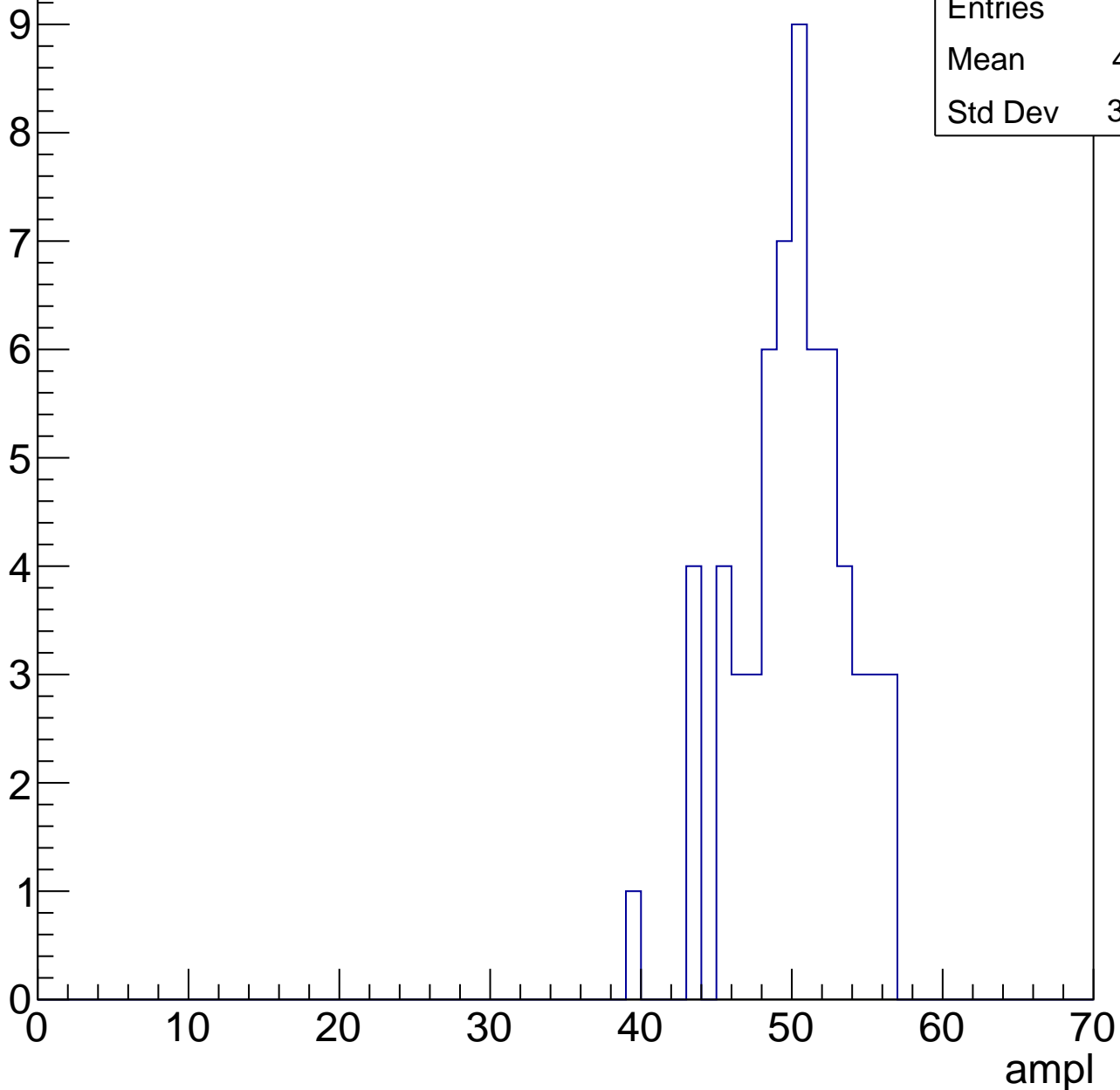


B1L100S, U25-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	49.61
Std Dev	3.616

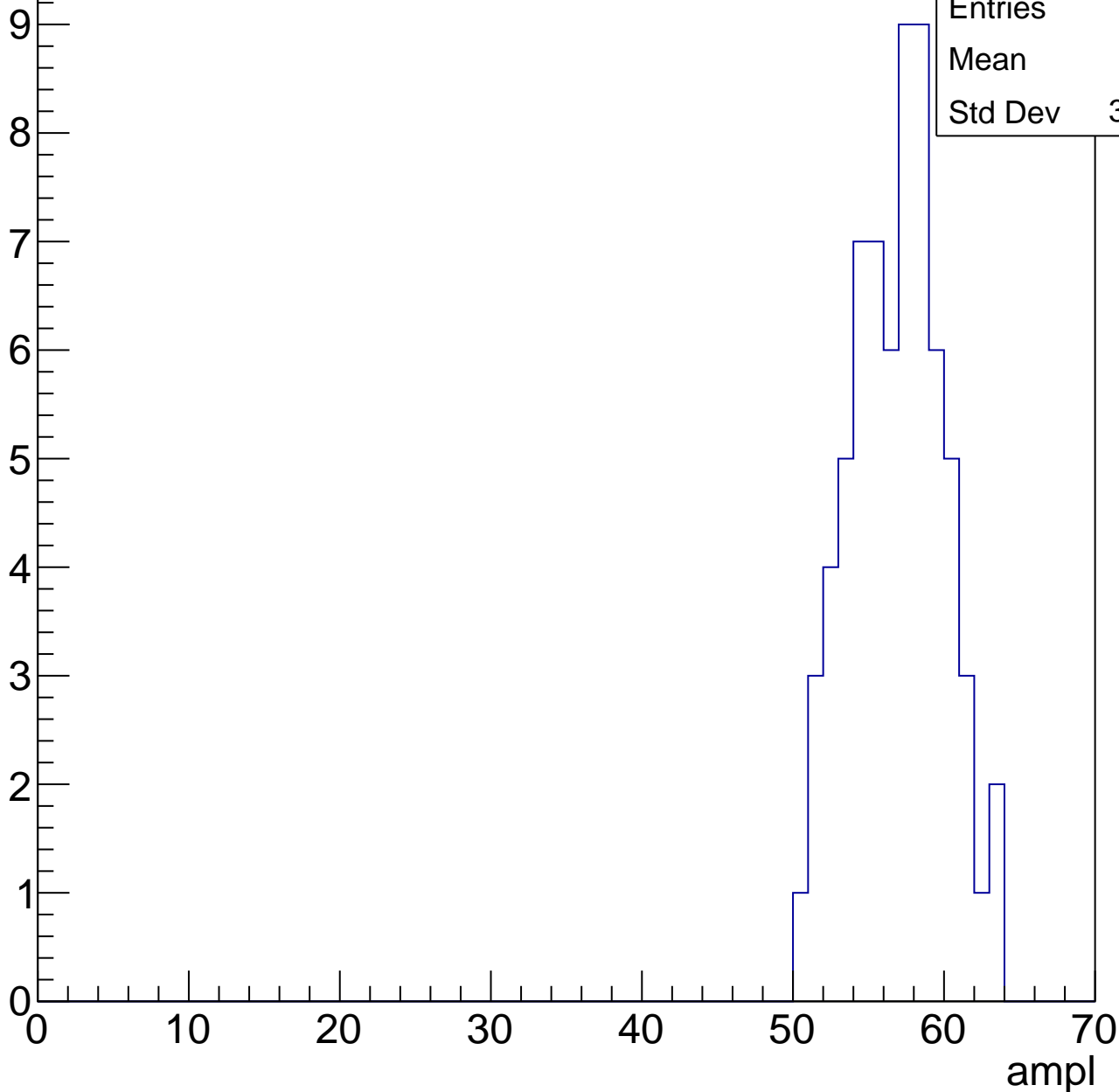


B1L100S, U25-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	56.4
Std Dev	3.049

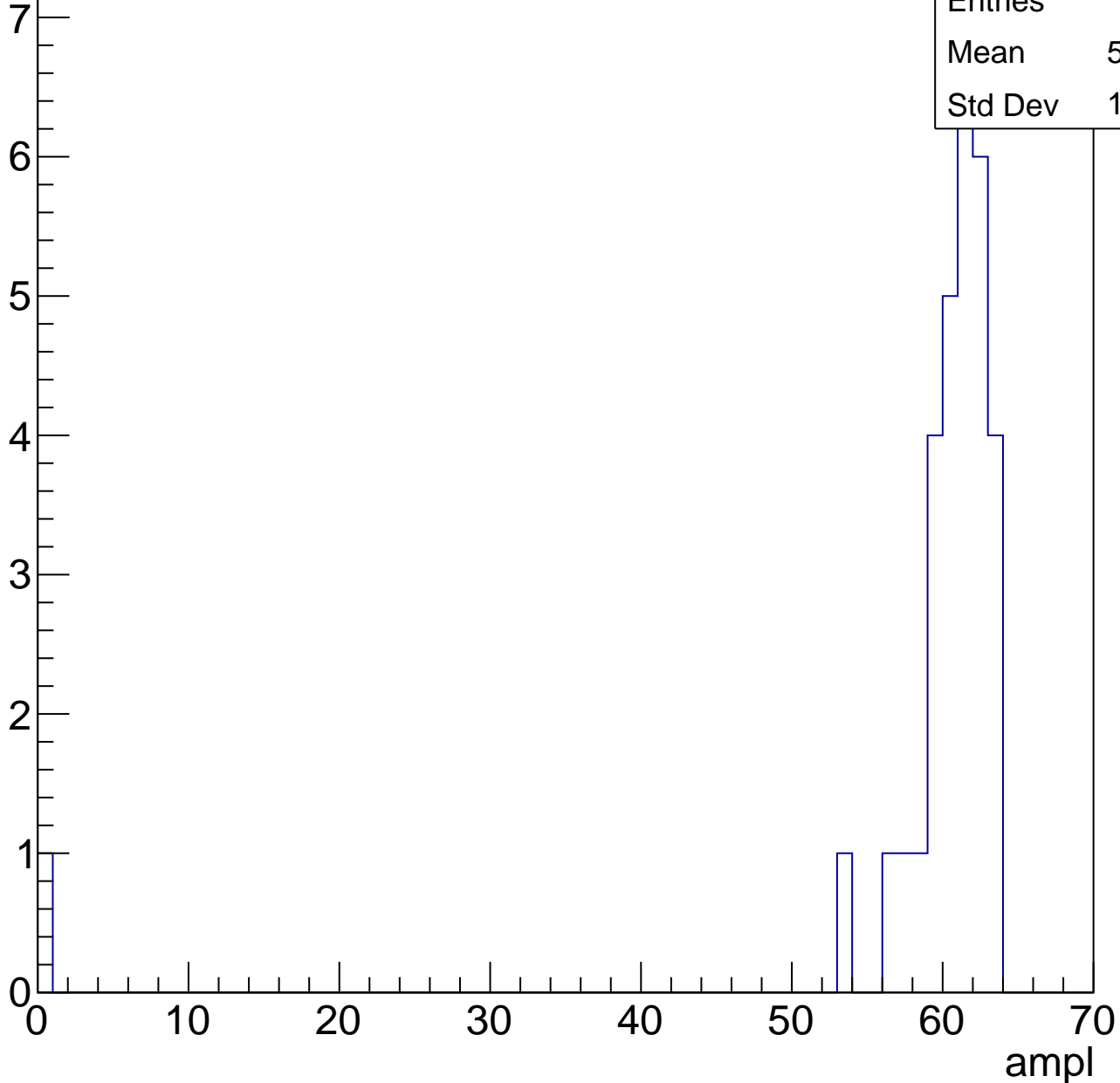


B1L100S, U25-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.42
Std Dev	10.88



B1L100S, U25-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

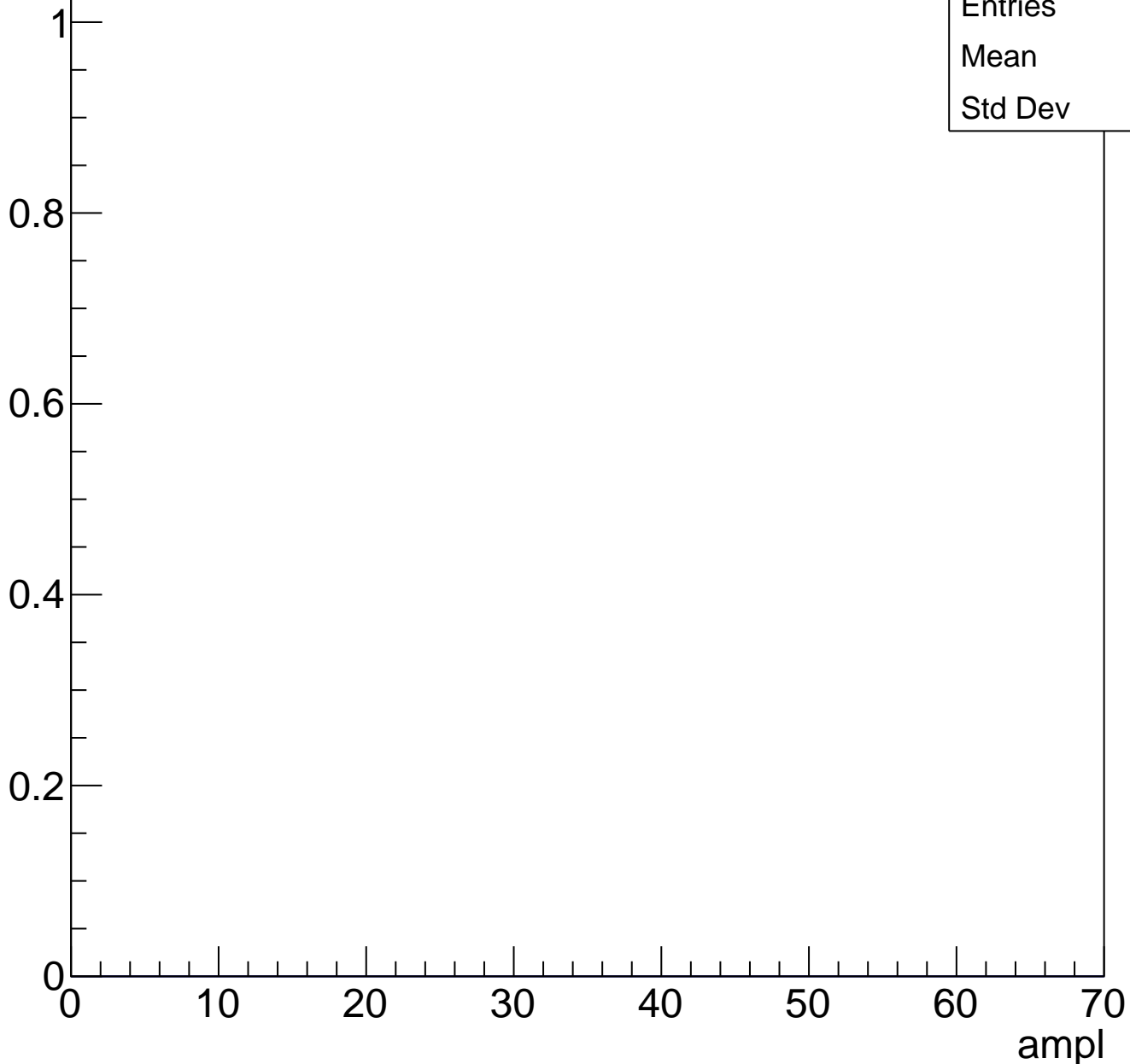
Entry



B1L100S, U25-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch119, adc0

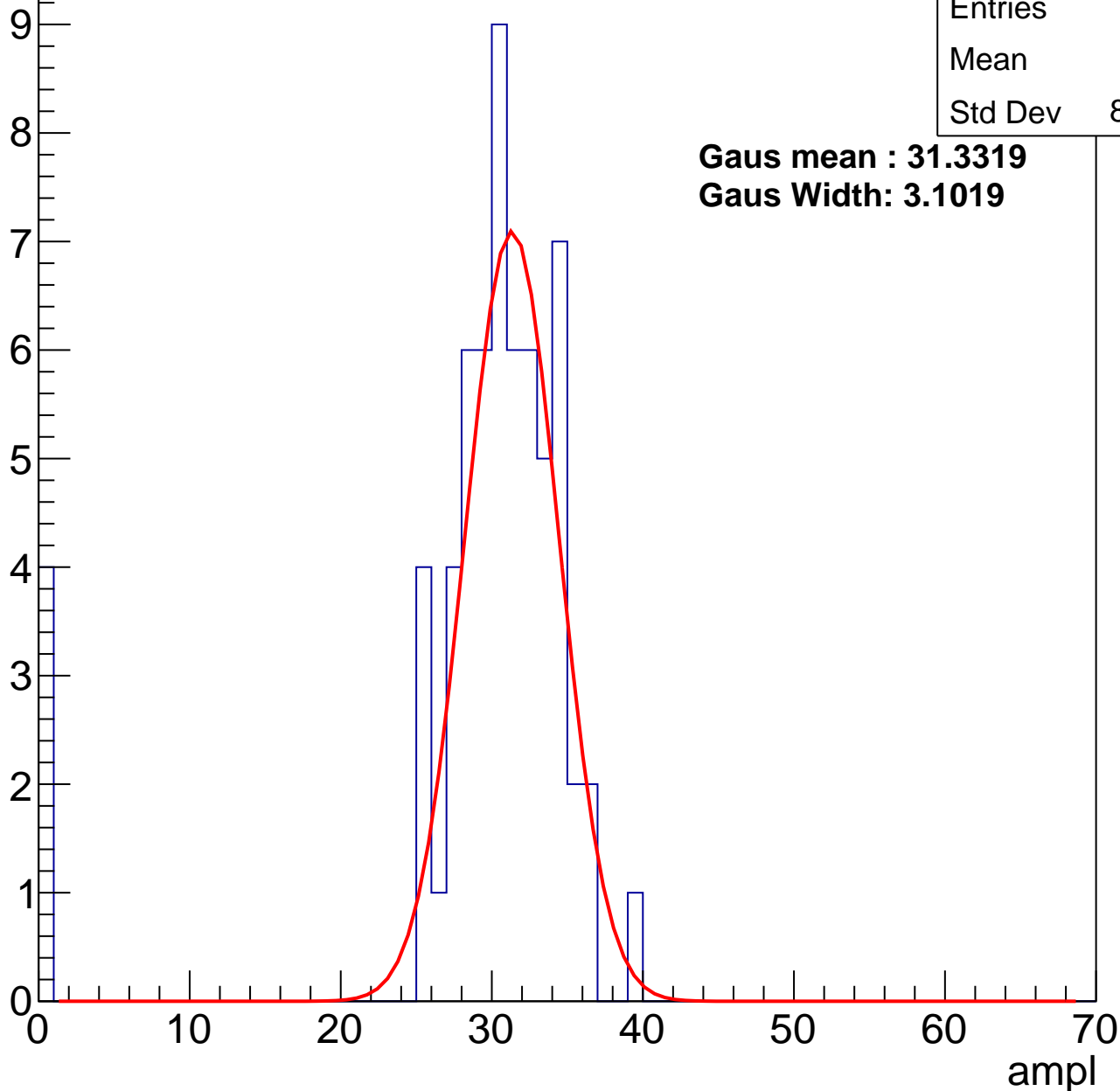
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.7
Std Dev	8.033

Gaus mean : 31.3319

Gaus Width: 3.1019



B1L100S, U25-ch119, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	36.94
Std Dev	3.257

Gaus mean : 37.7853

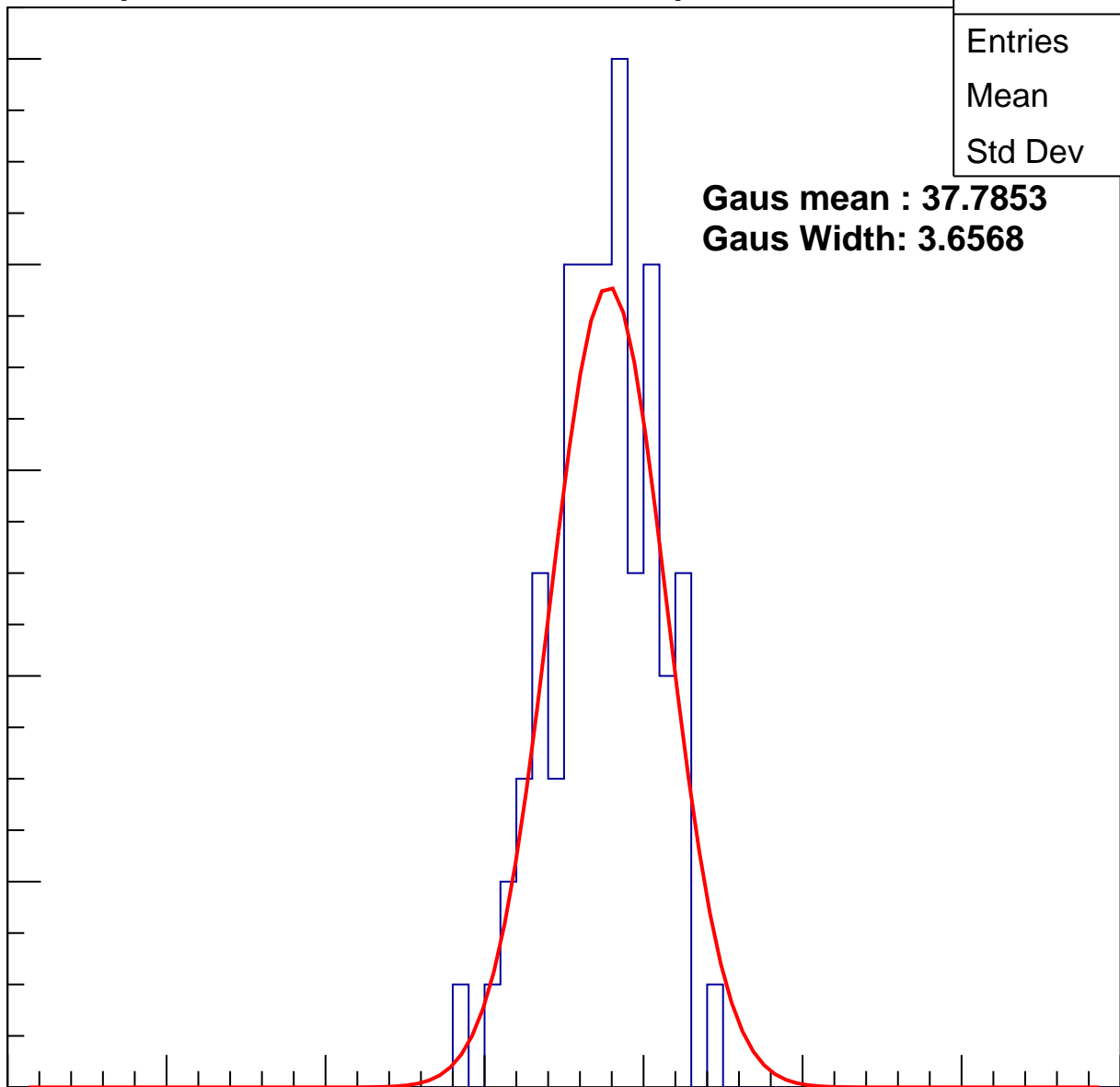
Gaus Width: 3.6568

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch119, adc2

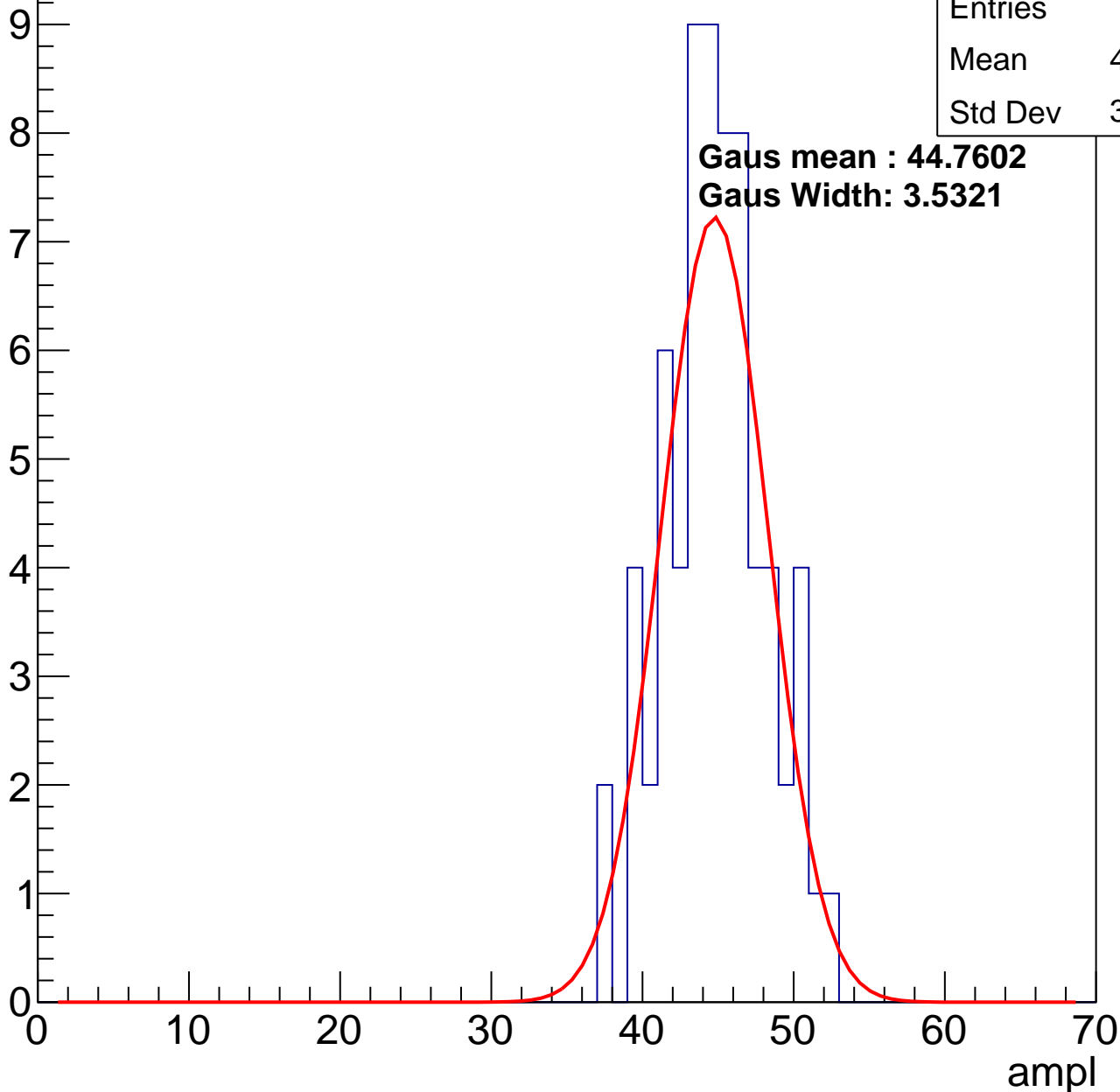
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.35
Std Dev	3.316

Gaus mean : 44.7602

Gaus Width: 3.5321

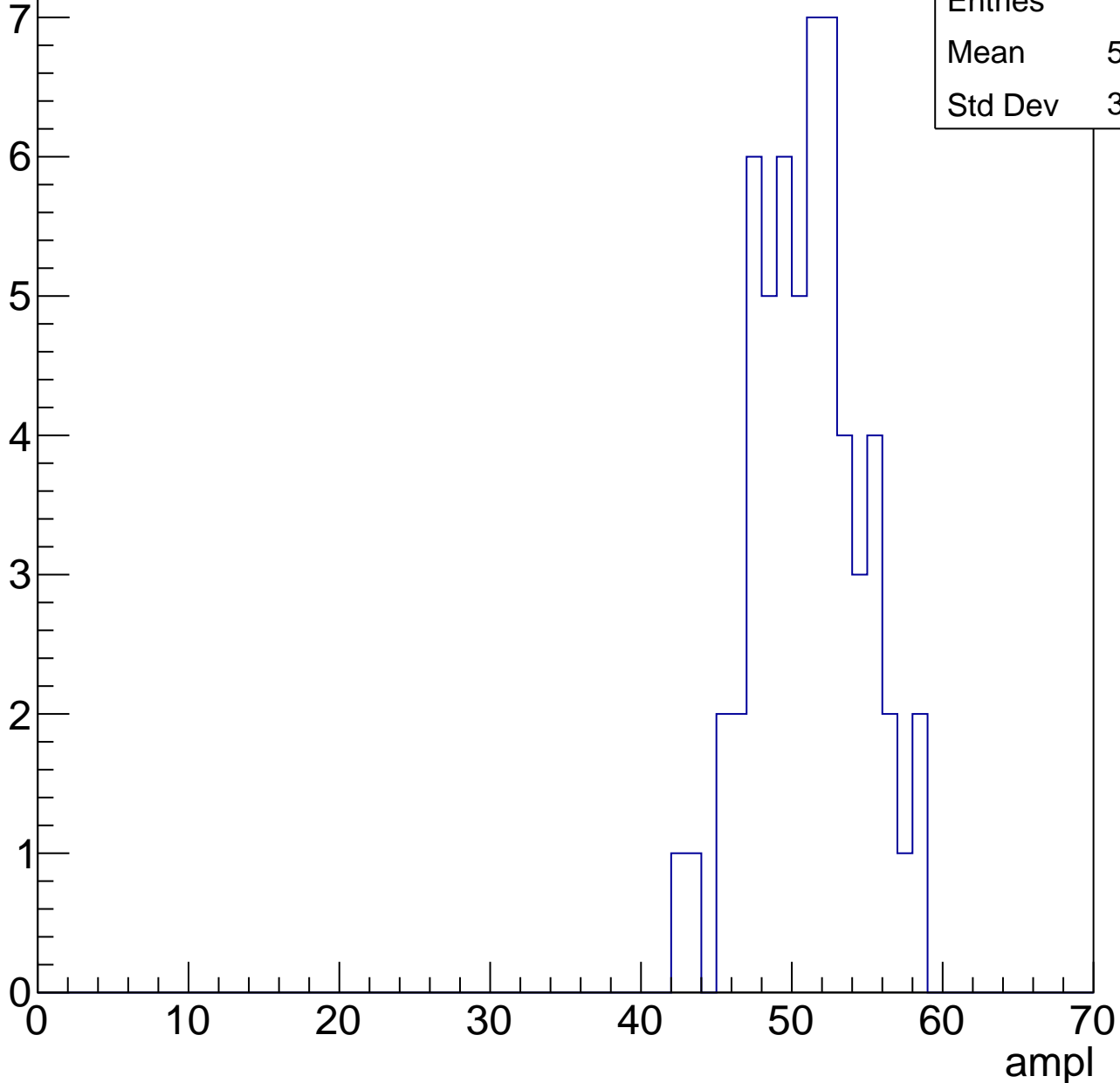


B1L100S, U25-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	50.57
Std Dev	3.534

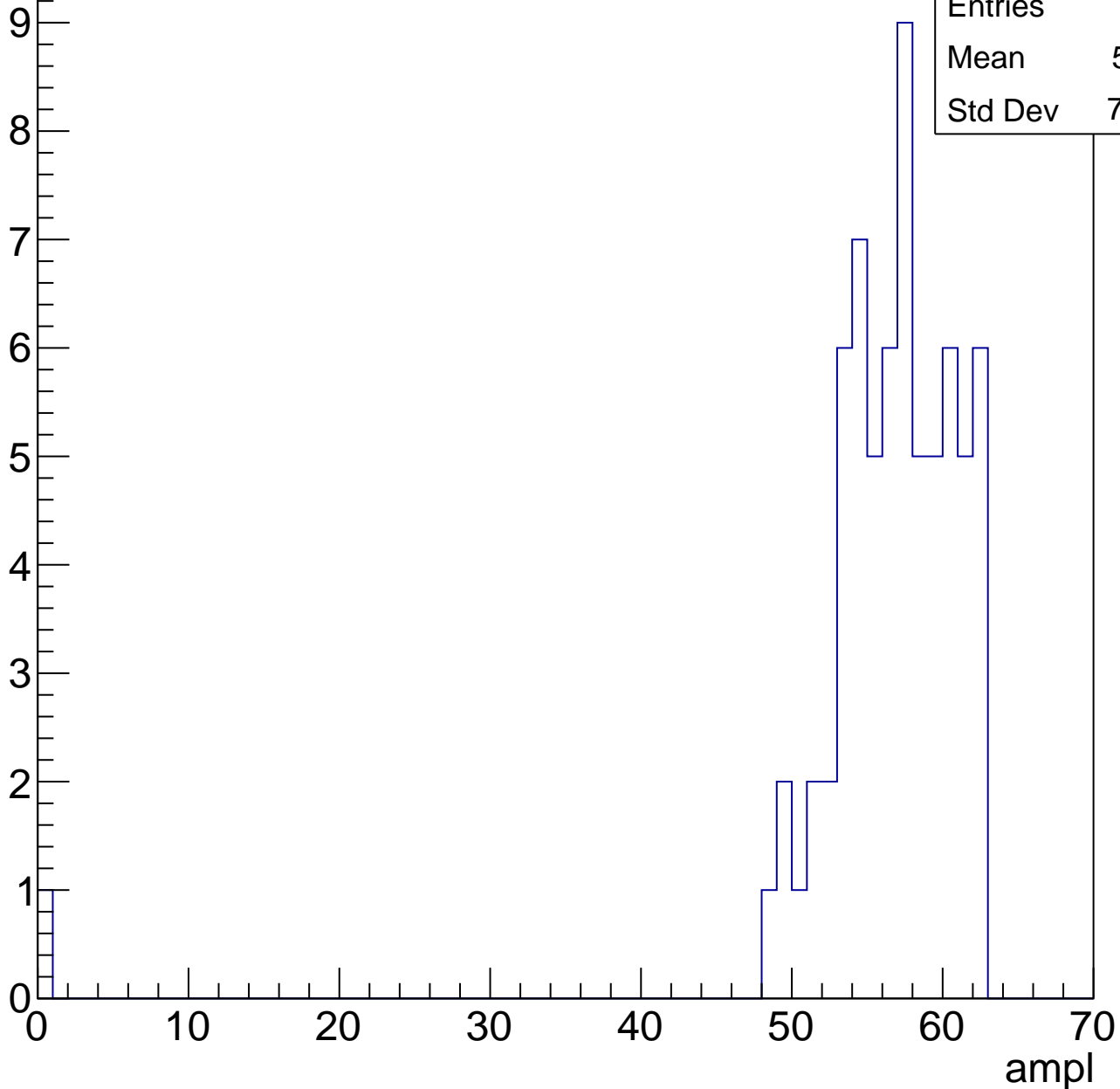


B1L100S, U25-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	55.71
Std Dev	7.622

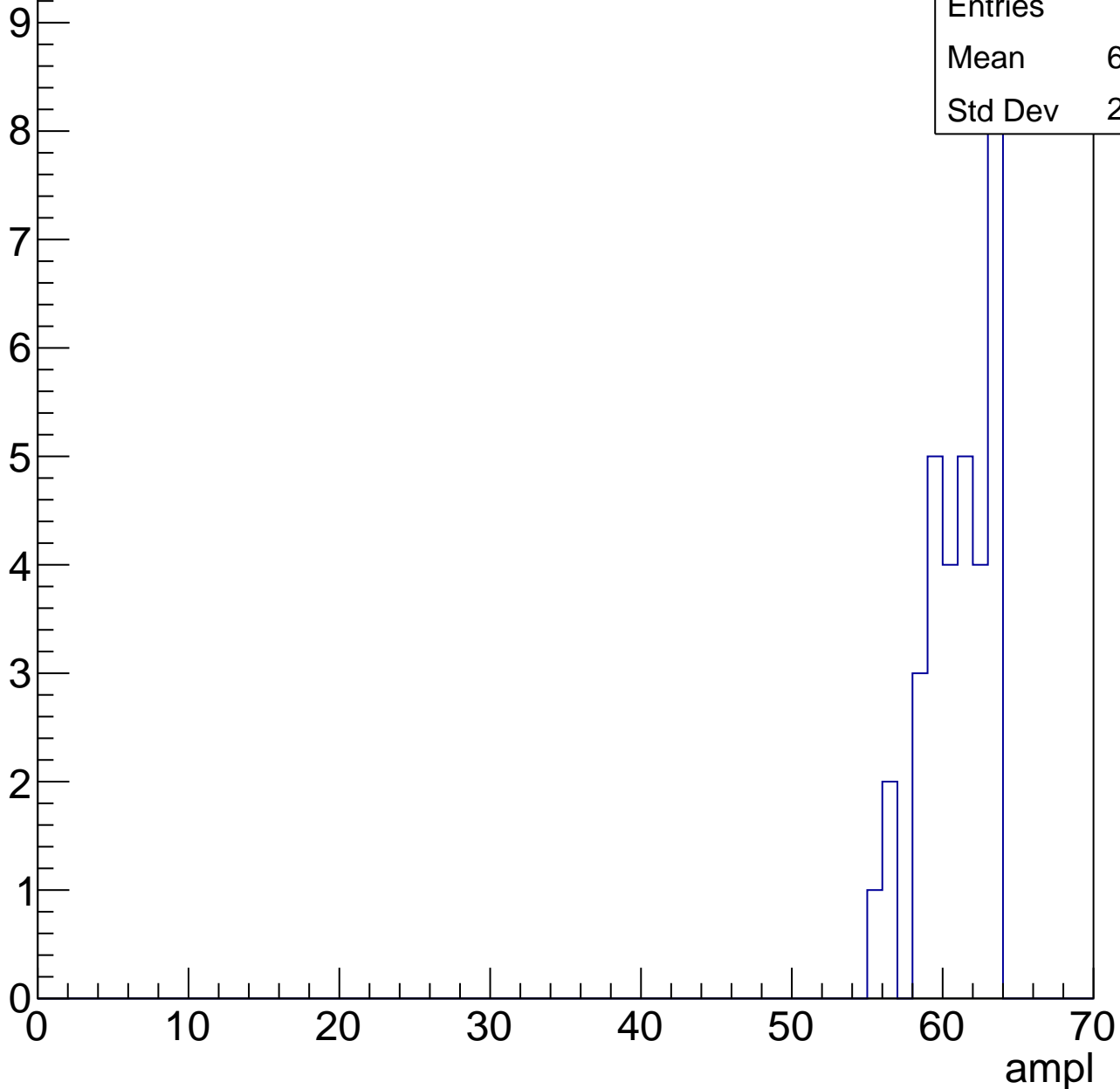


B1L100S, U25-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	60.48
Std Dev	2.258



B1L100S, U25-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U25-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch120, adc0

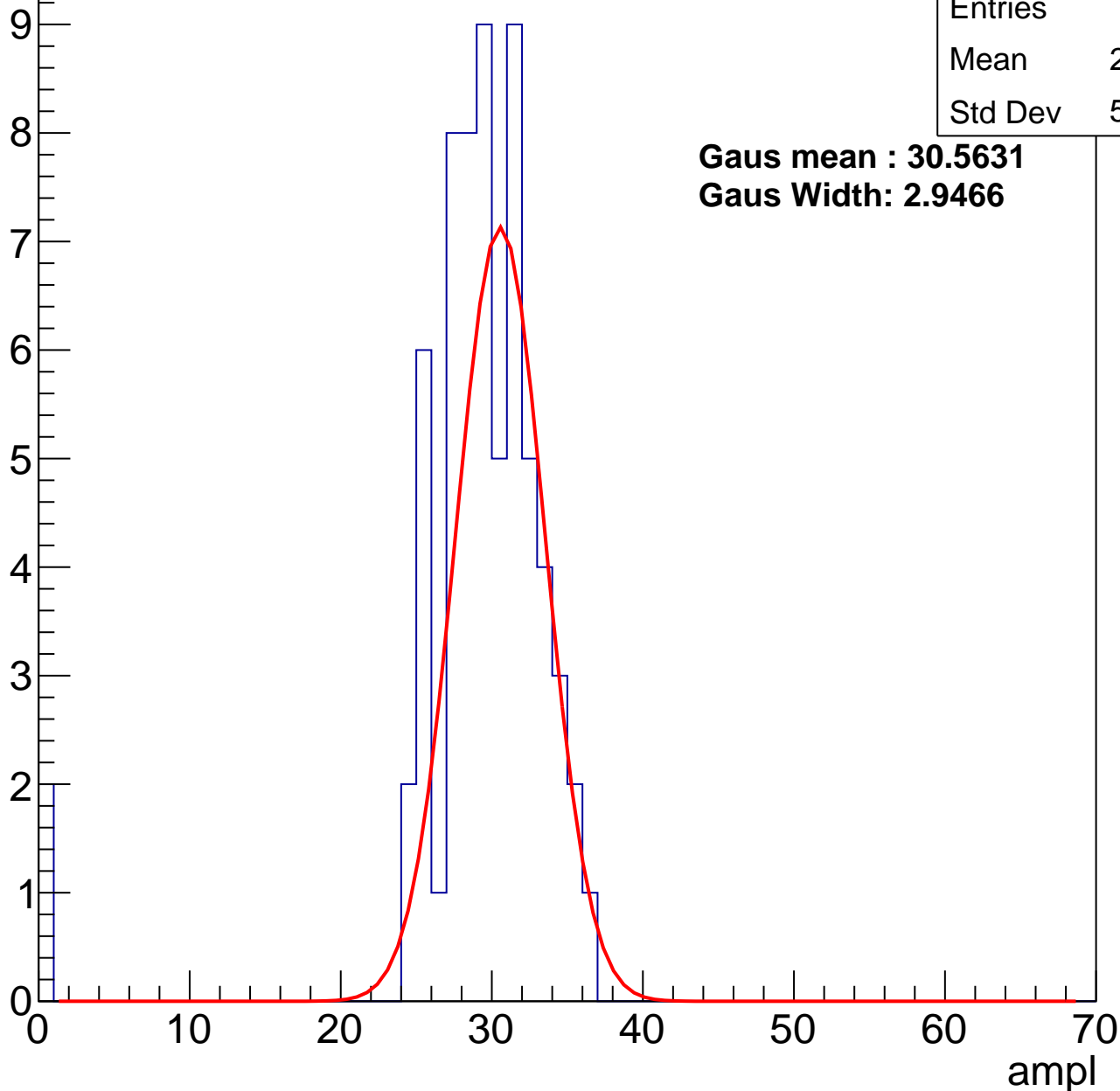
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.52
Std Dev	5.829

Gaus mean : 30.5631

Gaus Width: 2.9466



B1L100S, U25-ch120, adc1

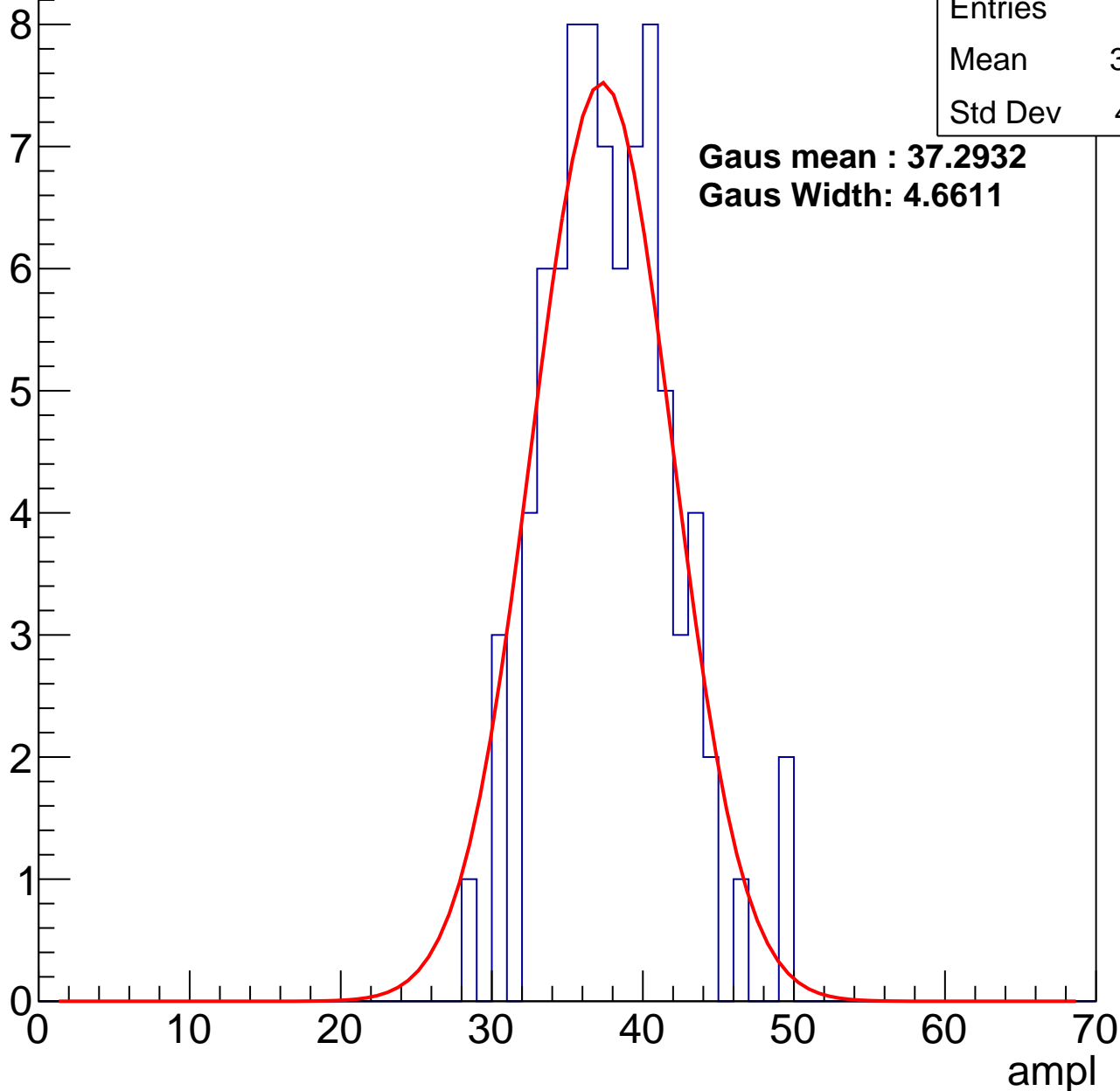
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	37.42
Std Dev	4.121

Gaus mean : 37.2932

Gaus Width: 4.6611



B1L100S, U25-ch120, adc2

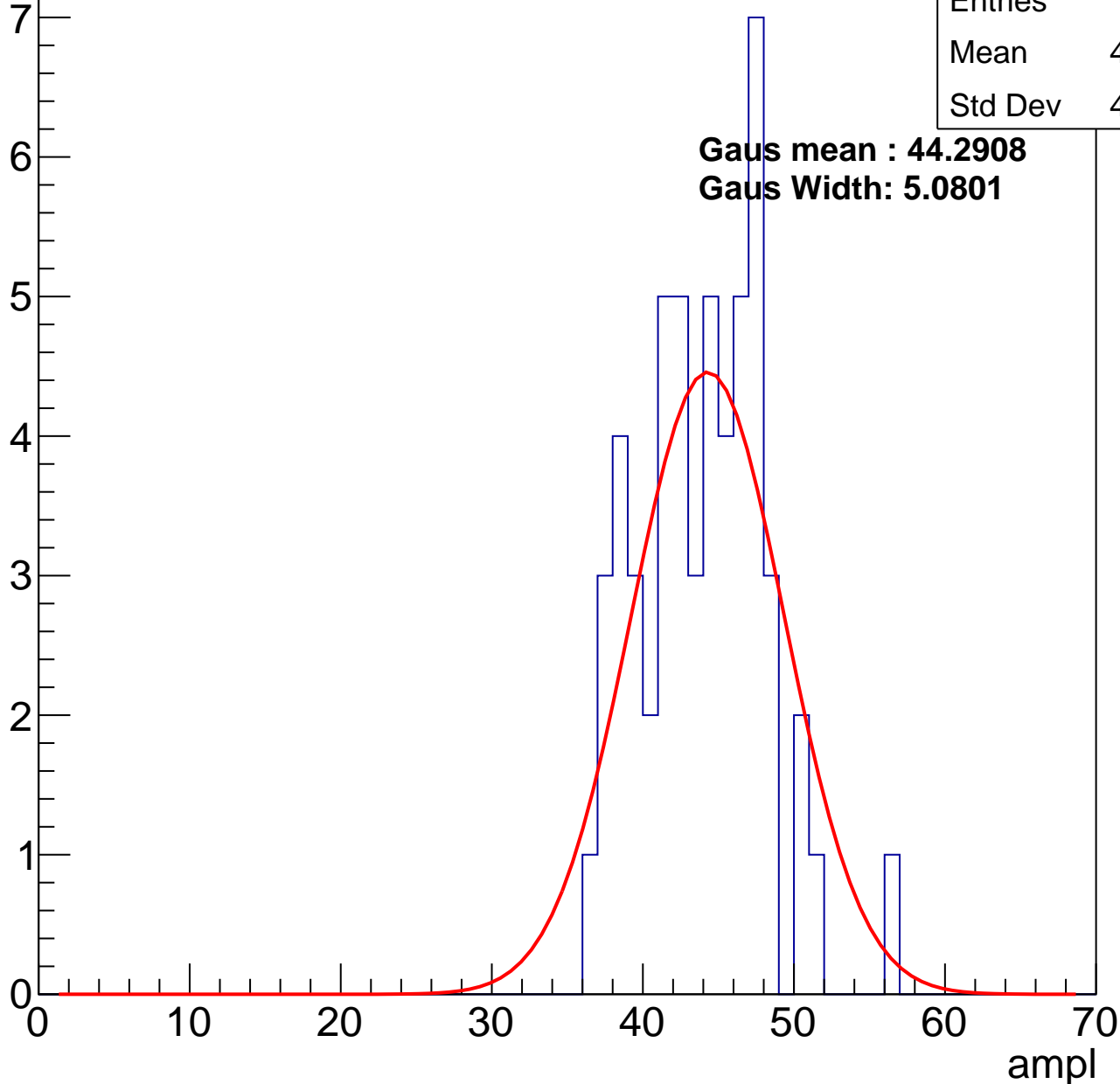
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.52
Std Dev	4.122

Gaus mean : 44.2908

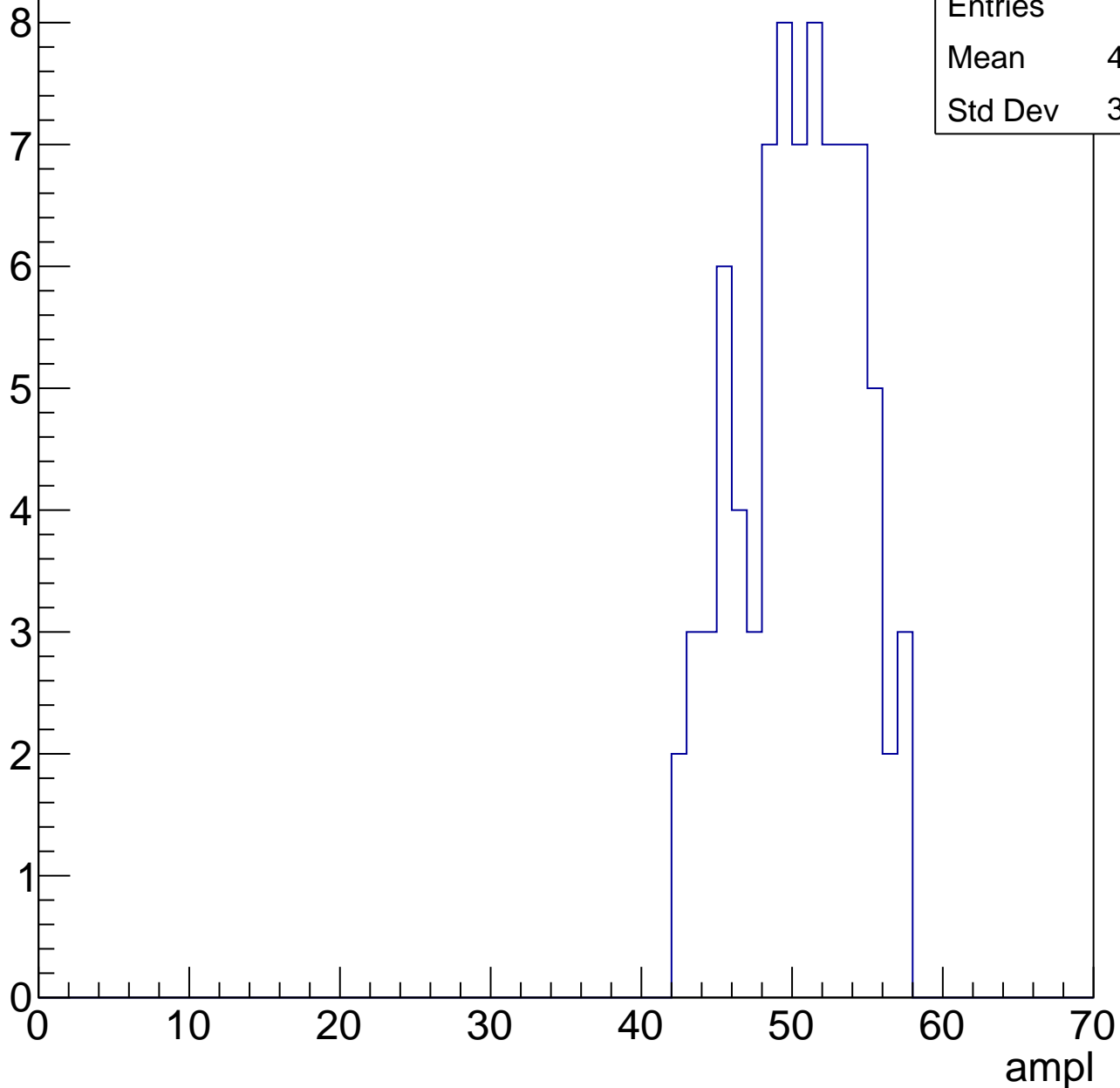
Gaus Width: 5.0801



B1L100S, U25-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

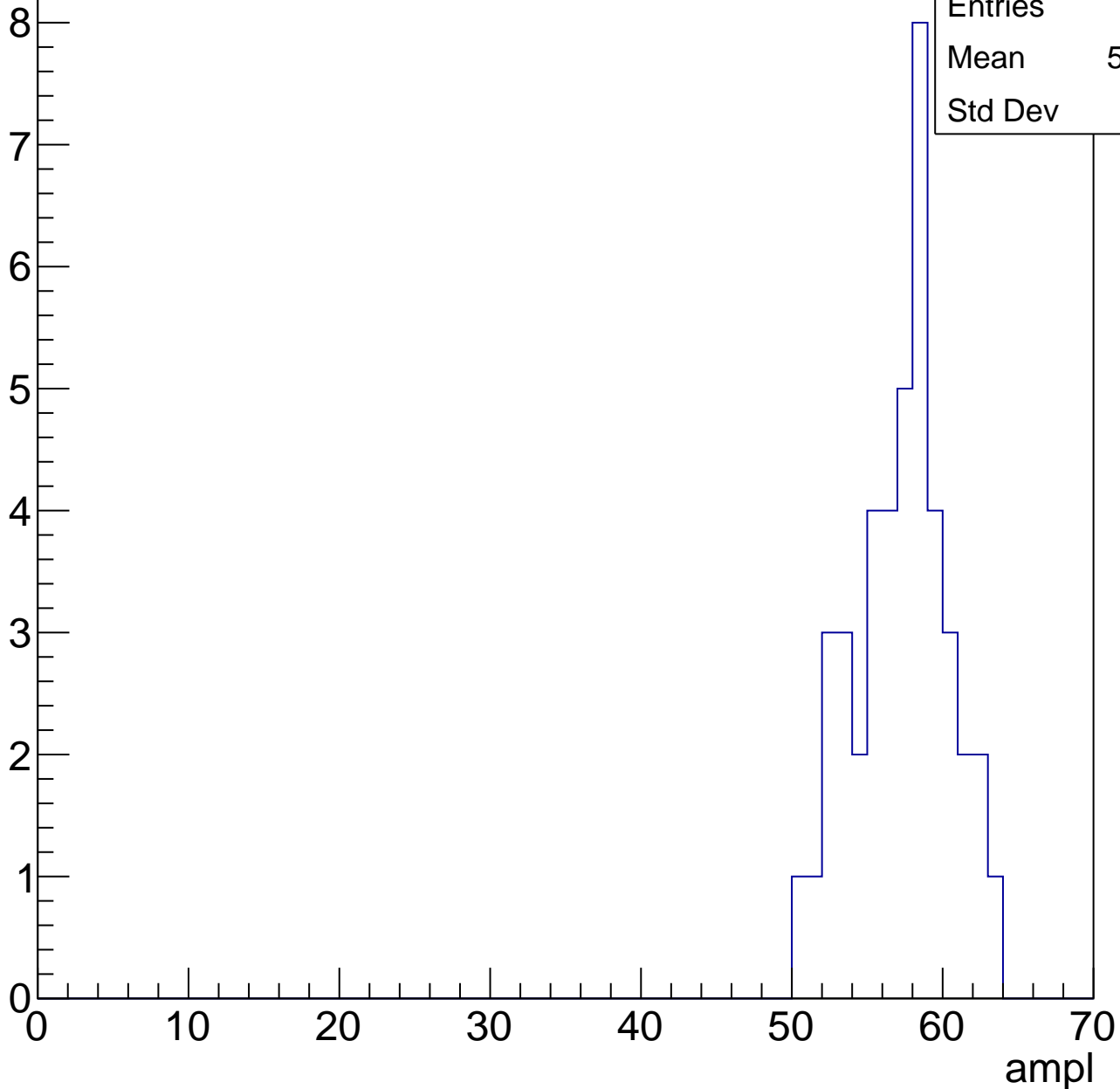


B1L100S, U25-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	56.79
Std Dev	3.1

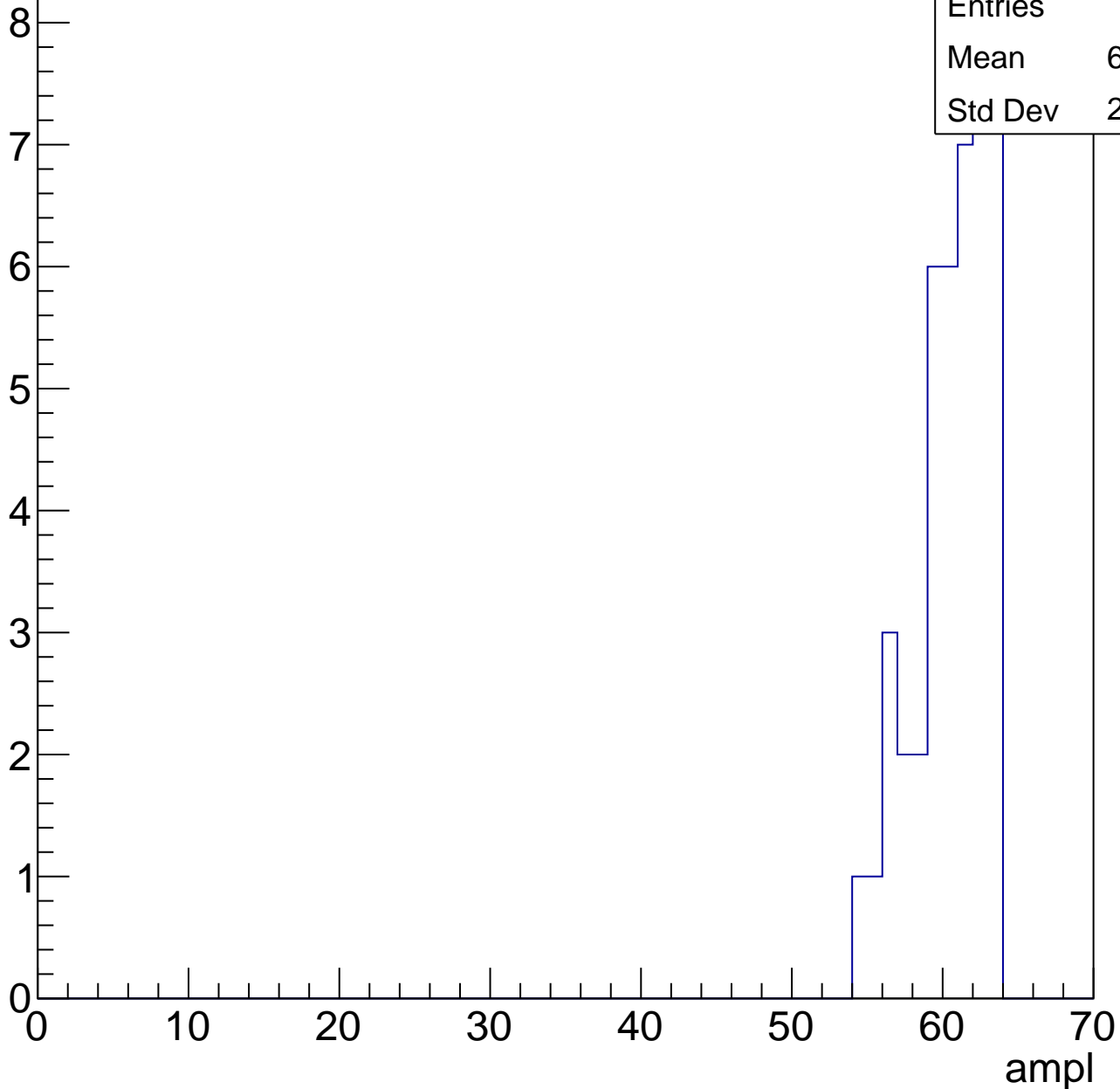


B1L100S, U25-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

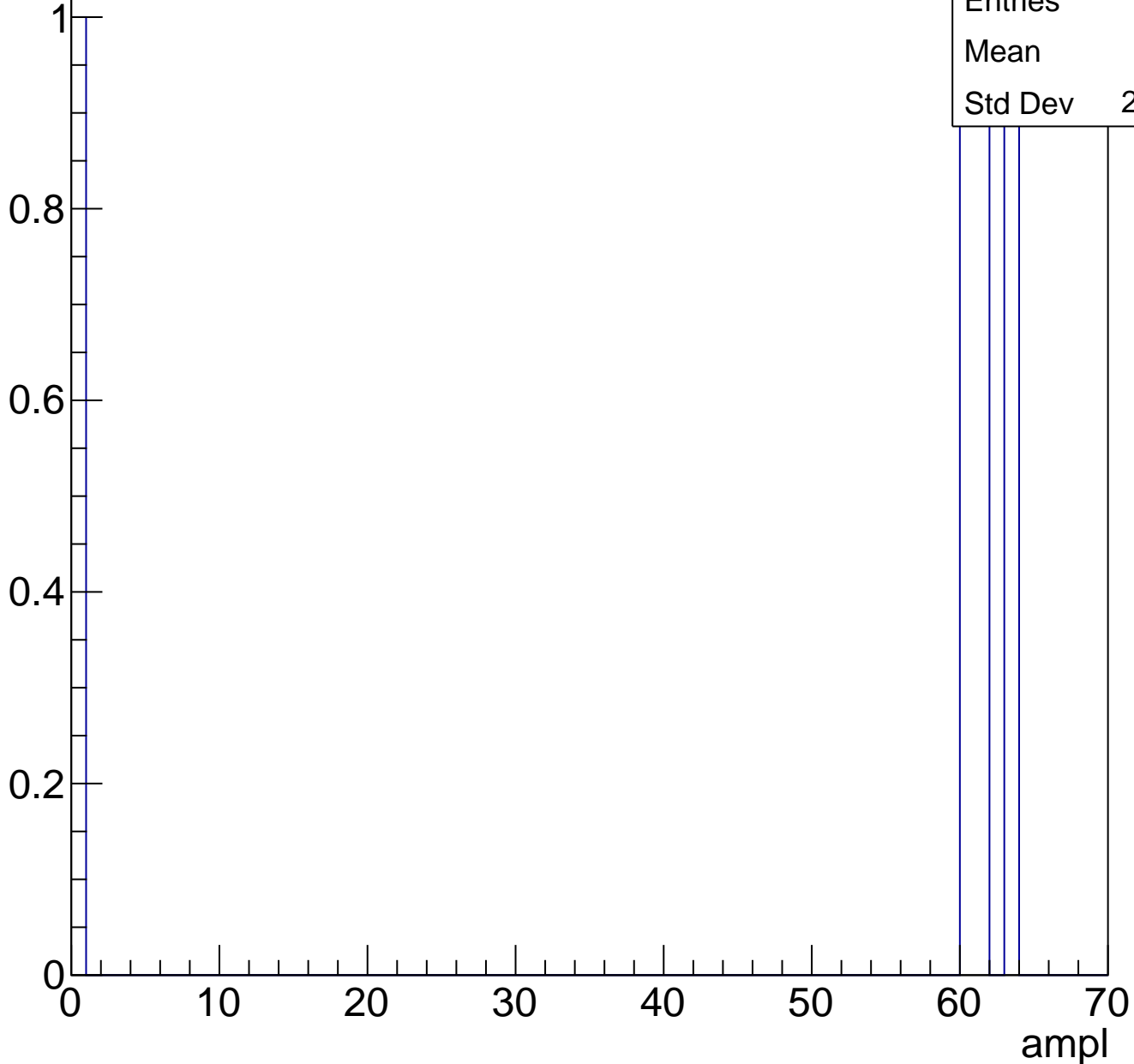
Entries	44
Mean	60.18
Std Dev	2.386



B1L100S, U25-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch121, adc0

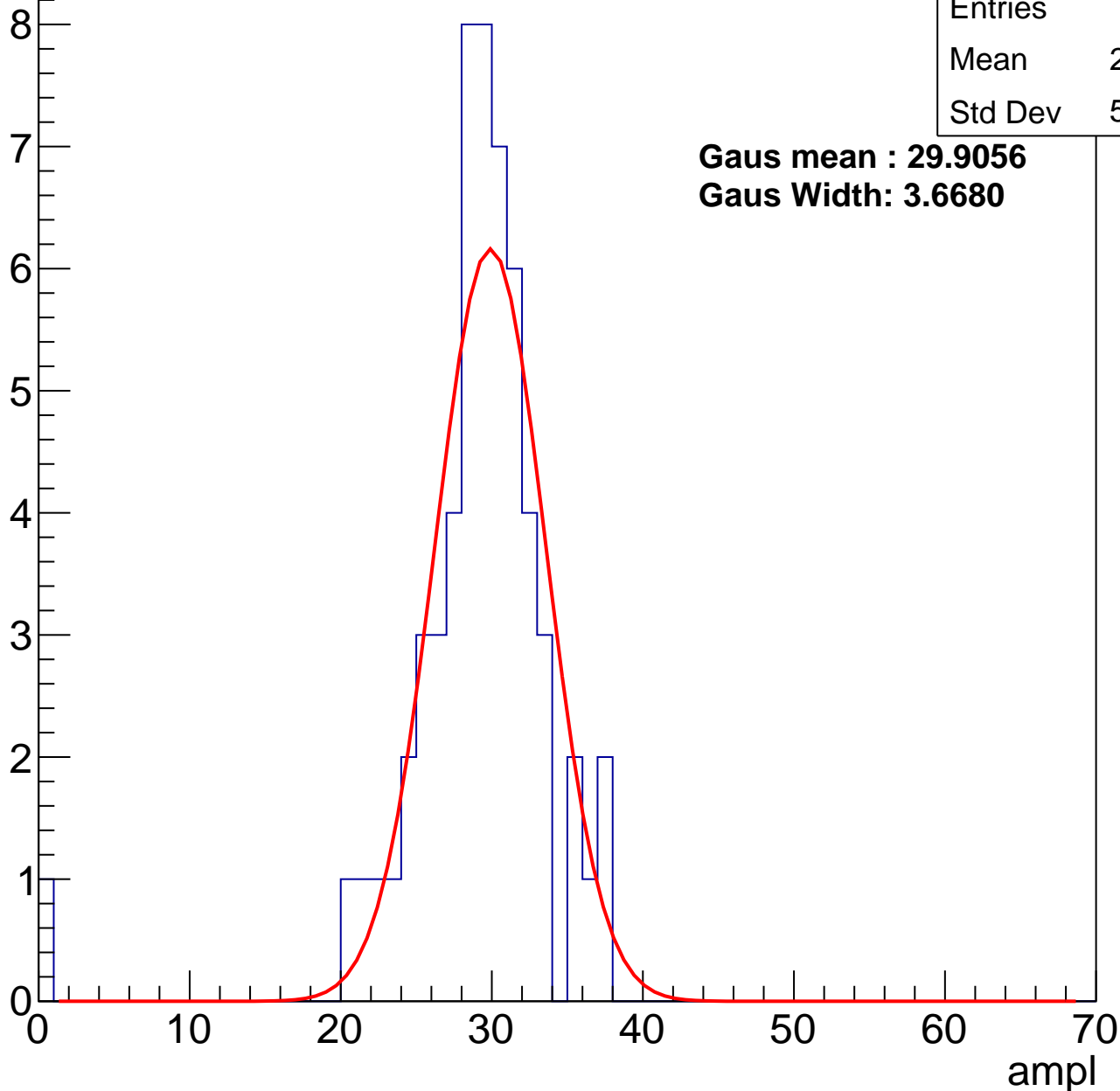
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.52
Std Dev	5.204

Gaus mean : 29.9056

Gaus Width: 3.6680



B1L100S, U25-ch121, adc1

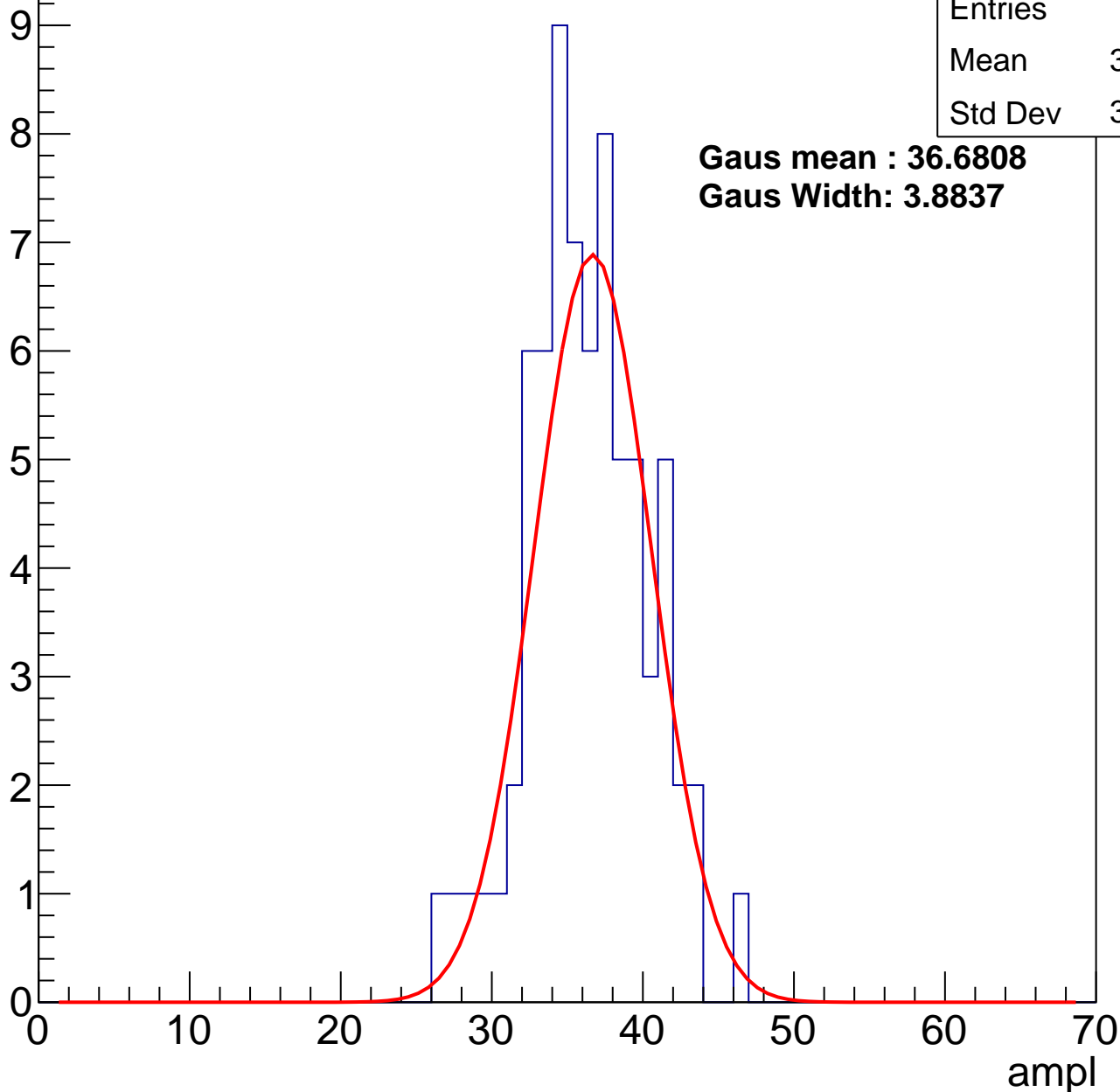
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	35.85
Std Dev	3.886

Gaus mean : 36.6808

Gaus Width: 3.8837



B1L100S, U25-ch121, adc2

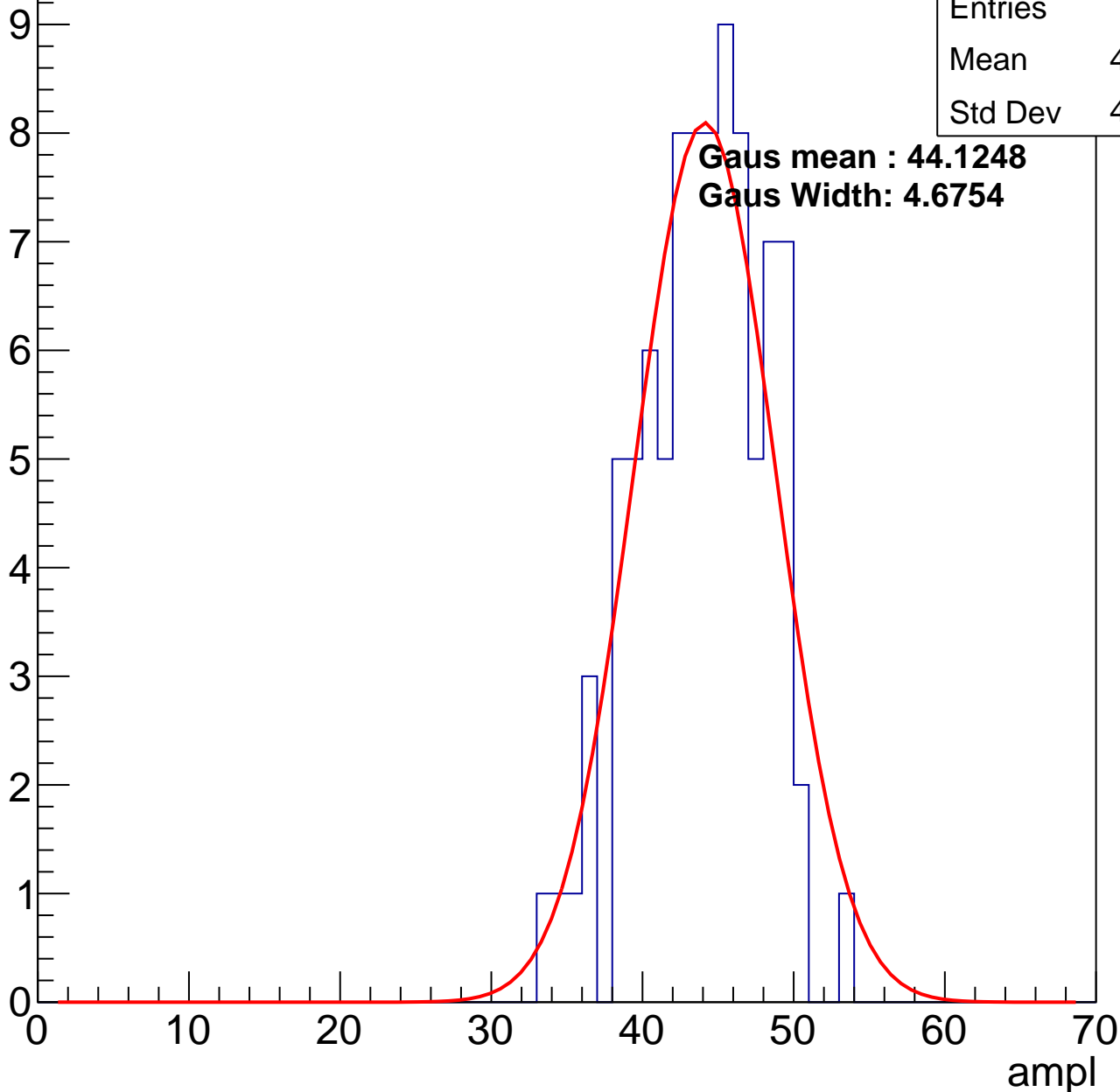
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	43.47
Std Dev	4.056

Gaus mean : 44.1248

Gaus Width: 4.6754

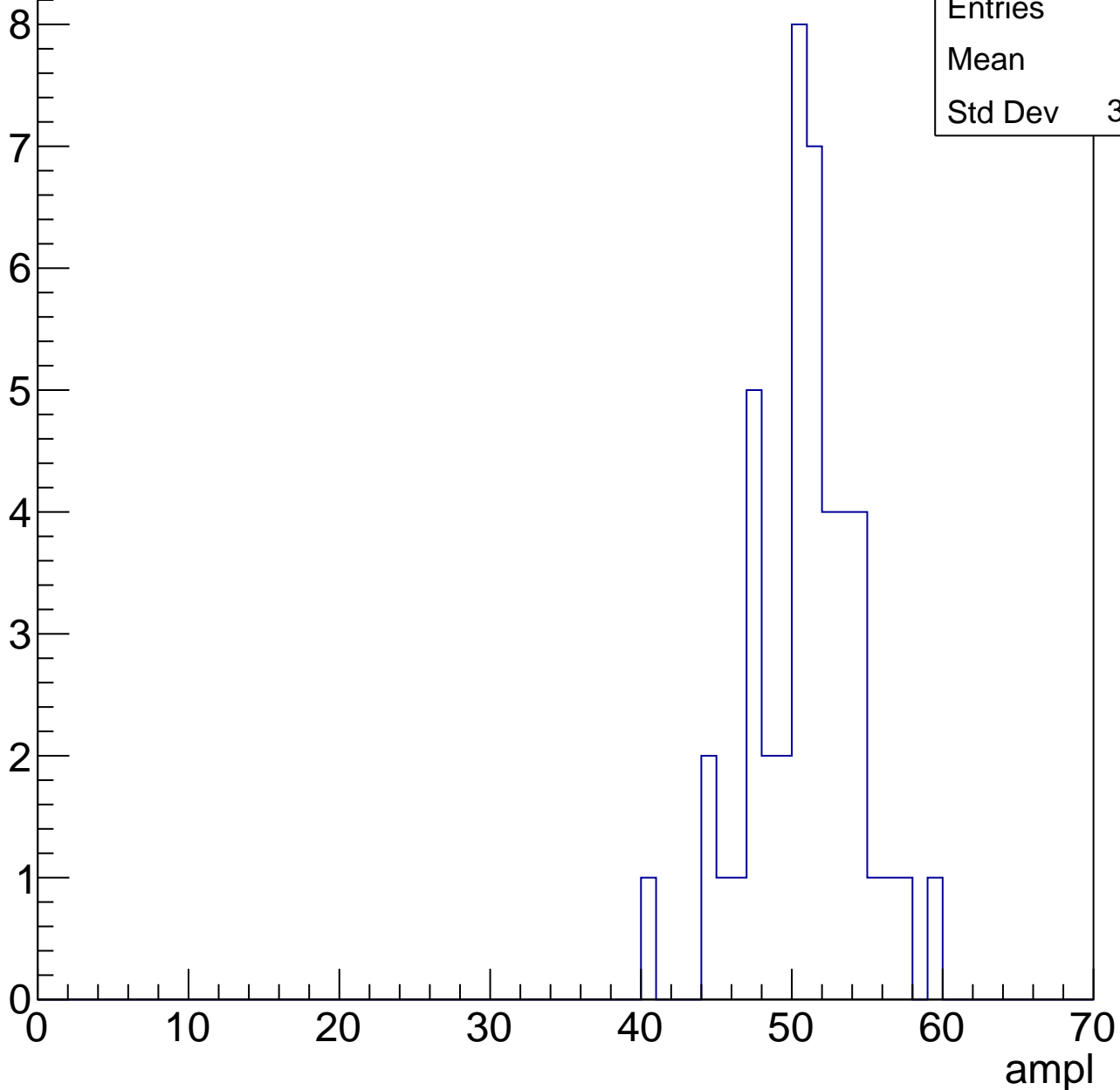


B1L100S, U25-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	50.4
Std Dev	3.574

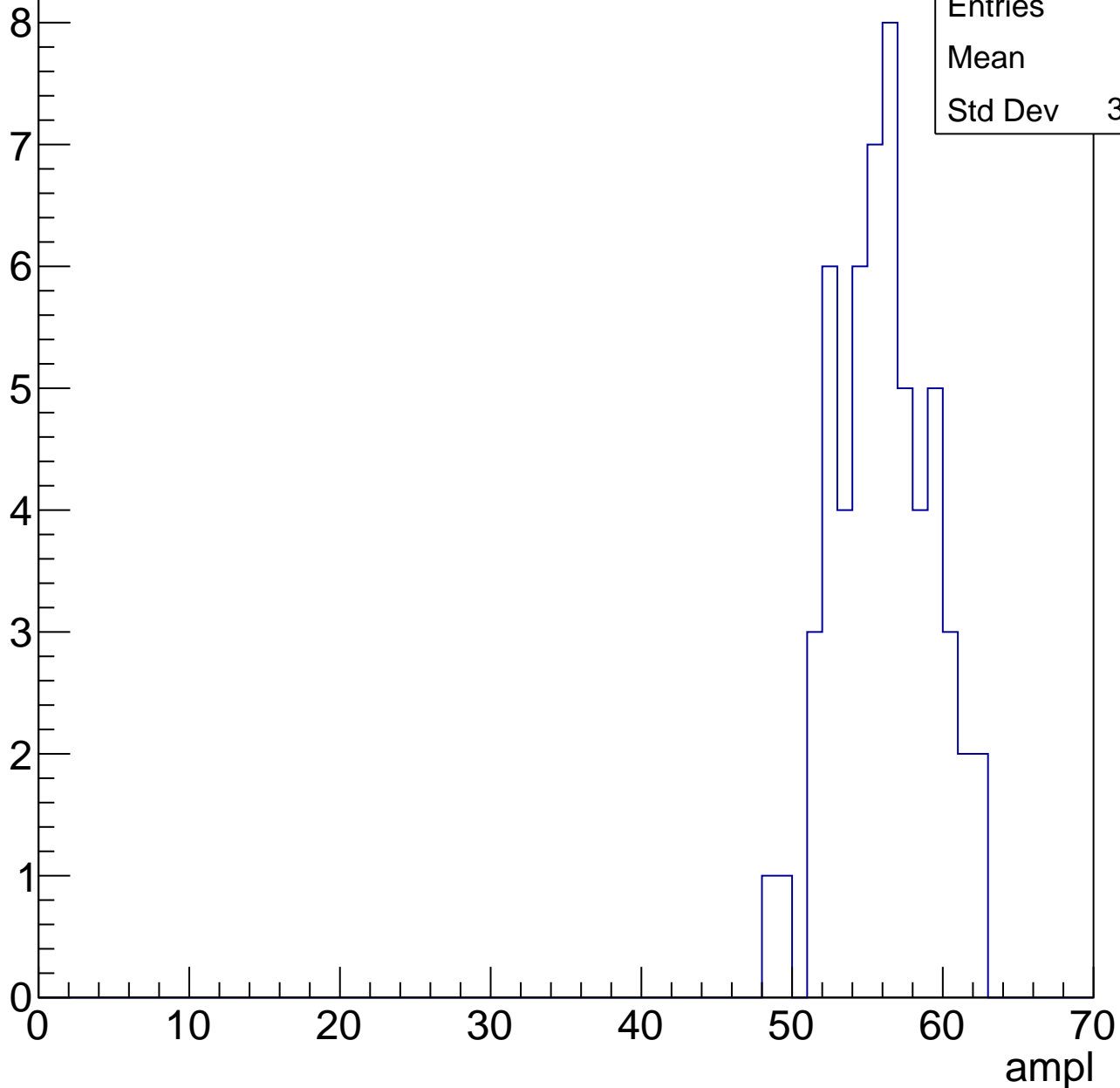


B1L100S, U25-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.6
Std Dev	3.184

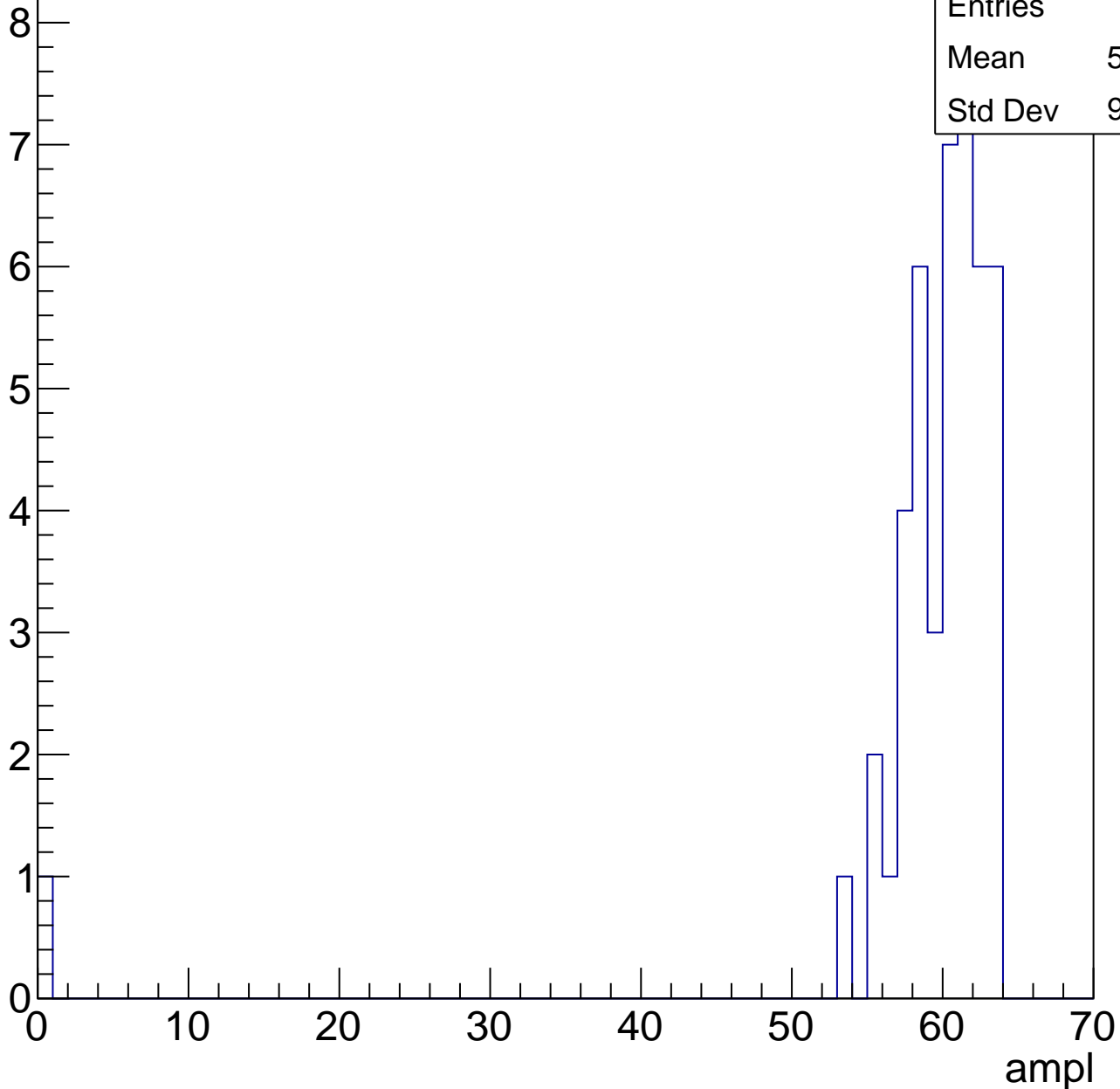


B1L100S, U25-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

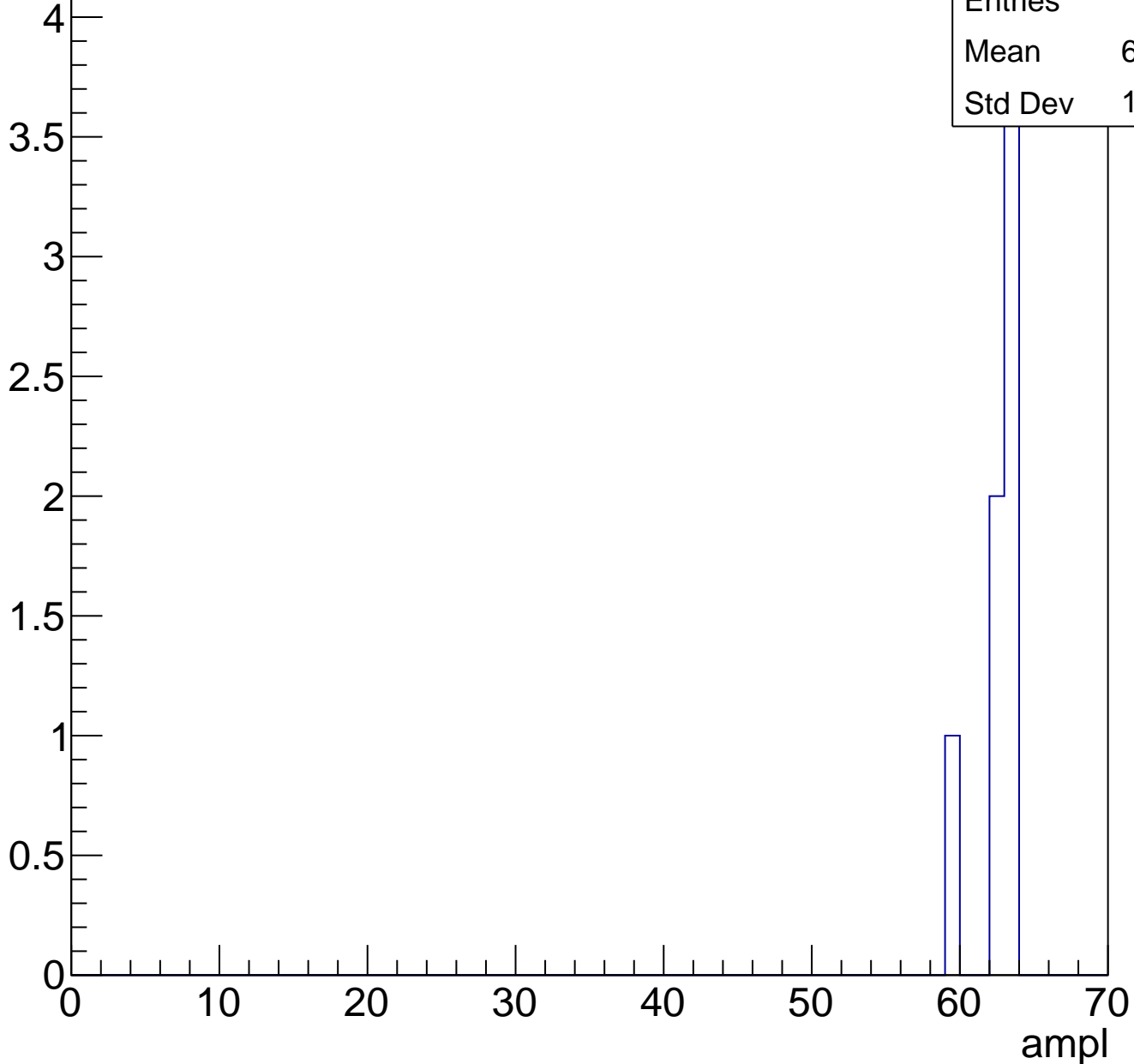
Entries	45
Mean	58.44
Std Dev	9.135



B1L100S, U25-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch122, adc0

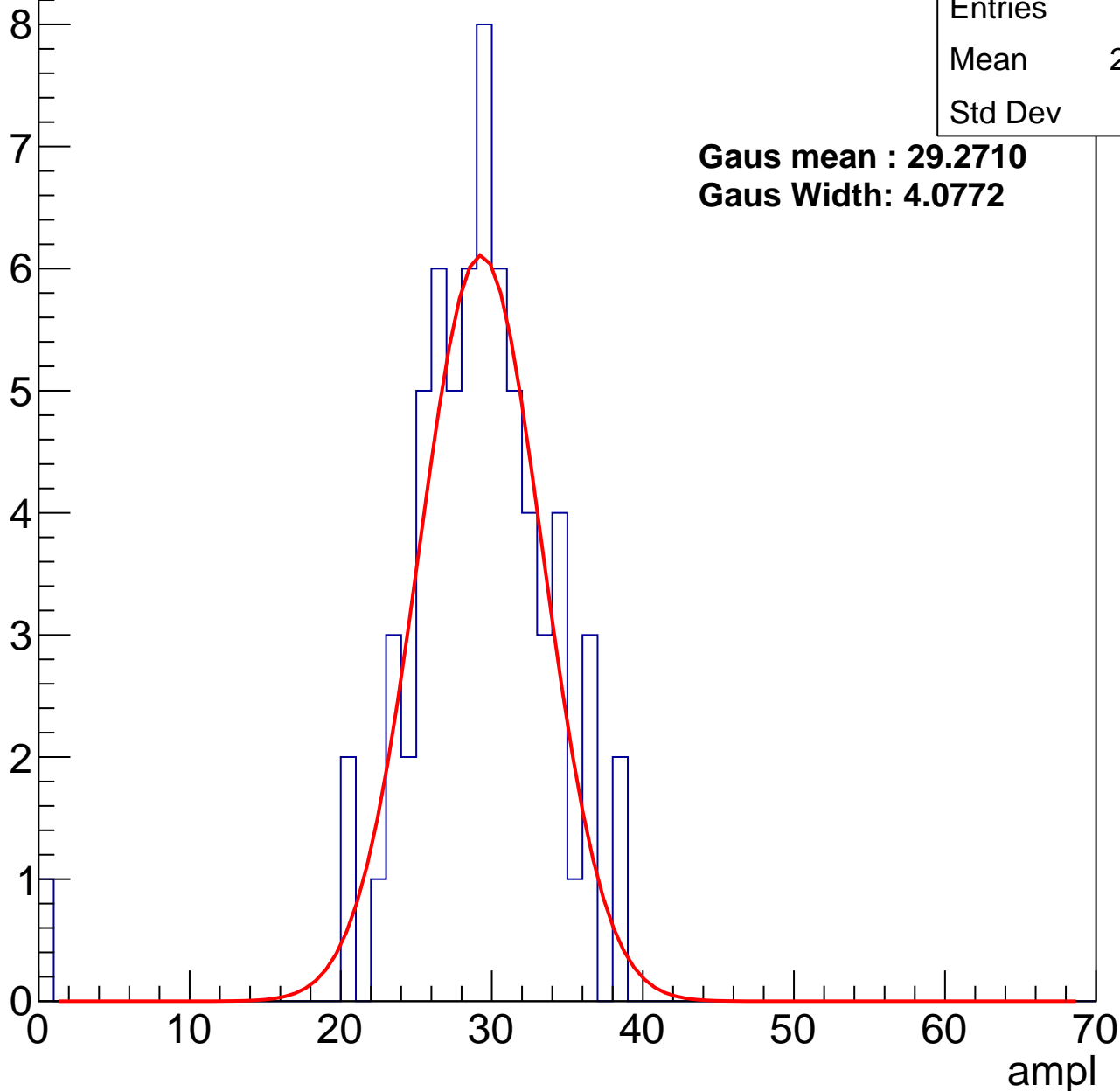
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.54
Std Dev	5.34

Gaus mean : 29.2710

Gaus Width: 4.0772



B1L100S, U25-ch122, adc1

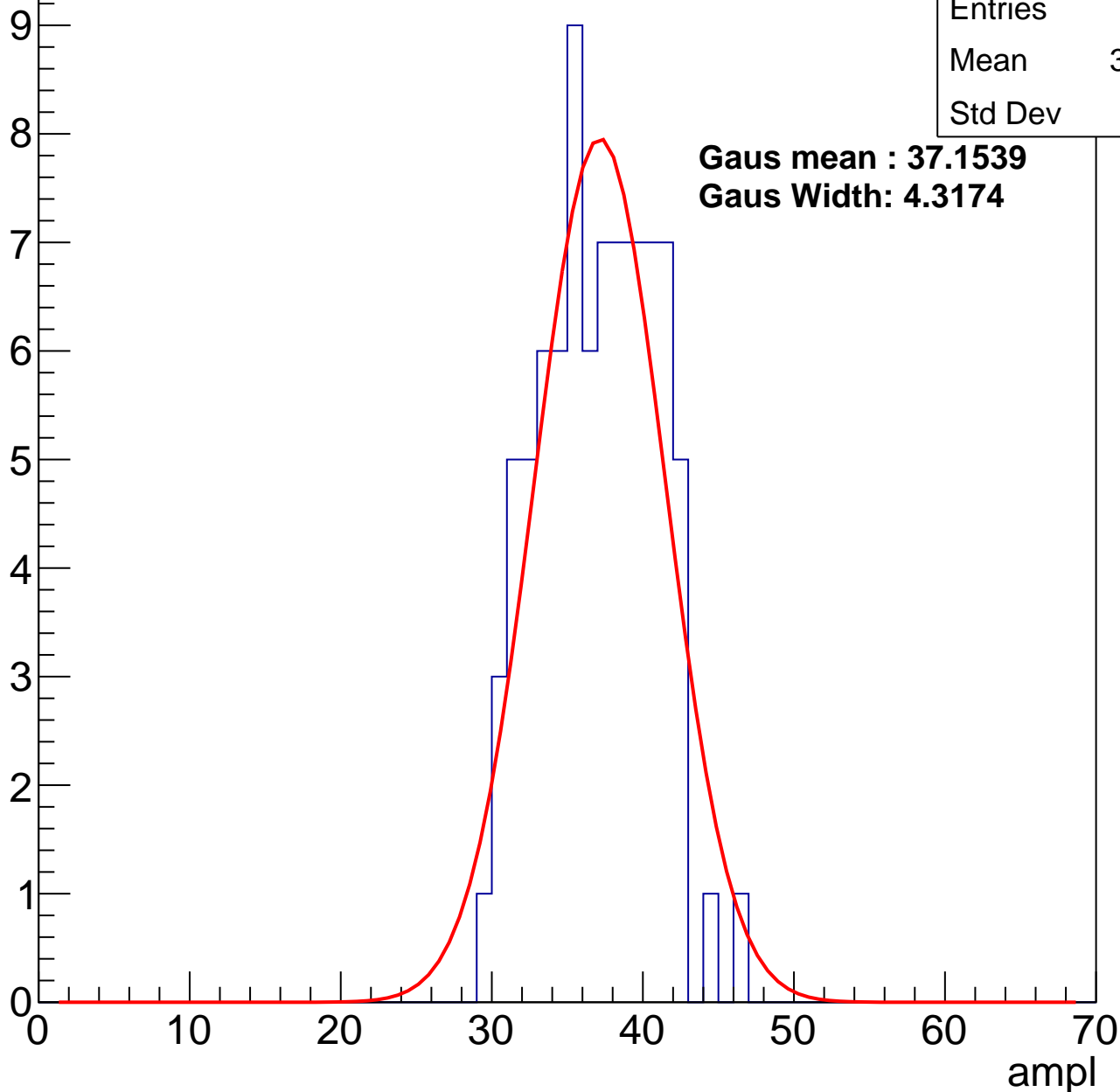
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	36.53
Std Dev	3.73

Gaus mean : 37.1539

Gaus Width: 4.3174



B1L100S, U25-ch122, adc2

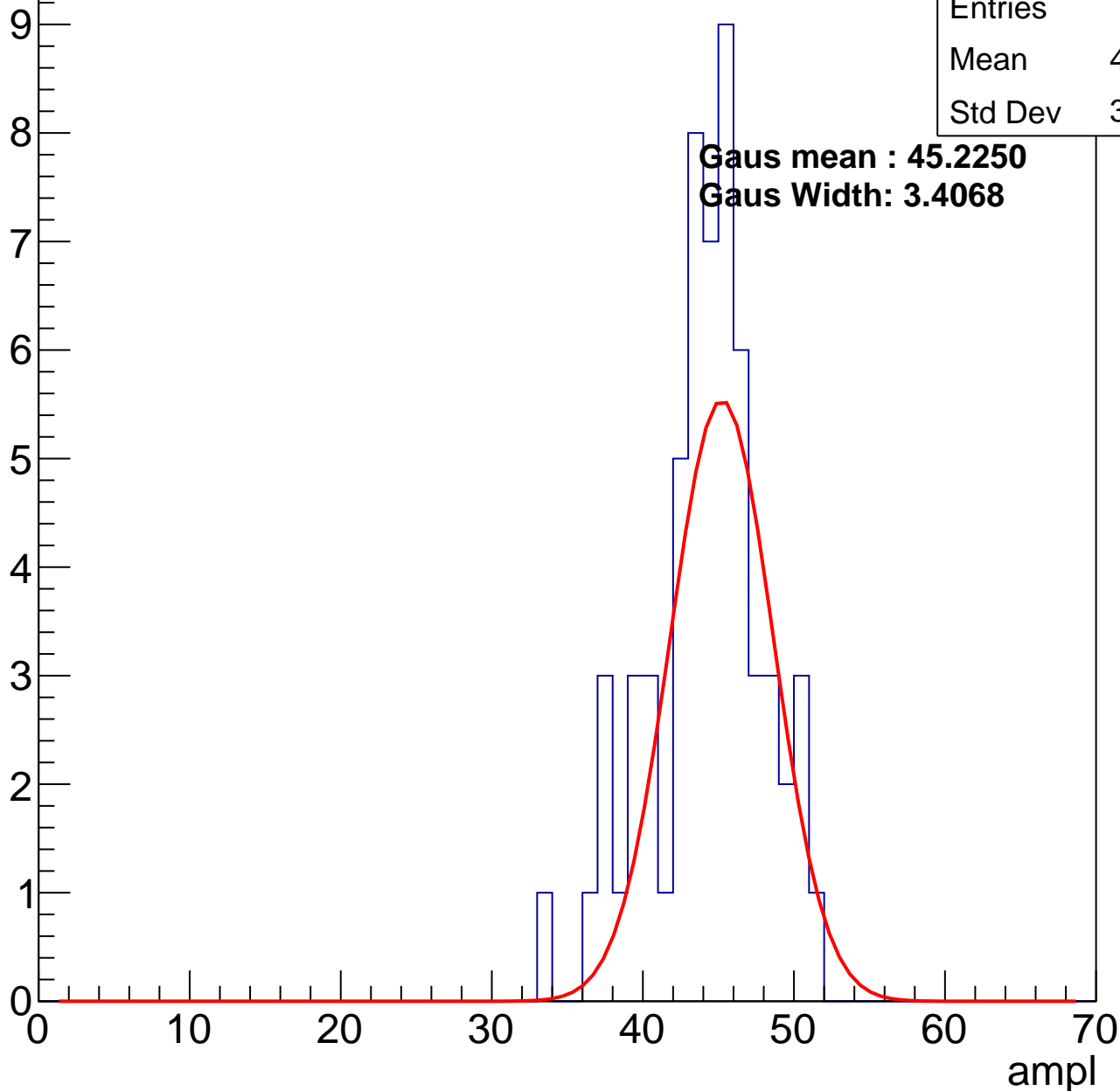
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.72
Std Dev	3.738

Gaus mean : 45.2250

Gaus Width: 3.4068

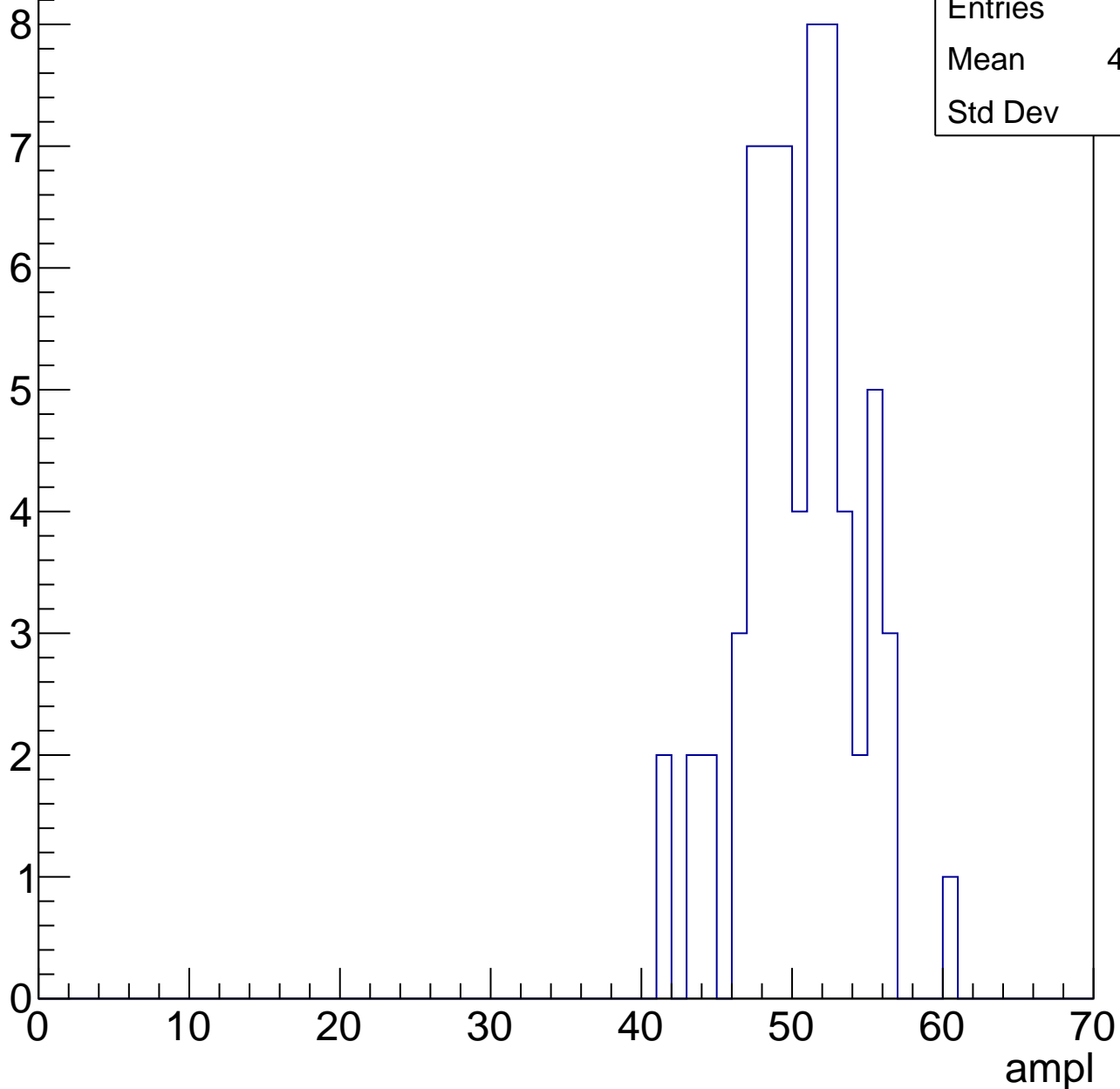


B1L100S, U25-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.98
Std Dev	3.76

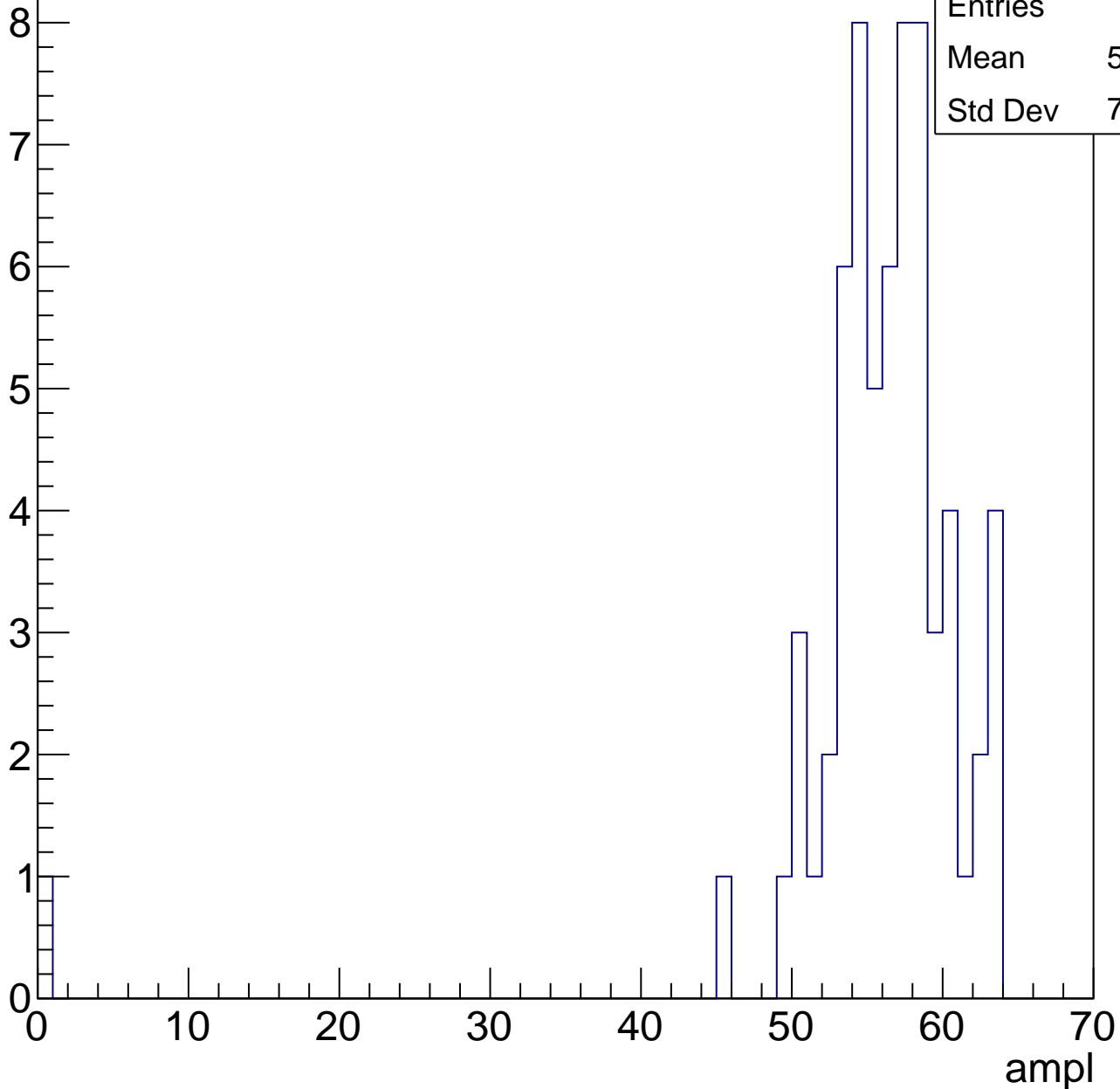


B1L100S, U25-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.22
Std Dev	7.853



B1L100S, U25-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.36
Std Dev	1.797

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8

B1L100S, U25-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

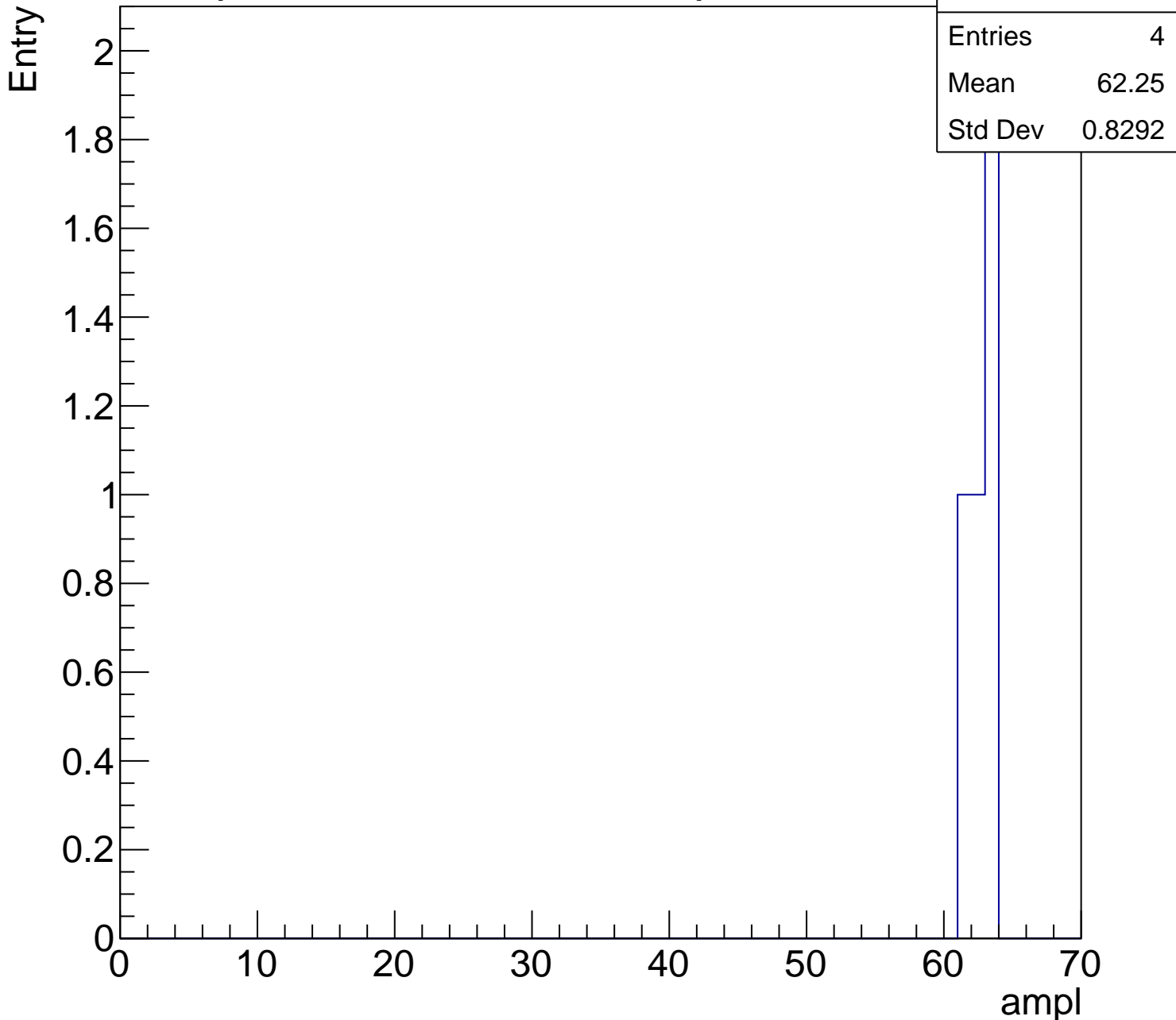
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B1L100S, U25-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch123, adc0

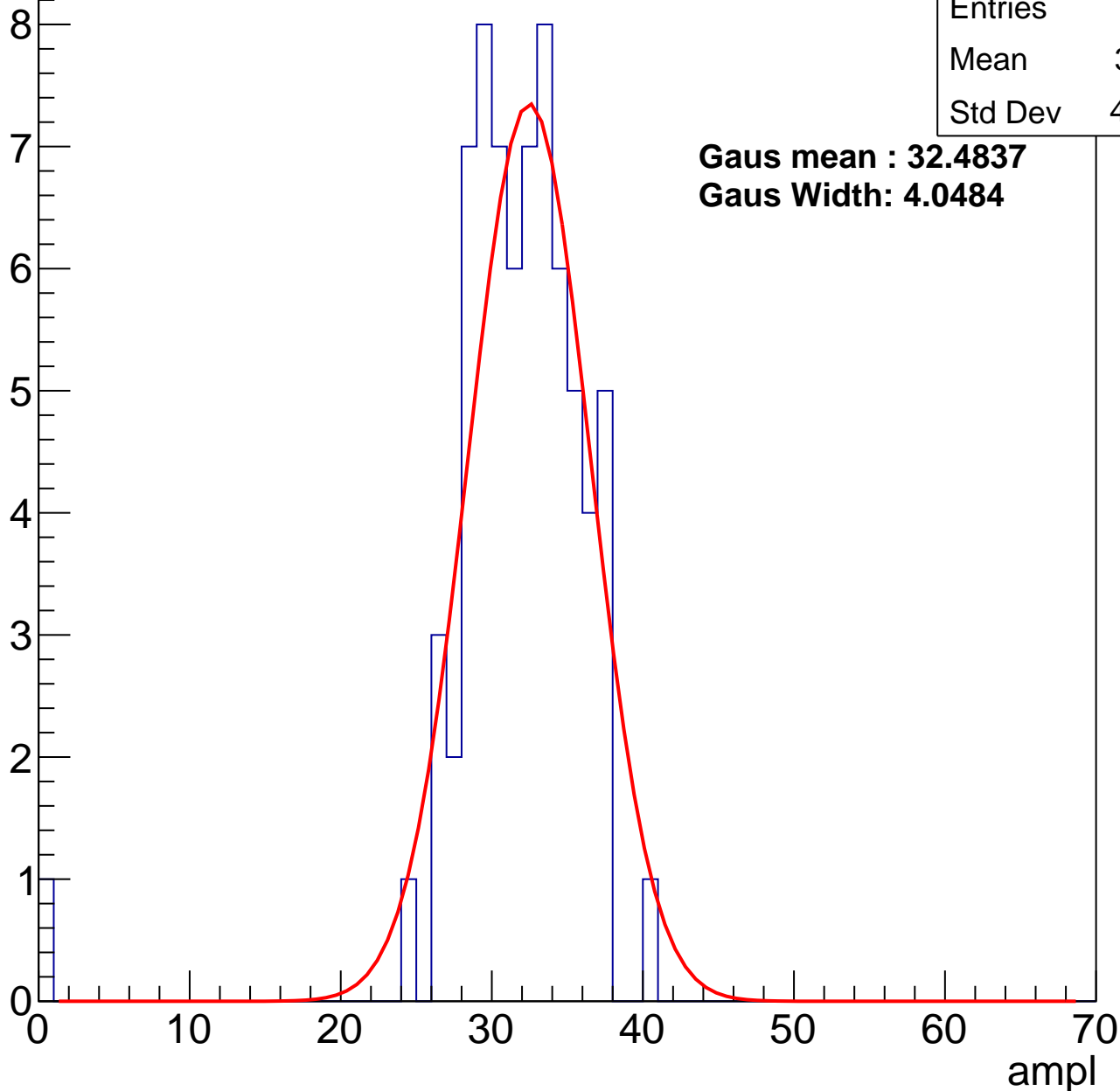
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	31.21
Std Dev	4.964

Gaus mean : 32.4837

Gaus Width: 4.0484



B1L100S, U25-ch123, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	38.55
Std Dev	3.445

Gaus mean : 39.2996

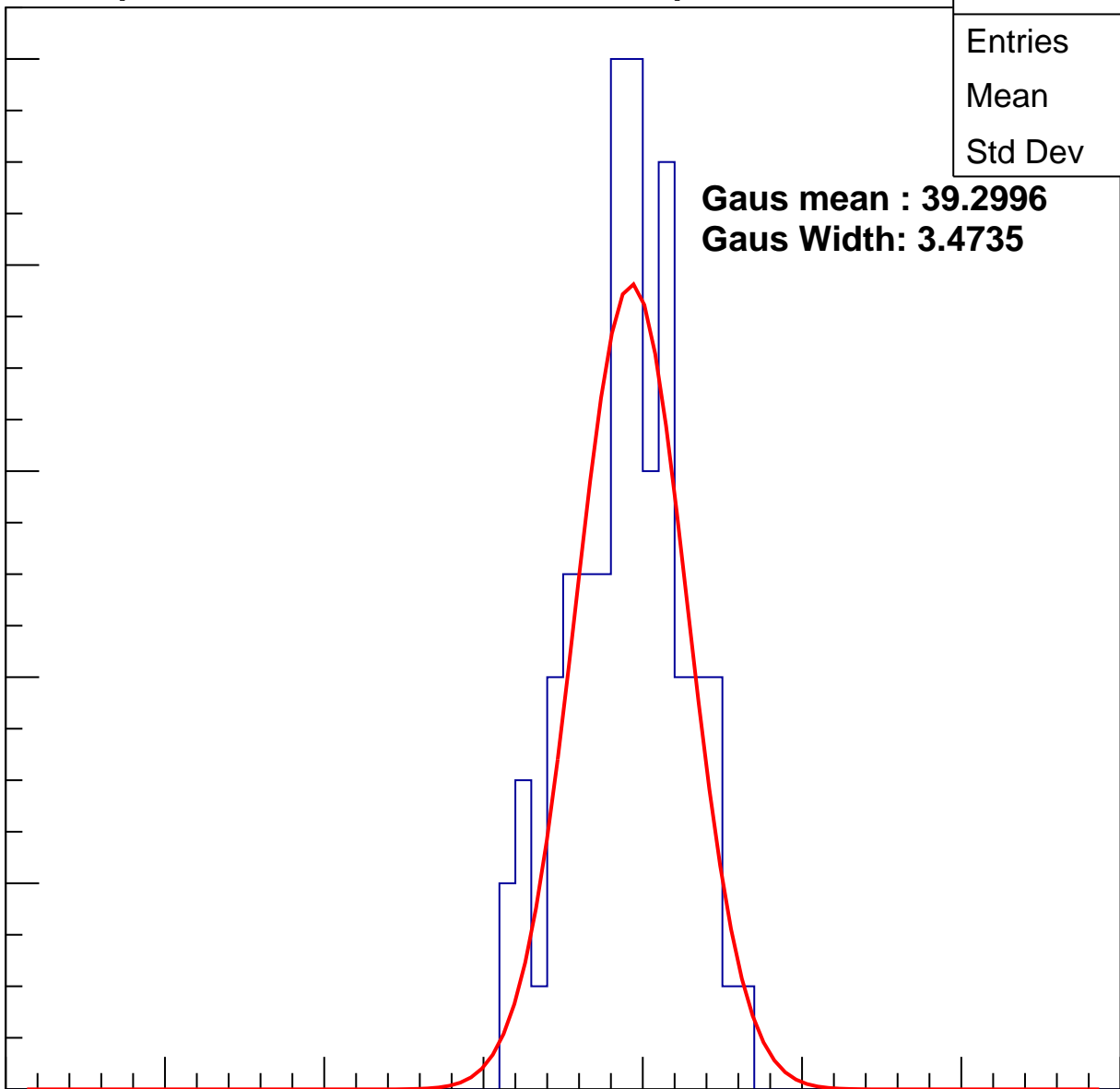
Gaus Width: 3.4735

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch123, adc2

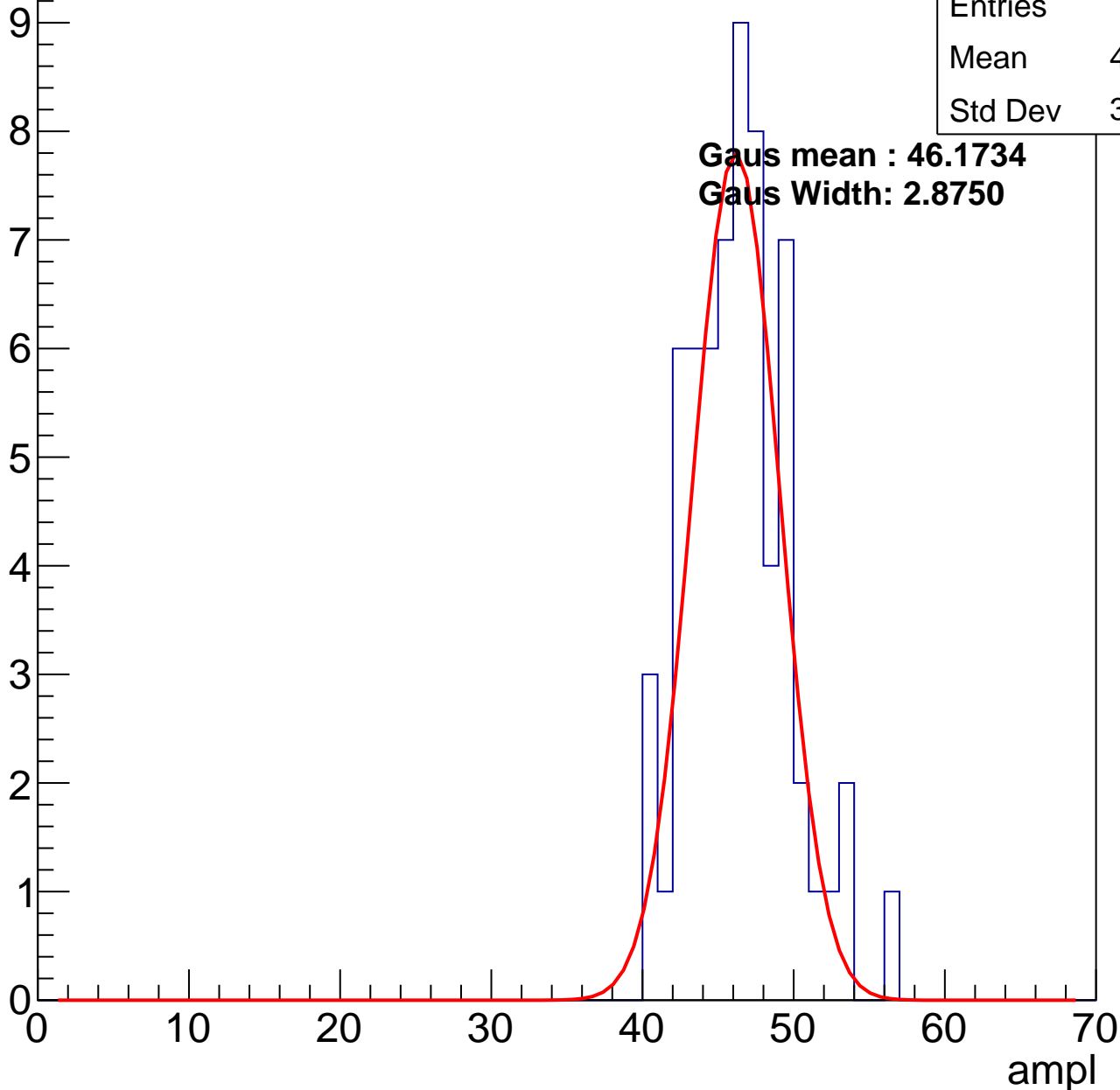
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	45.94
Std Dev	3.288

Gaus mean : 46.1734

Gaus Width: 2.8750

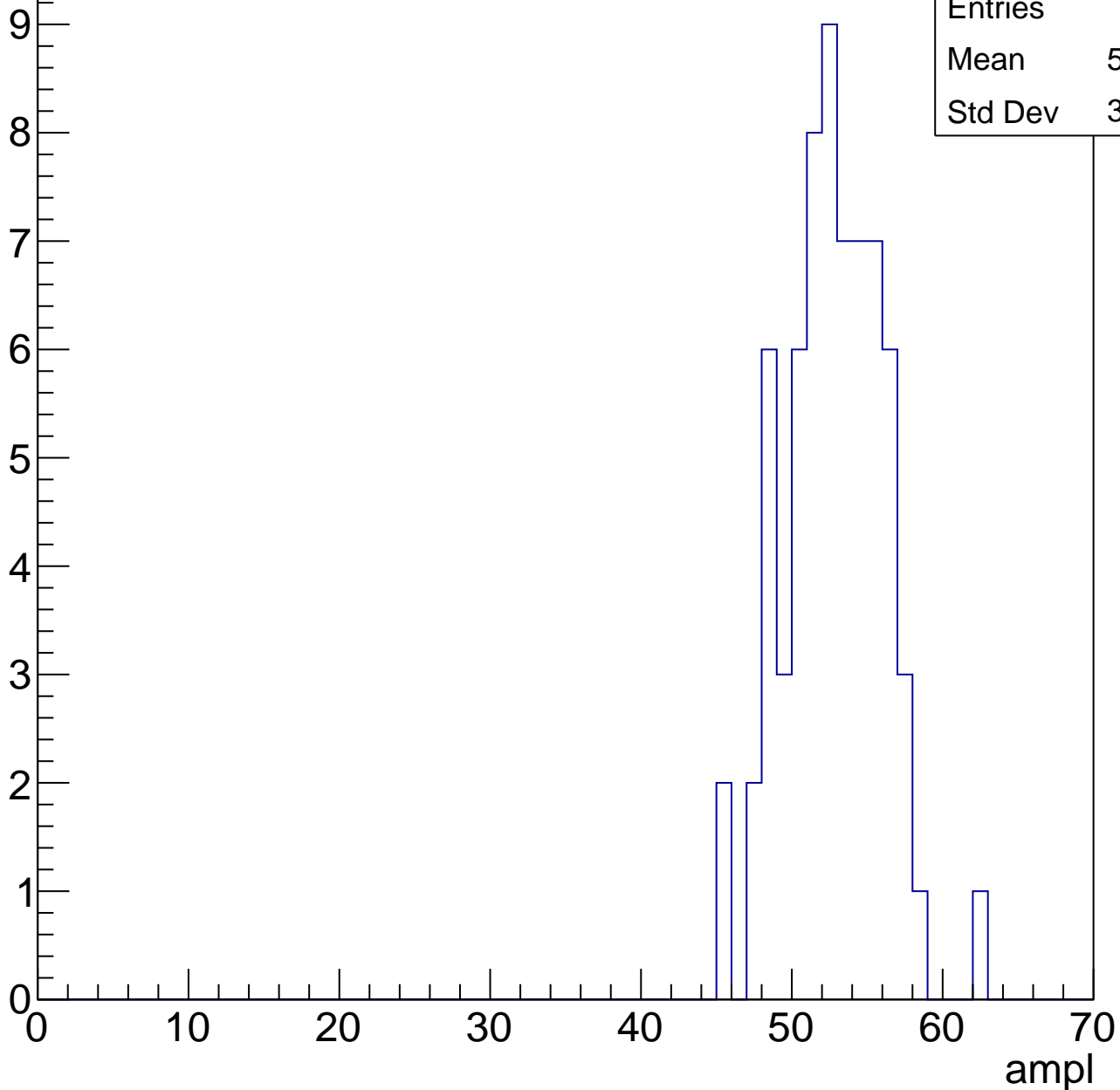


B1L100S, U25-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	52.29
Std Dev	3.213

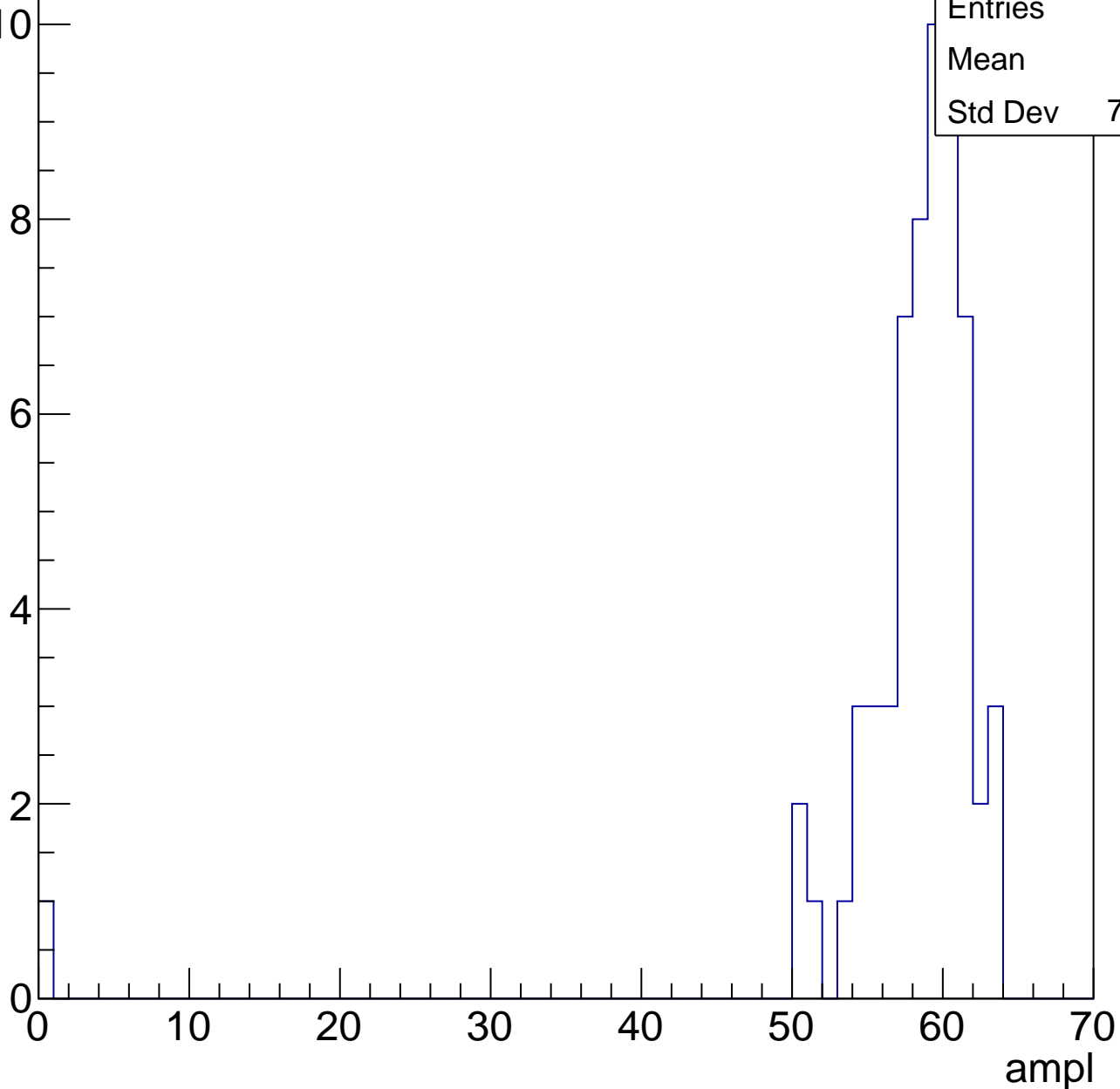


B1L100S, U25-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	57.2
Std Dev	7.997

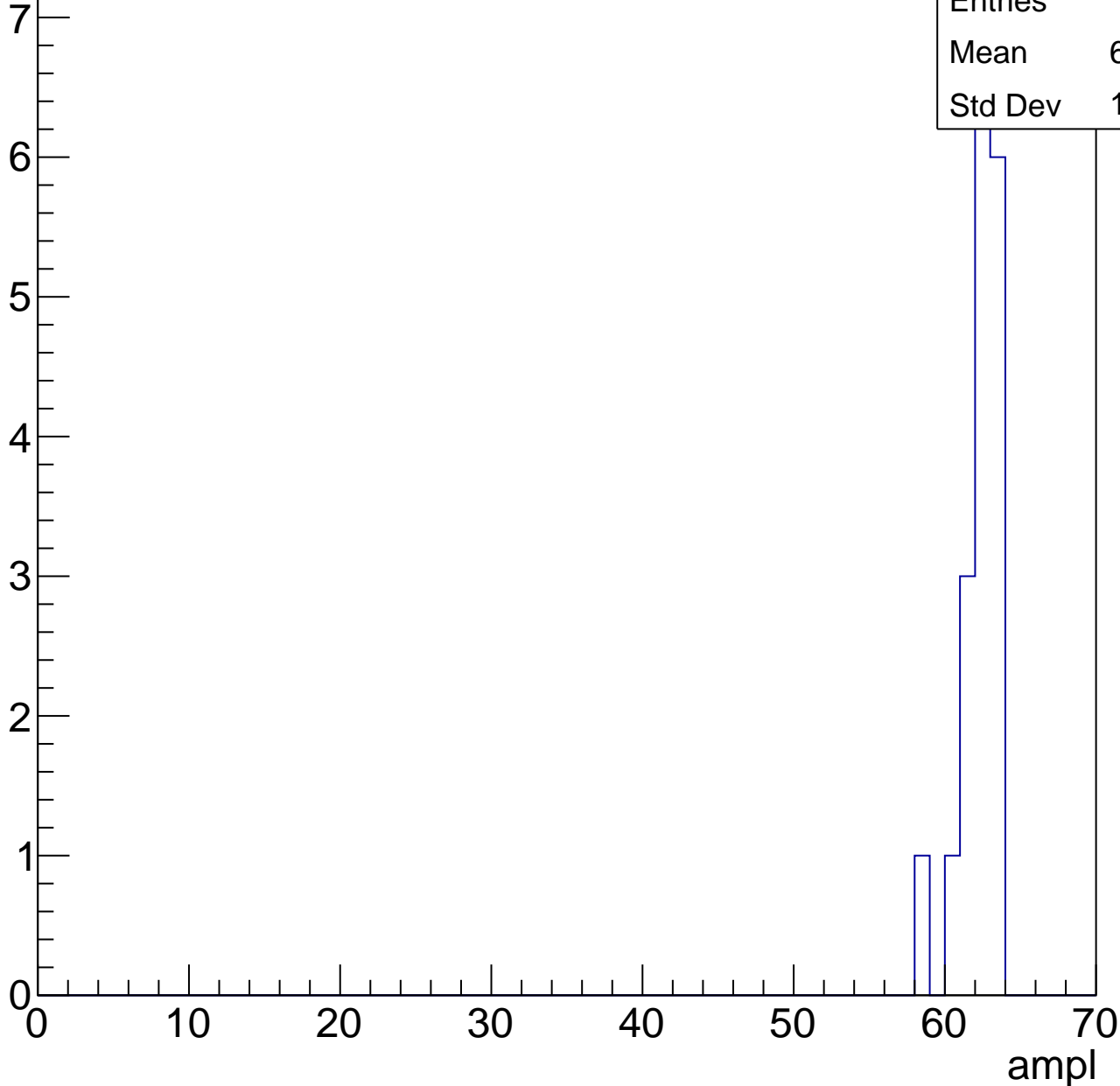


B1L100S, U25-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	61.83
Std Dev	1.258



B1L100S, U25-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U25-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U25-ch124, adc0

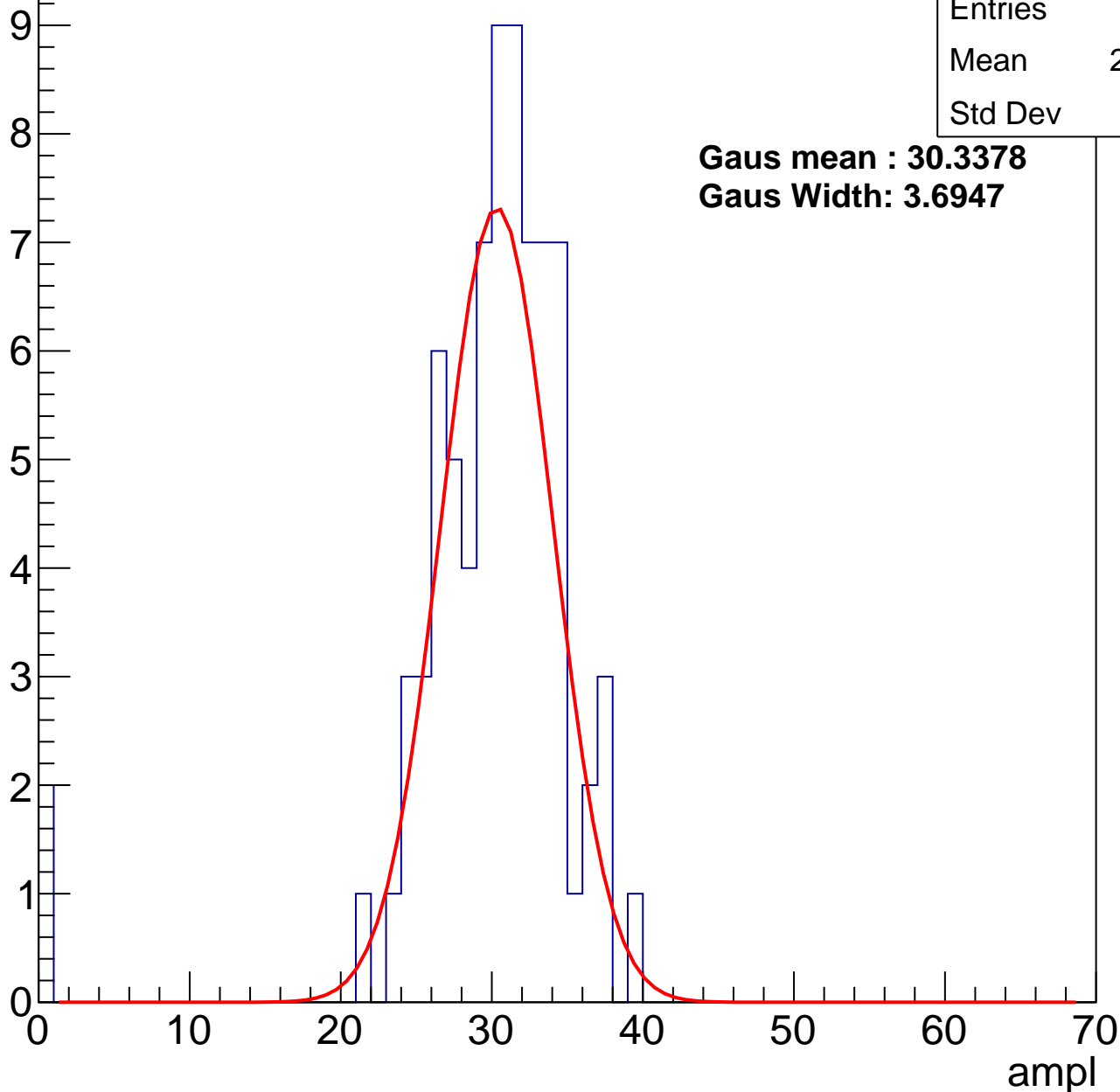
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.44
Std Dev	5.98

Gaus mean : 30.3378

Gaus Width: 3.6947



B1L100S, U25-ch124, adc1

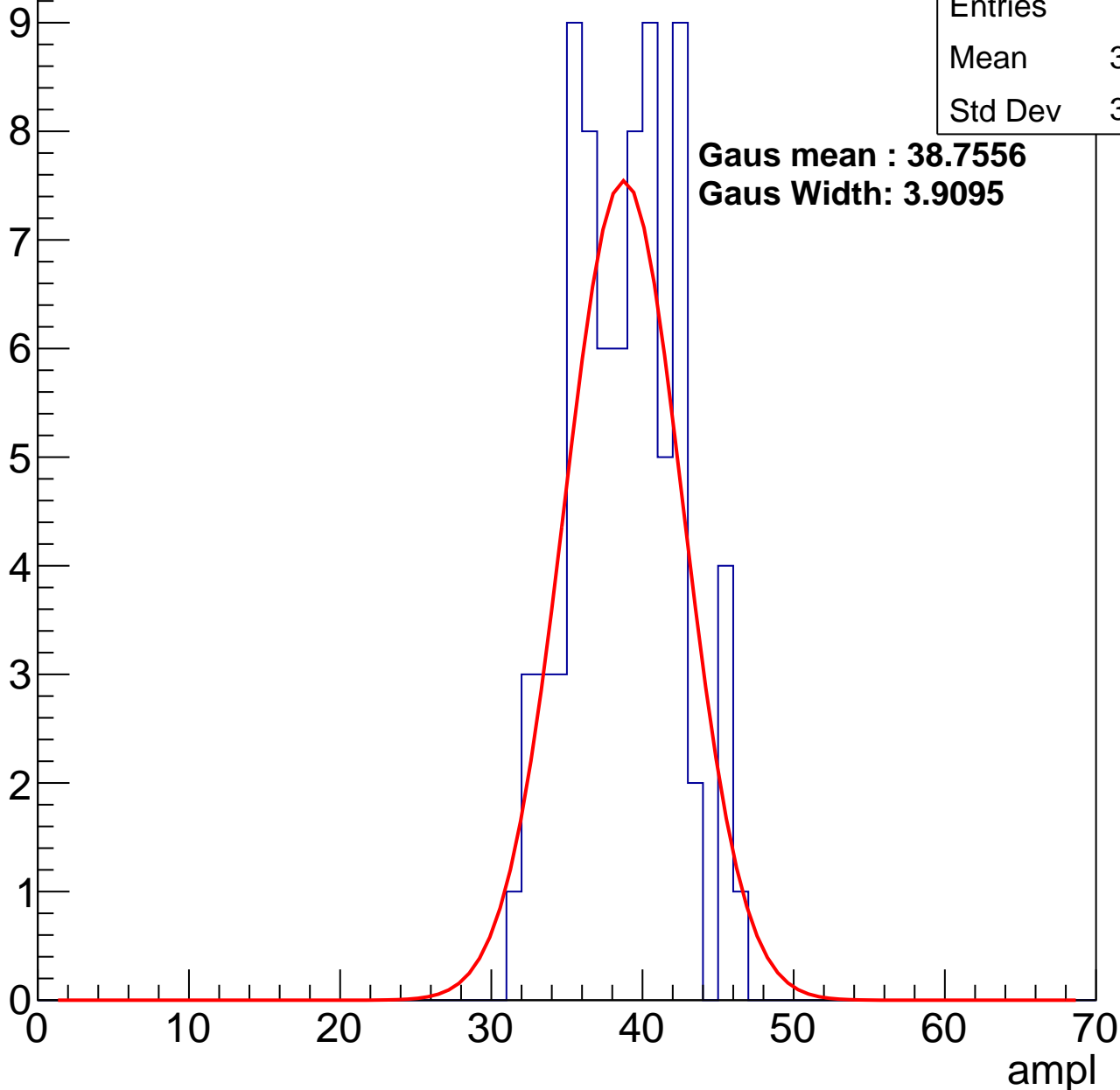
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.29
Std Dev	3.486

Gaus mean : 38.7556

Gaus Width: 3.9095



B1L100S, U25-ch124, adc2

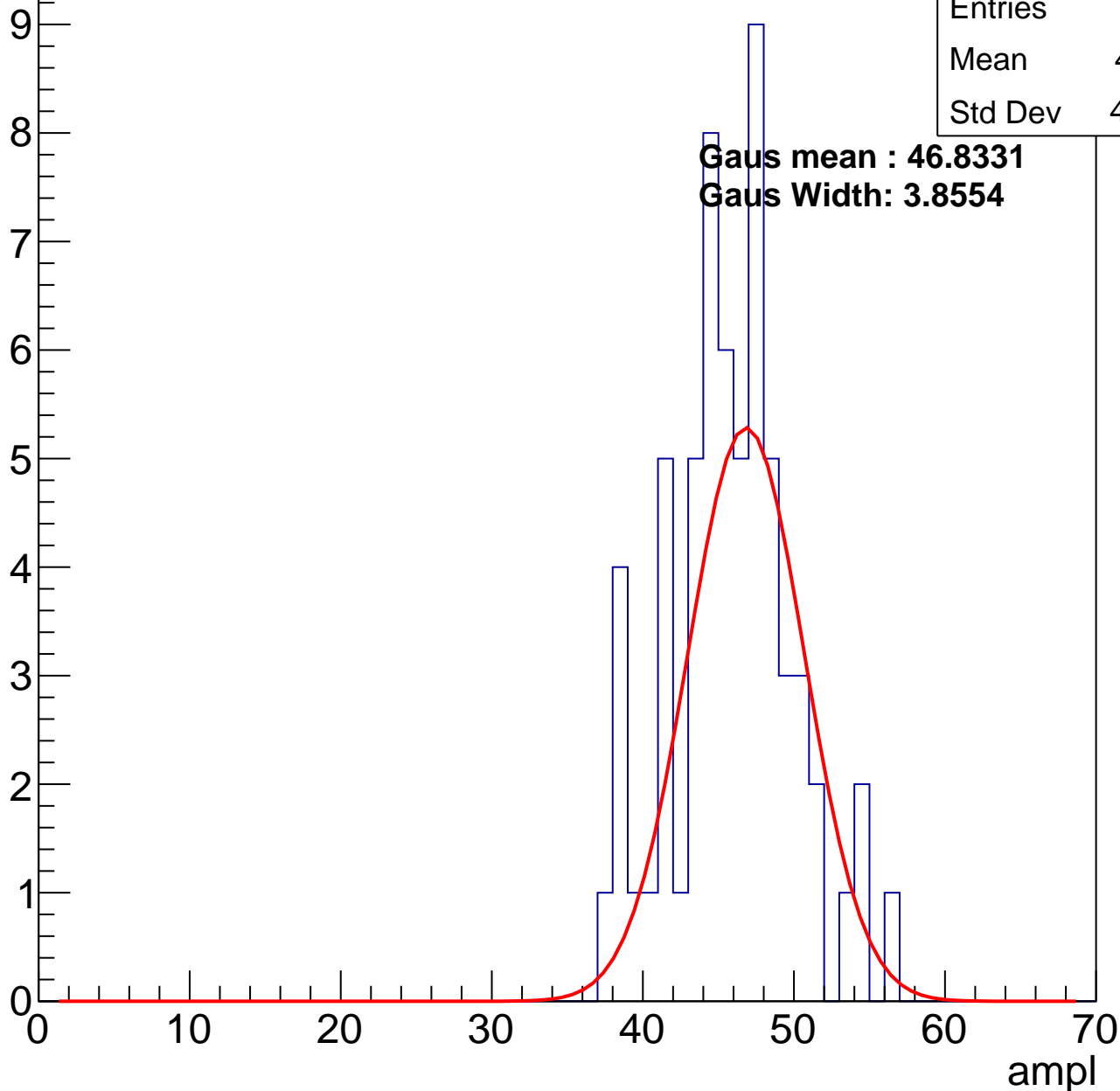
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	45.41
Std Dev	4.093

Gaus mean : 46.8331

Gaus Width: 3.8554

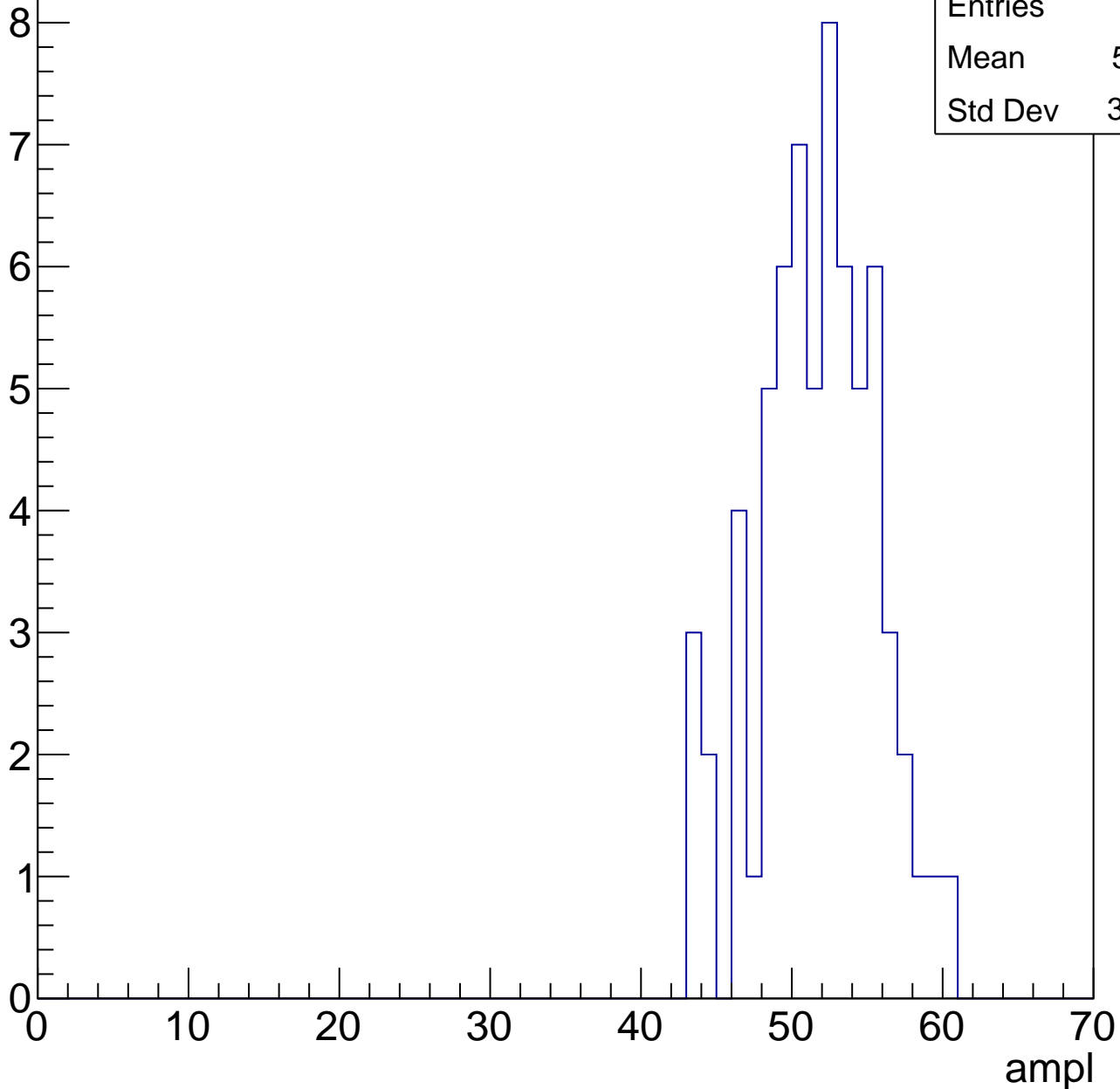


B1L100S, U25-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	51.21
Std Dev	3.879

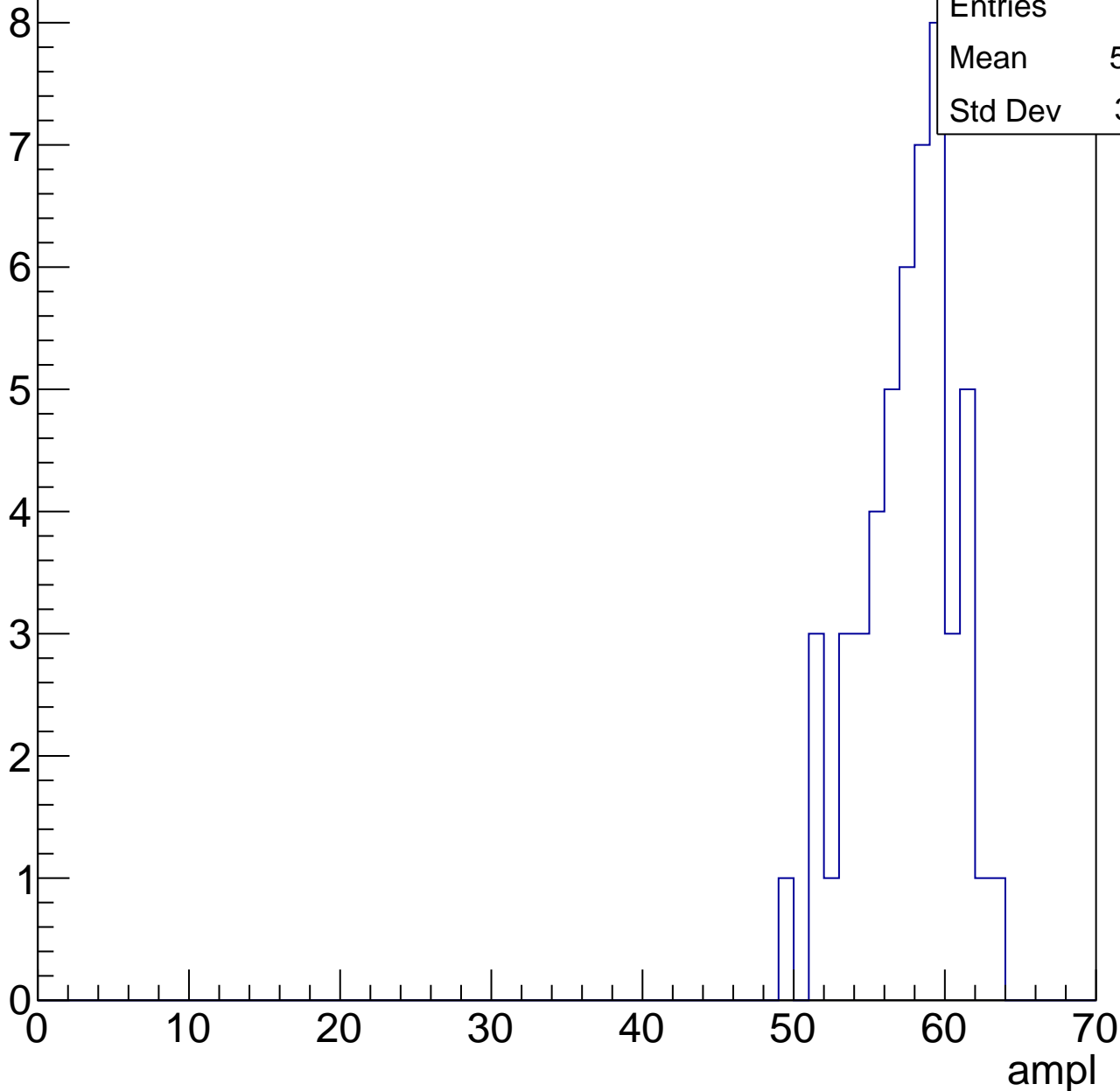


B1L100S, U25-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	56.96
Std Dev	3.131

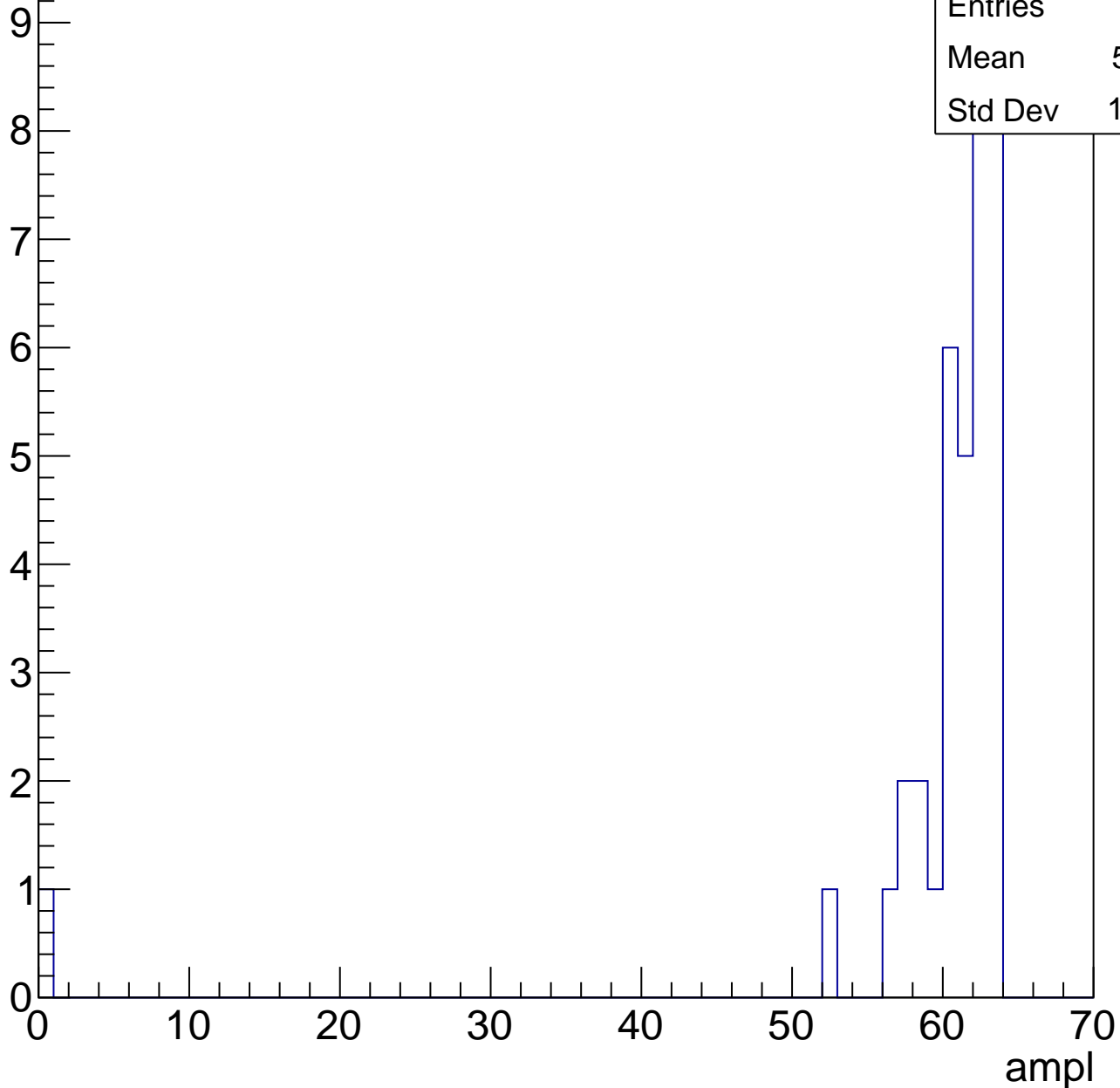


B1L100S, U25-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.11
Std Dev	10.13



B1L100S, U25-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch125, adc0

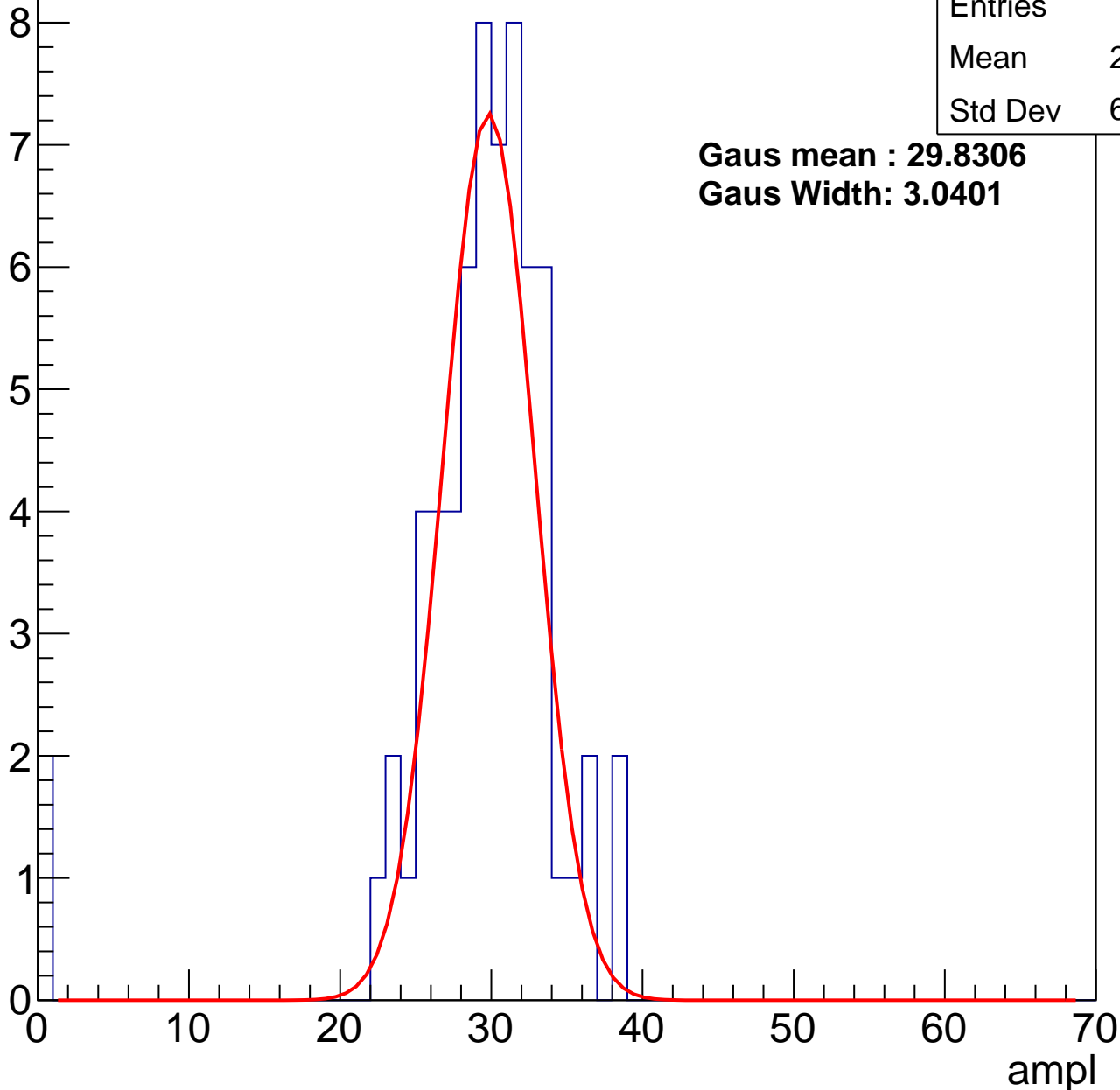
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.75
Std Dev	6.147

Gaus mean : 29.8306

Gaus Width: 3.0401



B1L100S, U25-ch125, adc1

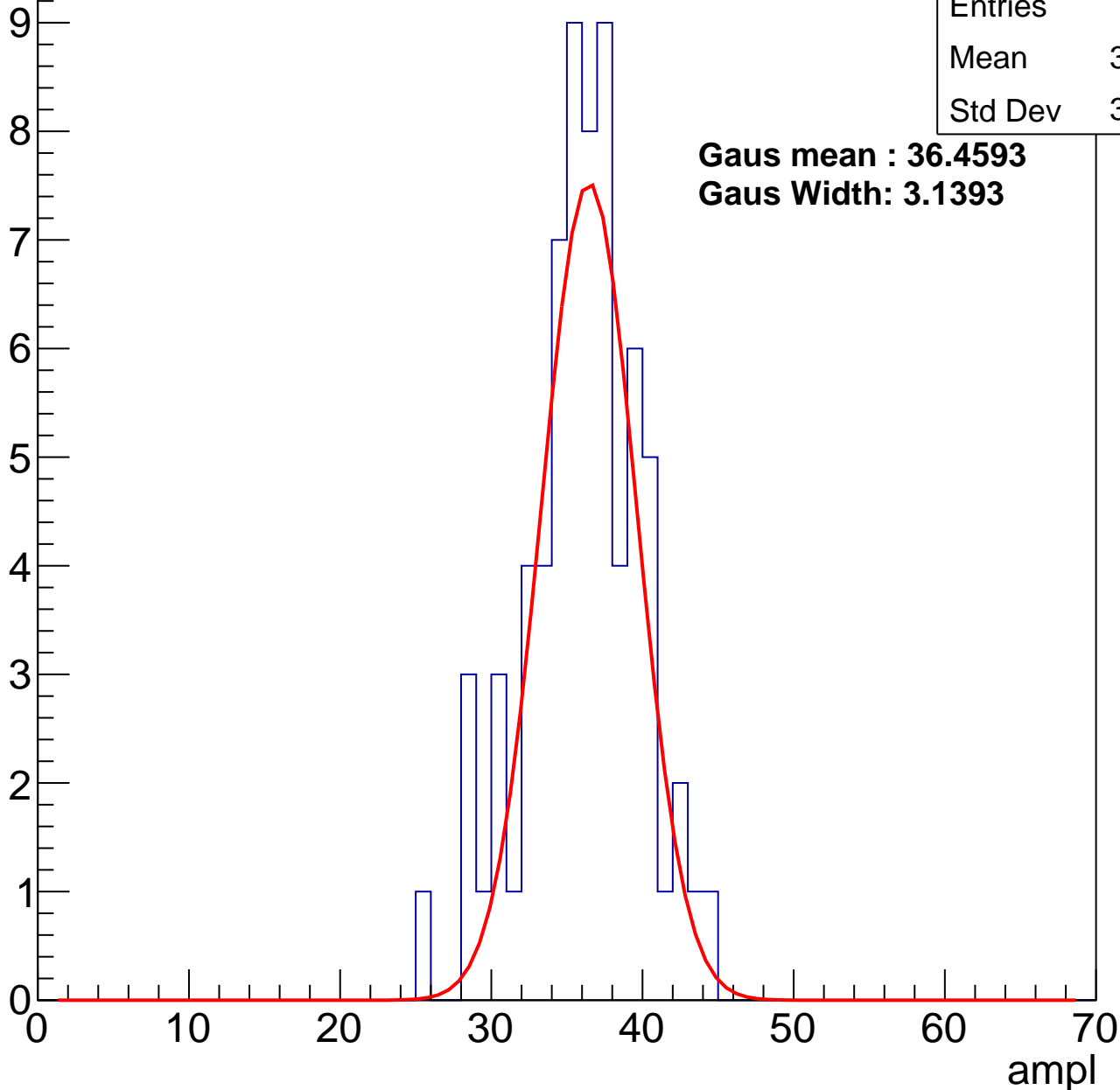
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.59
Std Dev	3.759

Gaus mean : 36.4593

Gaus Width: 3.1393



B1L100S, U25-ch125, adc2

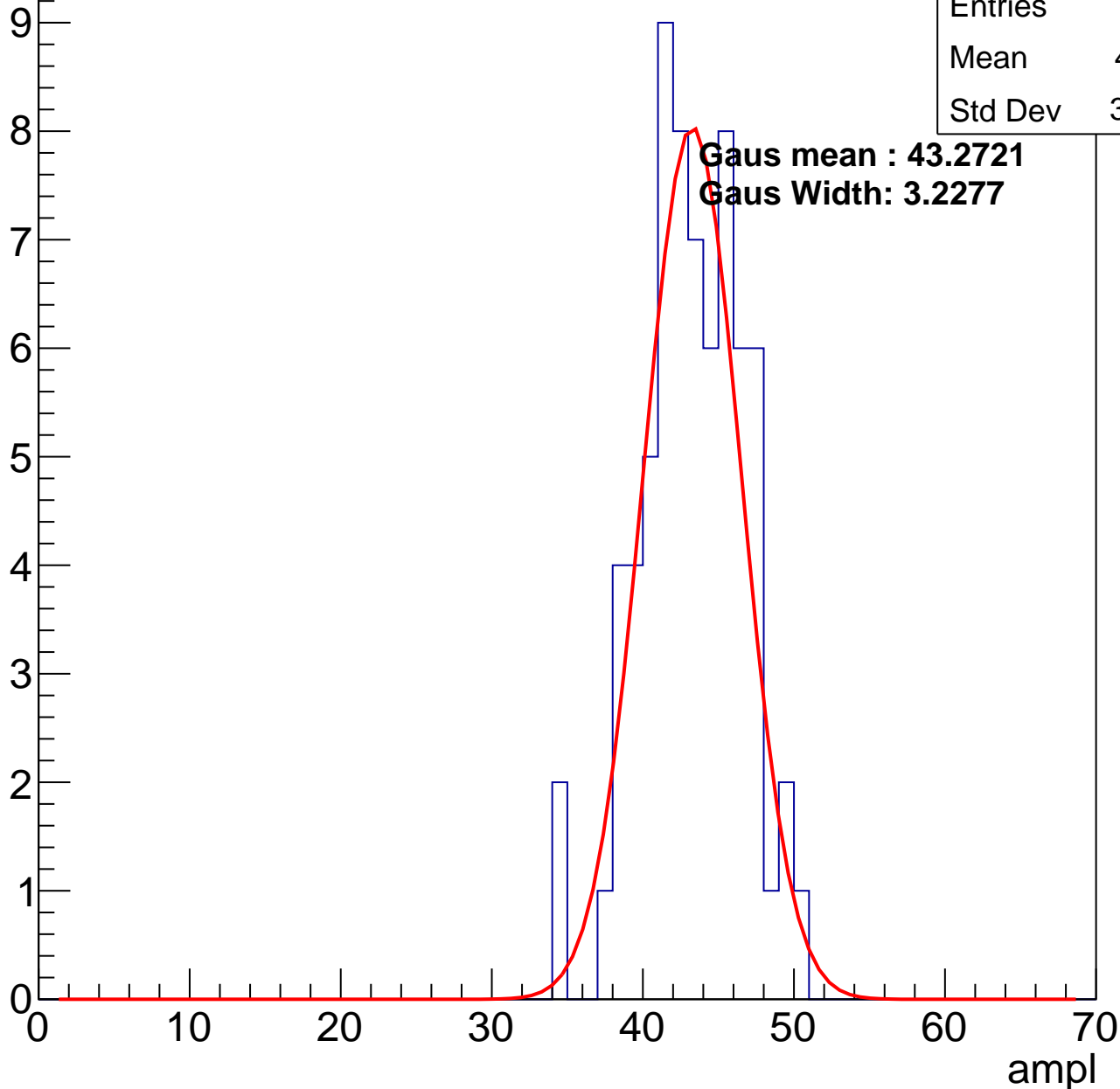
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.81
Std Dev	3.344

Gaus mean : 43.2721

Gaus Width: 3.2277

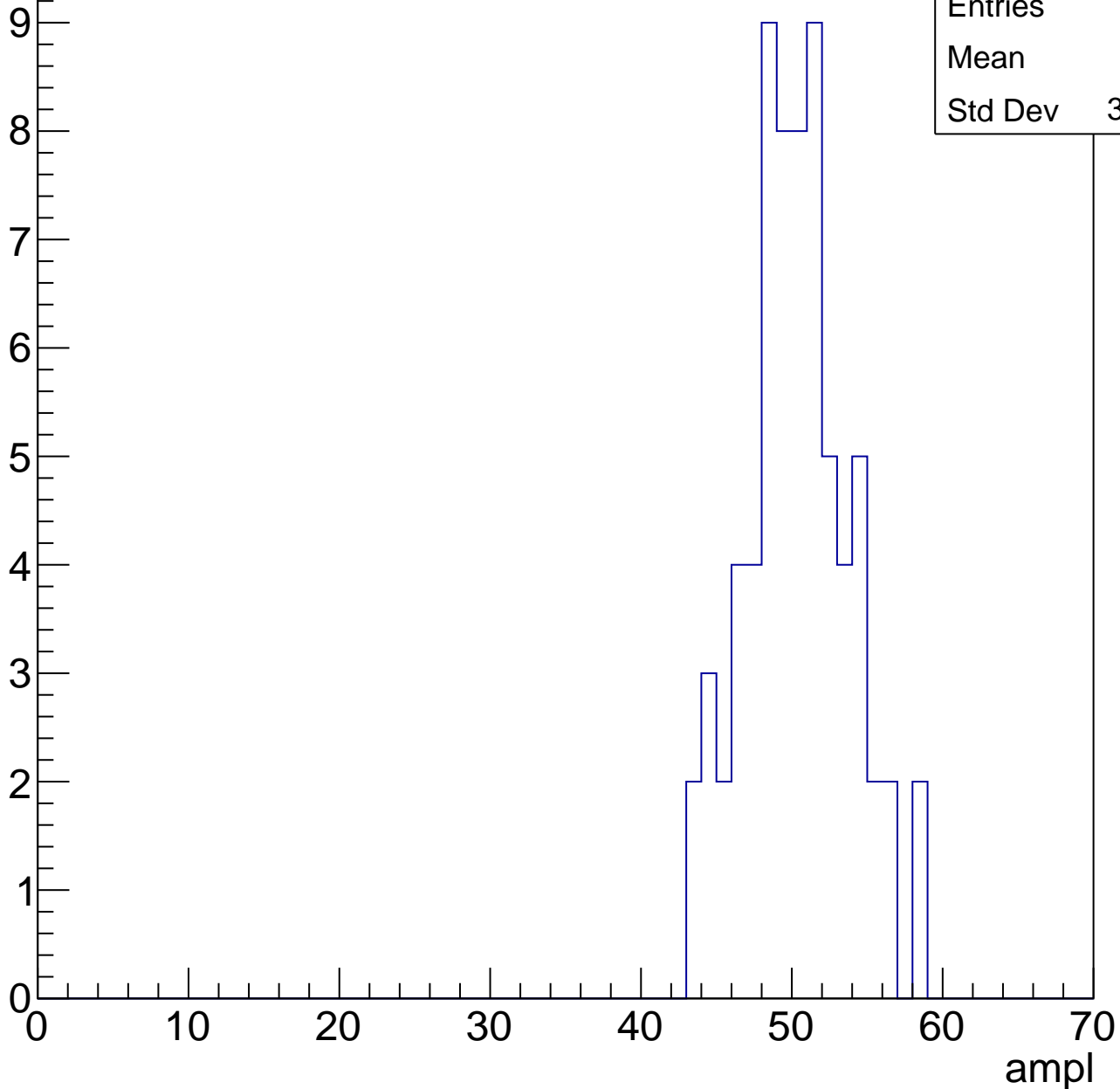


B1L100S, U25-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

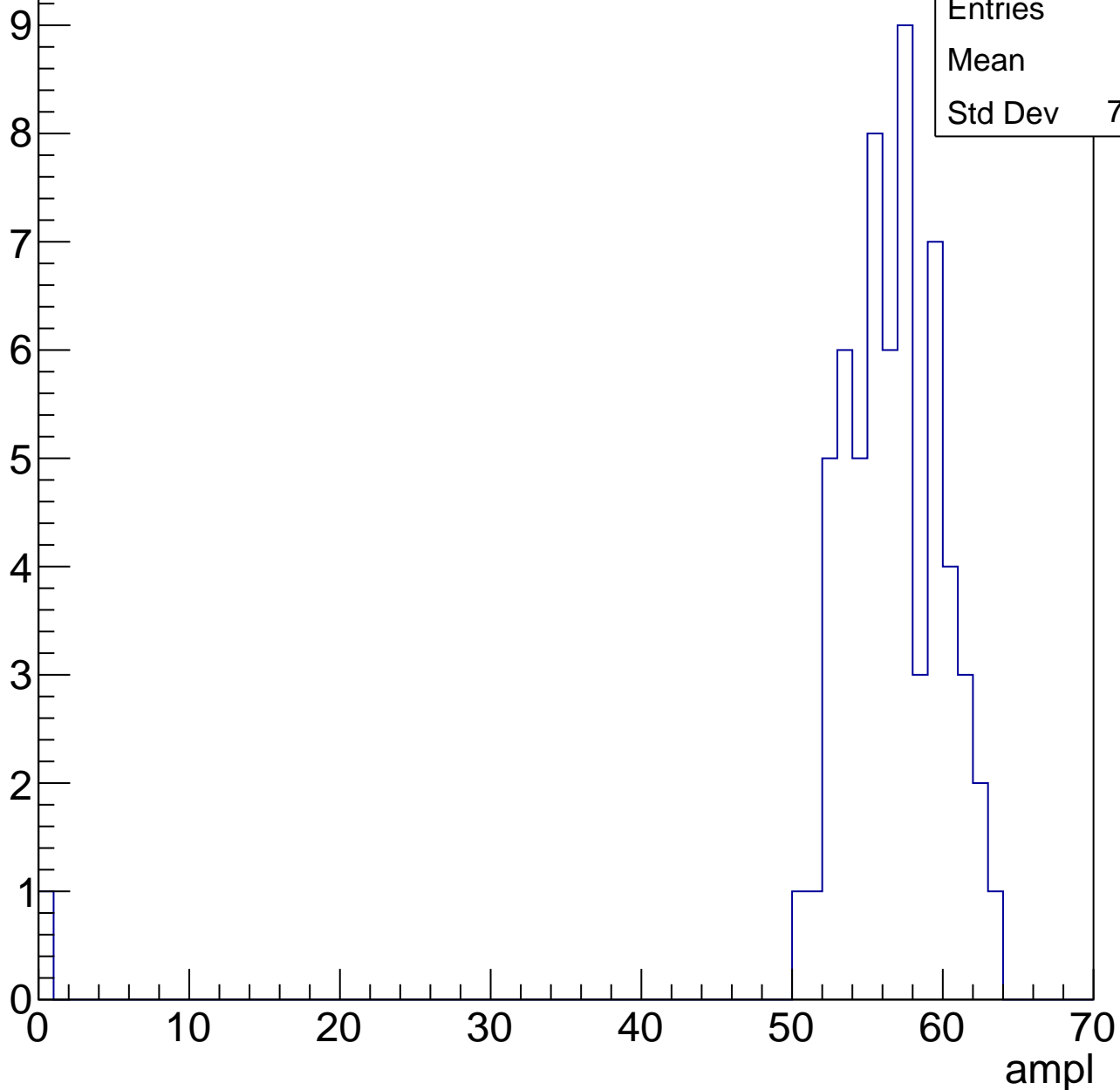
Entries	69
Mean	49.9
Std Dev	3.393



B1L100S, U25-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

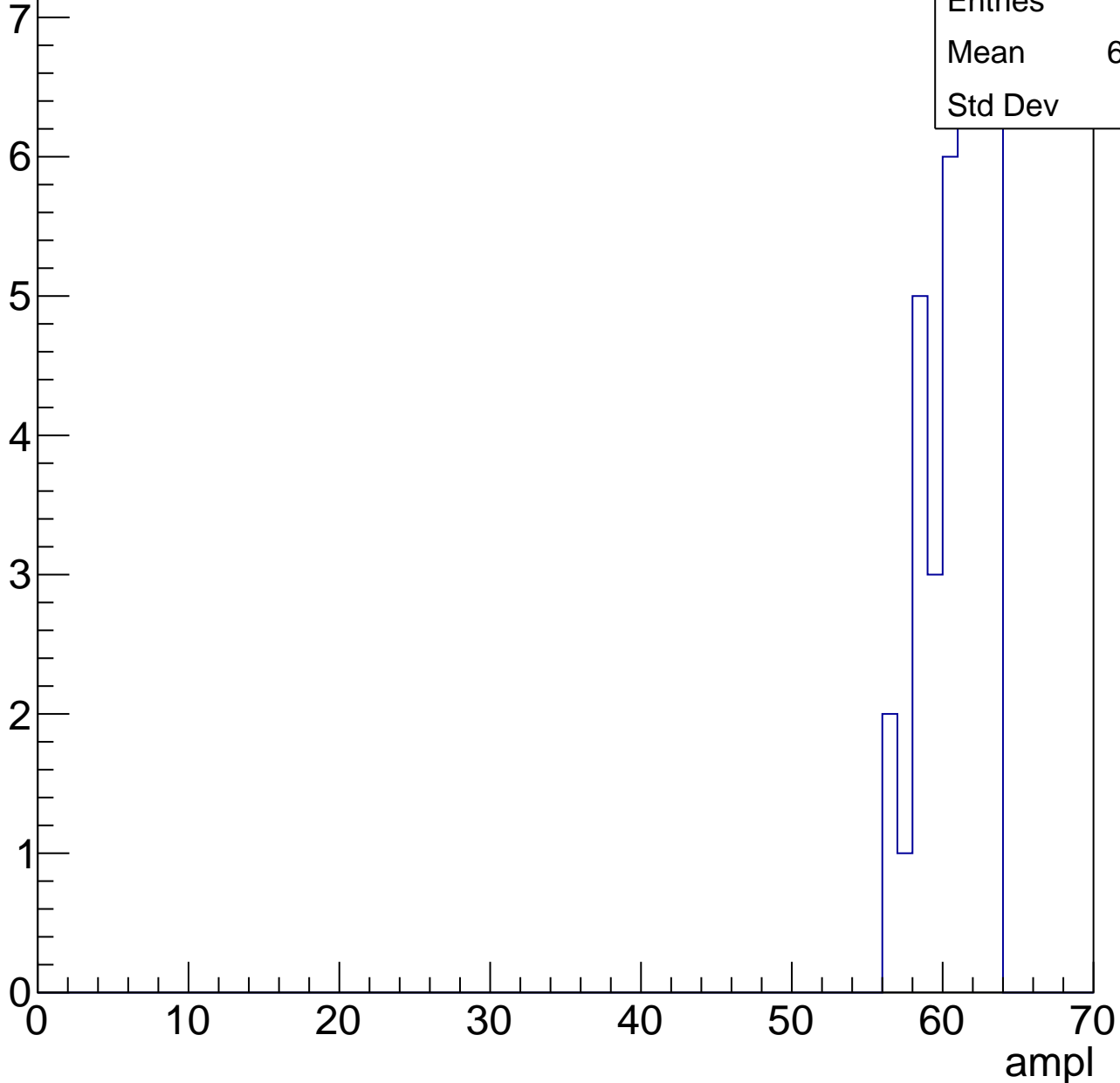


B1L100S, U25-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.47
Std Dev	2.01



B1L100S, U25-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U25-ch126, adc0

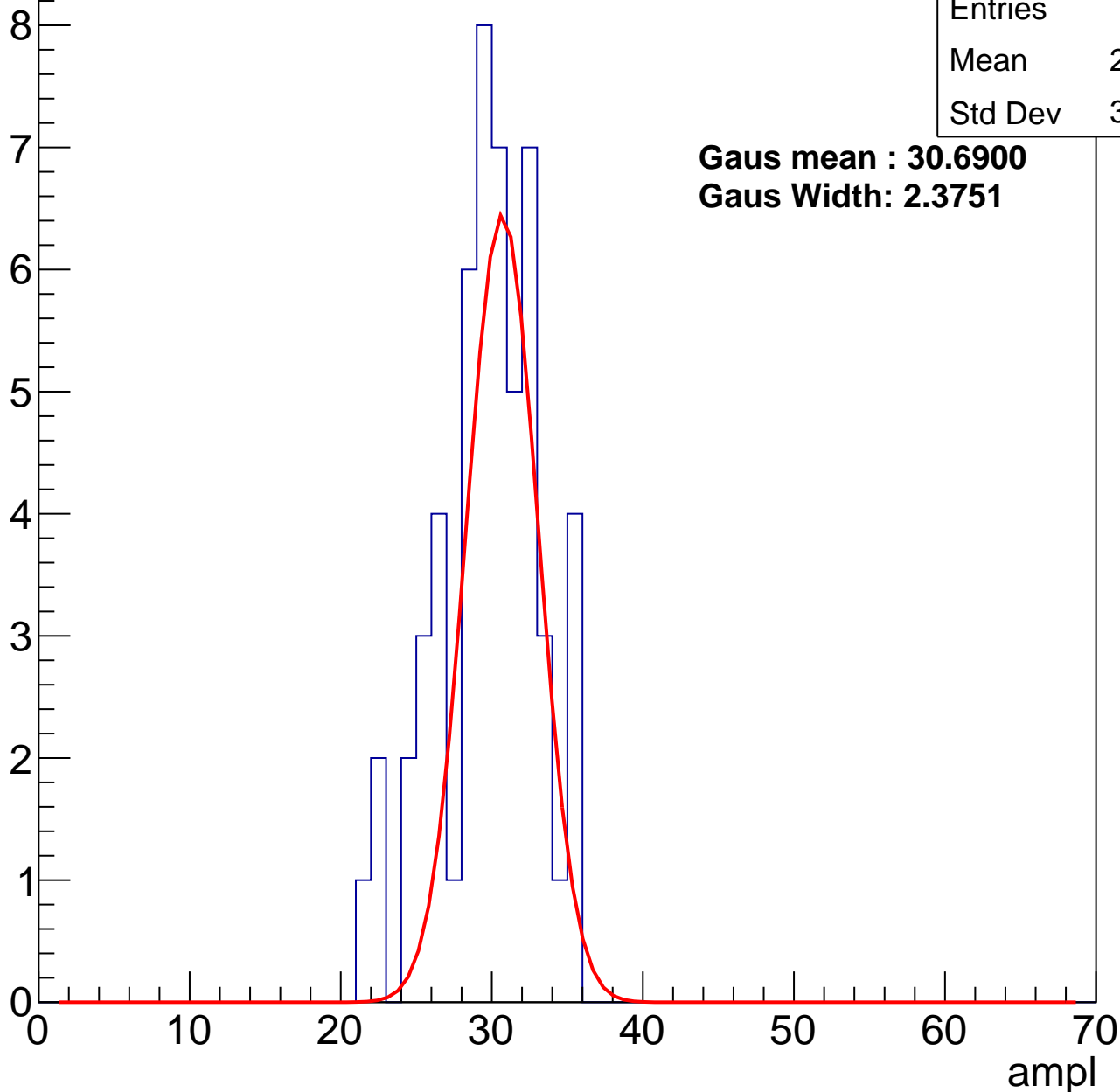
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	29.28
Std Dev	3.358

Gaus mean : 30.6900

Gaus Width: 2.3751



B1L100S, U25-ch126, adc1

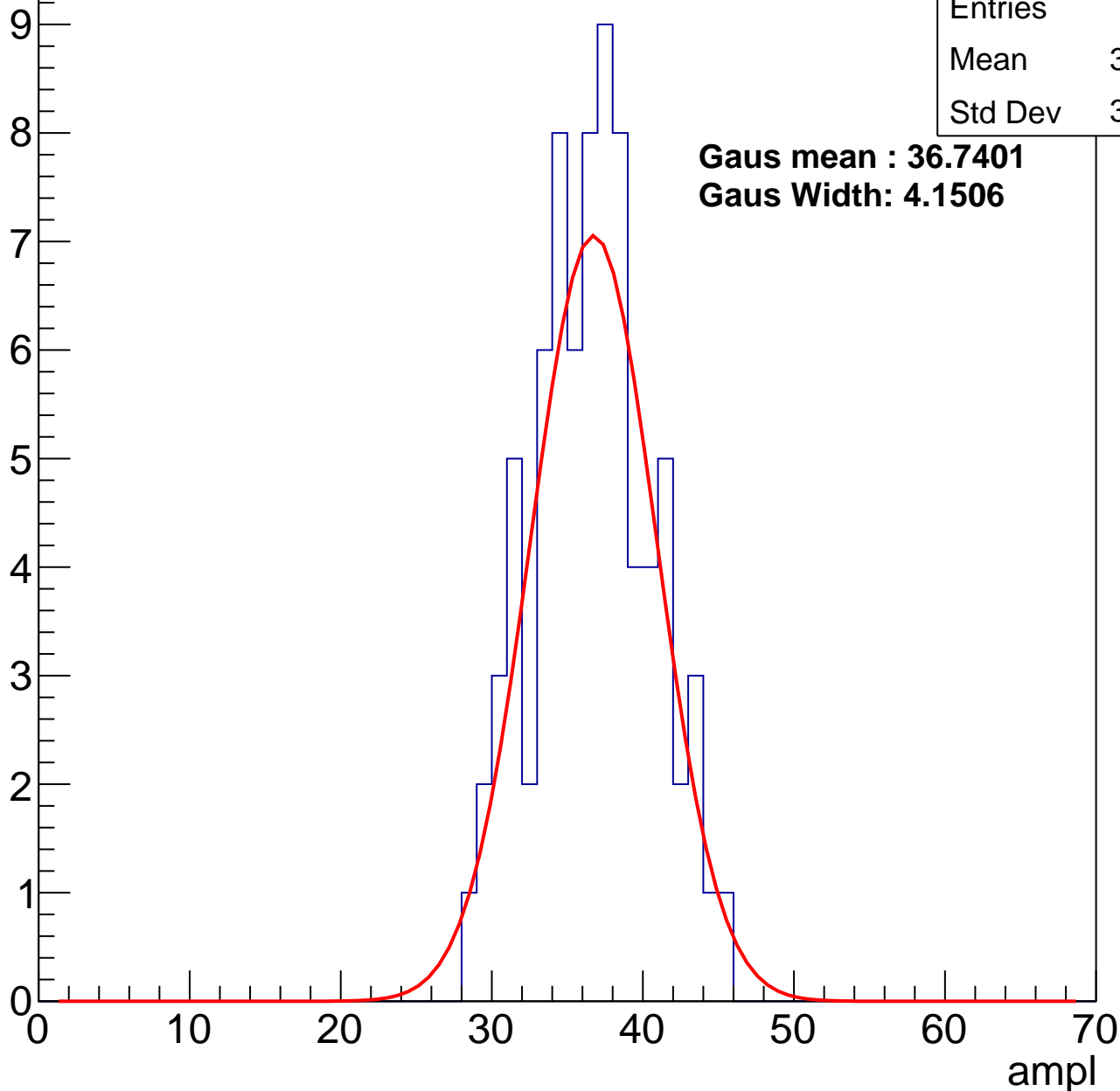
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.19
Std Dev	3.833

Gaus mean : 36.7401

Gaus Width: 4.1506

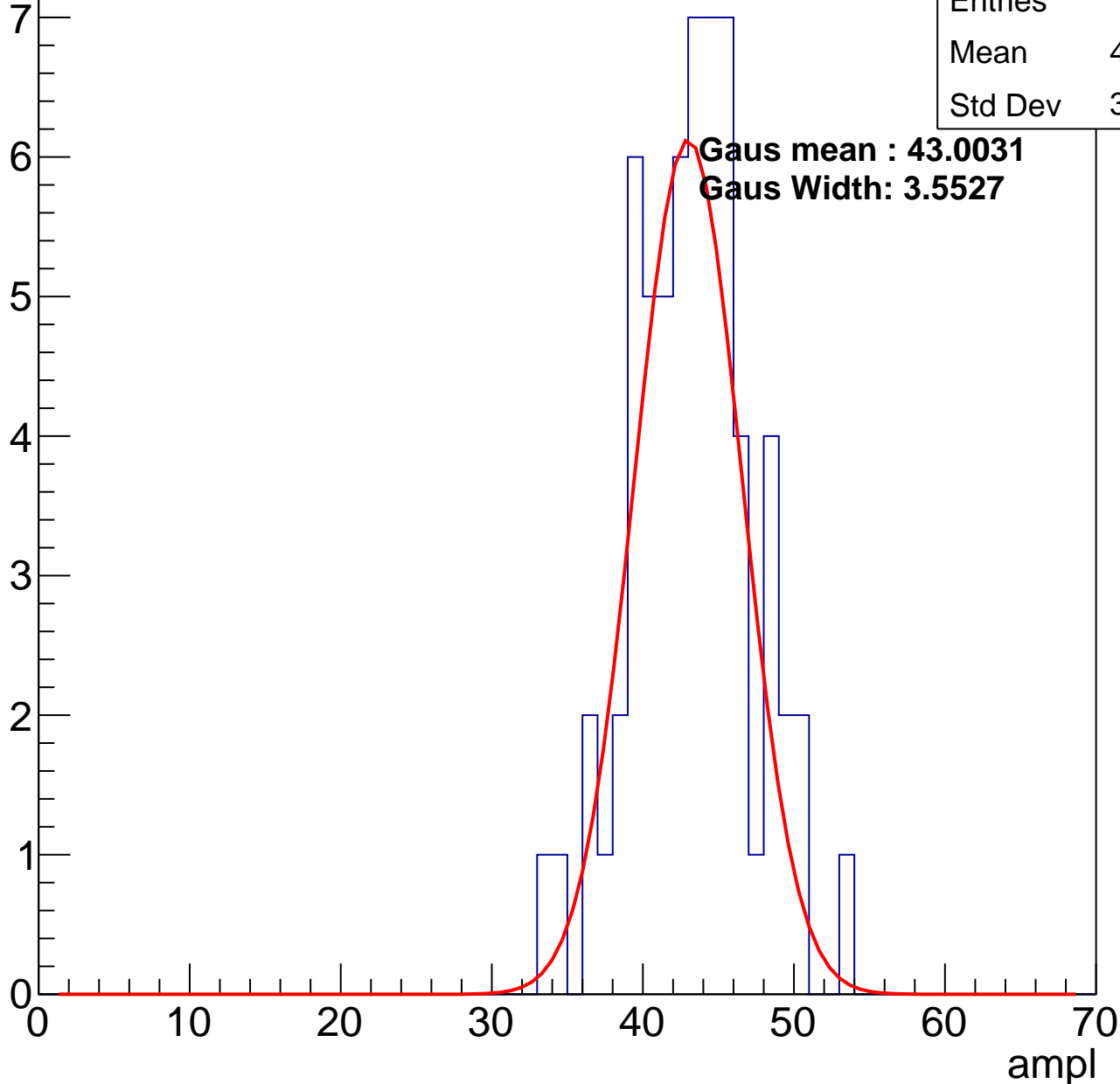


B1L100S, U25-ch126, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.83
Std Dev	3.927

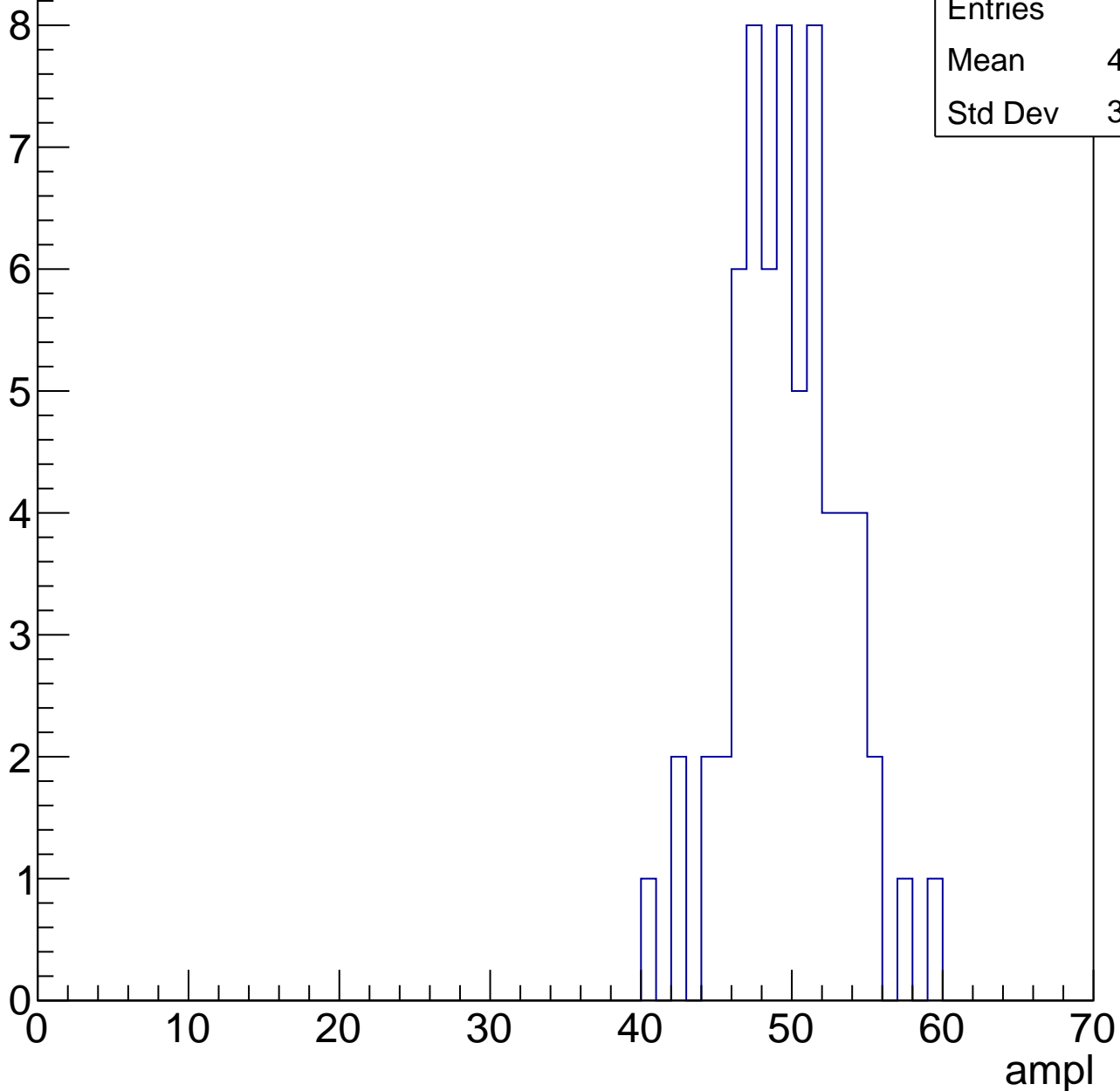


B1L100S, U25-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.28
Std Dev	3.582

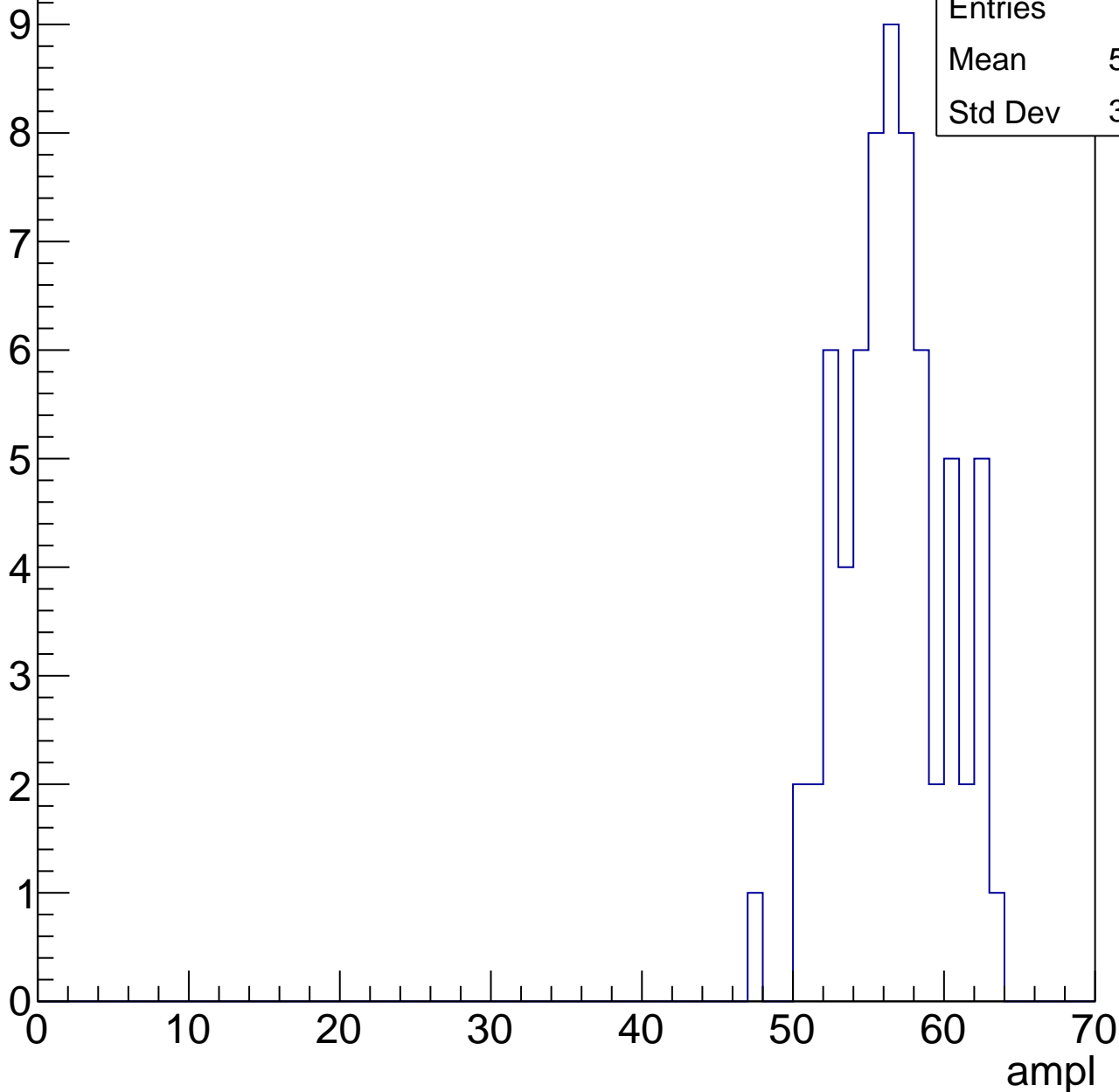


B1L100S, U25-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	56.09
Std Dev	3.402

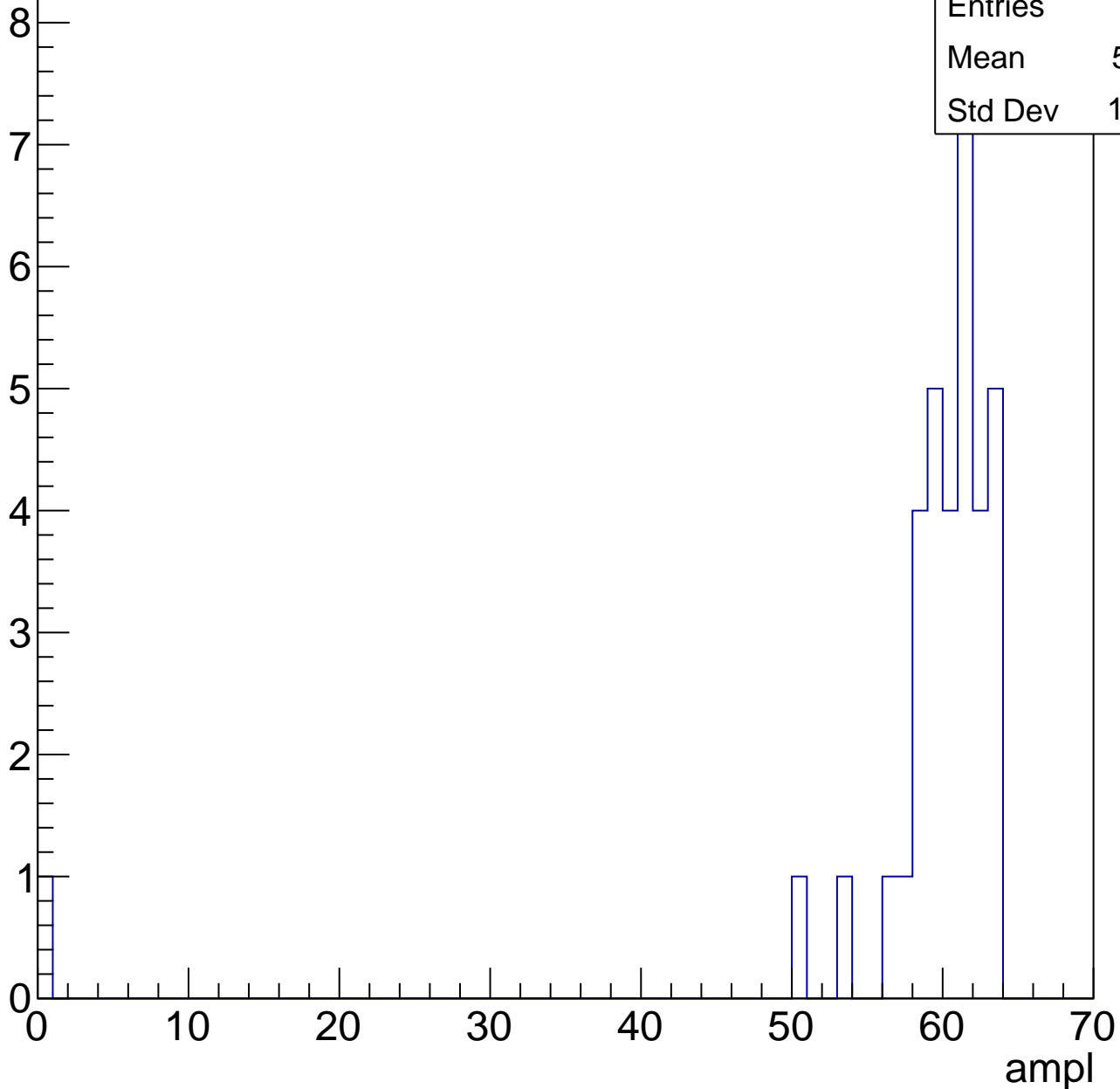


B1L100S, U25-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

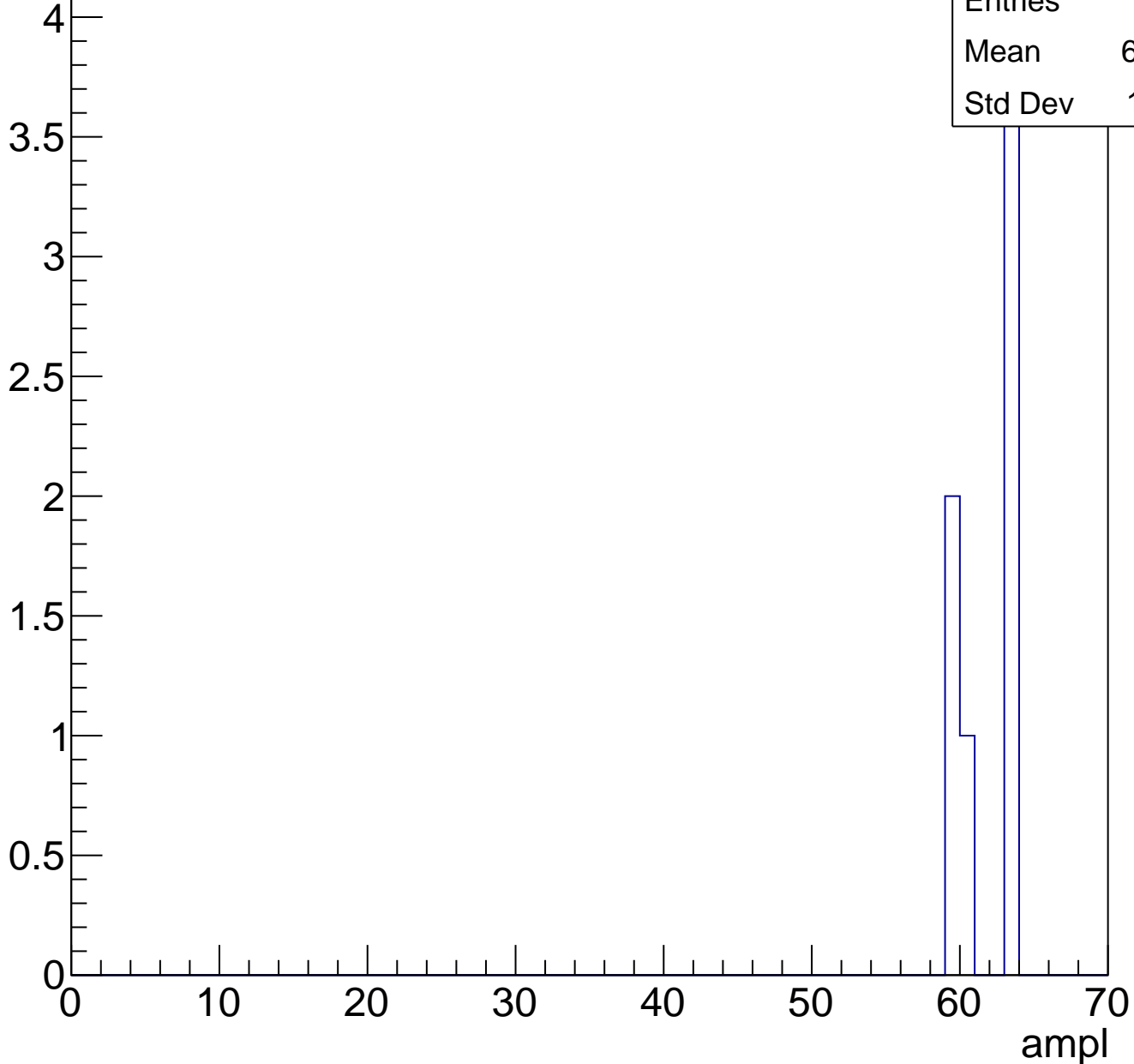
Entries	35
Mean	58.11
Std Dev	10.34



B1L100S, U25-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U25-ch127, adc0

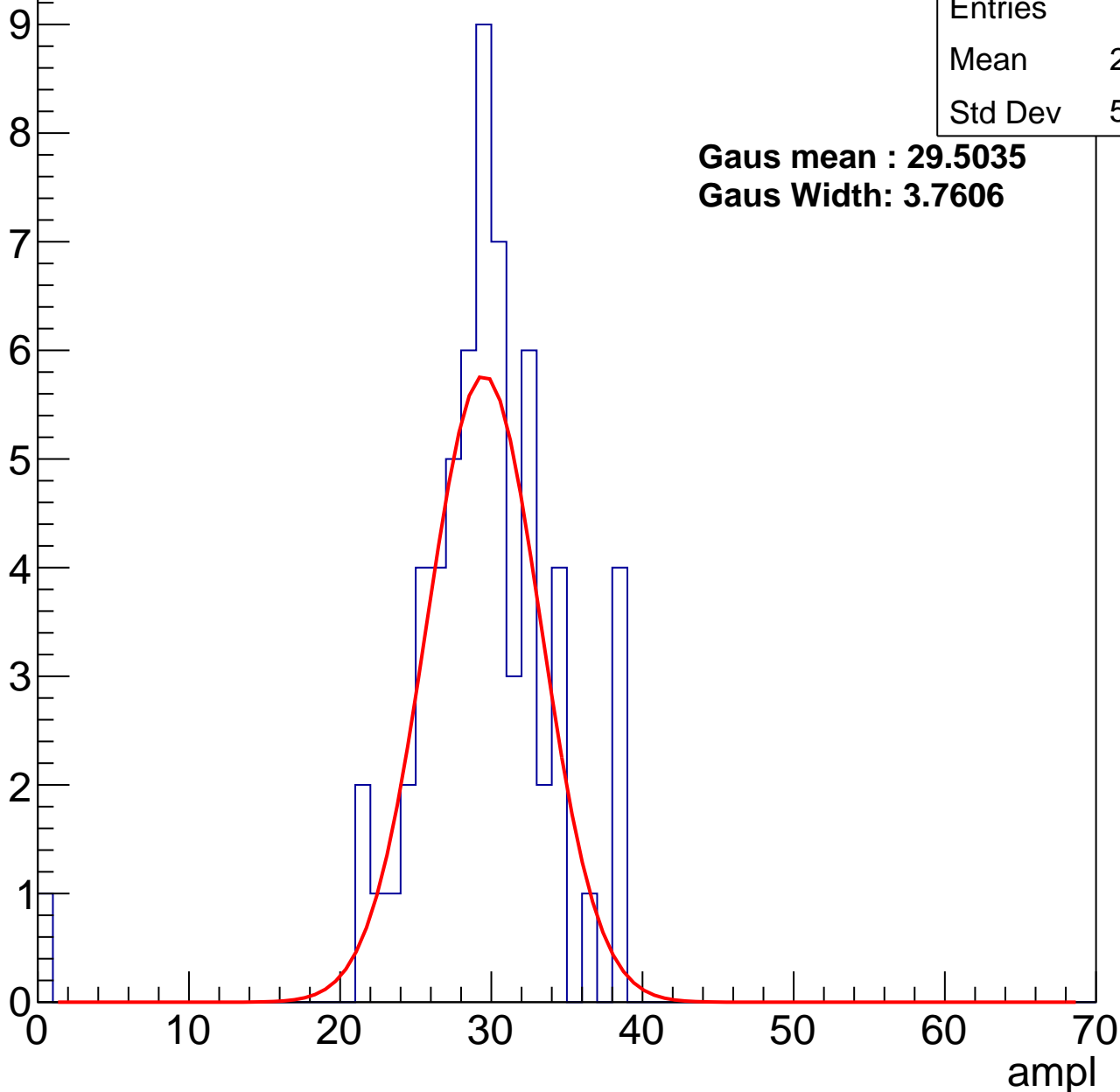
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.84
Std Dev	5.398

Gaus mean : 29.5035

Gaus Width: 3.7606



B1L100S, U25-ch127, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	36.06
Std Dev	4.269

Gaus mean : 36.2971

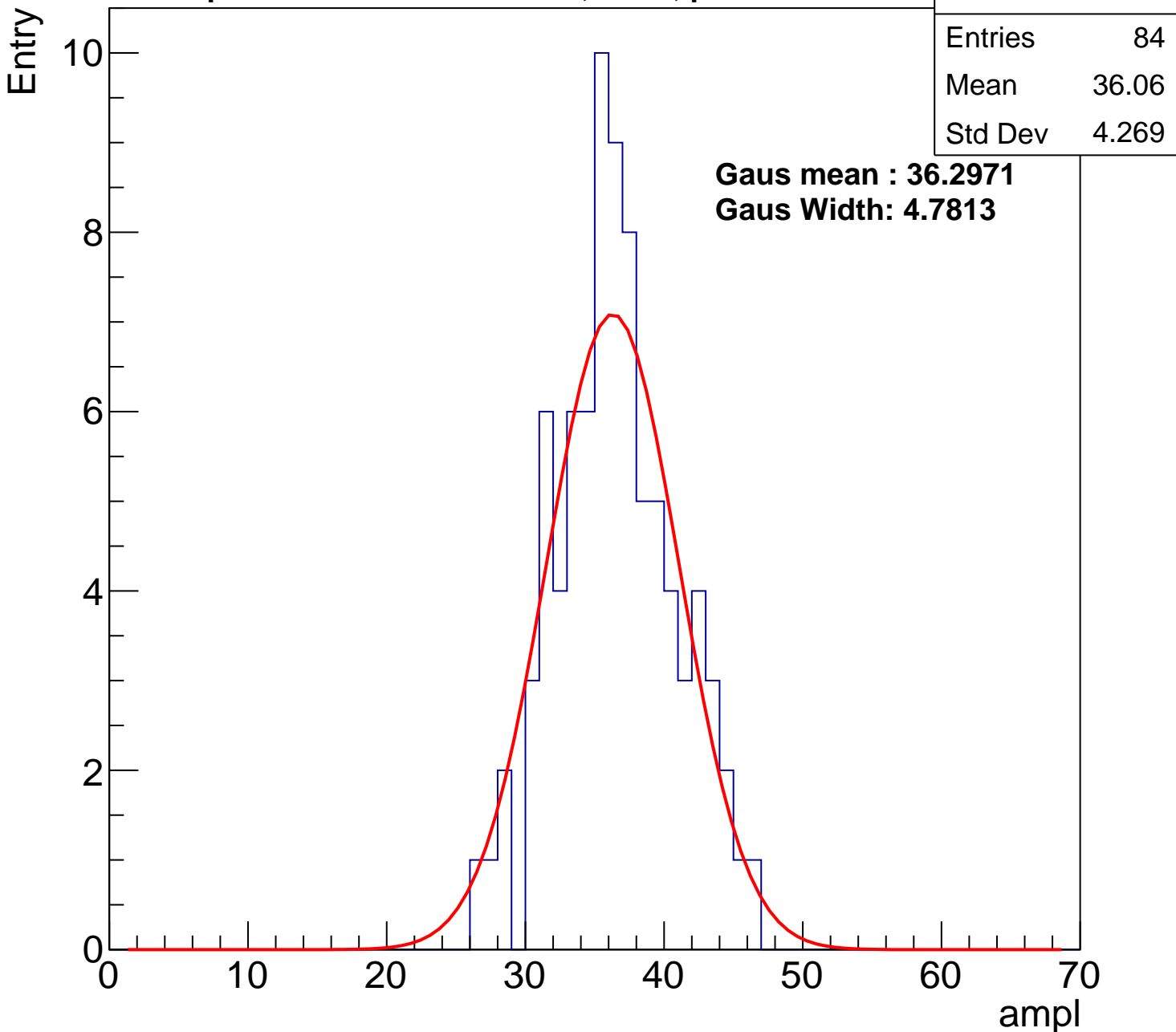
Gaus Width: 4.7813

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch127, adc2

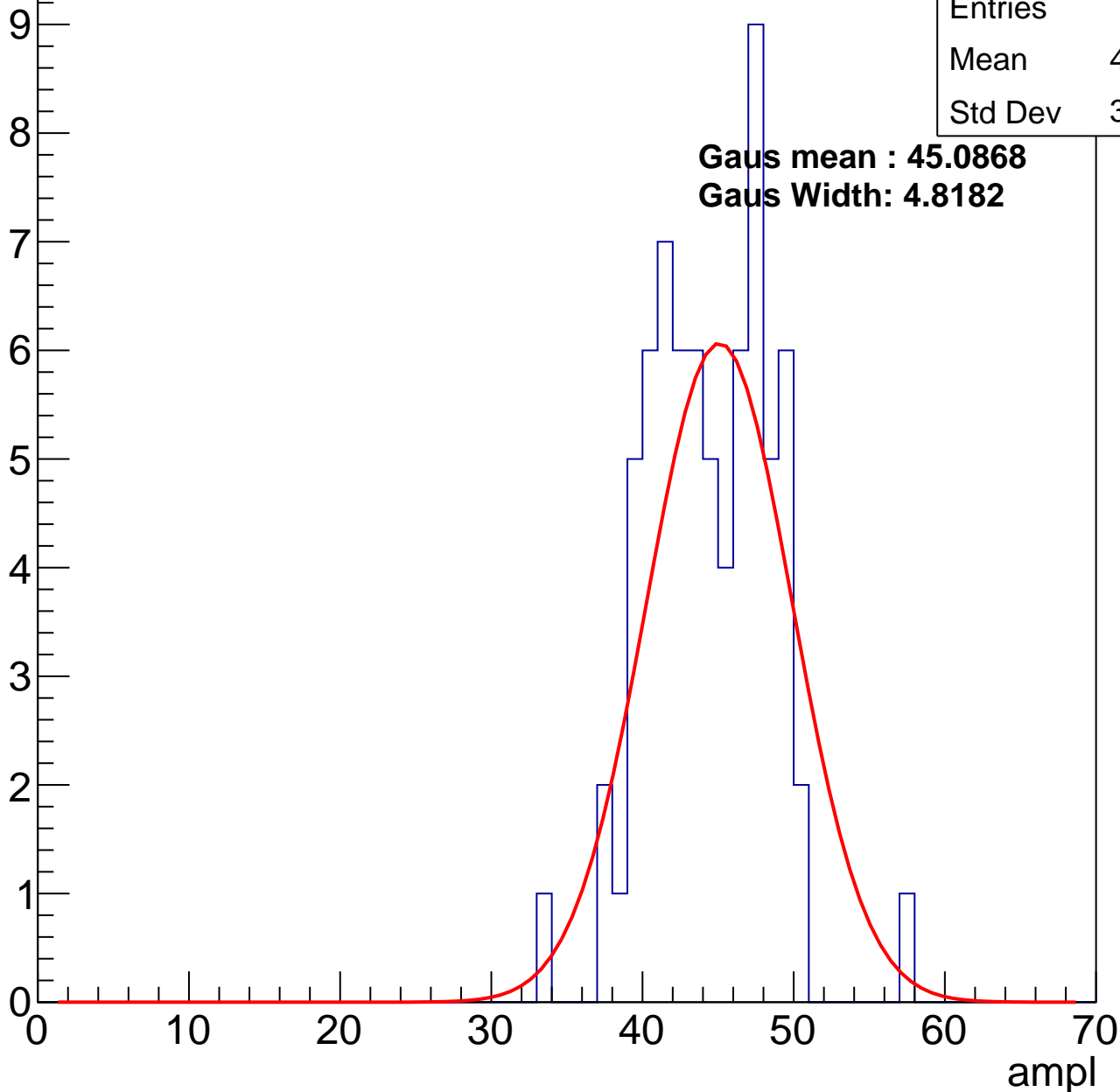
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.99
Std Dev	3.995

Gaus mean : 45.0868

Gaus Width: 4.8182

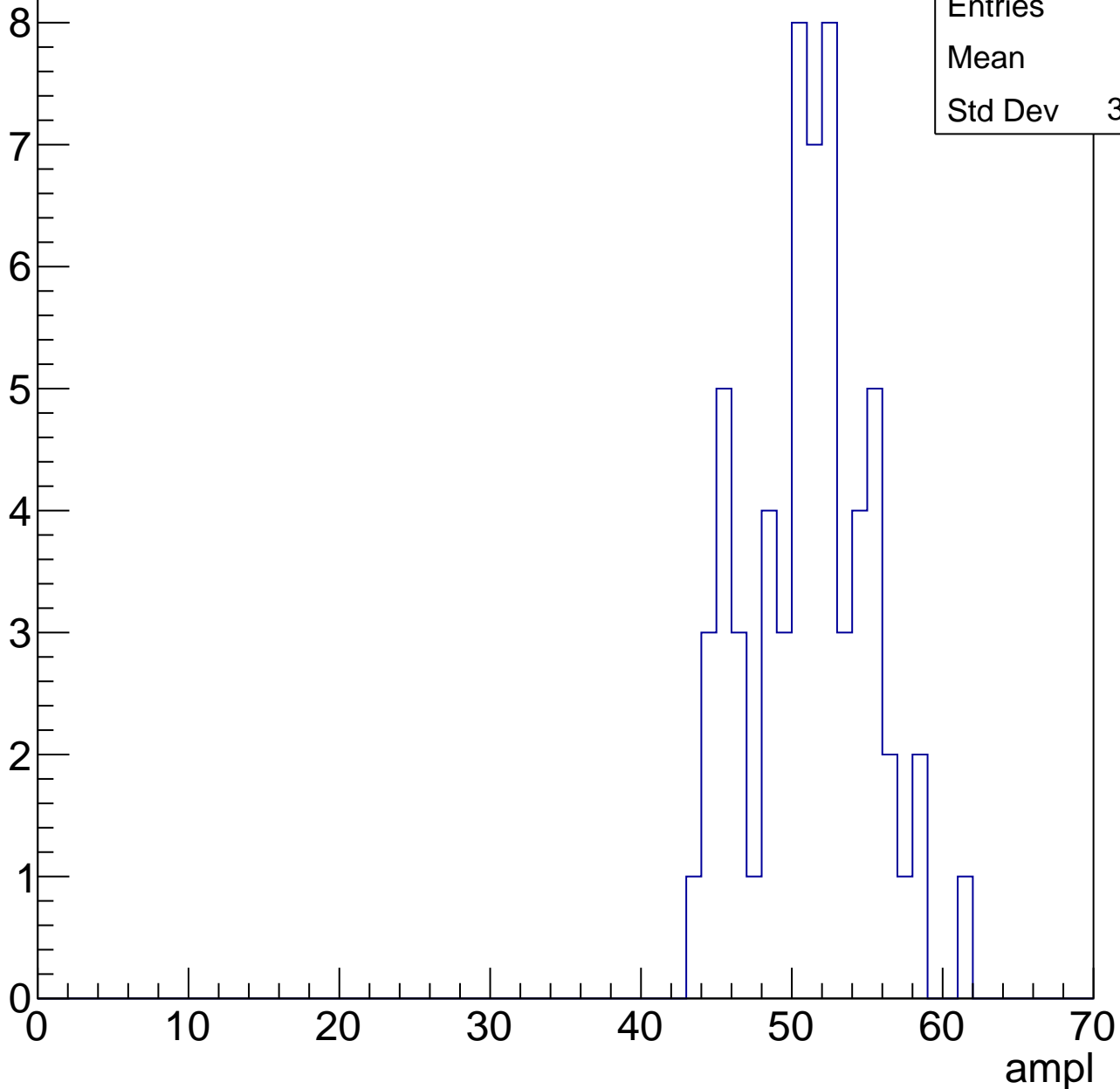


B1L100S, U25-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.7
Std Dev	3.952

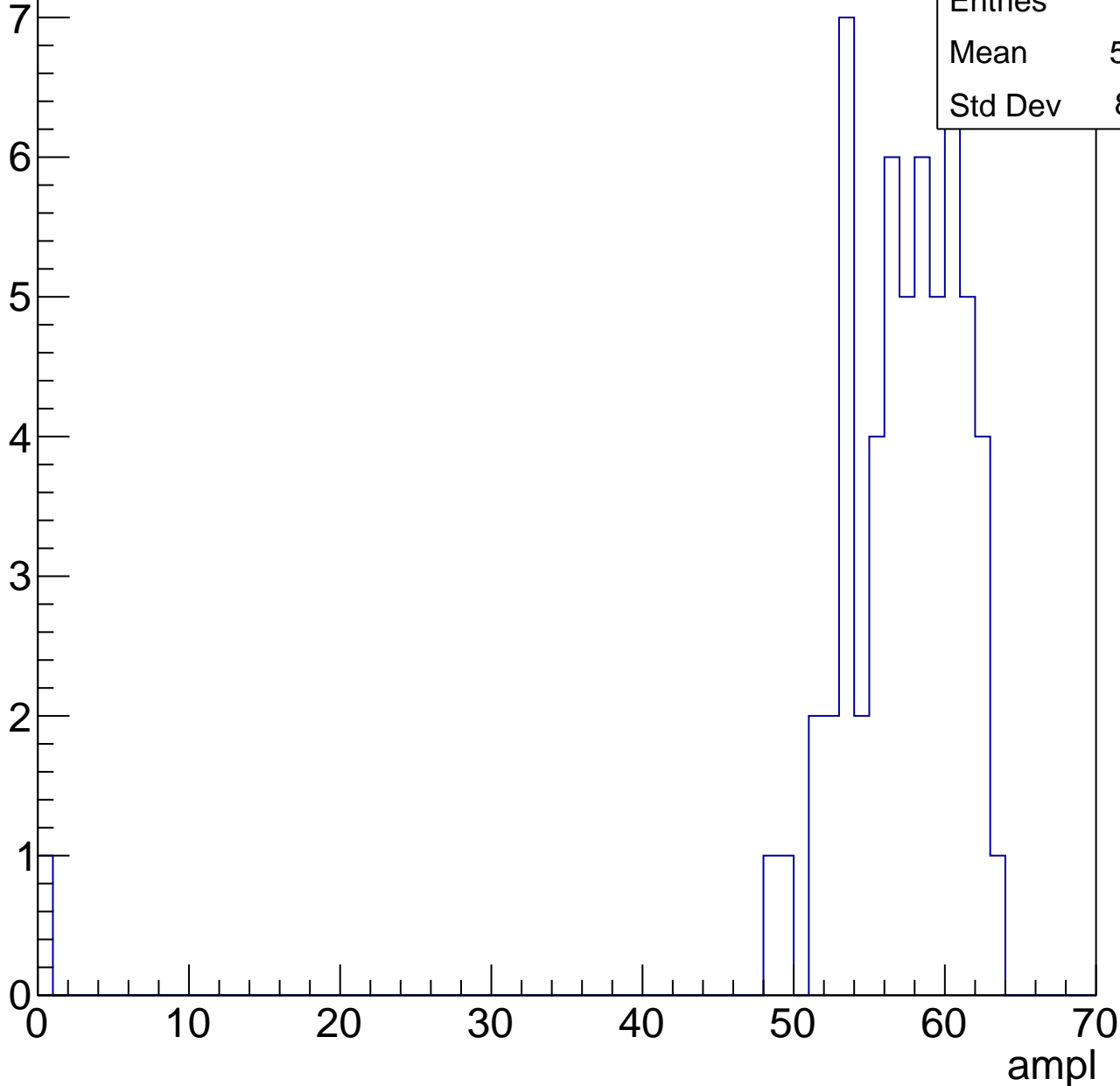


B1L100S, U25-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	55.97
Std Dev	8.141

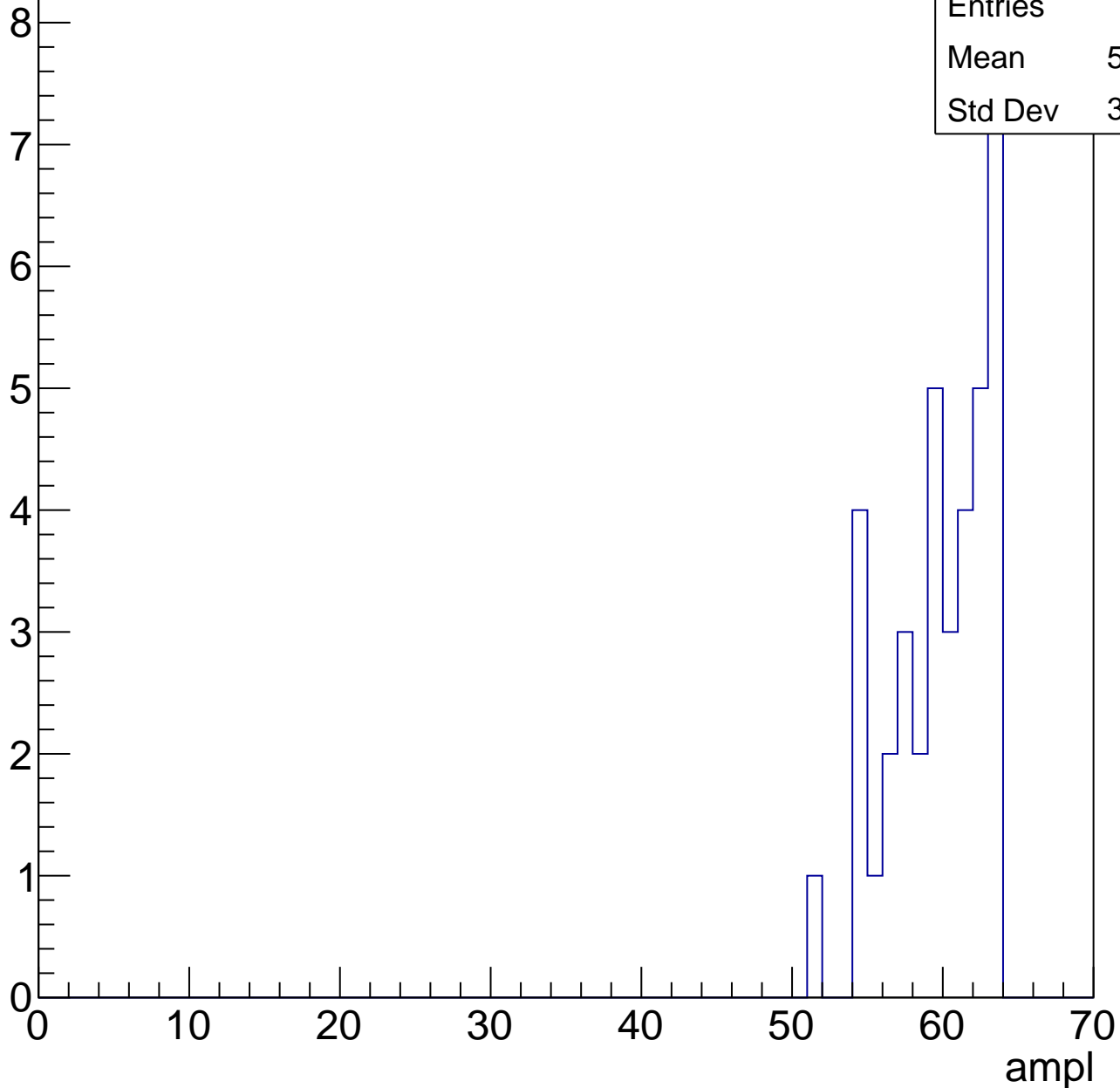


B1L100S, U25-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	59.32
Std Dev	3.245



B1L100S, U25-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0