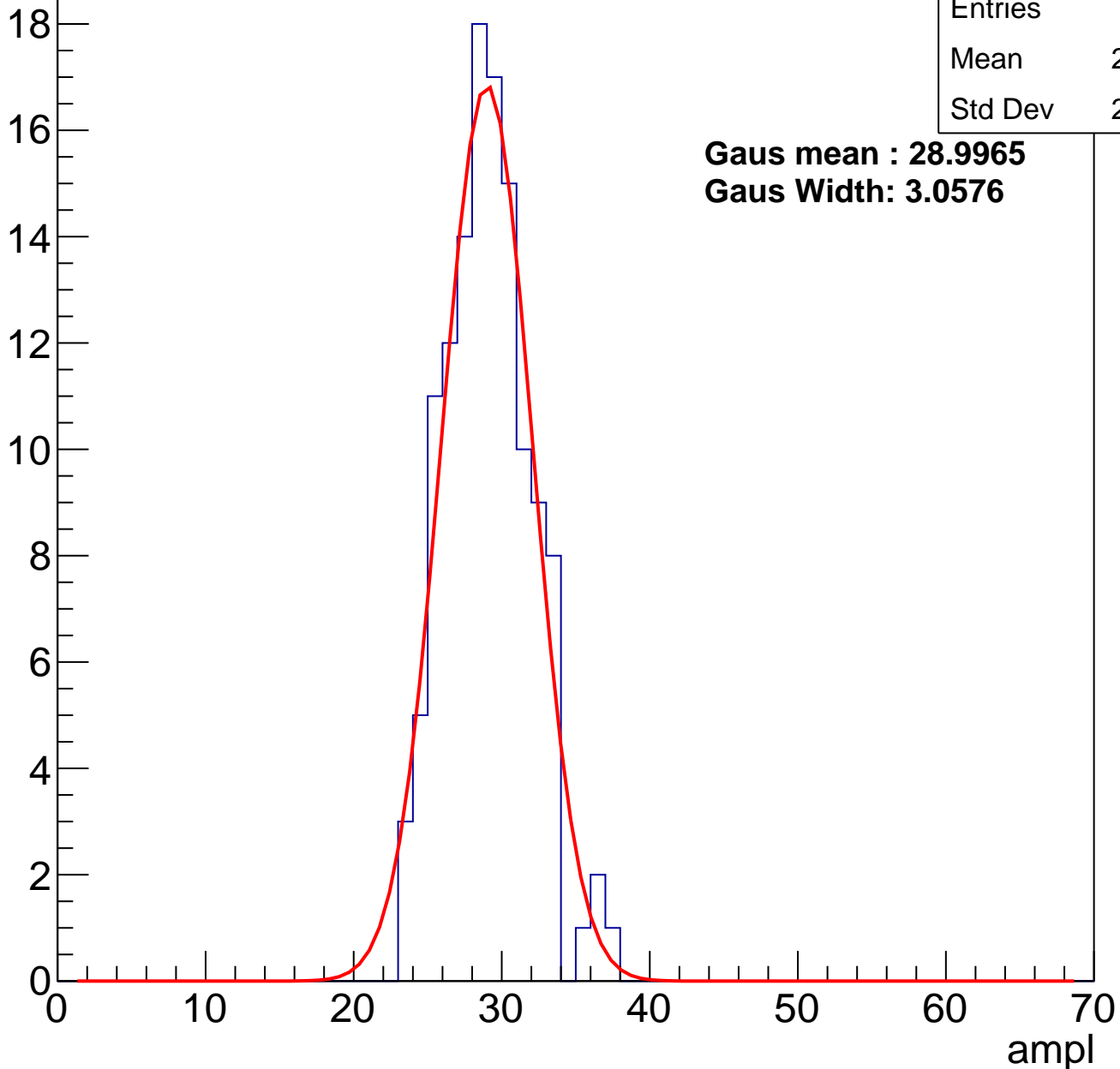


B1L001S, U22-ch0, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch0, adc1

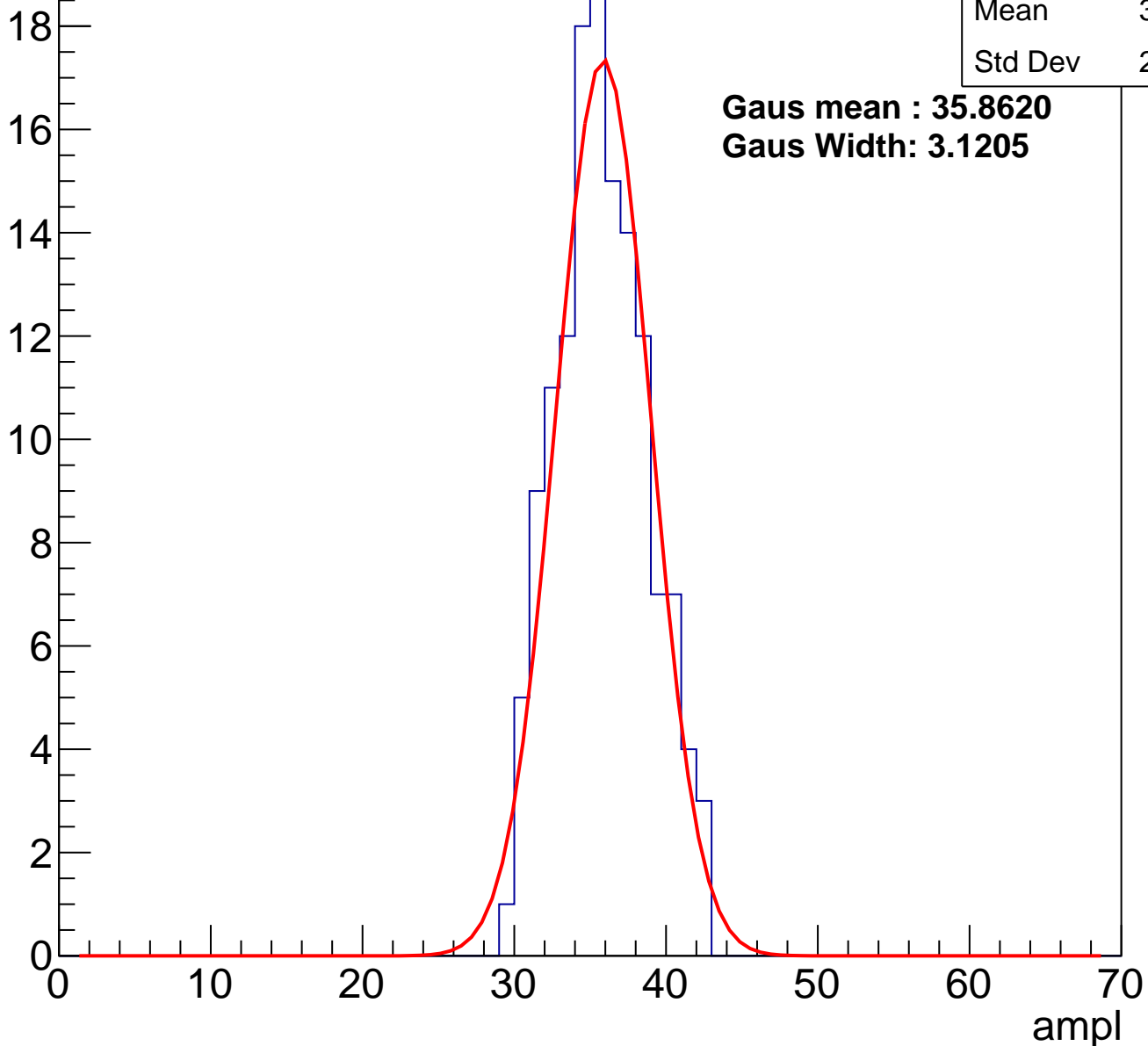
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.33
Std Dev	2.975

Gaus mean : 35.8620

Gaus Width: 3.1205

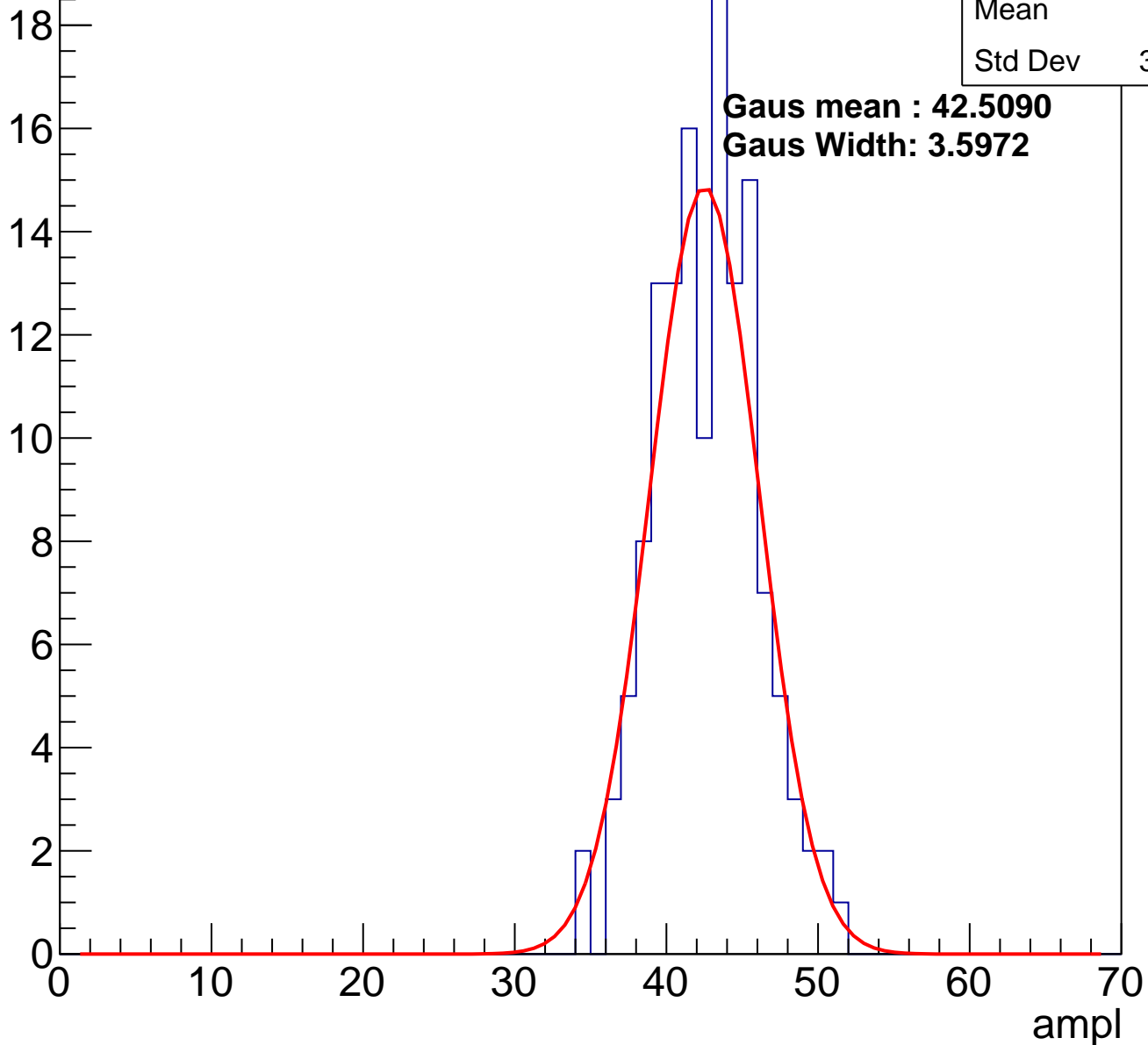
Entry



B1L001S, U22-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch0, adc3

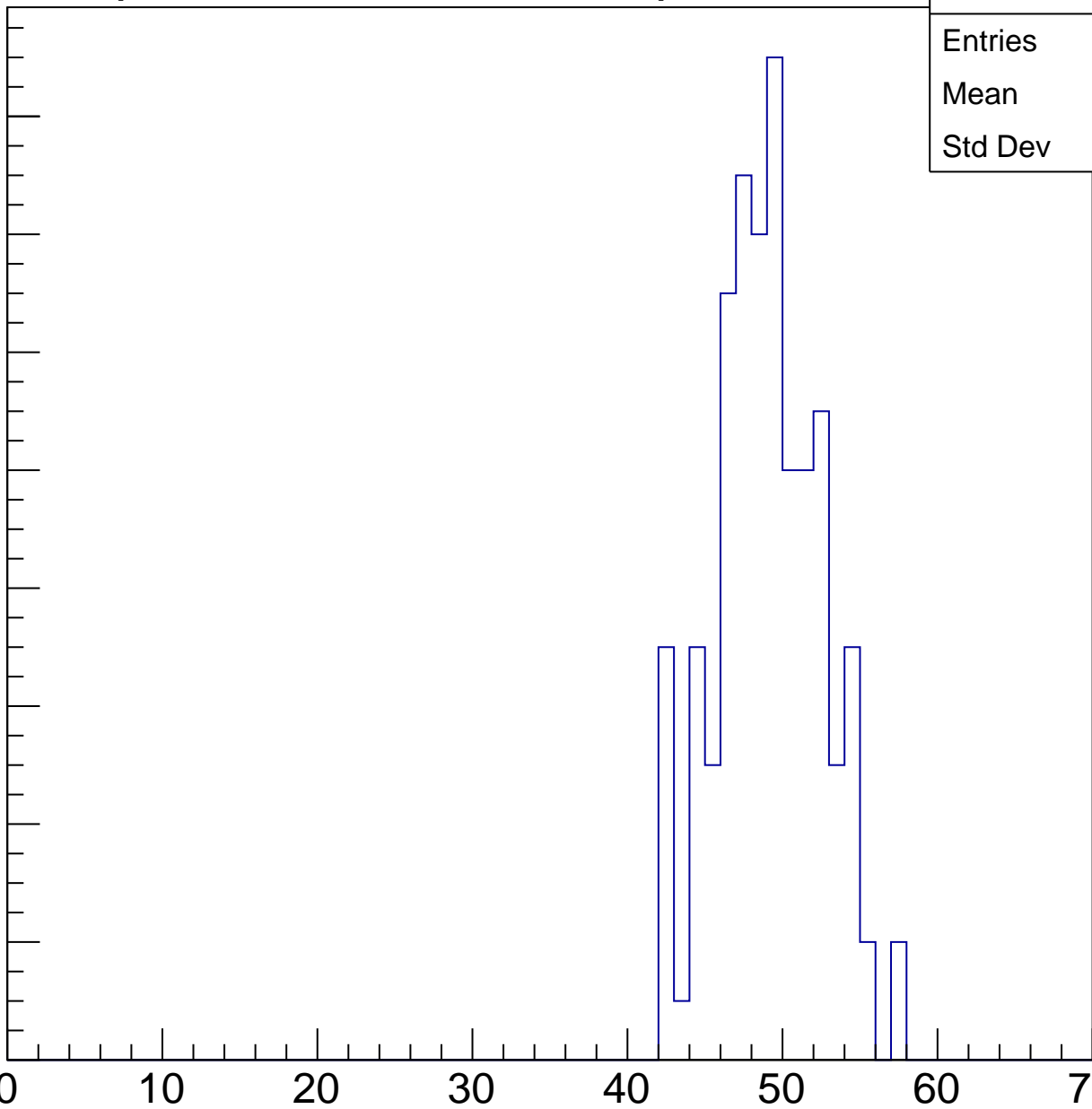
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	48.63
Std Dev	3.347

ampl



B1L001S, U22-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	55.13
Std Dev	3.393

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

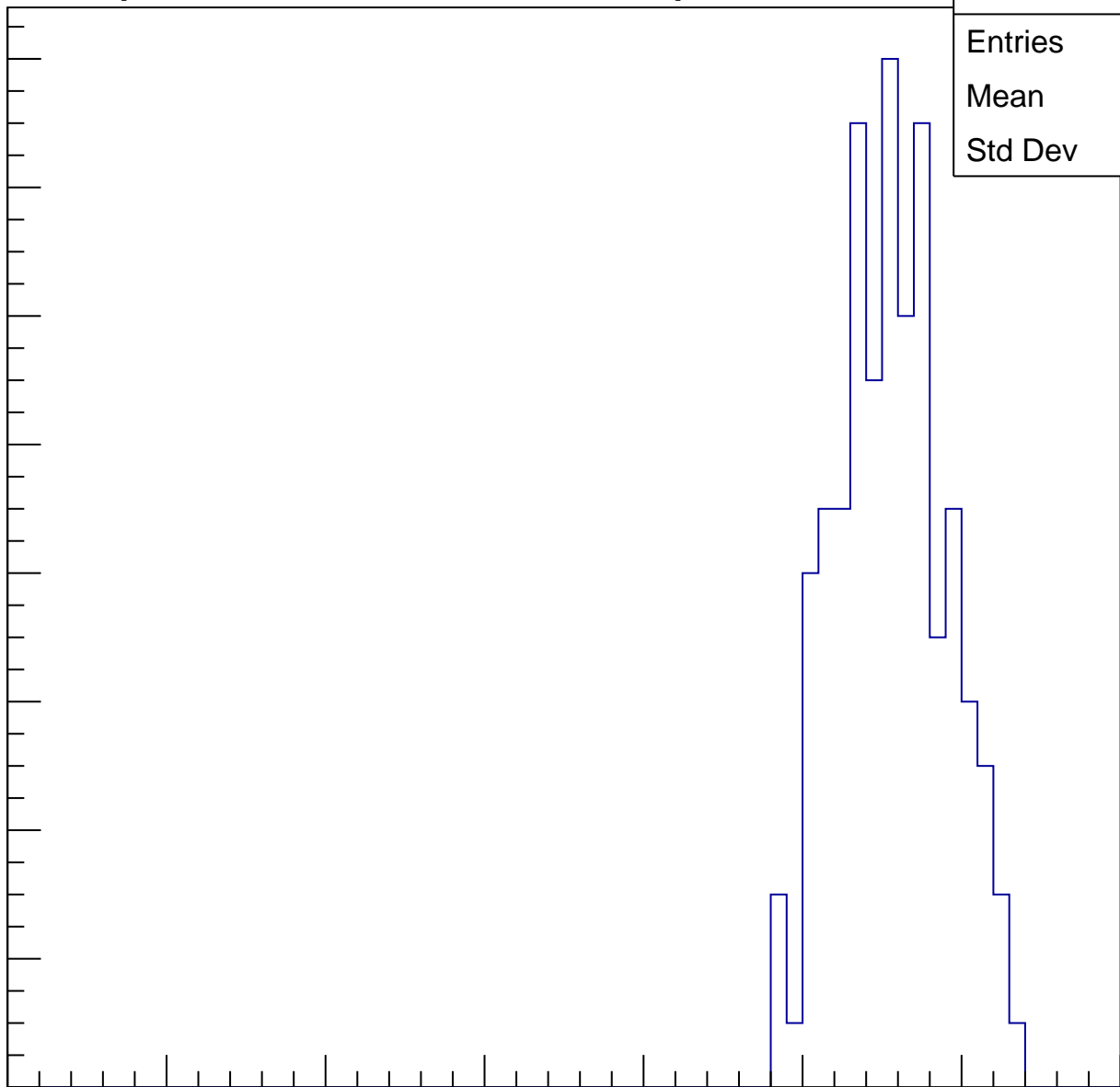
40

50

60

70

ampl



B1L001S, U22-ch0, adc5

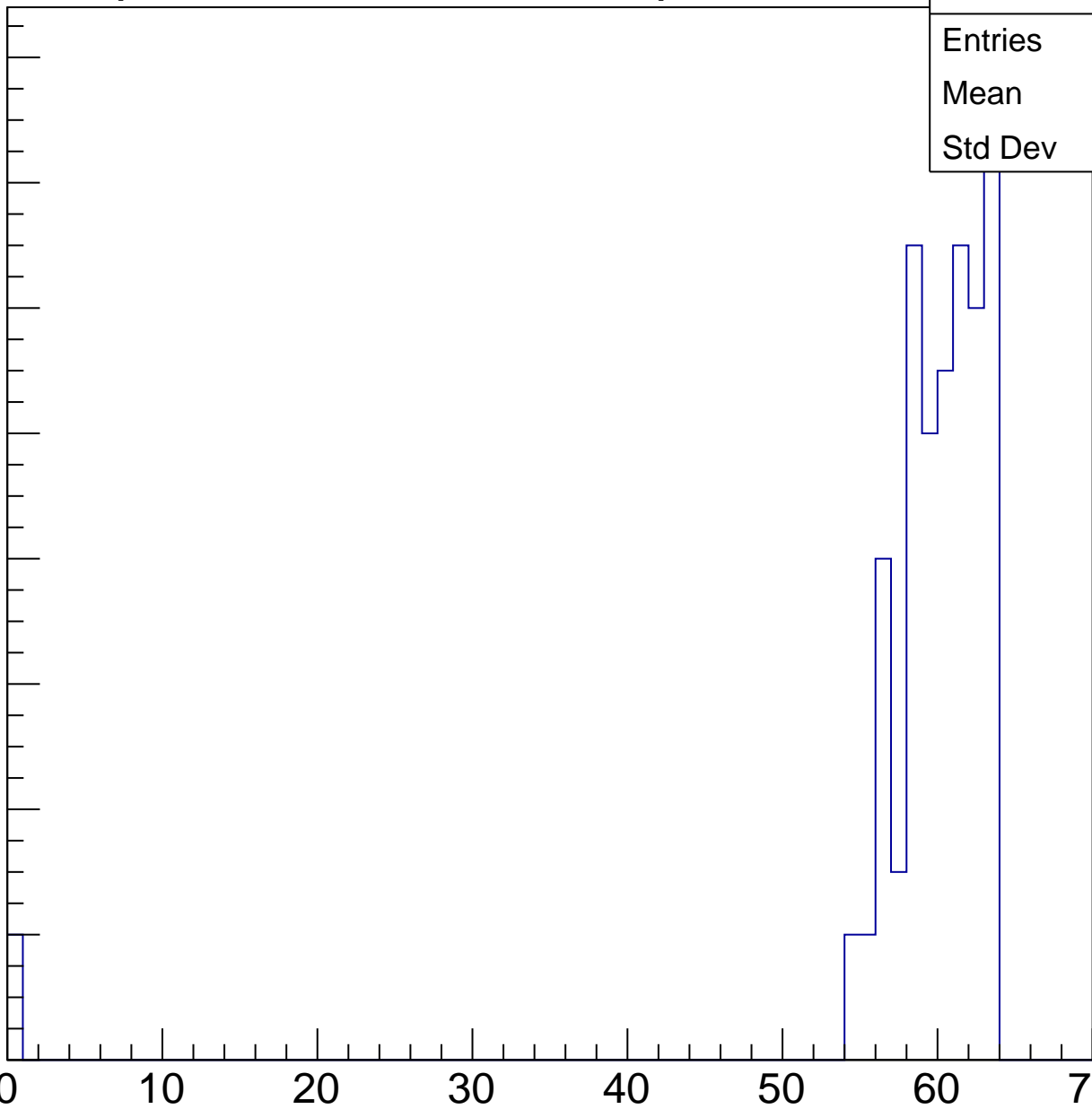
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.54
Std Dev	9.058

ampl

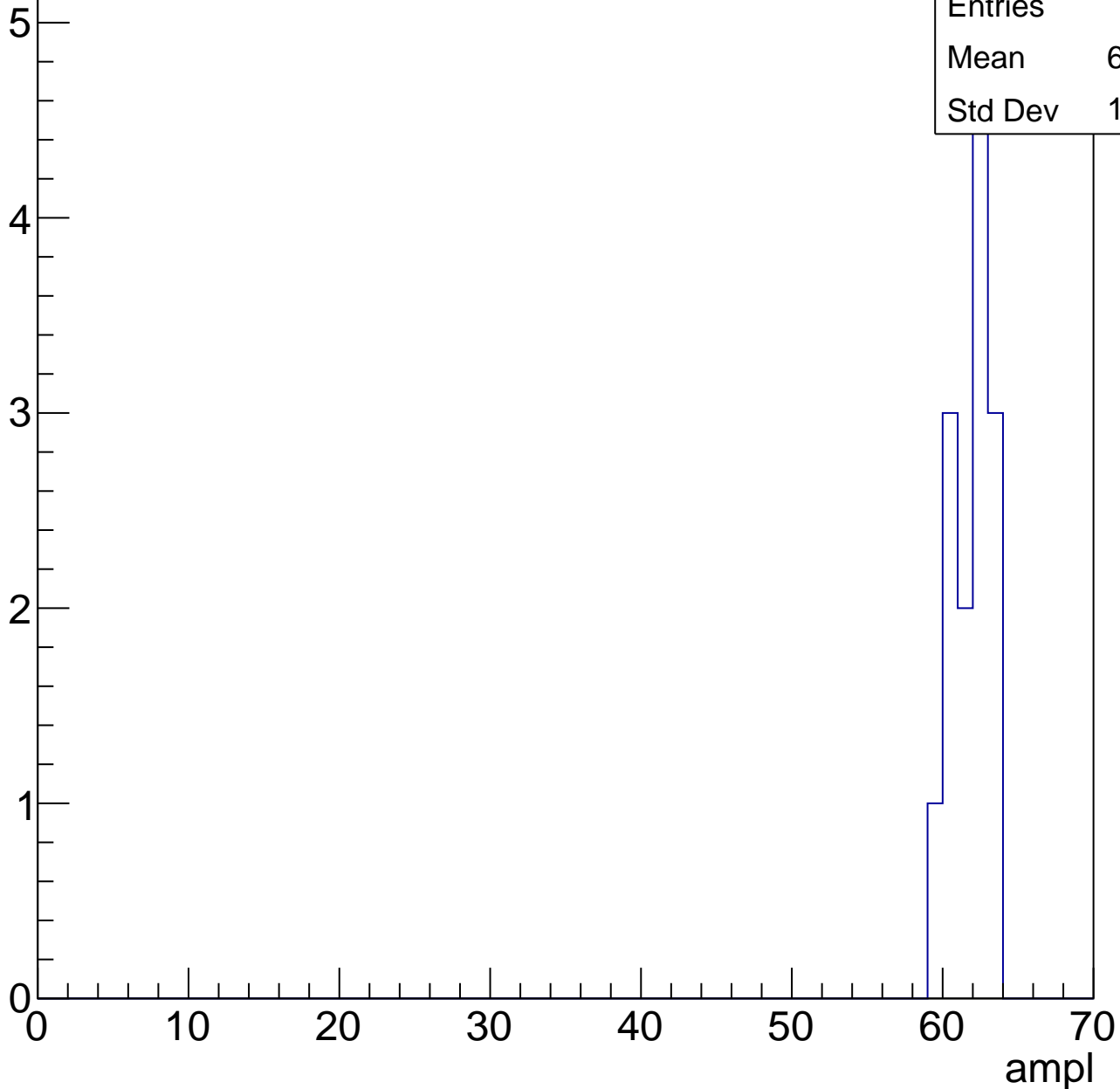


B1L001S, U22-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	61.43
Std Dev	1.237



B1L001S, U22-ch0, adc7

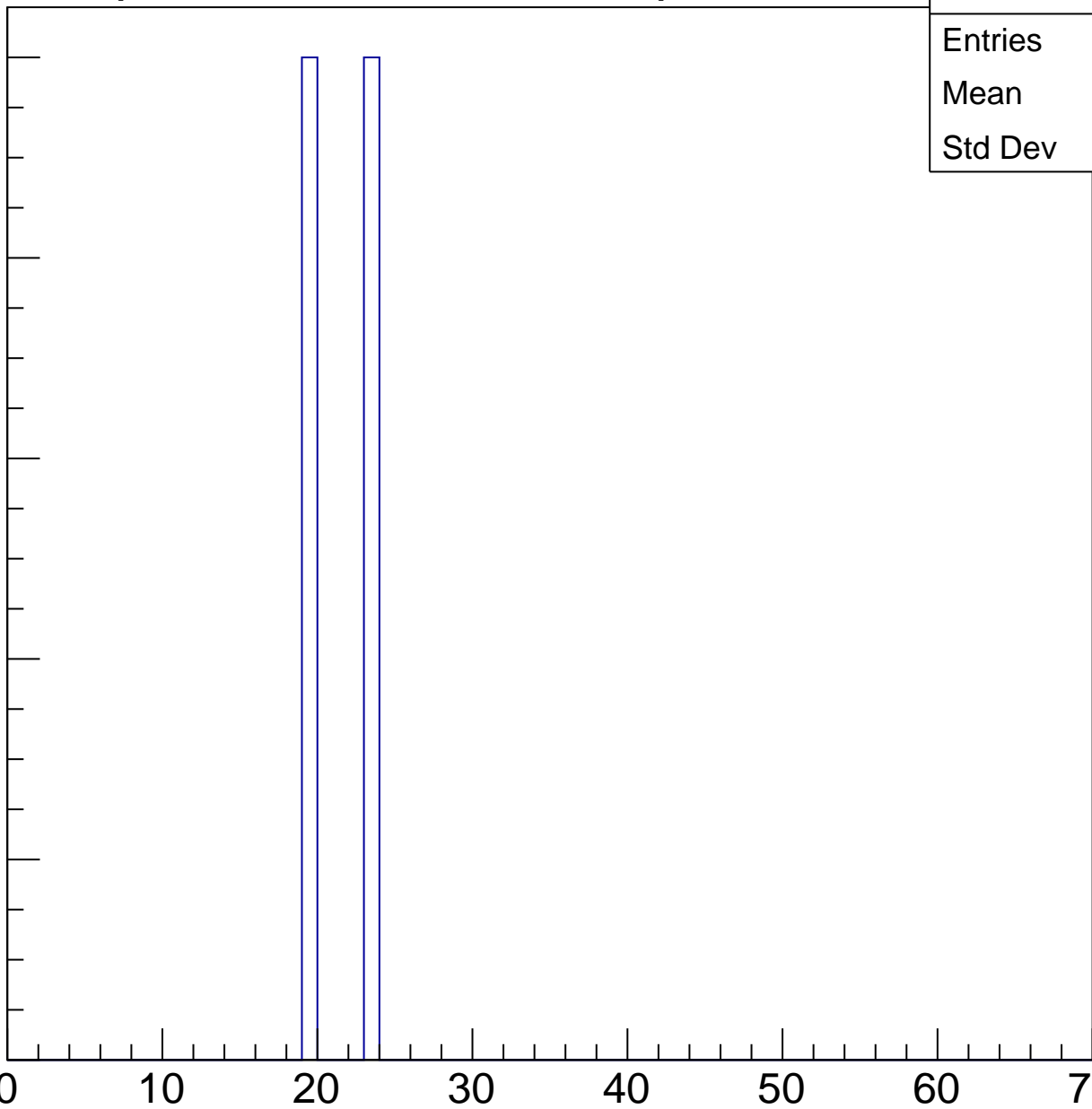
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	21
Std Dev	2

ampl



B1L001S, U22-ch1, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	31.31
Std Dev	2.908

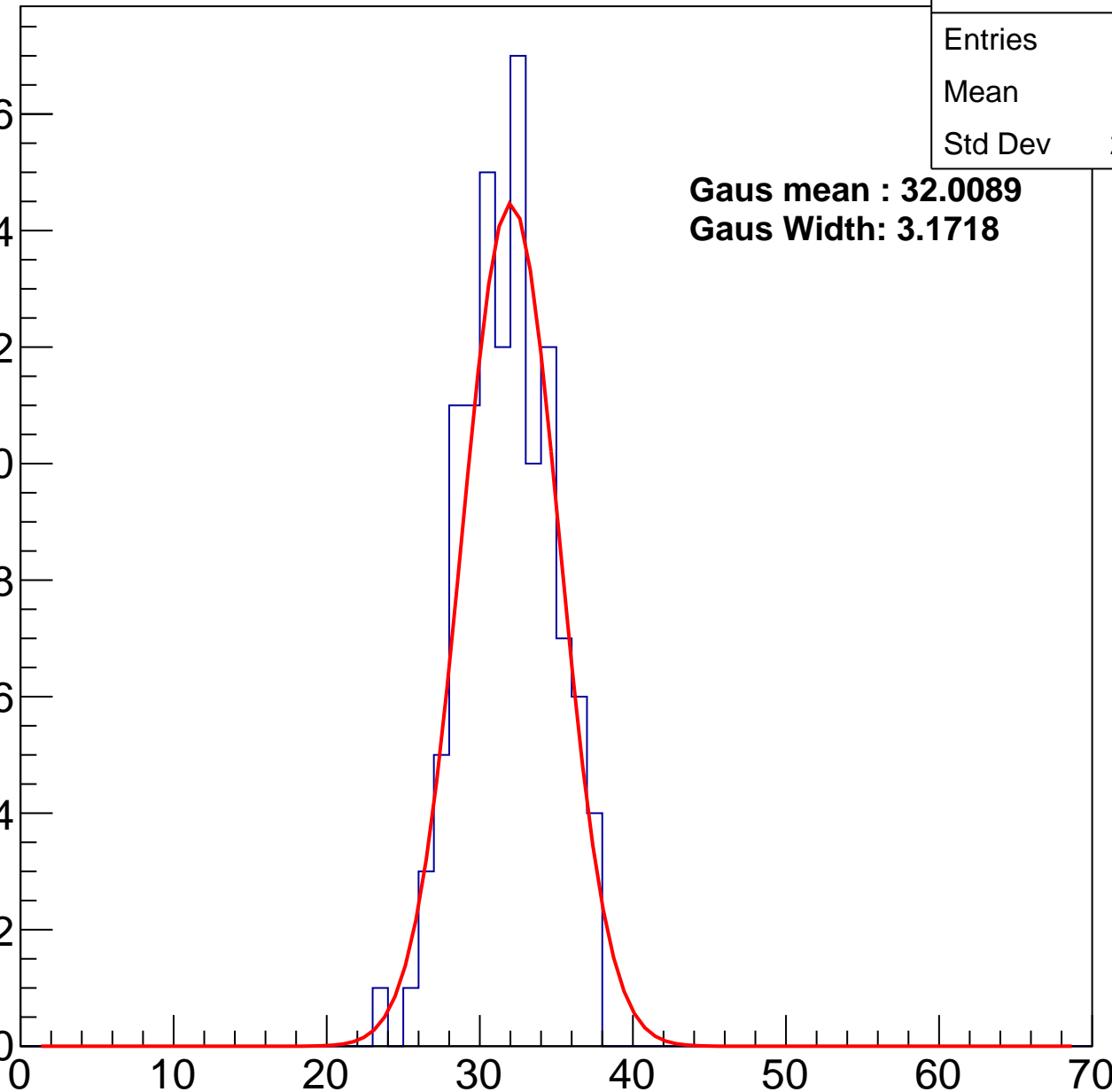
Gaus mean : 32.0089

Gaus Width: 3.1718

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

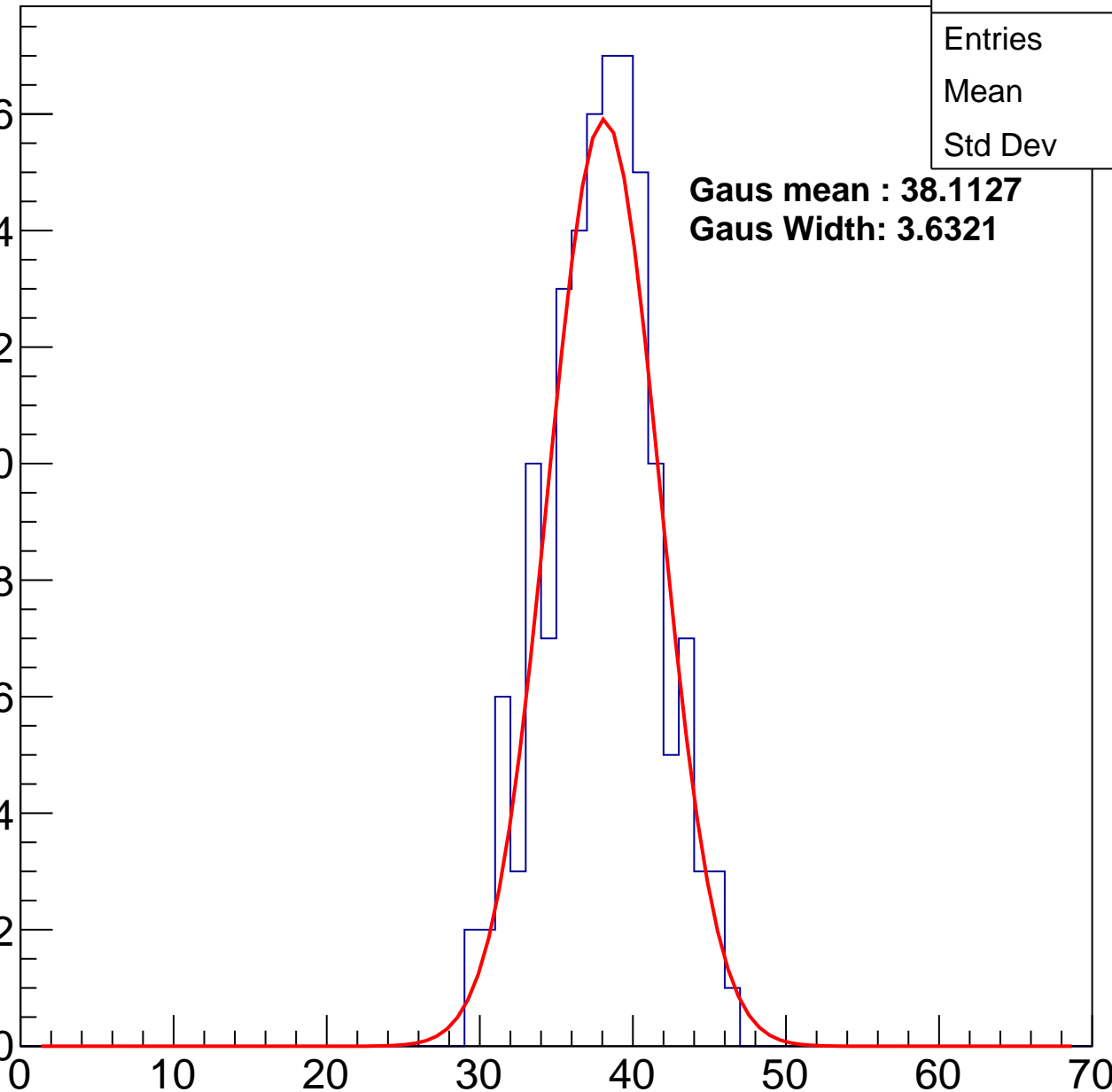
Entries	151
Mean	37.5
Std Dev	3.59

Gaus mean : 38.1127
Gaus Width: 3.6321

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

44.4

Std Dev

3.071

Gaus mean : 44.6933

Gaus Width: 3.1648

0

10

20

30

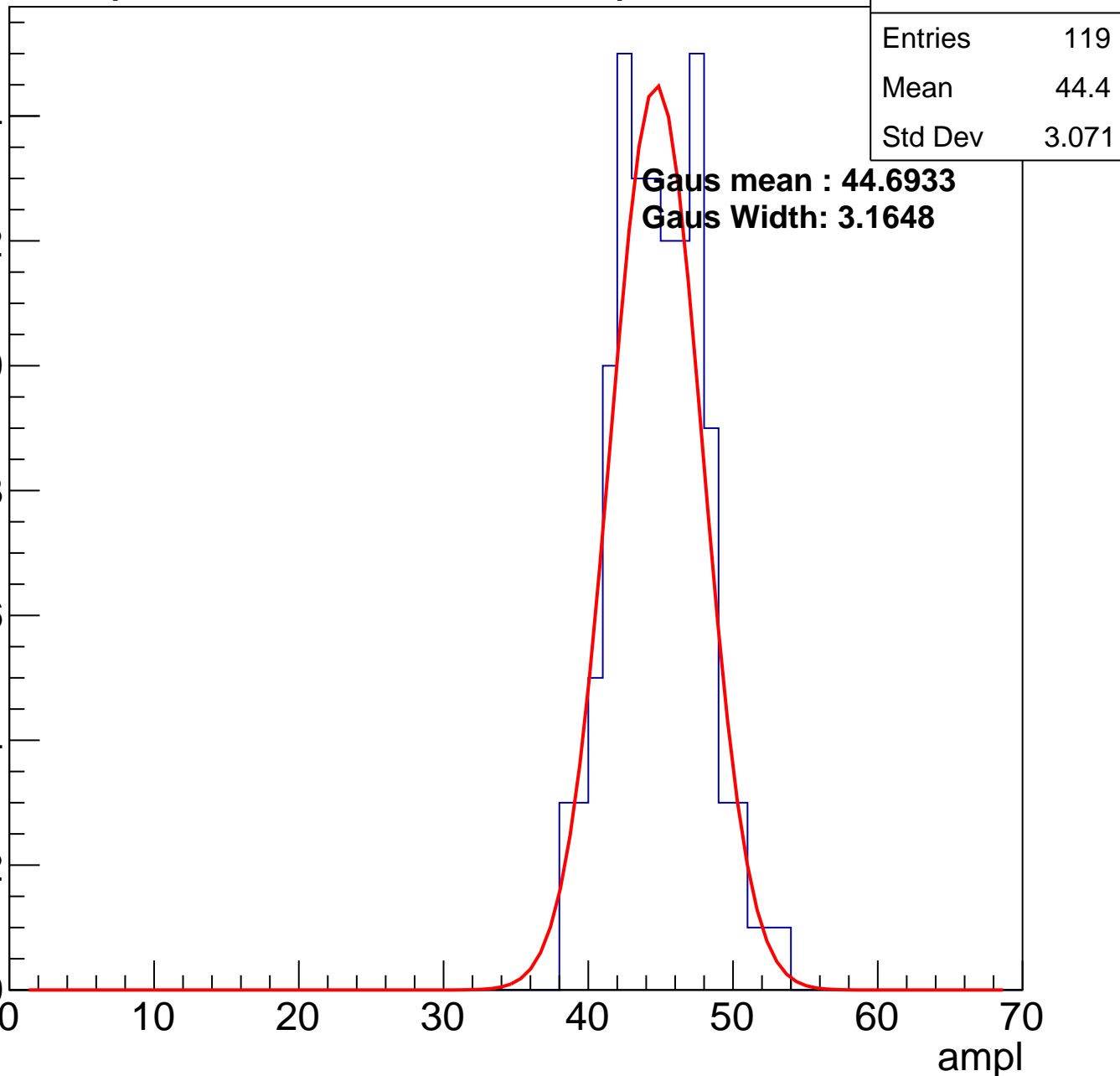
40

50

60

70

ampl

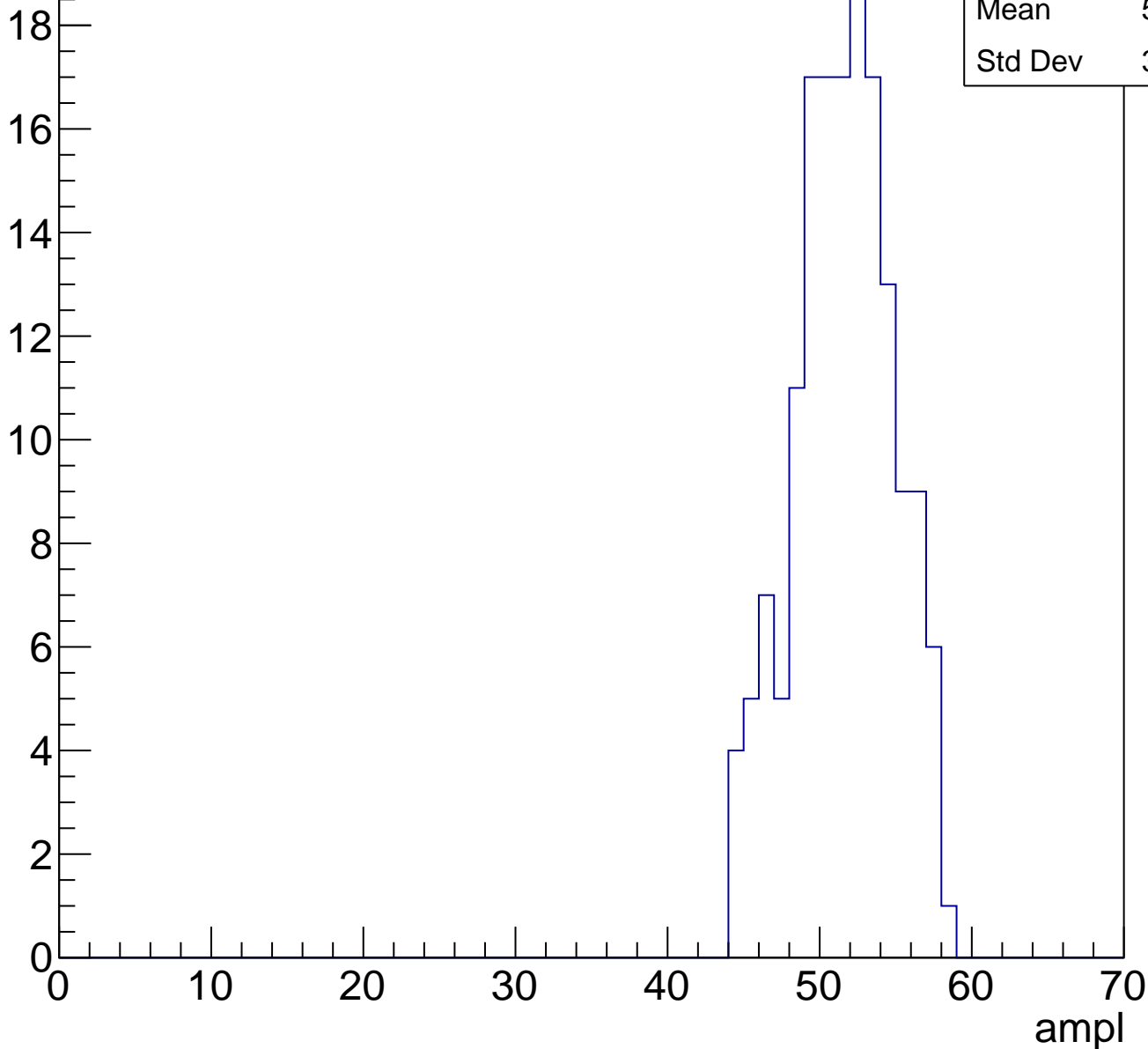


B1L001S, U22-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	51.12
Std Dev	3.243

Entry



B1L001S, U22-ch1, adc4

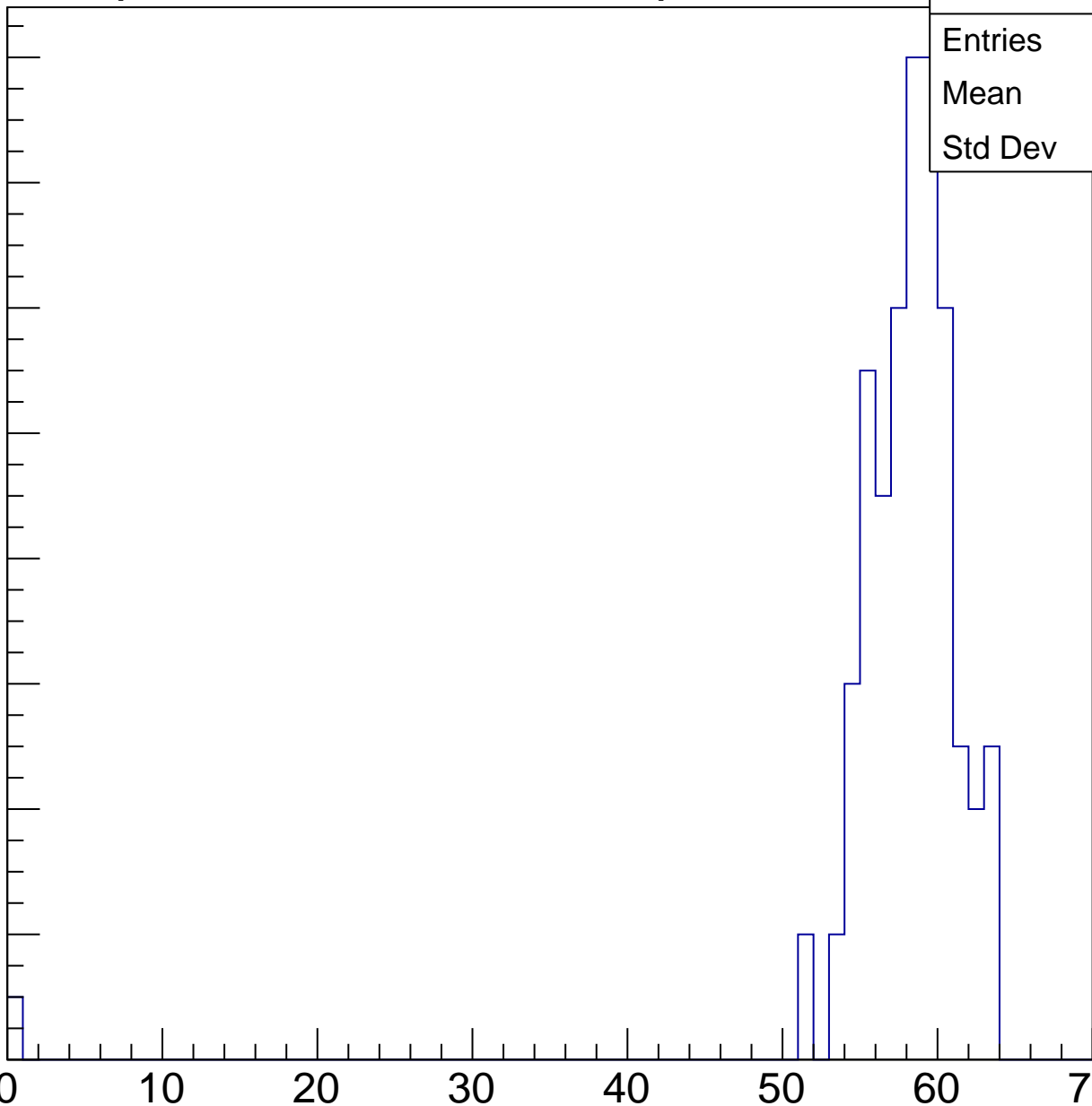
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	57.28
Std Dev	6.293

ampl



B1L001S, U22-ch1, adc5

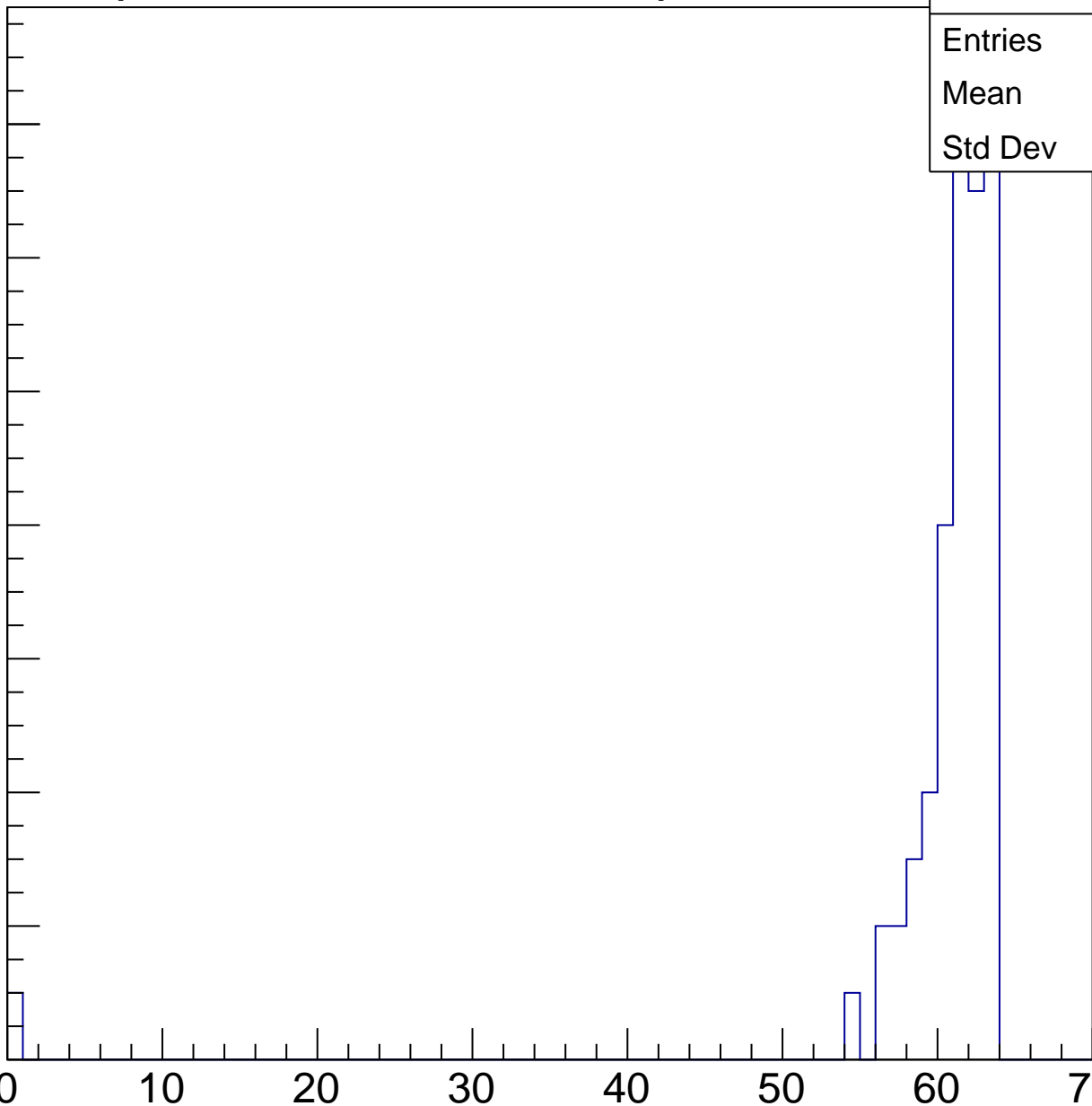
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	63
Mean	59.89
Std Dev	7.862

ampl



B1L001S, U22-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

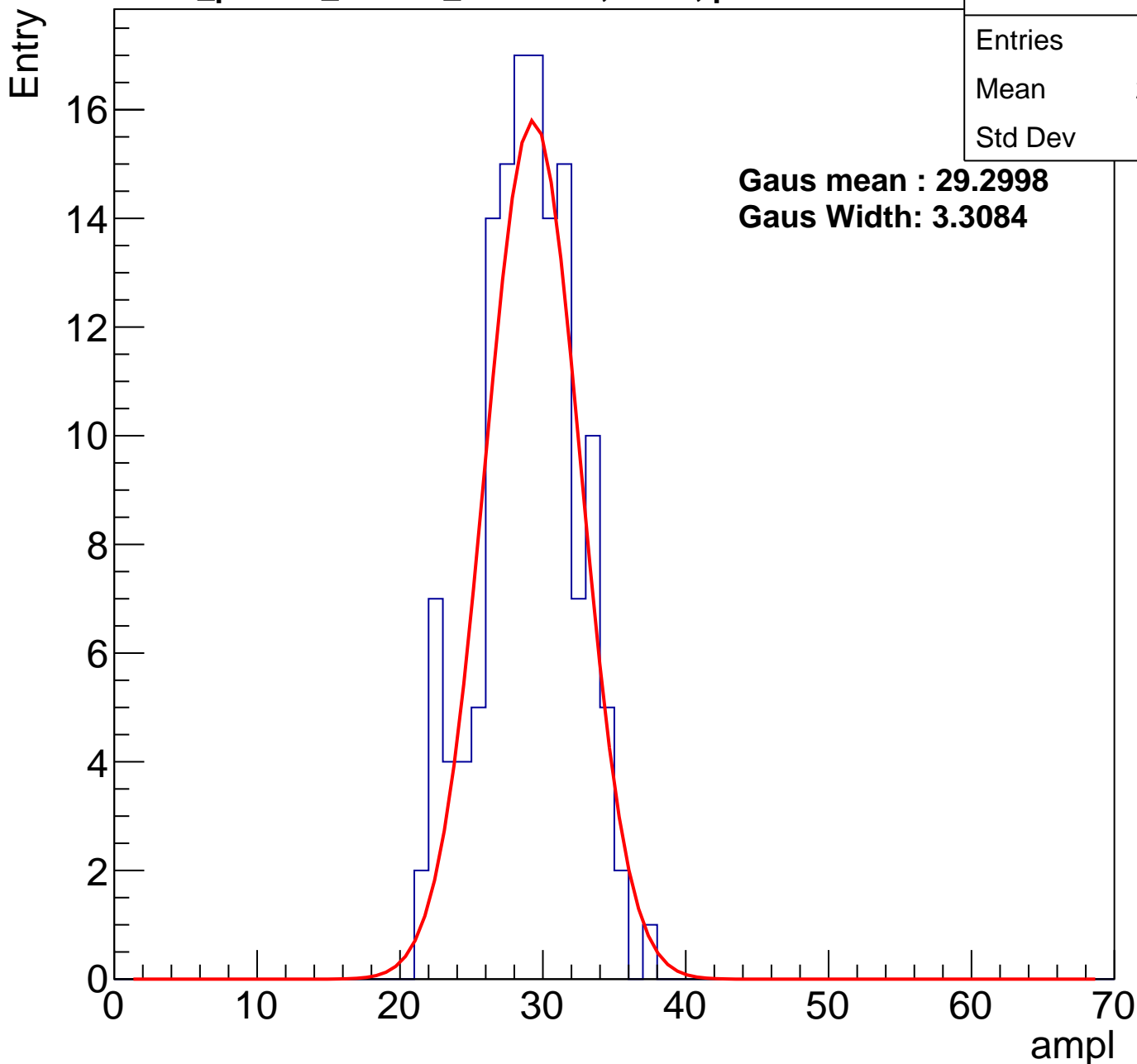
B1L001S, U22-ch2, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	28.51
Std Dev	3.31

Gaus mean : 29.2998

Gaus Width: 3.3084



B1L001S, U22-ch2, adc1

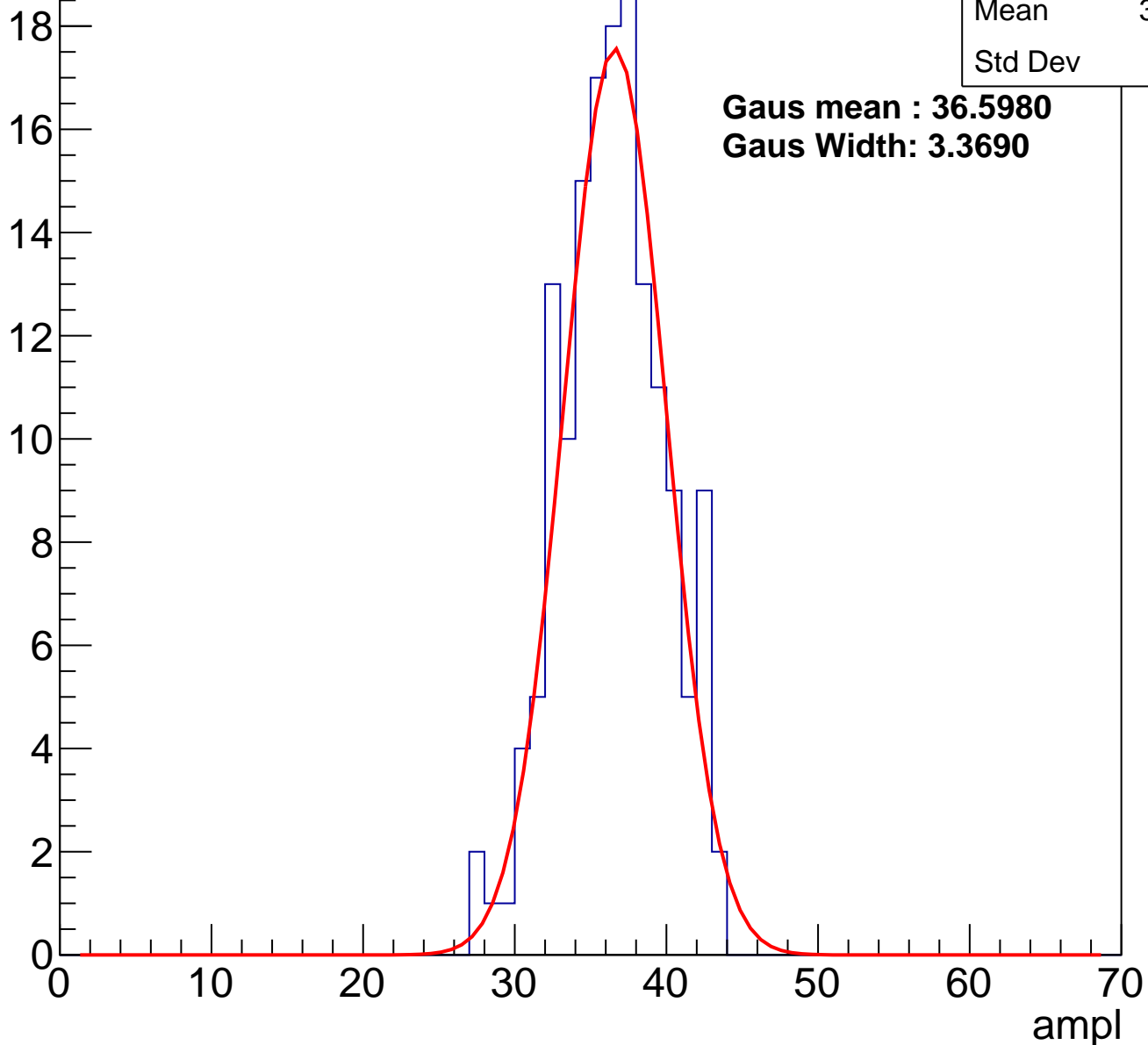
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	35.97
Std Dev	3.4

Gaus mean : 36.5980

Gaus Width: 3.3690

Entry



B1L001S, U22-ch2, adc2

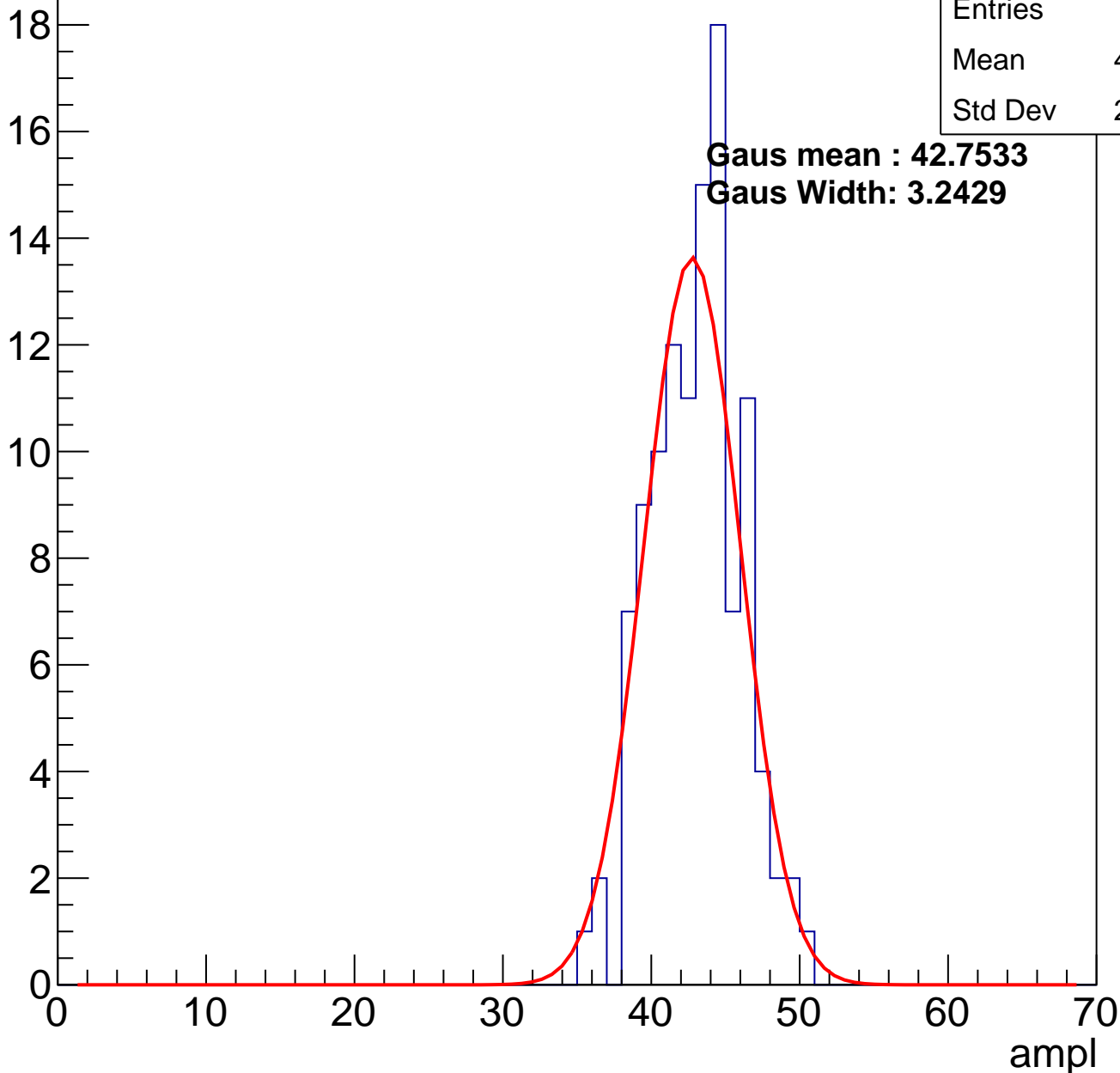
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	42.57
Std Dev	2.972

Gaus mean : 42.7533

Gaus Width: 3.2429

Entry



B1L001S, U22-ch2, adc3

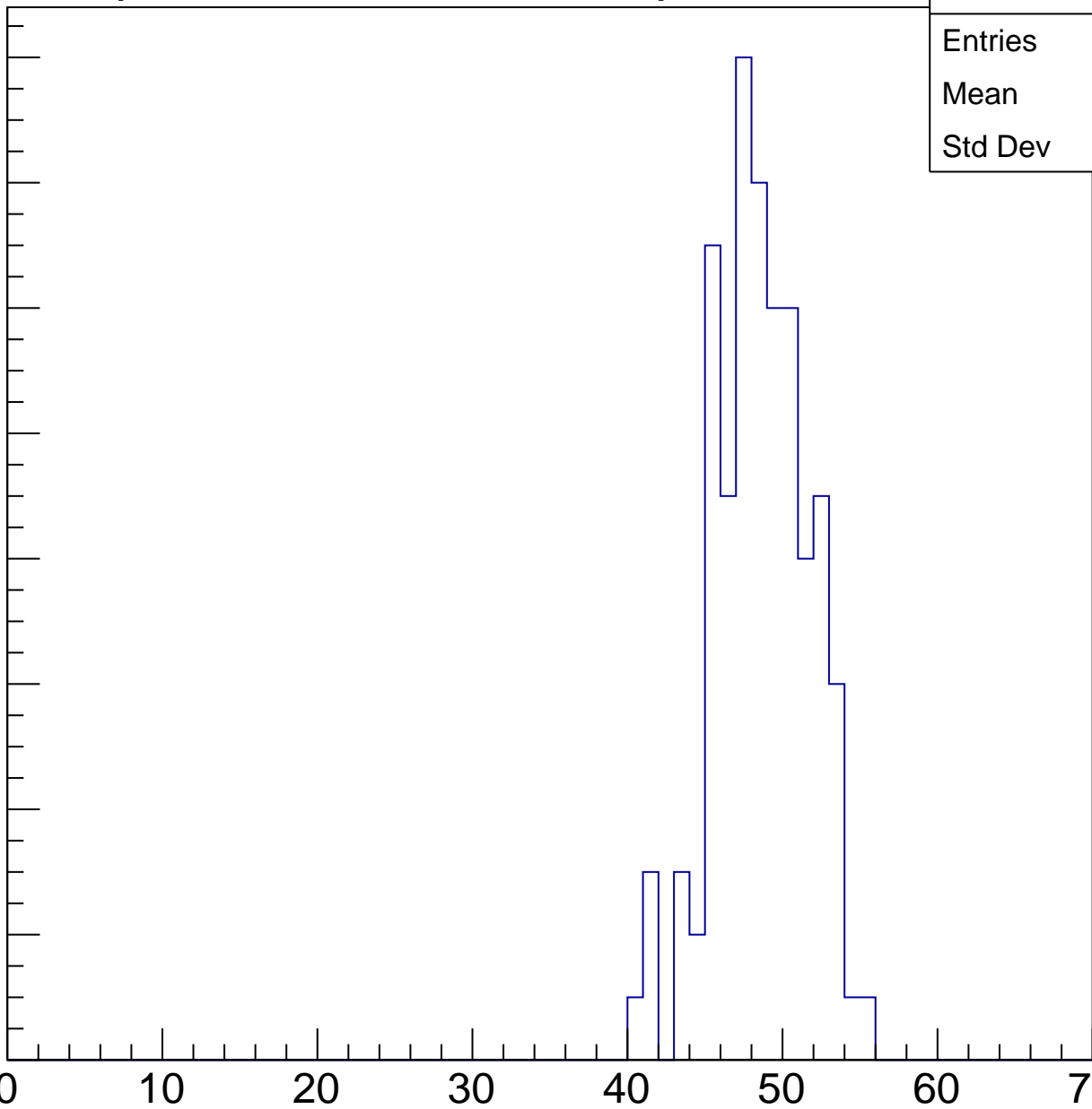
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	48.13
Std Dev	3.009

ampl

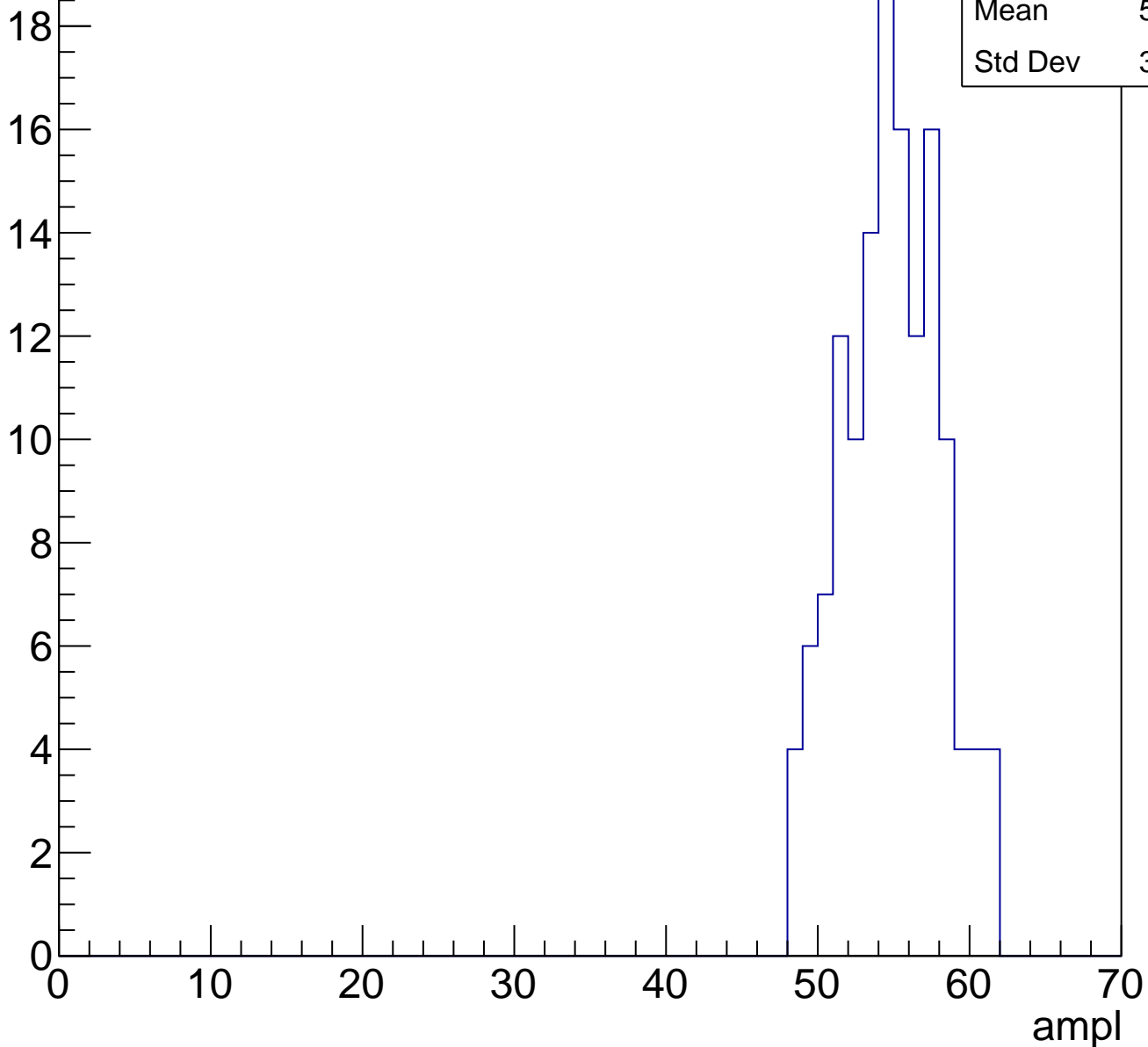


B1L001S, U22-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	54.35
Std Dev	3.138

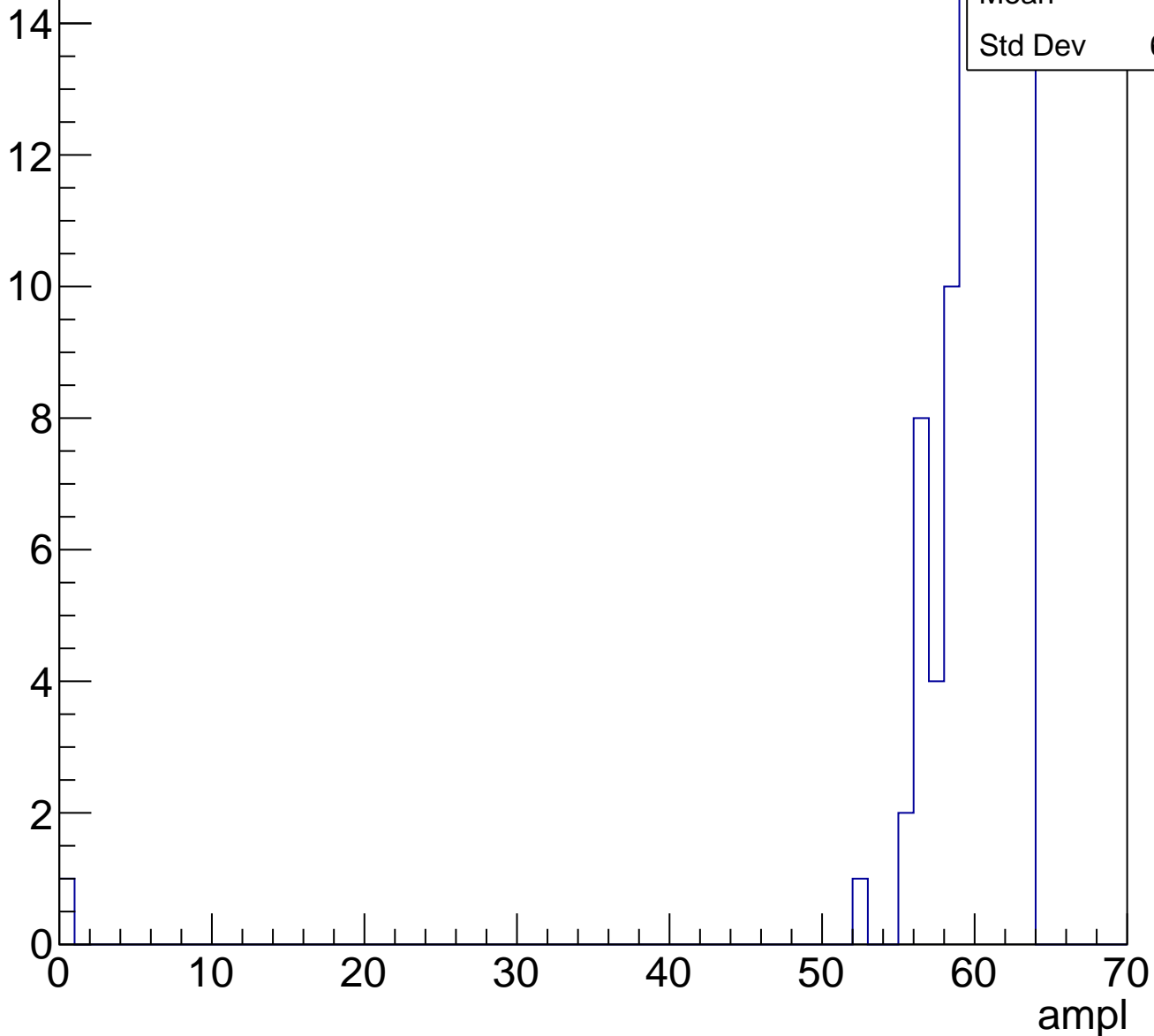
Entry



B1L001S, U22-ch2, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

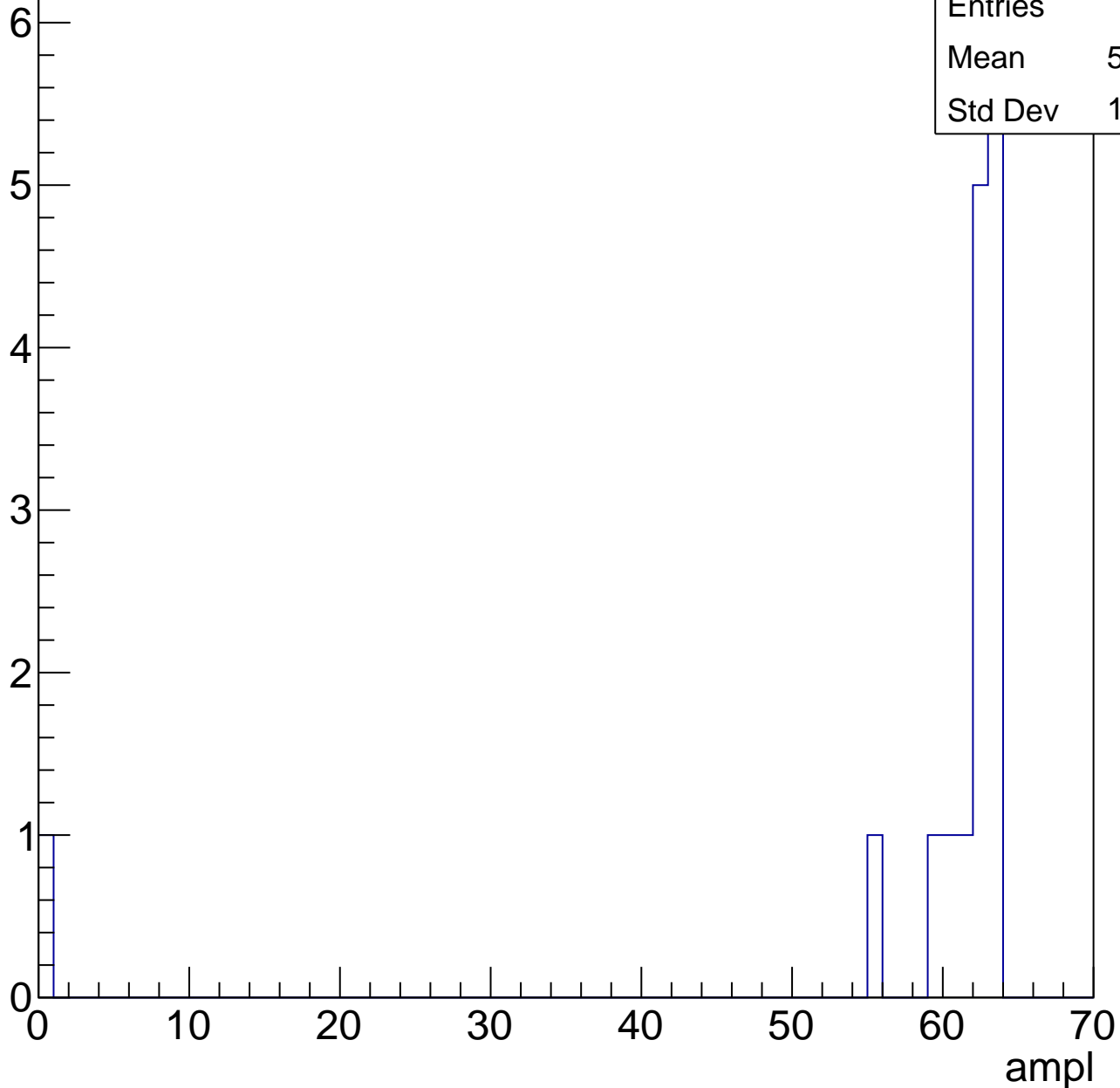


B1L001S, U22-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	57.69
Std Dev	15.03



B1L001S, U22-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch3, adc0

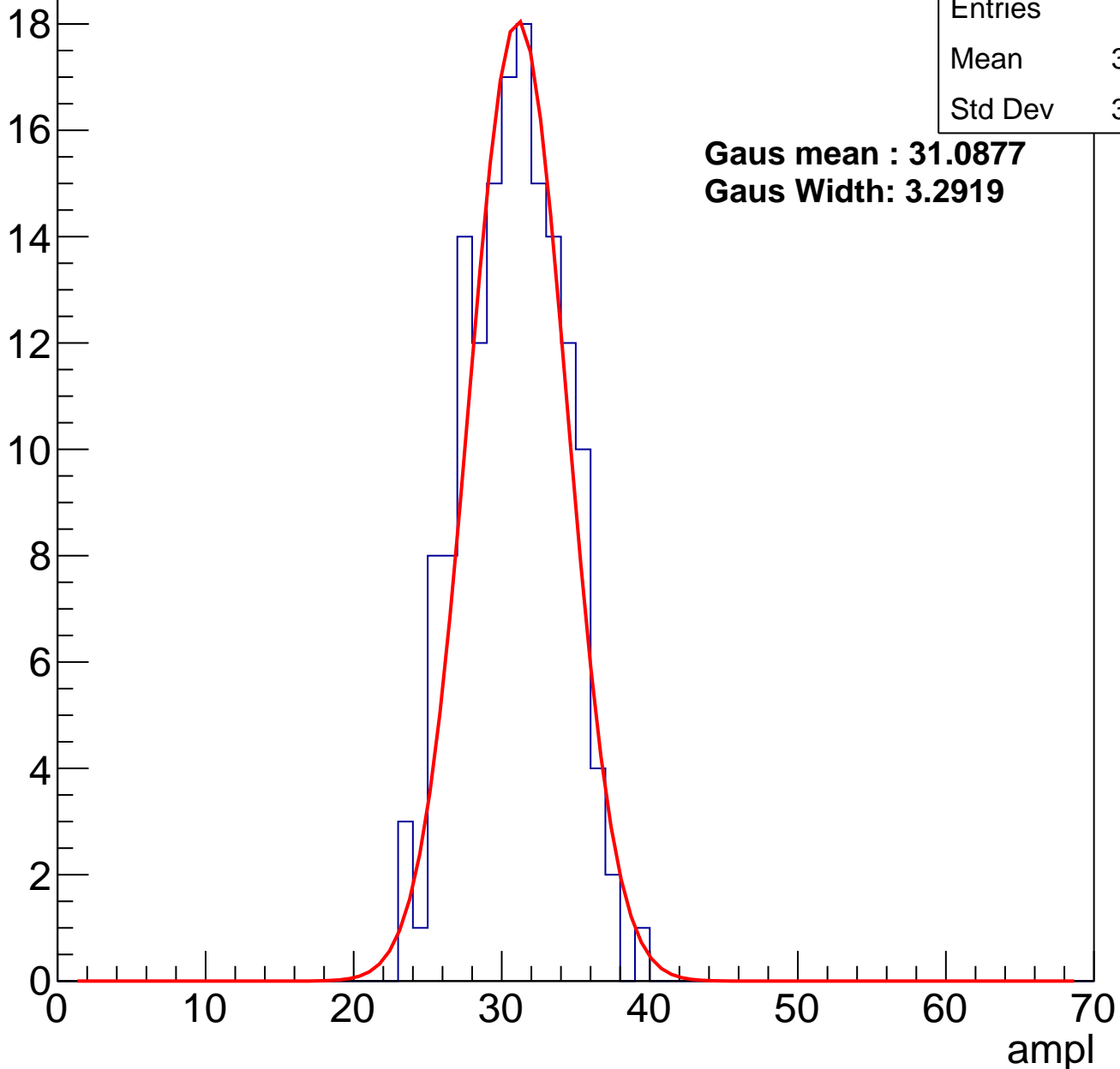
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	30.36
Std Dev	3.273

Gaus mean : 31.0877

Gaus Width: 3.2919

Entry



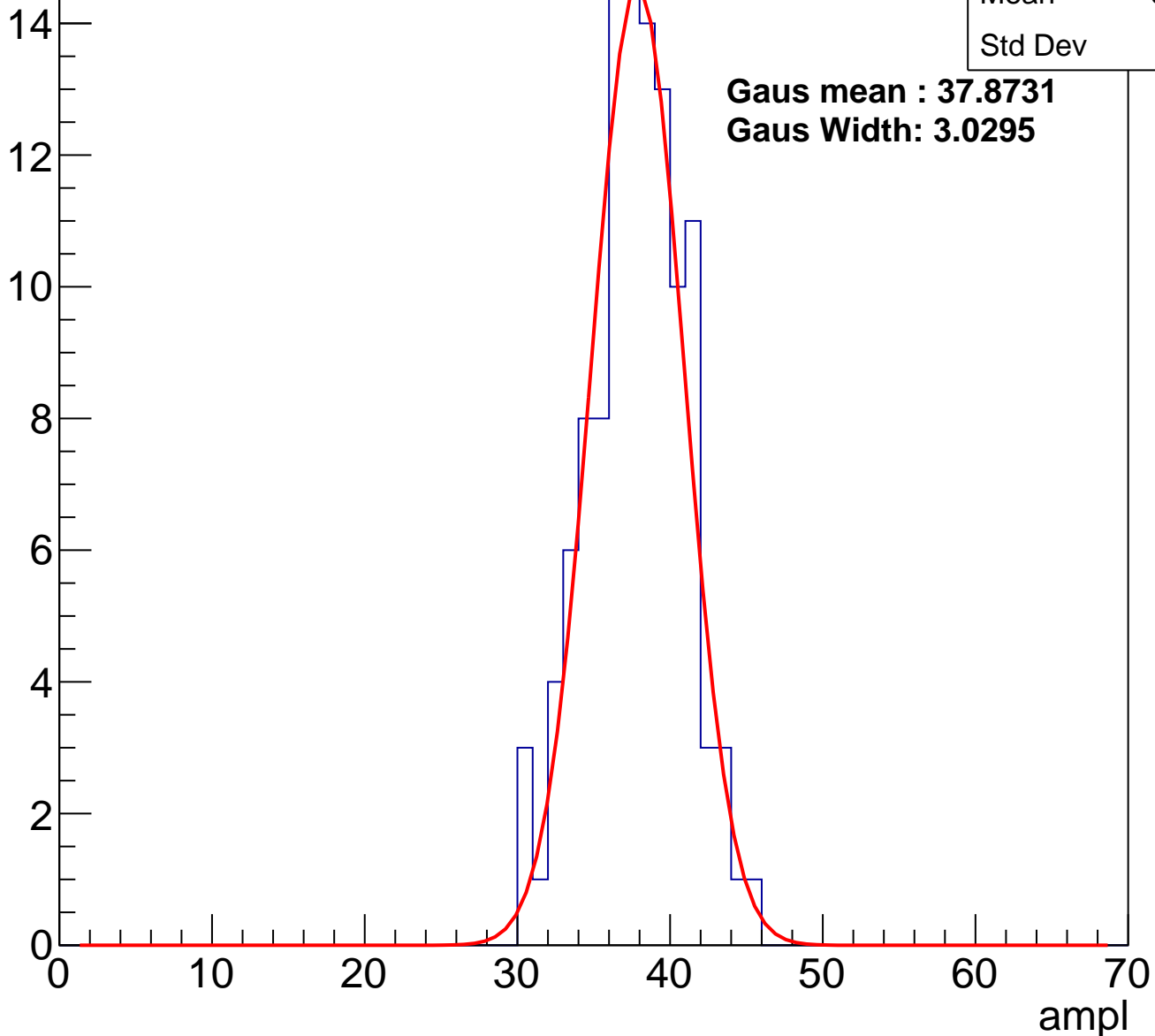
B1L001S, U22-ch3, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	37.31
Std Dev	3.1

Gaus mean : 37.8731
Gaus Width: 3.0295

Entry



B1L001S, U22-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

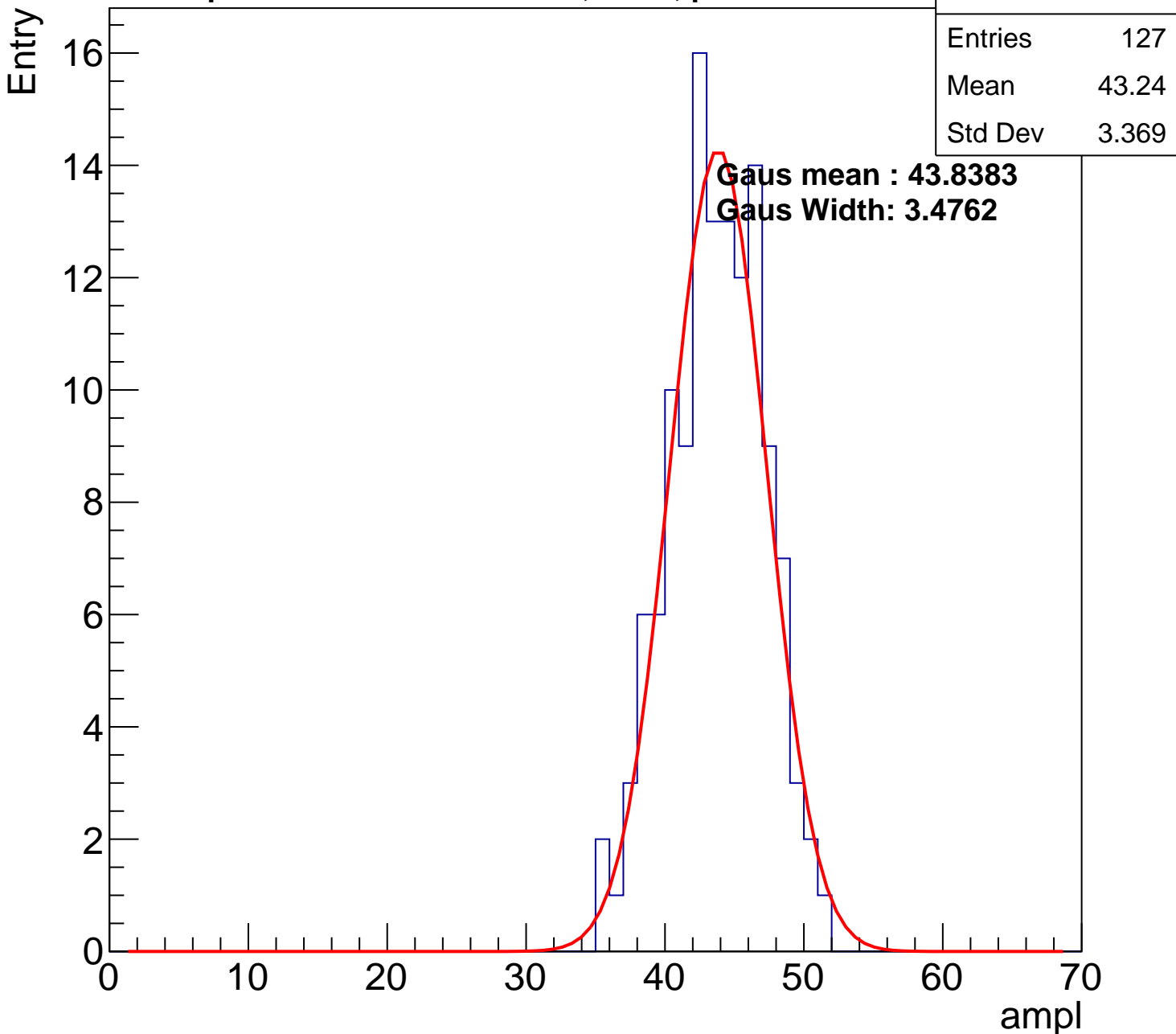
Entries	127
Mean	43.24
Std Dev	3.369

Gaus mean : 43.8383

Gaus Width: 3.4762

ampl

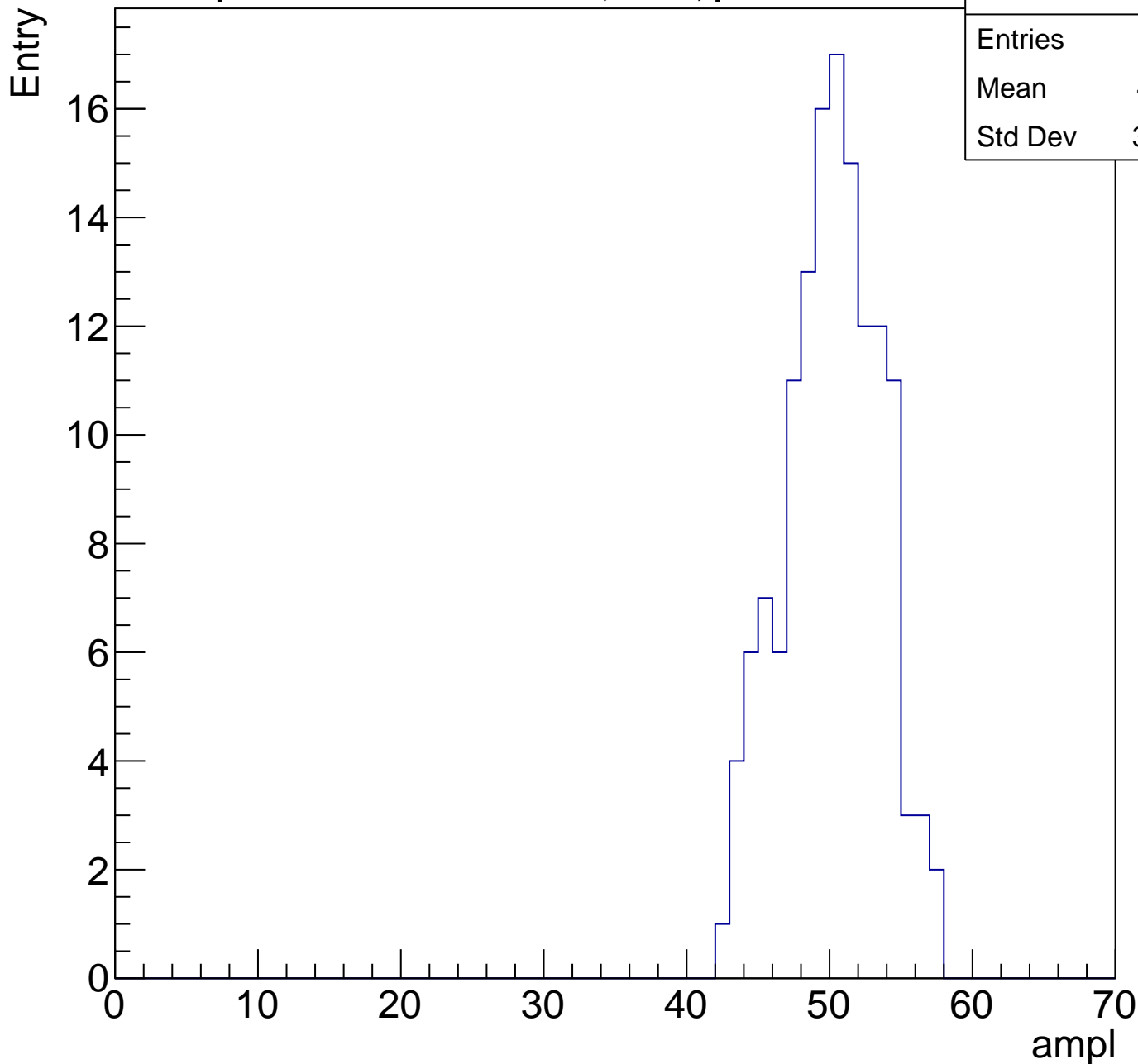
0 10 20 30 40 50 60 70



B1L001S, U22-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	49.71
Std Dev	3.318



B1L001S, U22-ch3, adc4

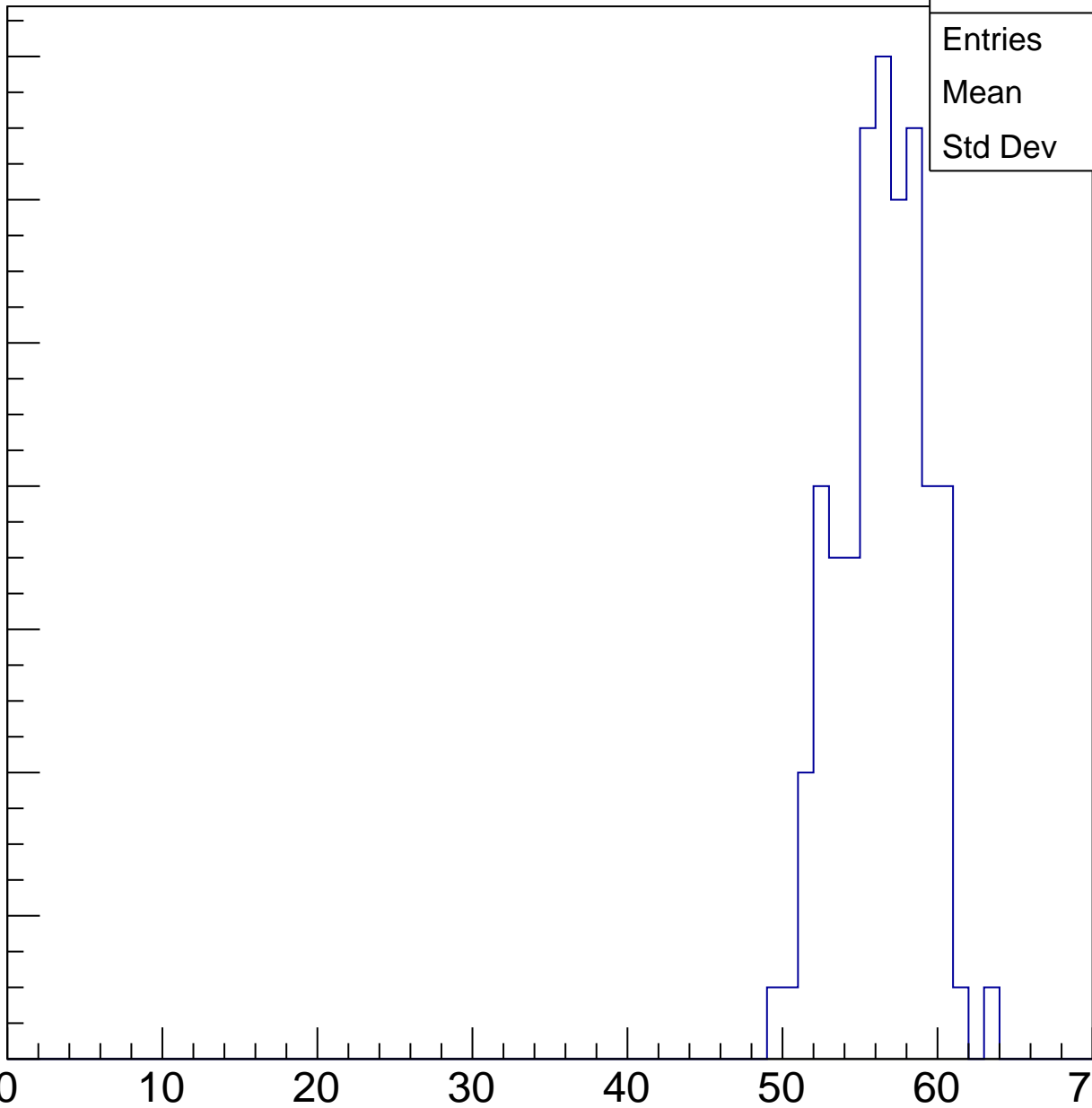
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	55.93
Std Dev	2.775

ampl



B1L001S, U22-ch3, adc5

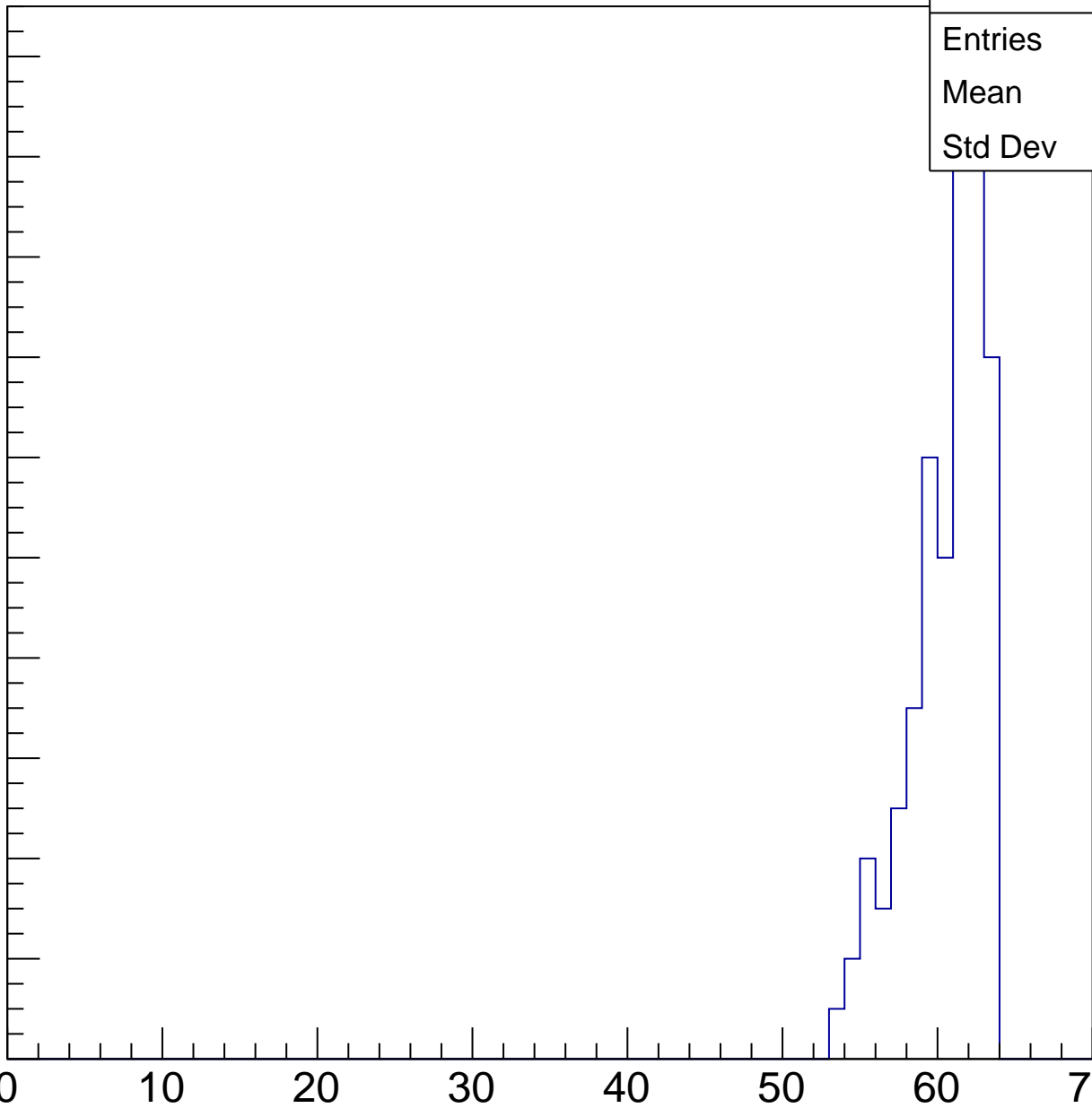
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	97
Mean	60.09
Std Dev	2.441

ampl

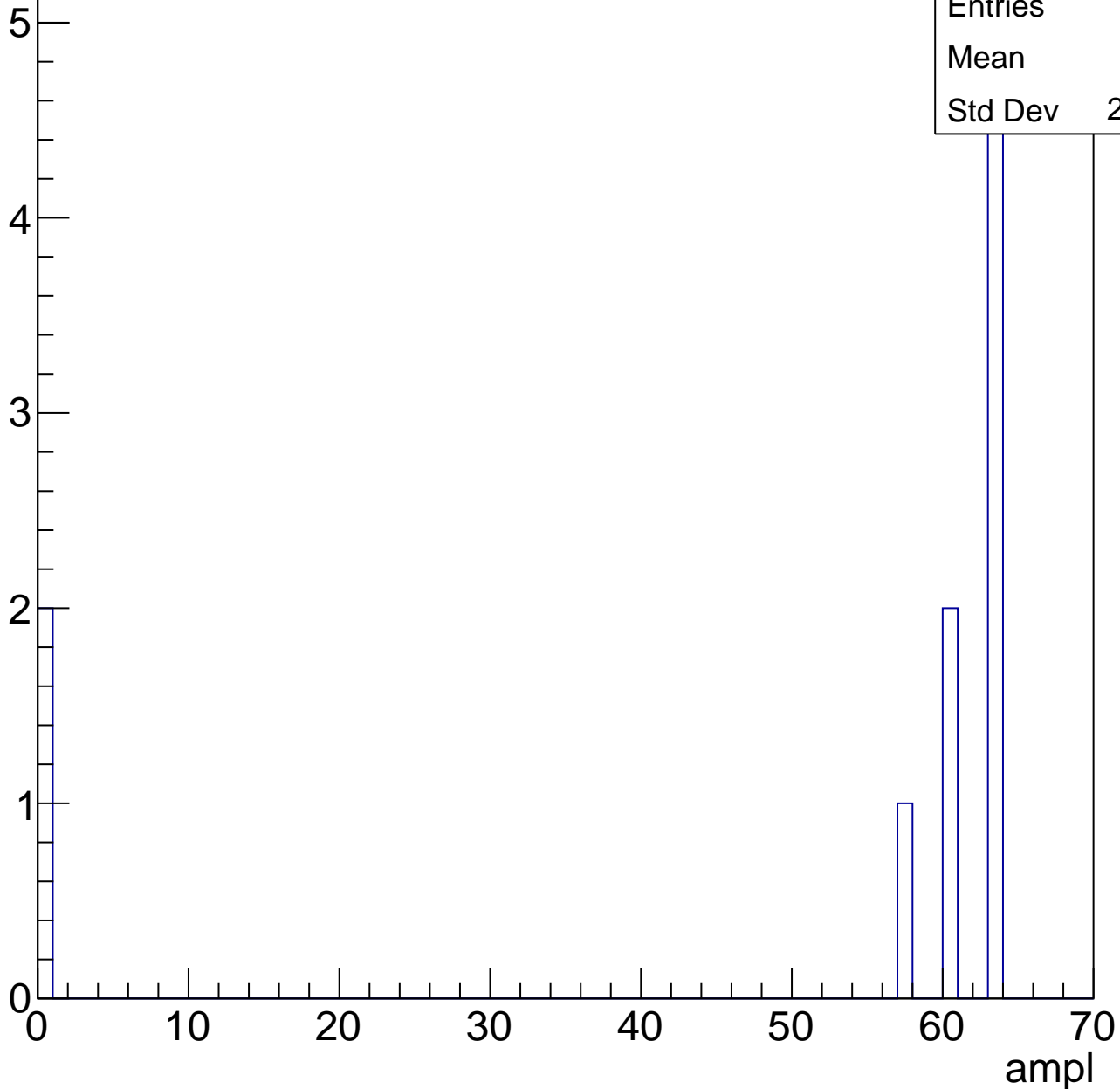


B1L001S, U22-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

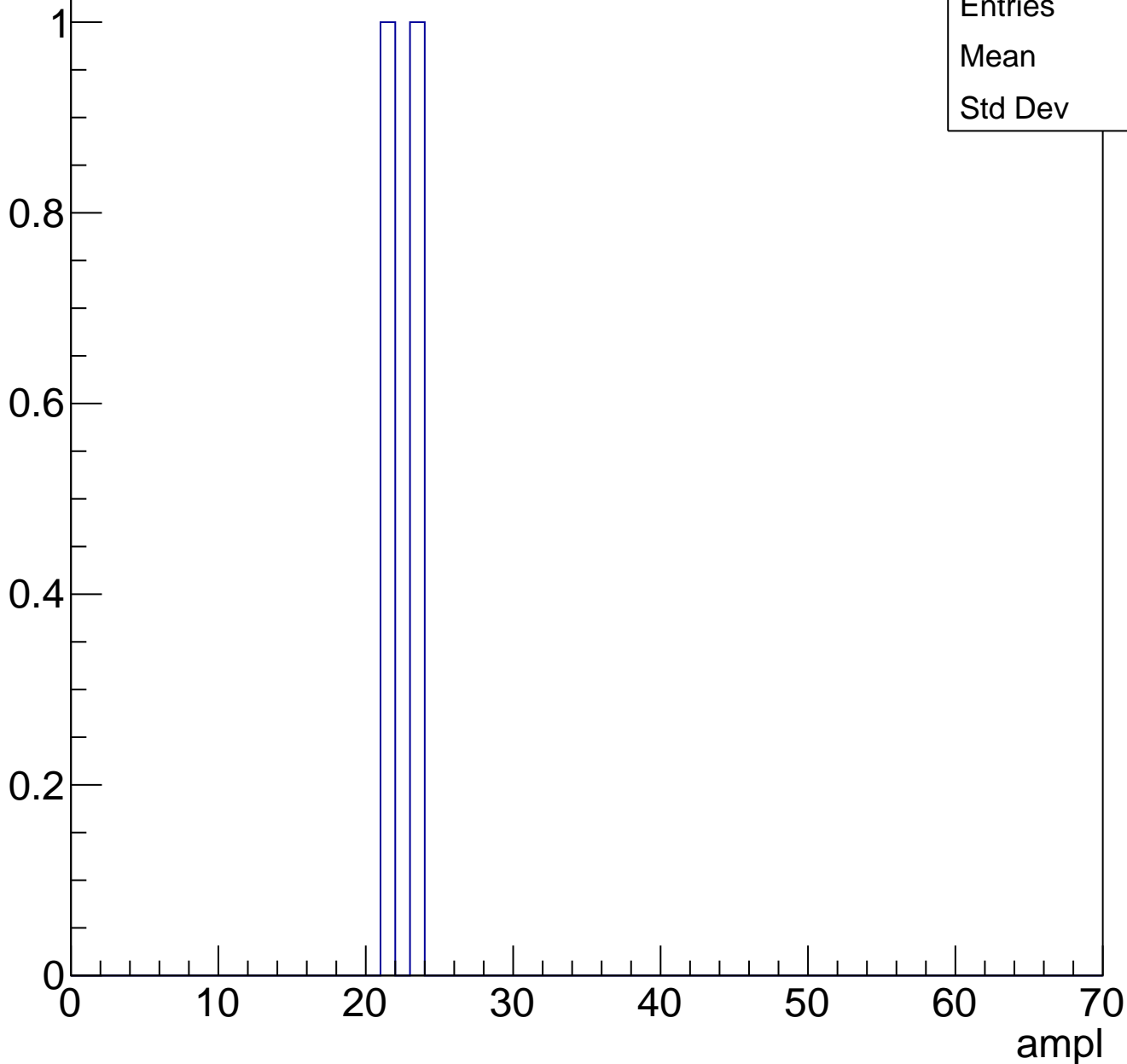
Entries	10
Mean	49.2
Std Dev	24.67



B1L001S, U22-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



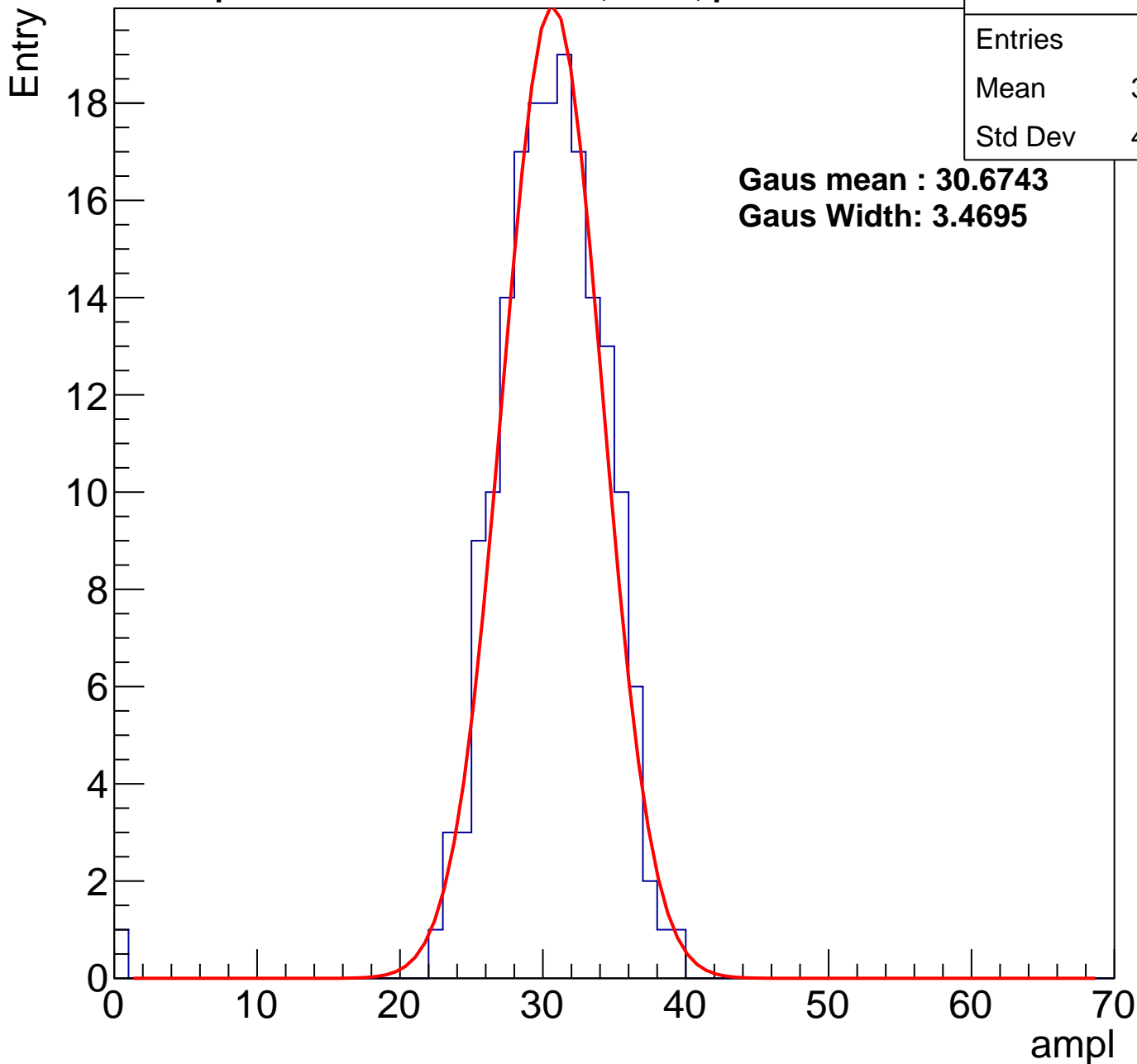
B1L001S, U22-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	177
Mean	30.05
Std Dev	4.079

Gaus mean : 30.6743

Gaus Width: 3.4695



B1L001S, U22-ch4, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	37.84
Std Dev	3.061

Gaus mean : 38.6522

Gaus Width: 3.4288

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

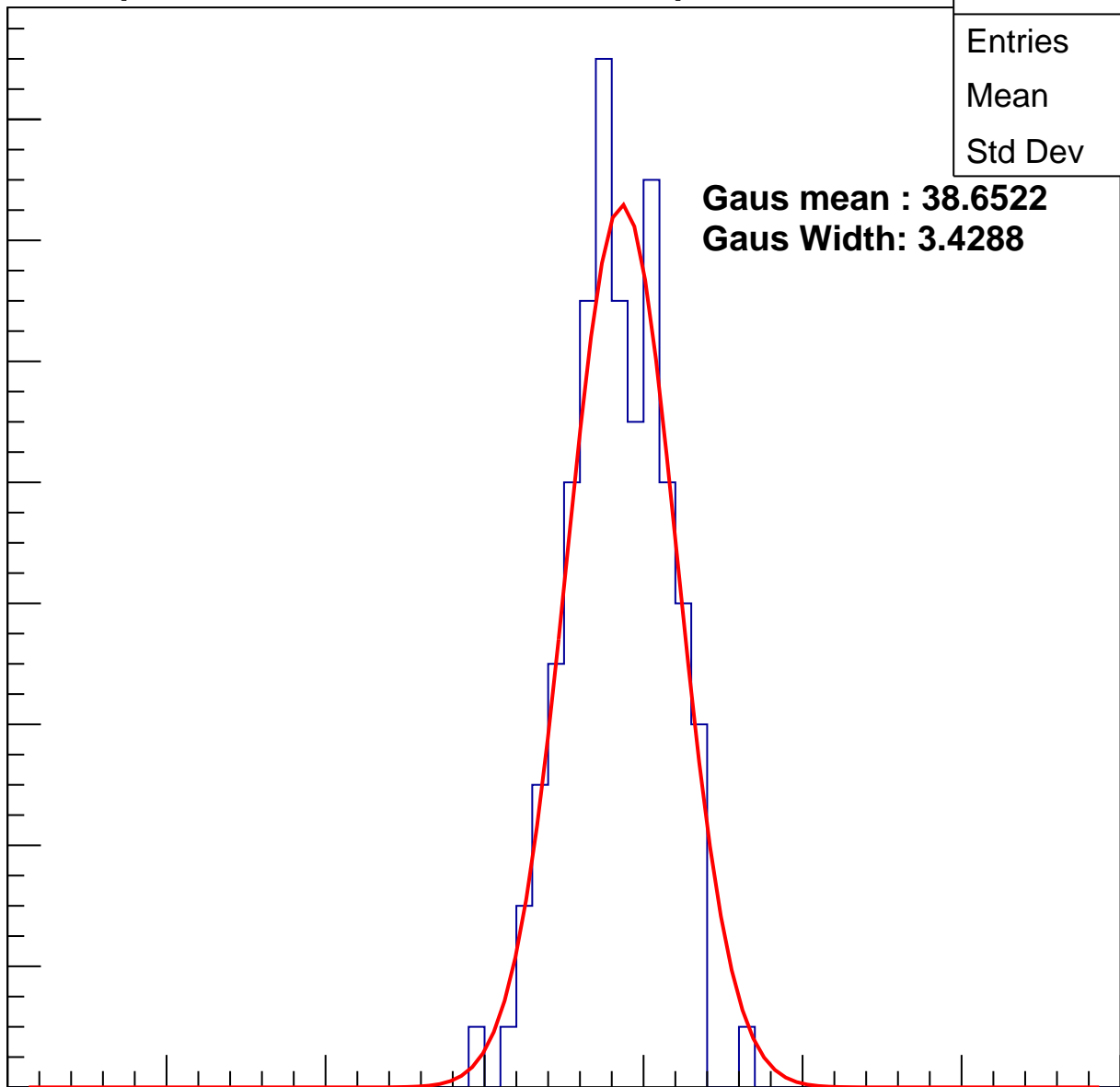
40

50

60

70

ampl



B1L001S, U22-ch4, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

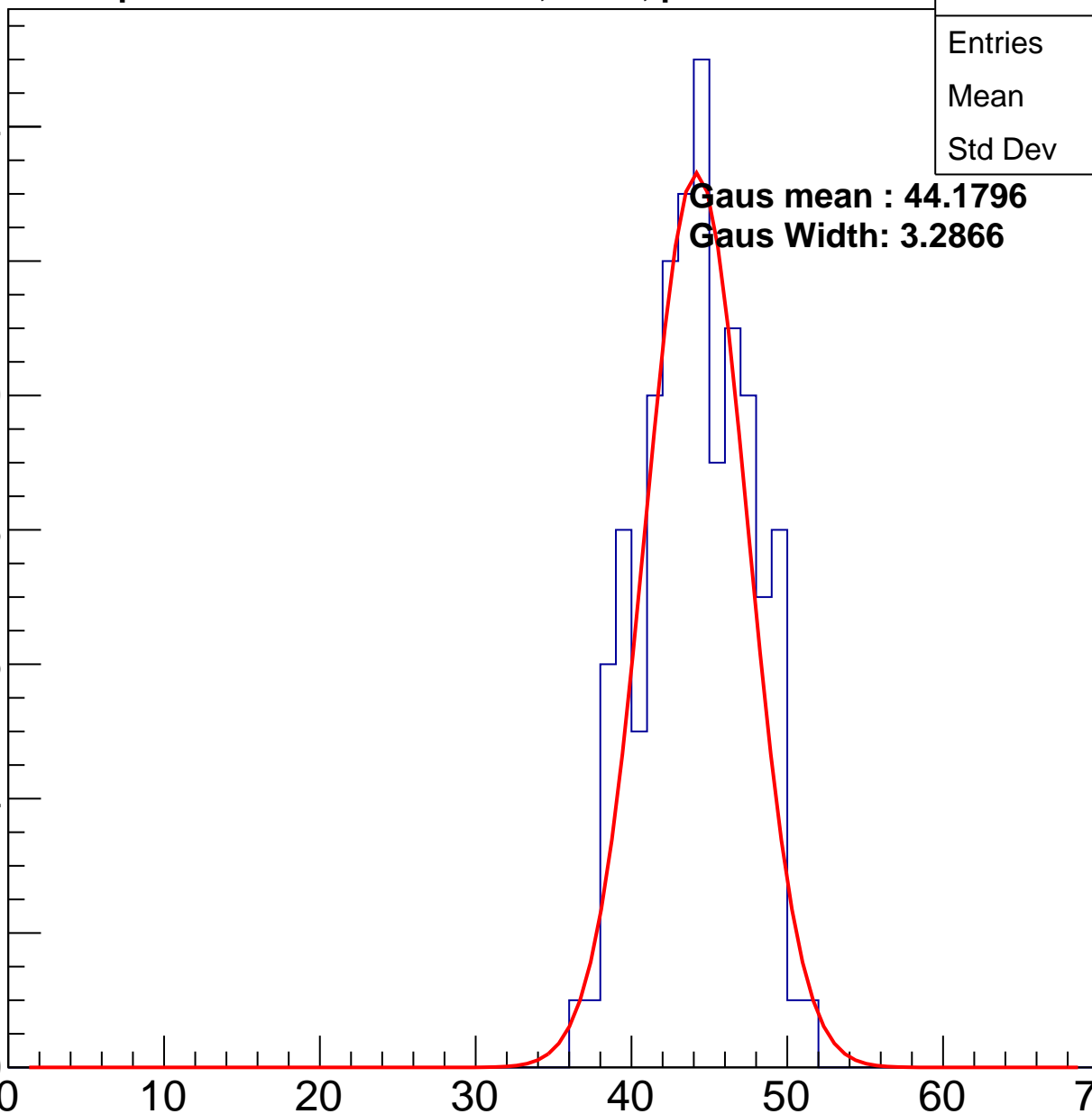
70

ampl

Entries	118
Mean	43.69
Std Dev	3.305

Gaus mean : 44.1796

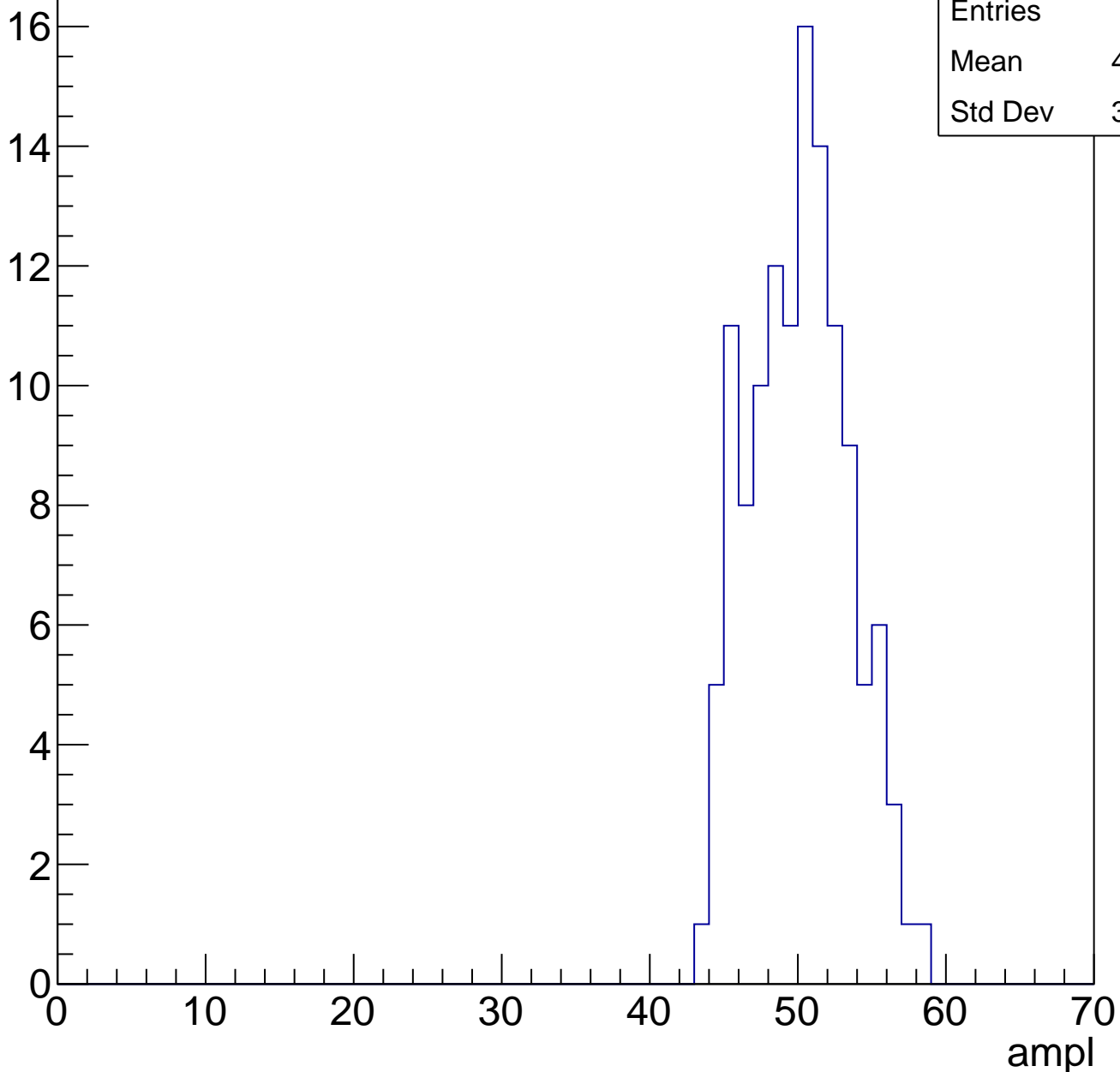
Gaus Width: 3.2866



B1L001S, U22-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch4, adc4

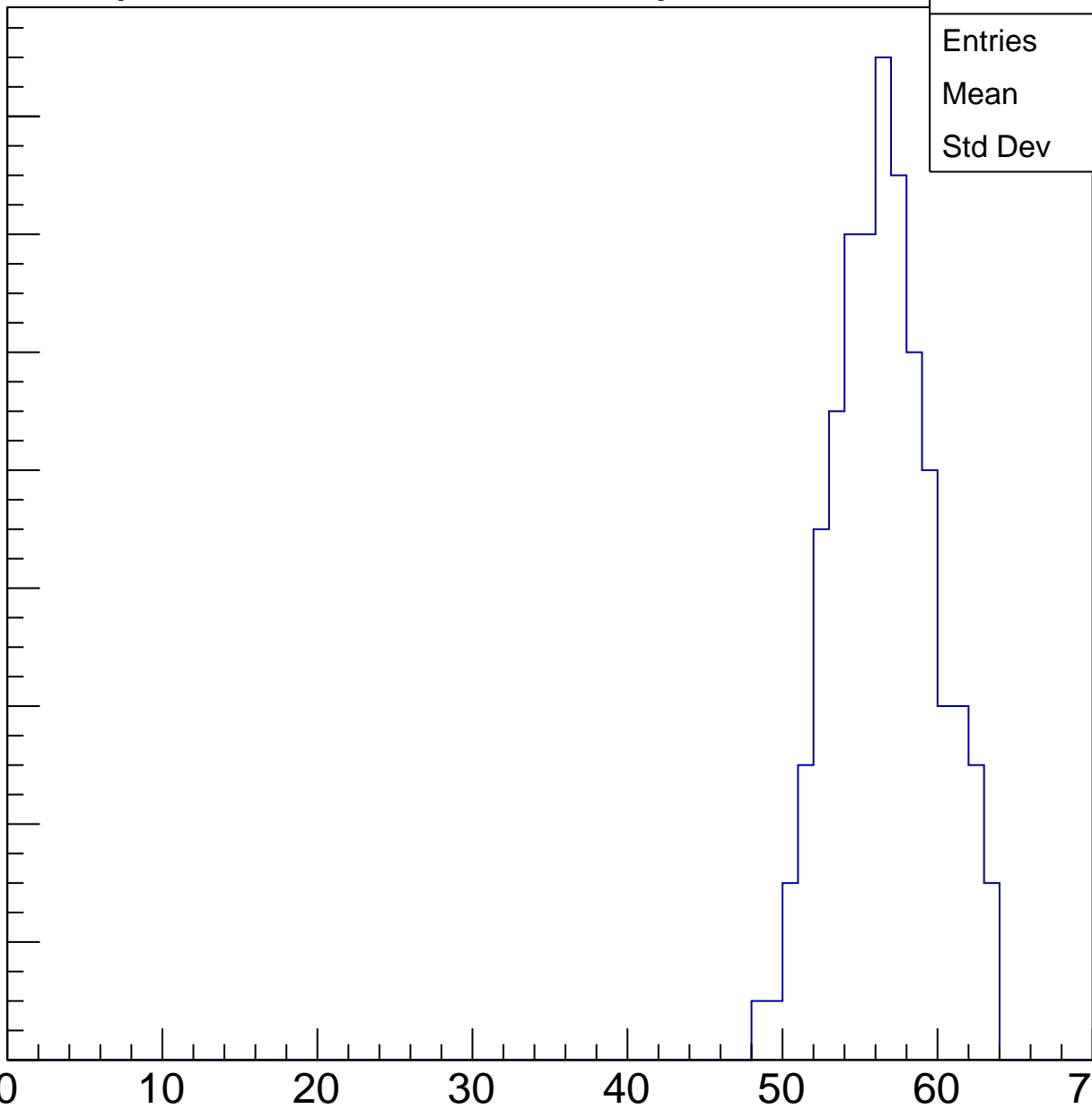
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	56.04
Std Dev	3.239

ampl



B1L001S, U22-ch4, adc5

calib_packv5_042523_0143.root, FC#2, port C2

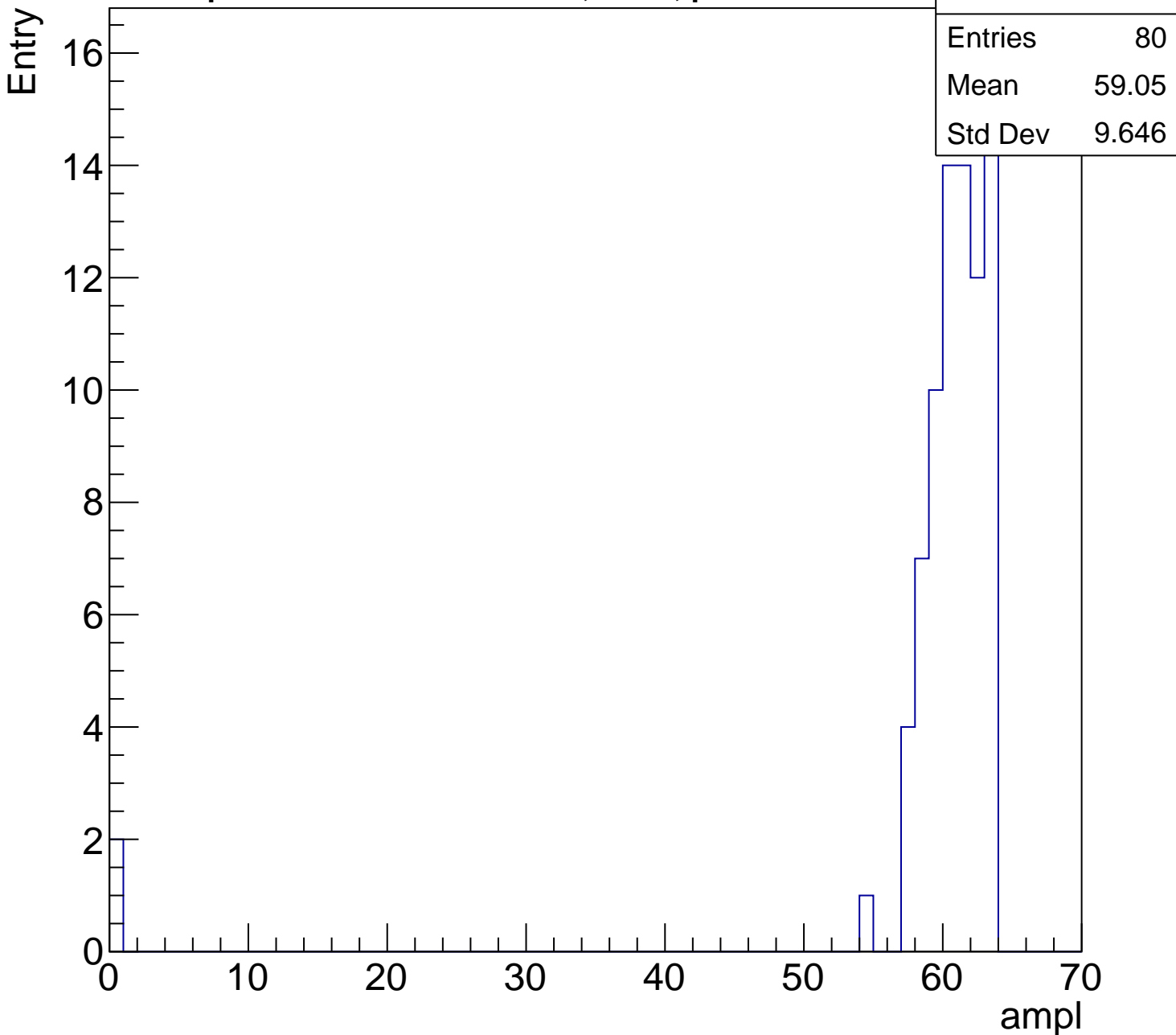
Entries	80
Mean	59.05
Std Dev	9.646

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

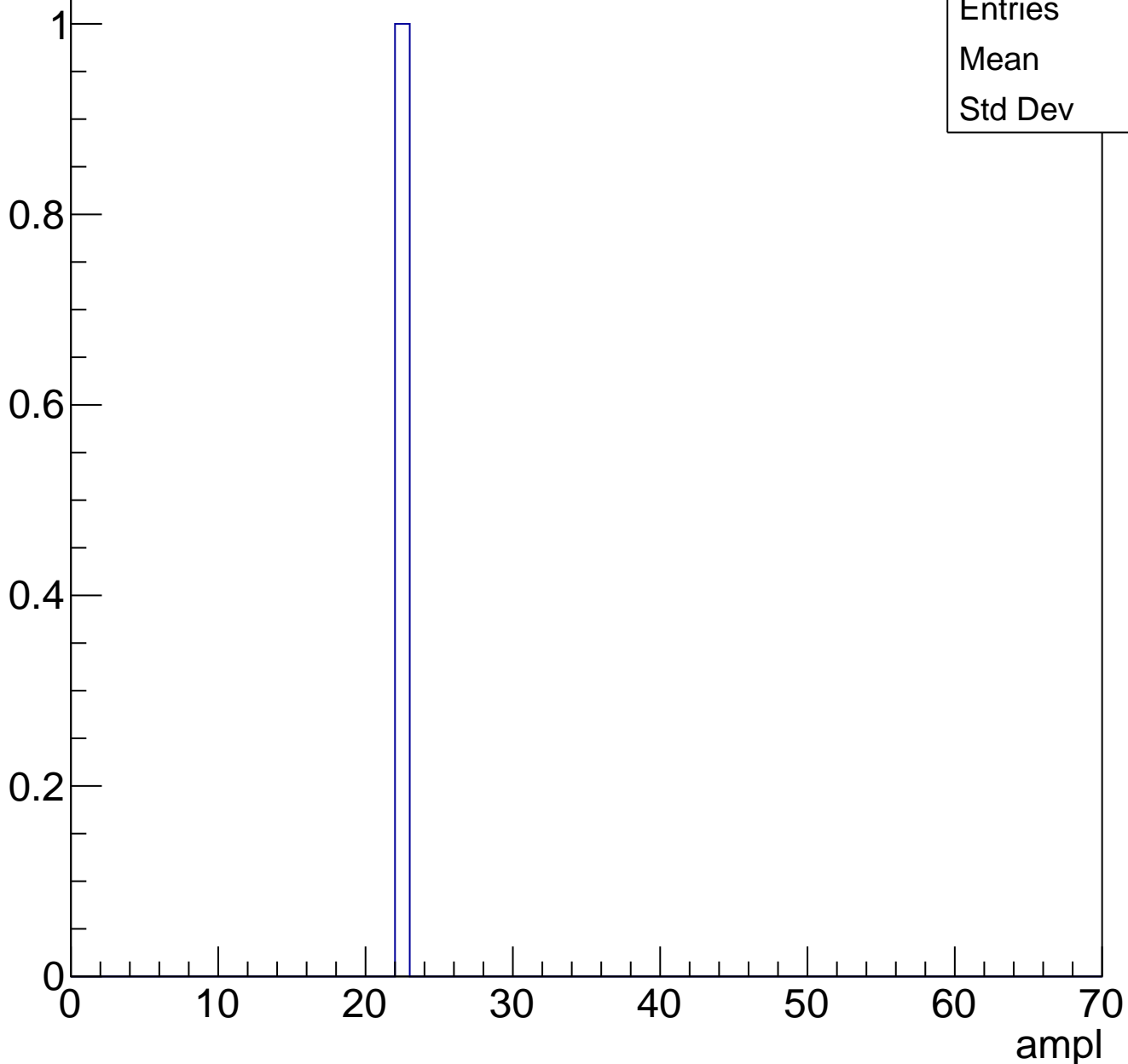
Entry



B1L001S, U22-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

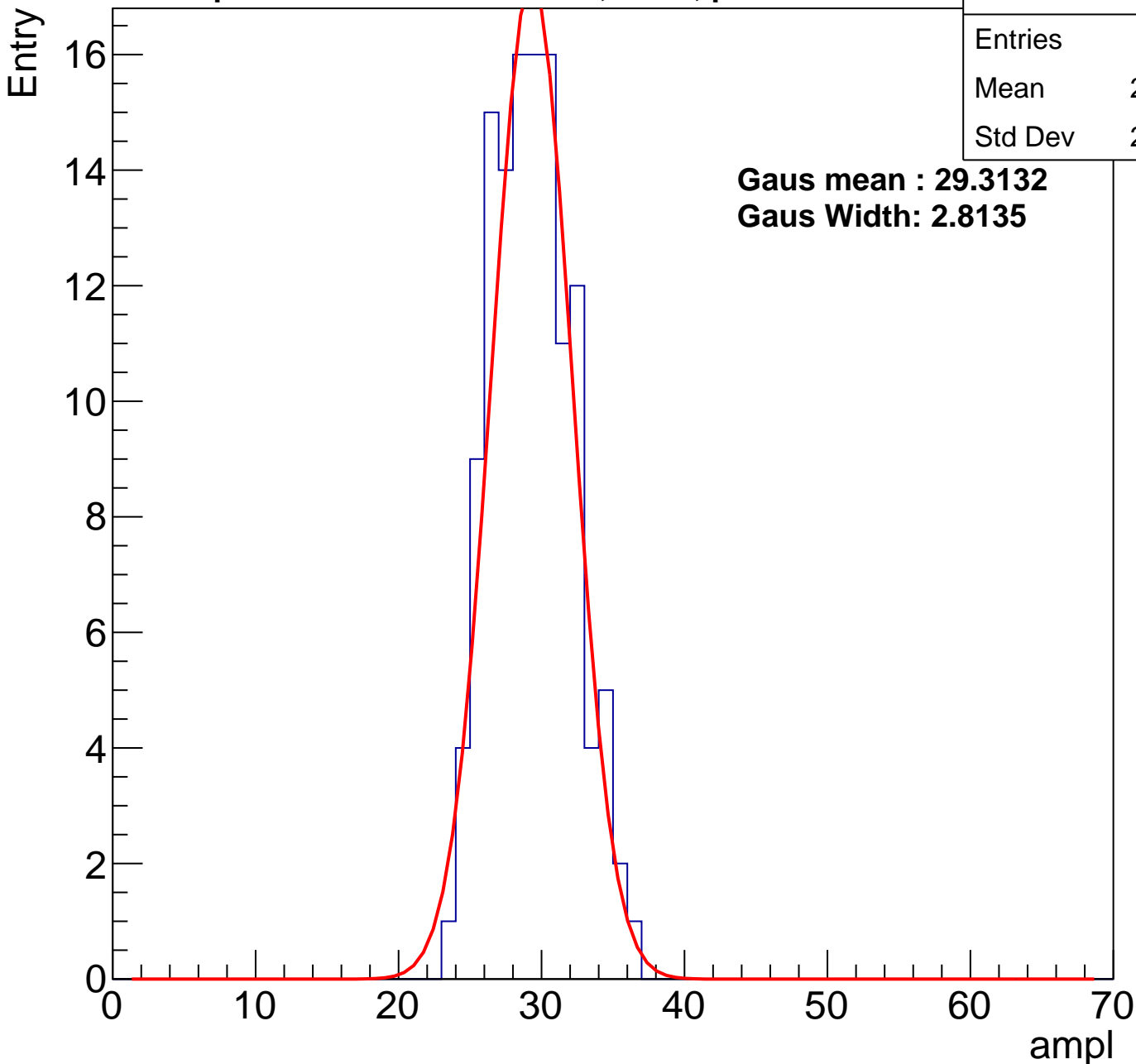
B1L001S, U22-ch5, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	28.87
Std Dev	2.764

Gaus mean : 29.3132

Gaus Width: 2.8135



B1L001S, U22-ch5, adc1

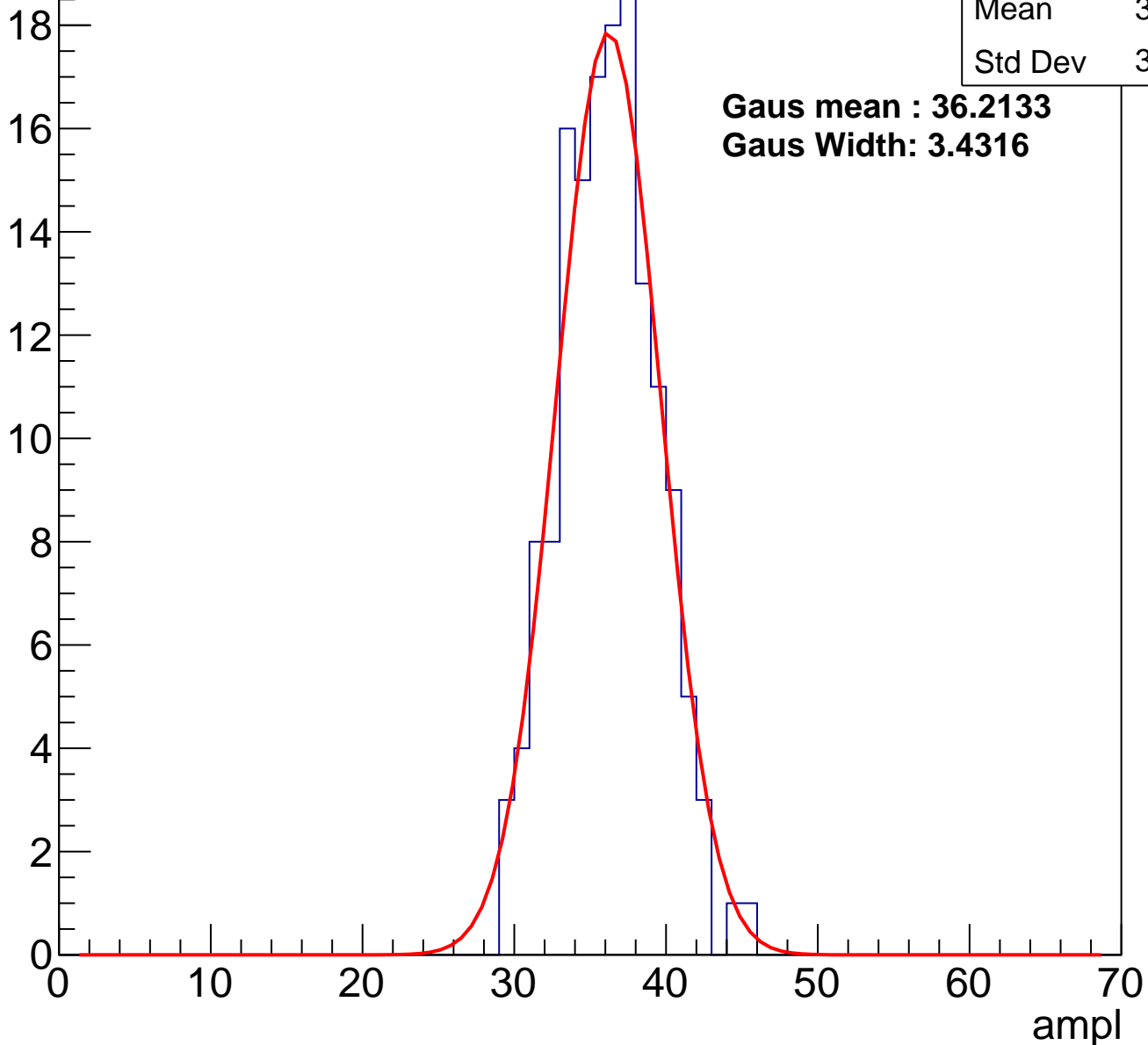
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.75
Std Dev	3.175

Gaus mean : 36.2133

Gaus Width: 3.4316

Entry



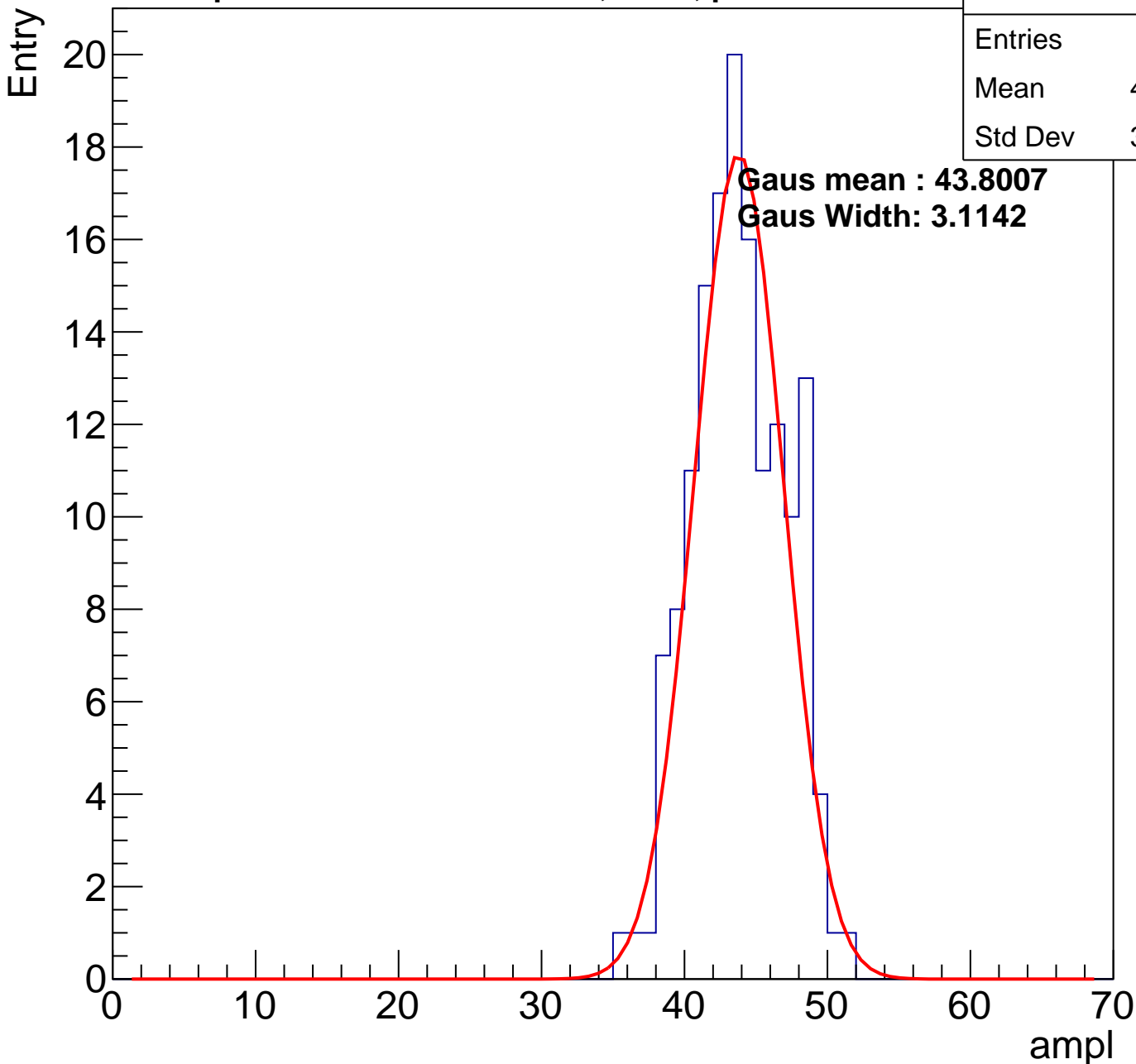
B1L001S, U22-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	43.34
Std Dev	3.195

Gaus mean : 43.8007

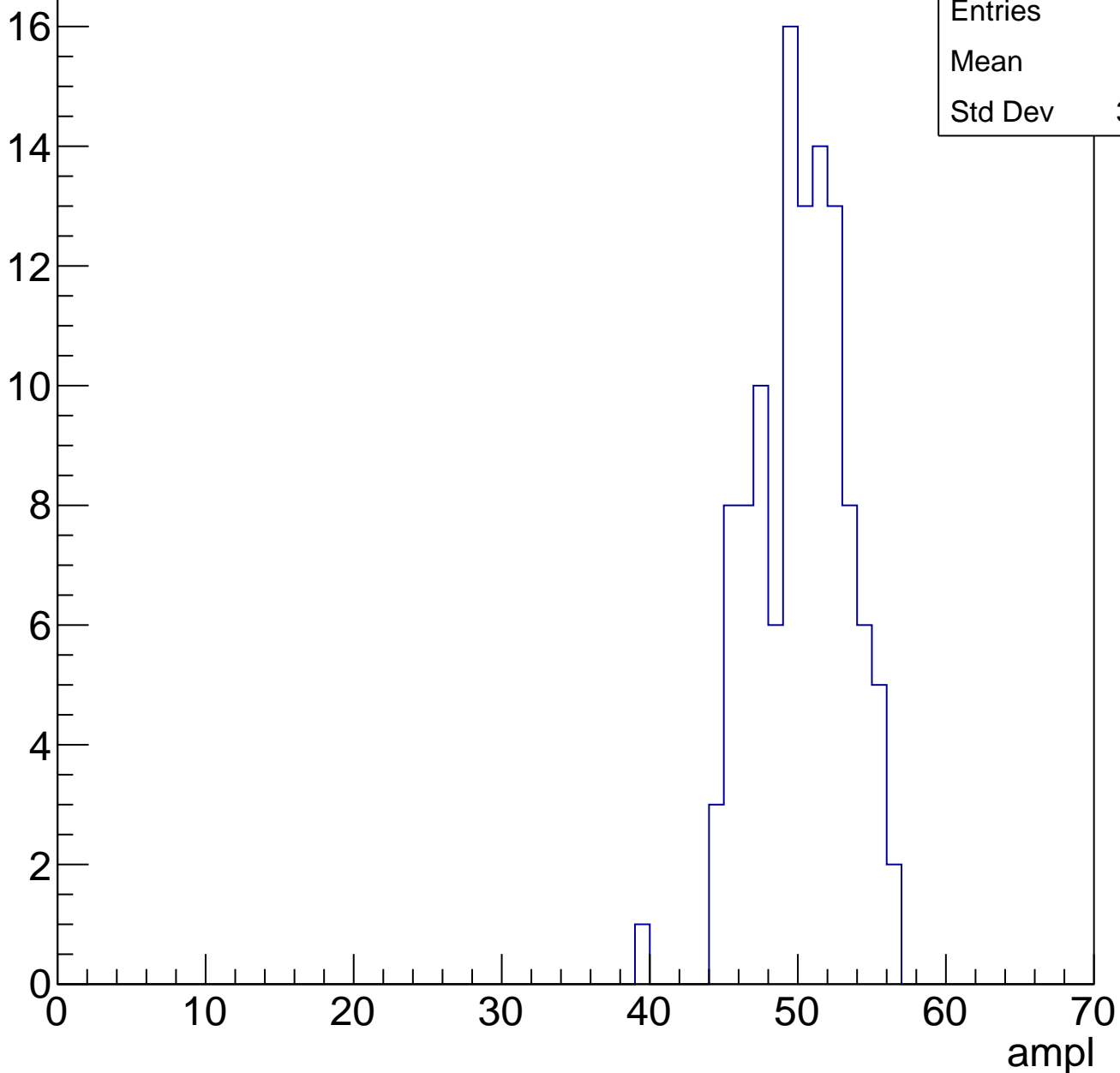
Gaus Width: 3.1142



B1L001S, U22-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch5, adc4

calib_packv5_042523_0143.root, FC#2, port C2

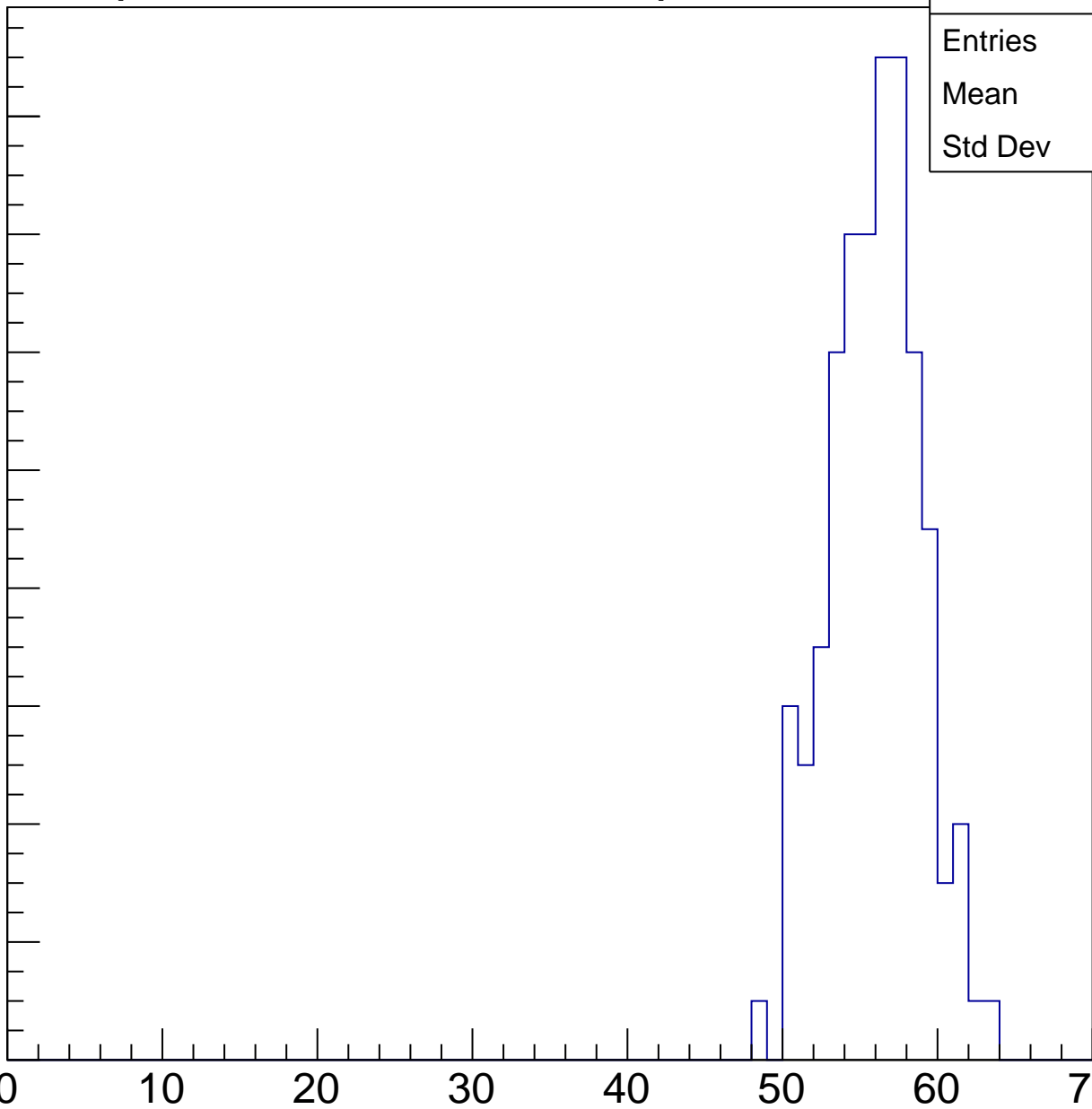
Entries	123
Mean	55.5
Std Dev	2.912

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch5, adc5

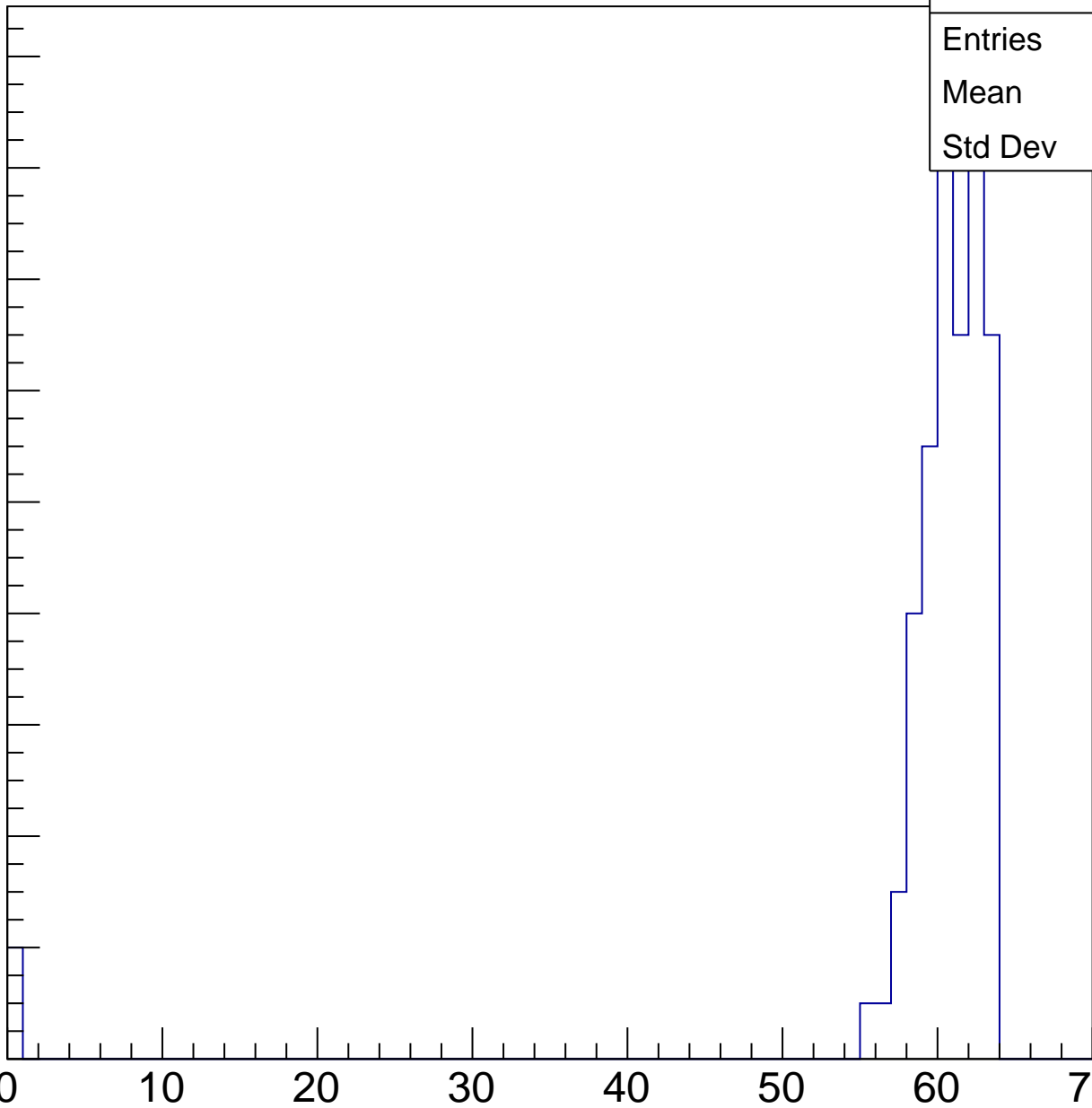
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.11
Std Dev	9.251

ampl

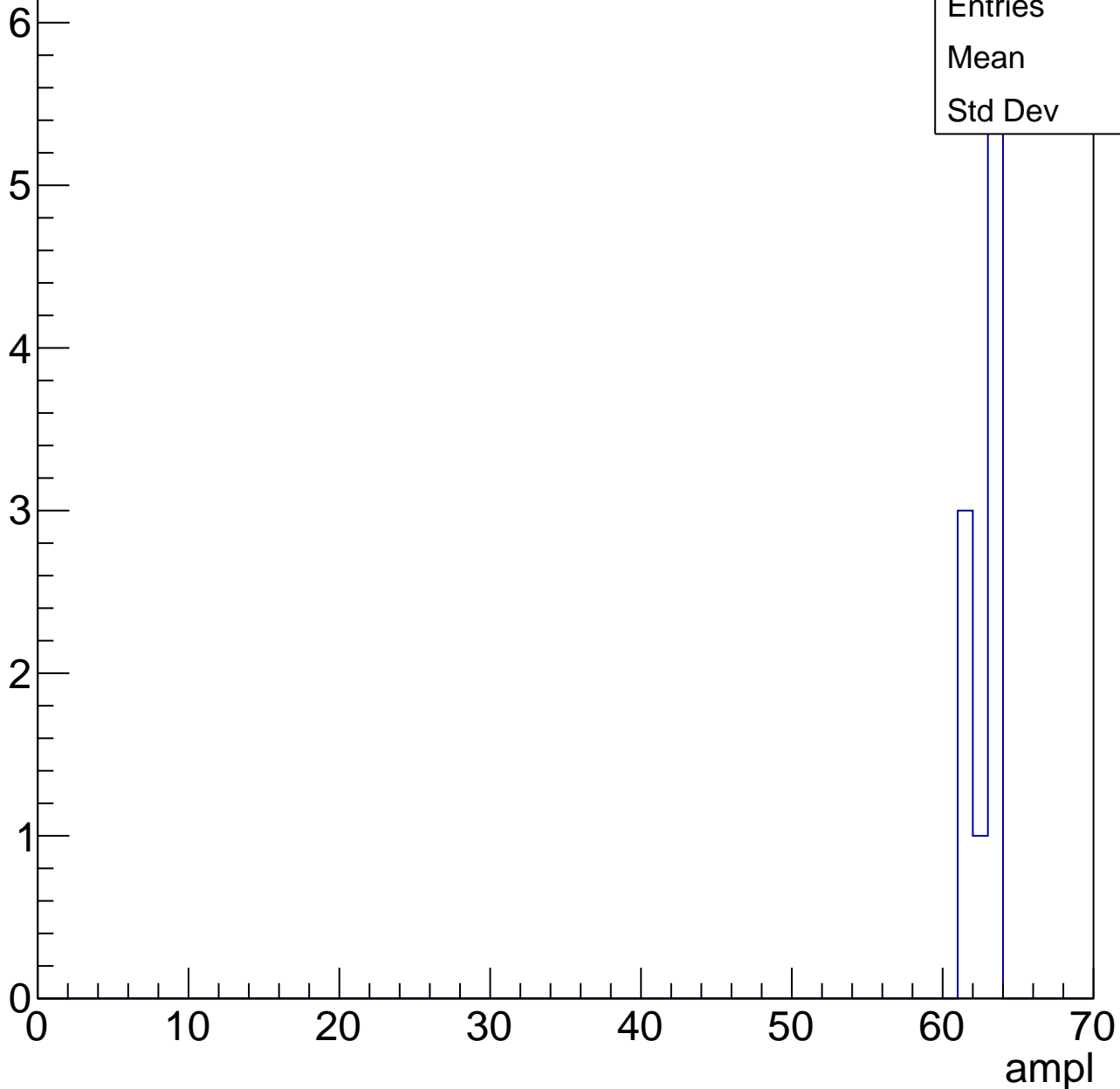


B1L001S, U22-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

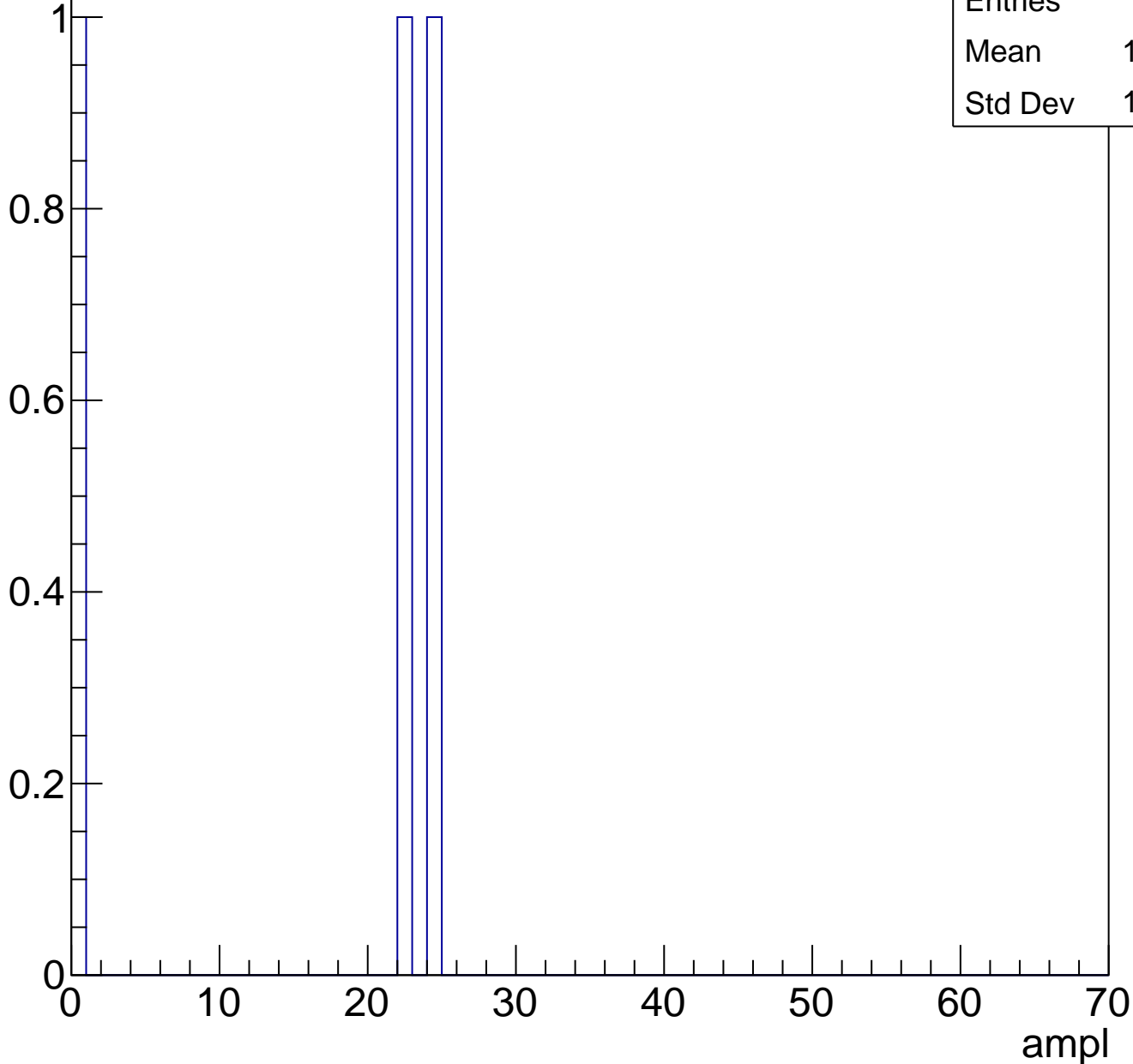
Entries	10
Mean	62.3
Std Dev	0.9



B1L001S, U22-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch6, adc0

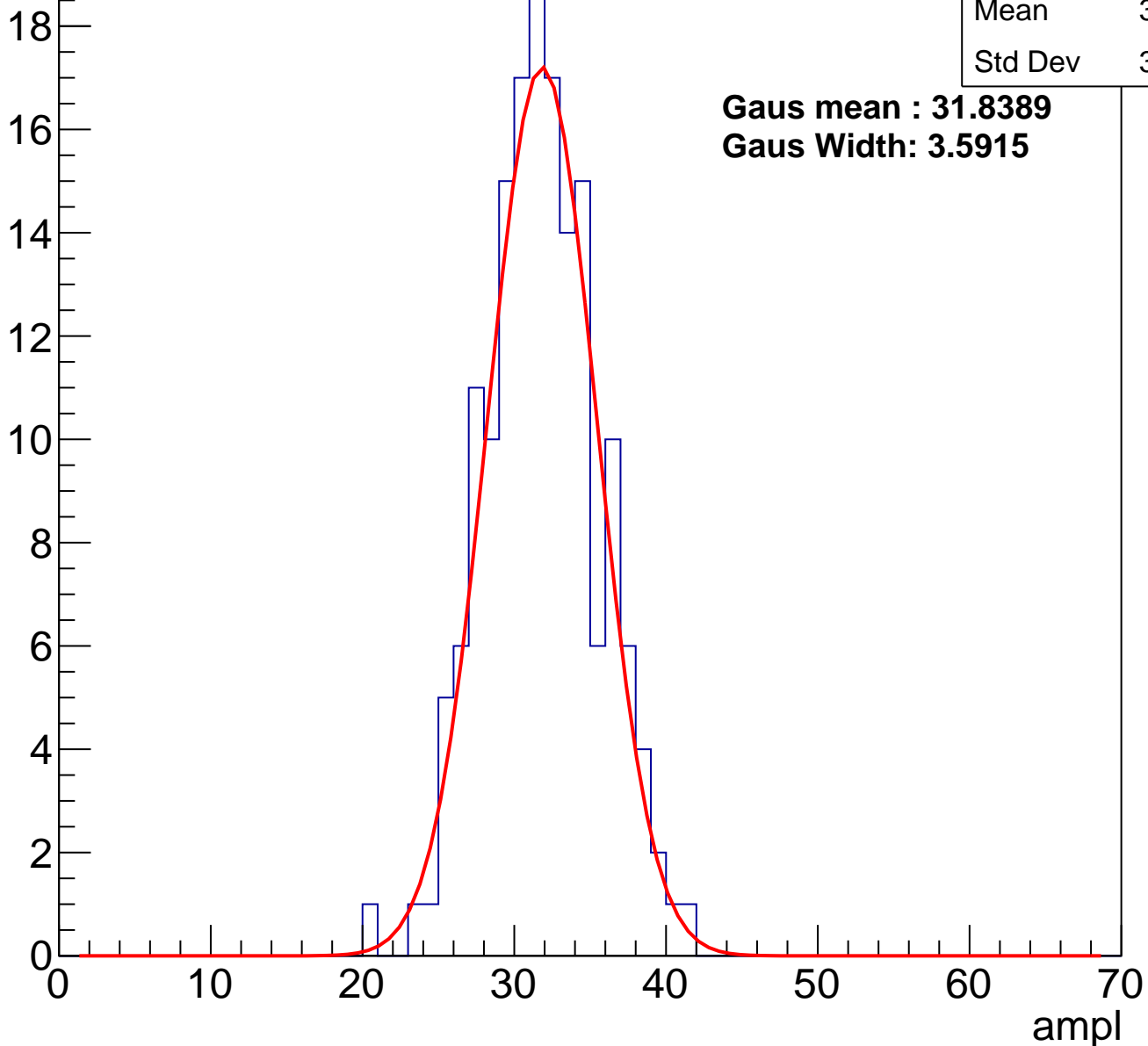
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	31.35
Std Dev	3.654

Gaus mean : 31.8389

Gaus Width: 3.5915

Entry



B1L001S, U22-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

127

Mean

38.3

Std Dev

3.276

Gaus mean : 38.7469

Gaus Width: 3.3064

0

10

20

30

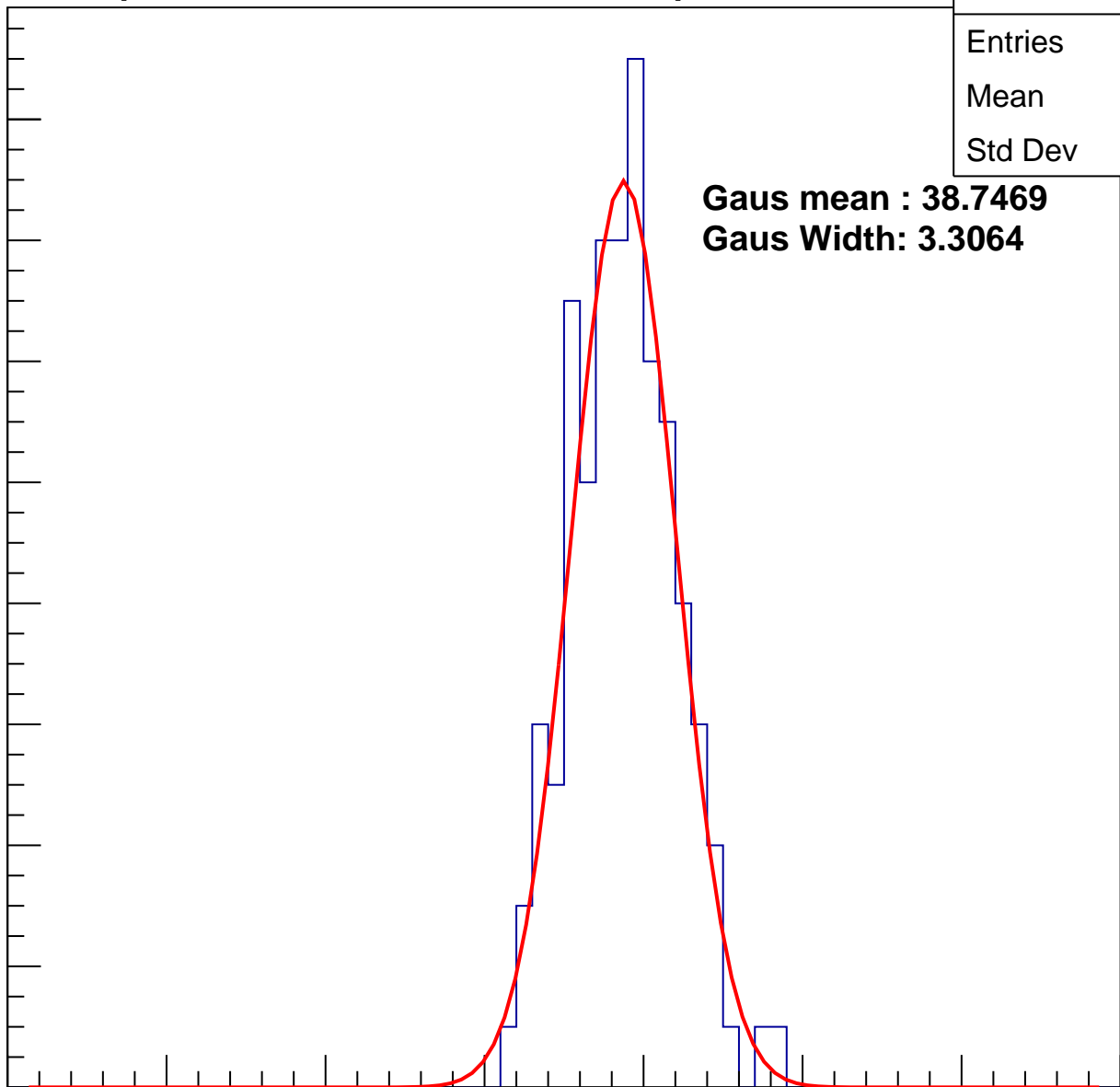
40

50

60

70

ampl



B1L001S, U22-ch6, adc2

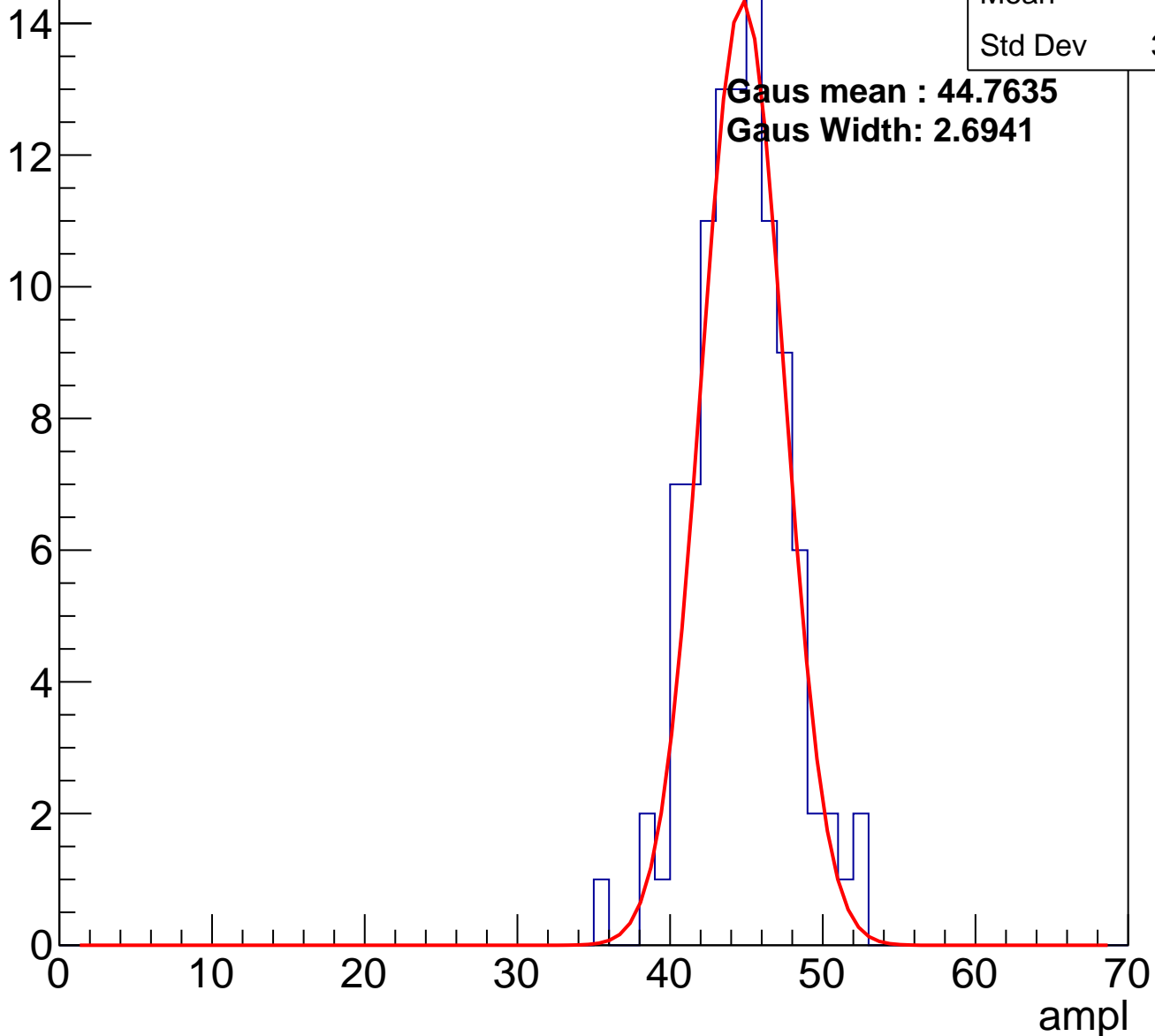
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	44.22
Std Dev	3.021

Gaus mean : 44.7635

Gaus Width: 2.6941

Entry

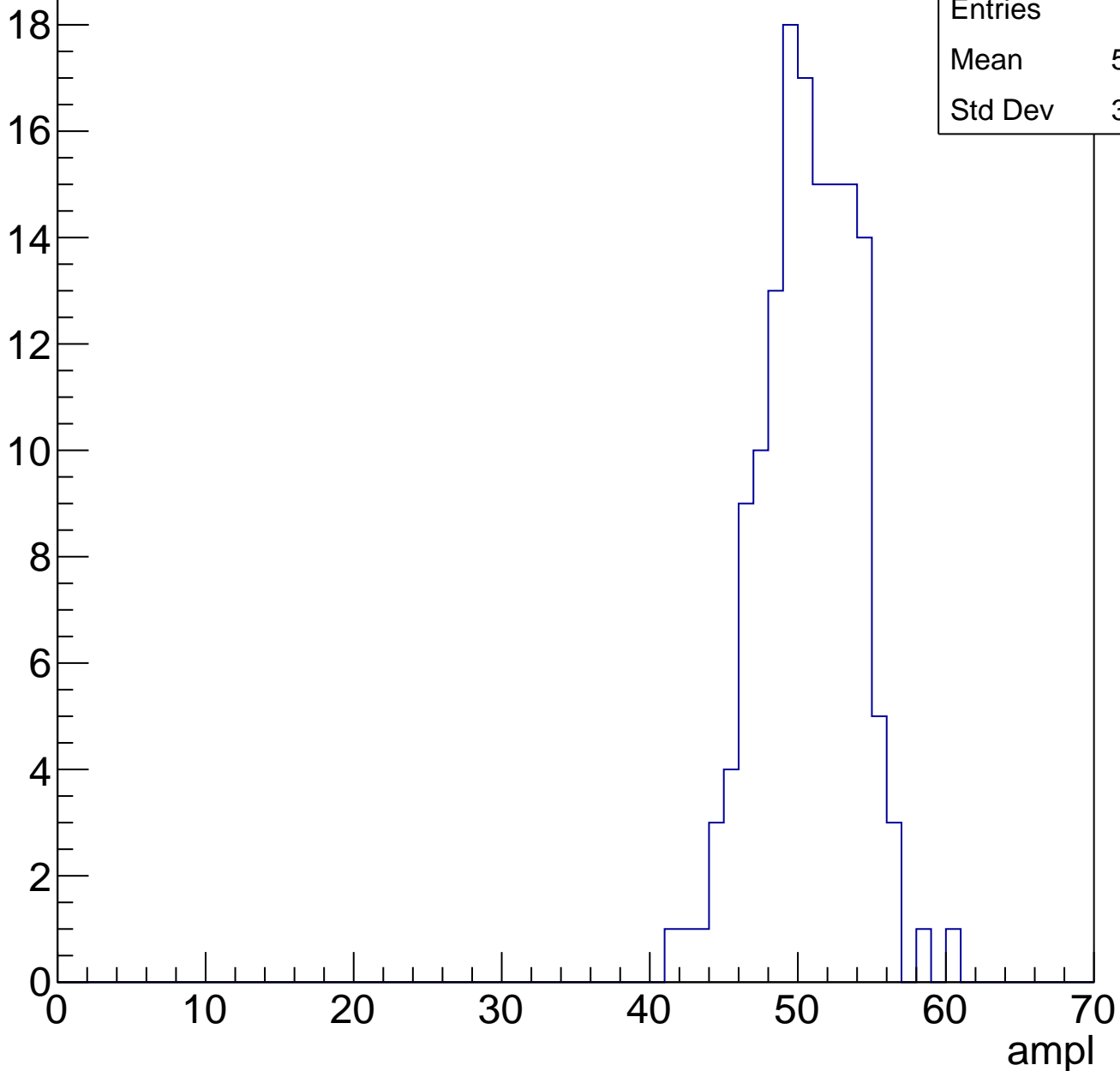


B1L001S, U22-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	50.24
Std Dev	3.236

Entry



B1L001S, U22-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

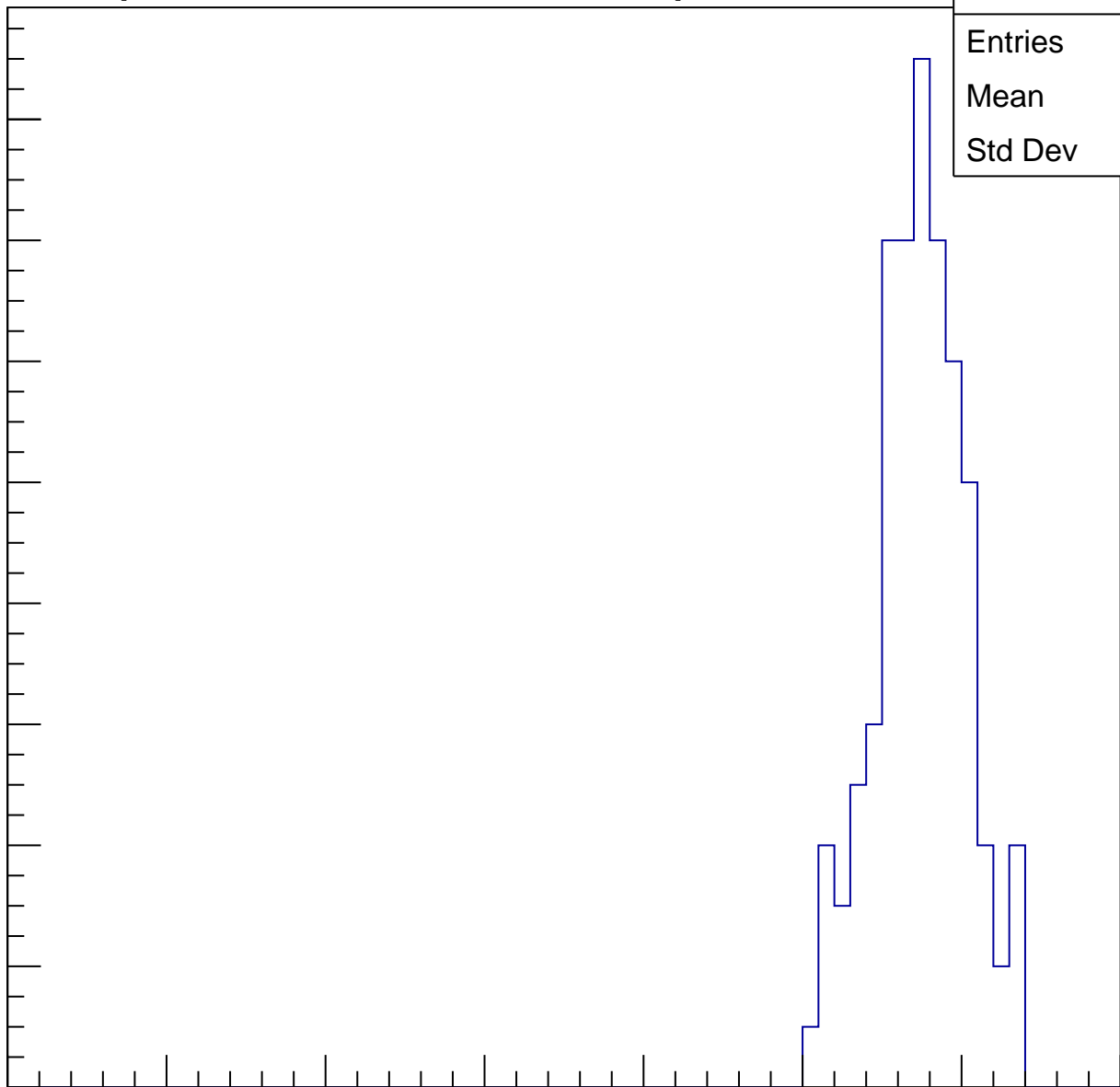
Entries	110
Mean	56.93
Std Dev	2.831

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch6, adc5

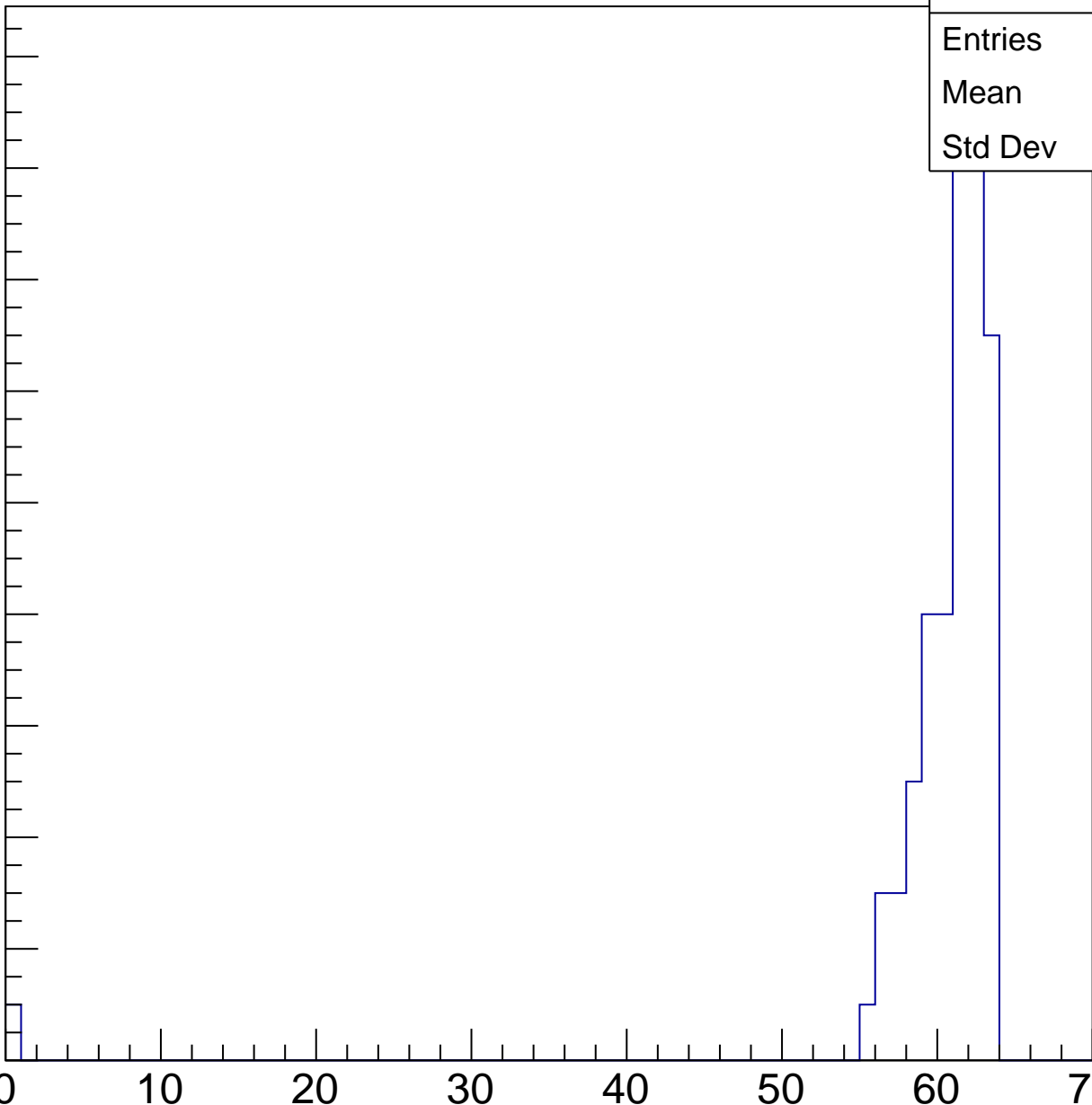
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.83
Std Dev	7.192

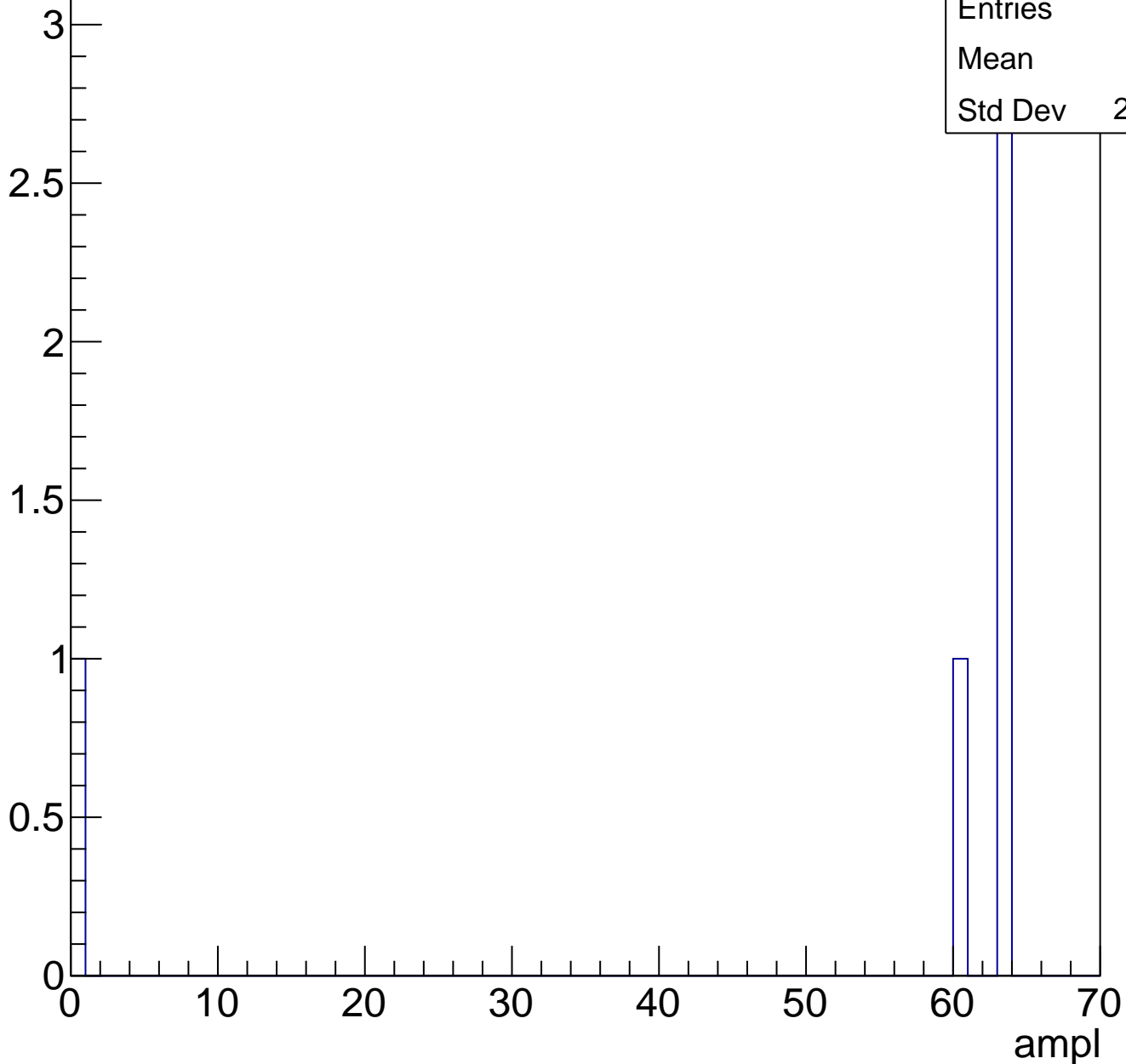
ampl



B1L001S, U22-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

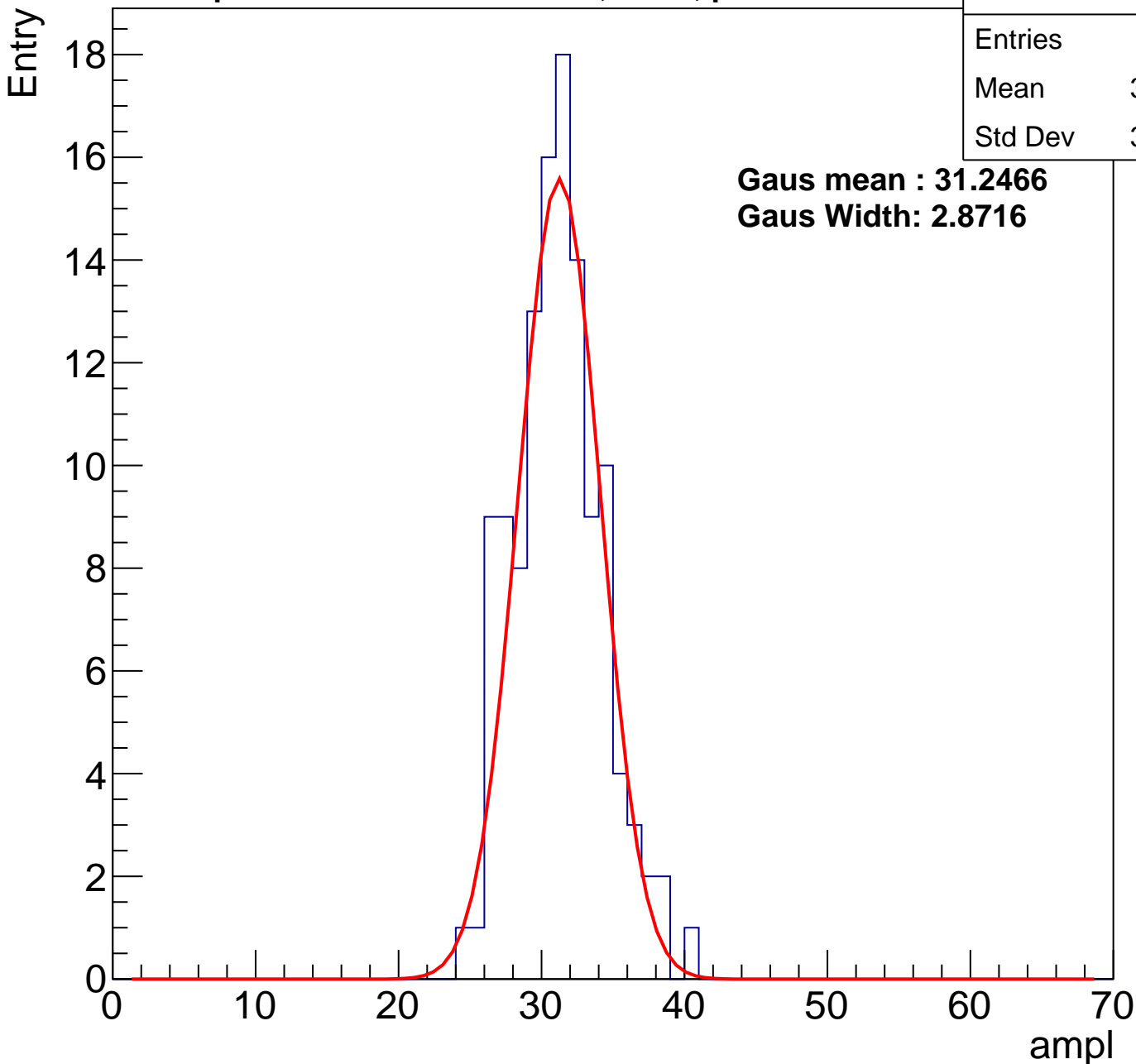
B1L001S, U22-ch7, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	30.73
Std Dev	3.049

Gaus mean : 31.2466

Gaus Width: 2.8716



B1L001S, U22-ch7, adc1

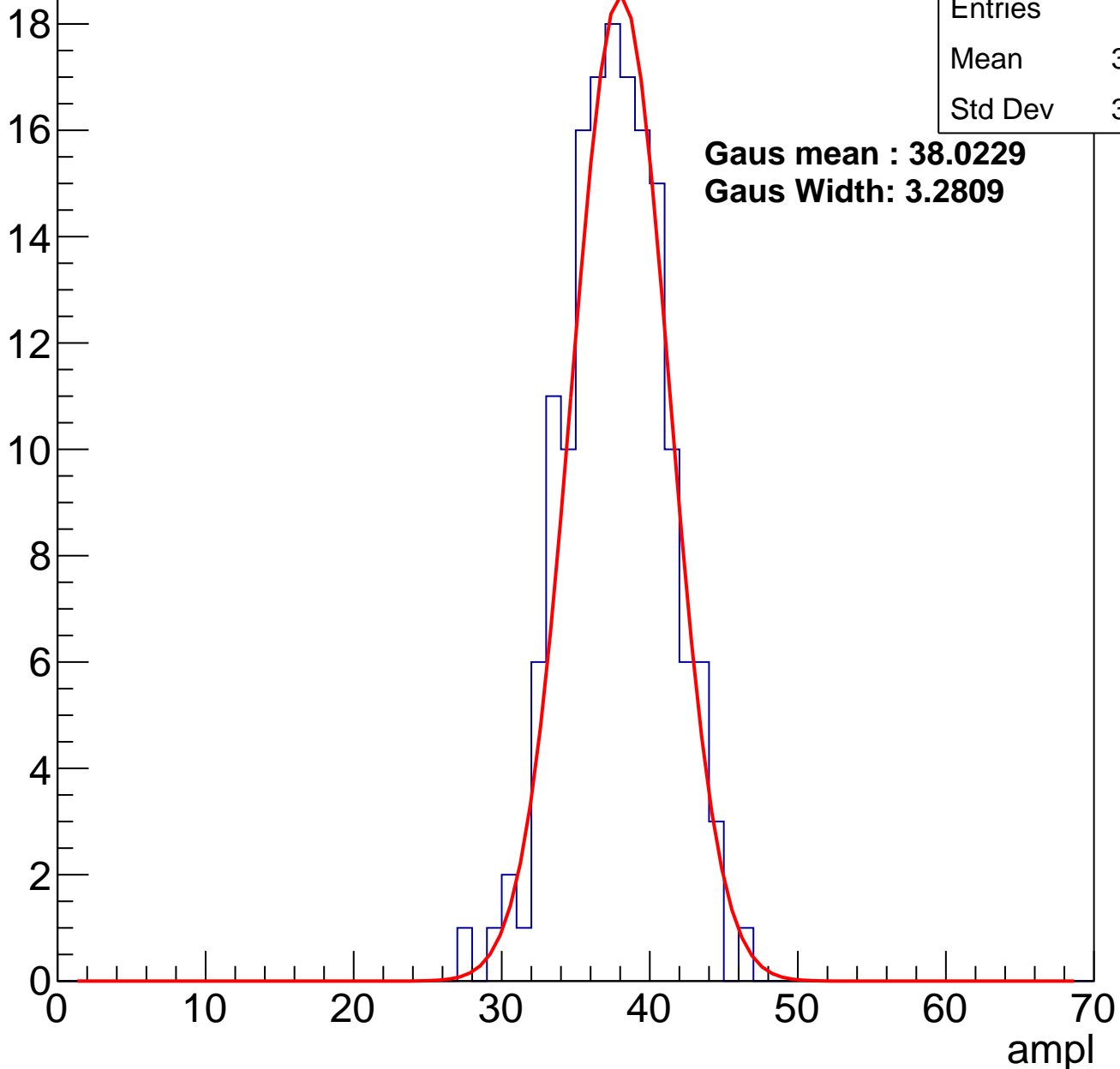
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	37.25
Std Dev	3.344

Gaus mean : 38.0229

Gaus Width: 3.2809

Entry



B1L001S, U22-ch7, adc2

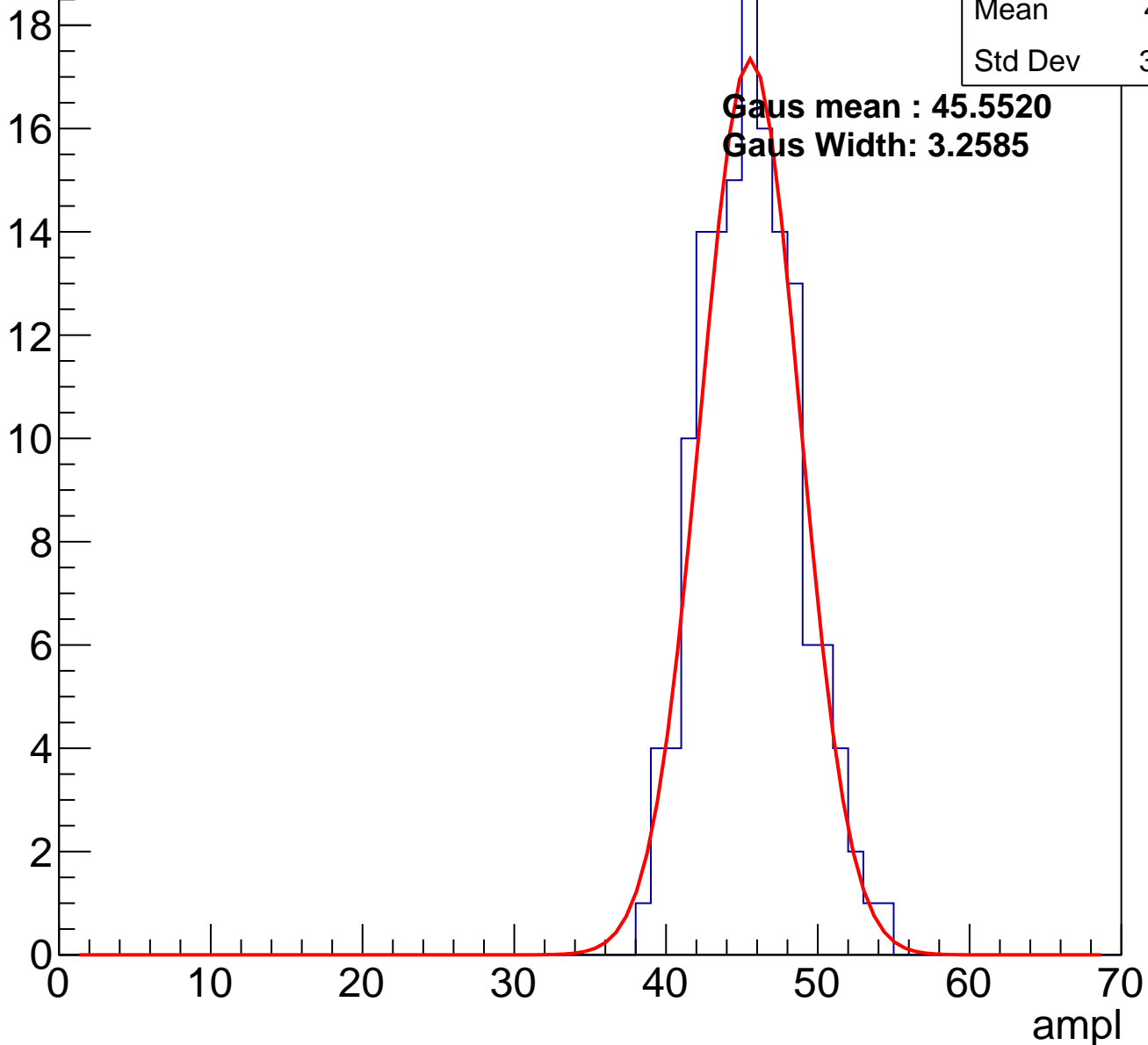
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	45.11
Std Dev	3.195

Gaus mean : 45.5520

Gaus Width: 3.2585

Entry

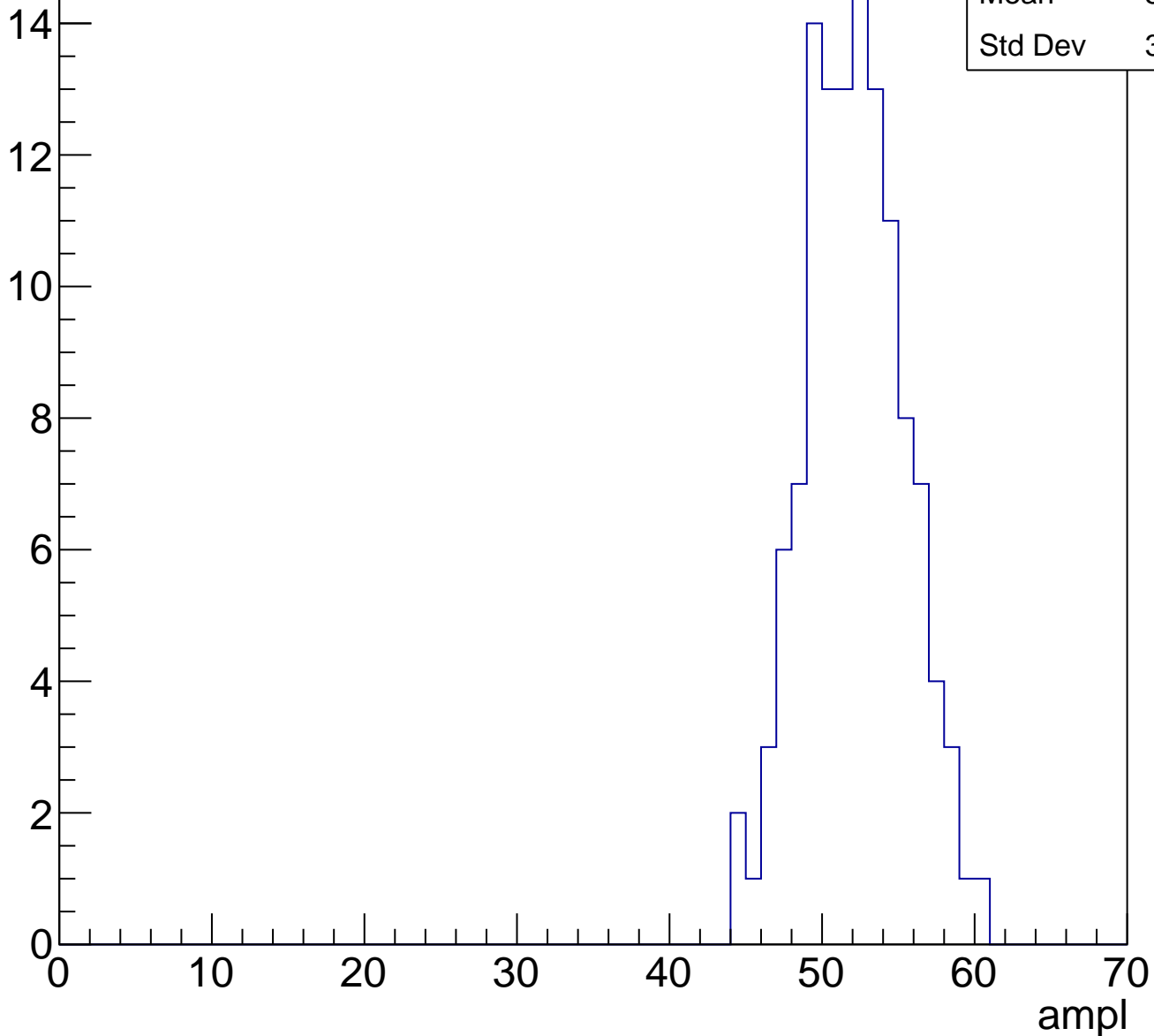


B1L001S, U22-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	51.67
Std Dev	3.248

Entry



B1L001S, U22-ch7, adc4

calib_packv5_042523_0143.root, FC#2, port C2

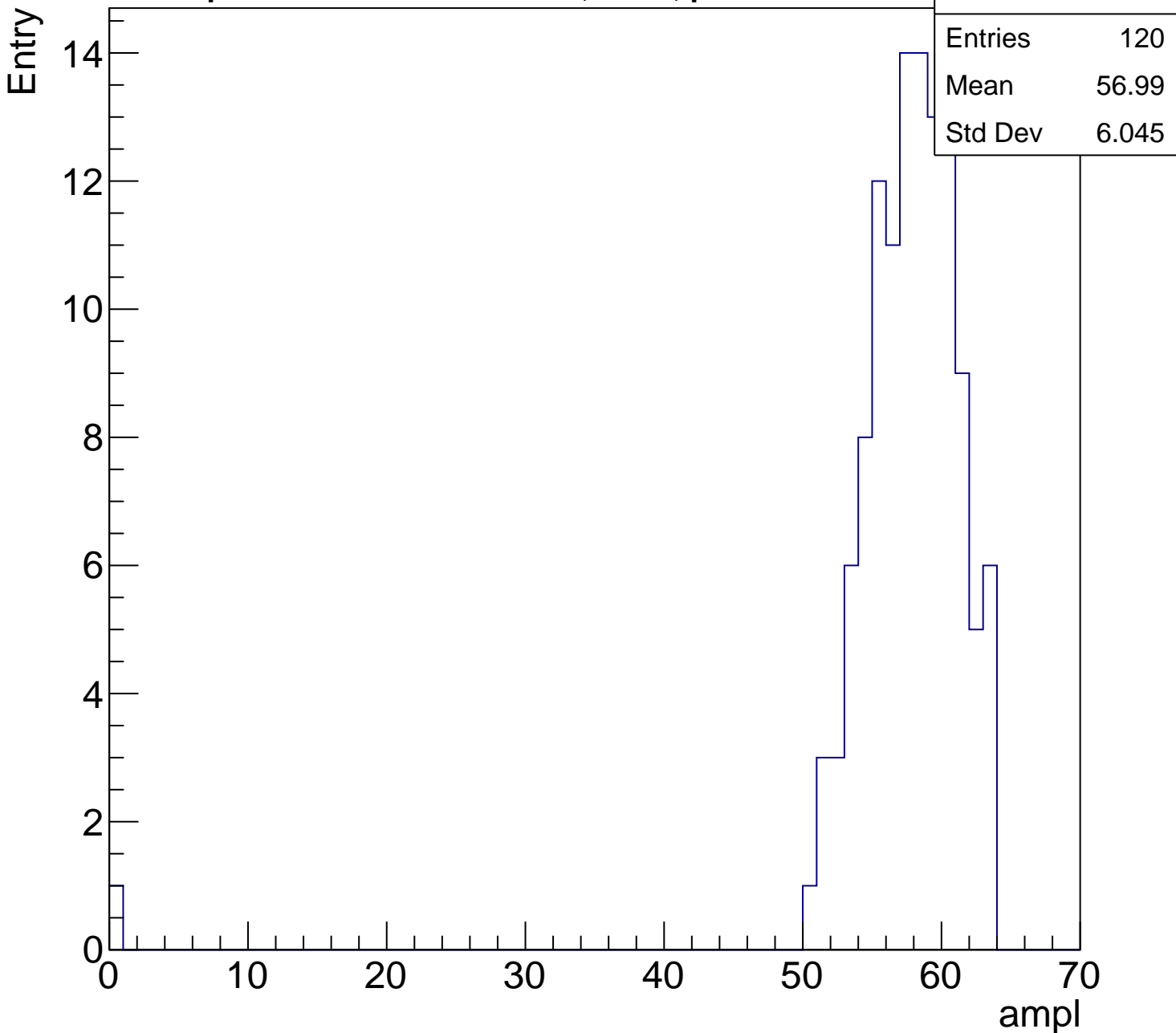
Entries	120
Mean	56.99
Std Dev	6.045

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch7, adc5

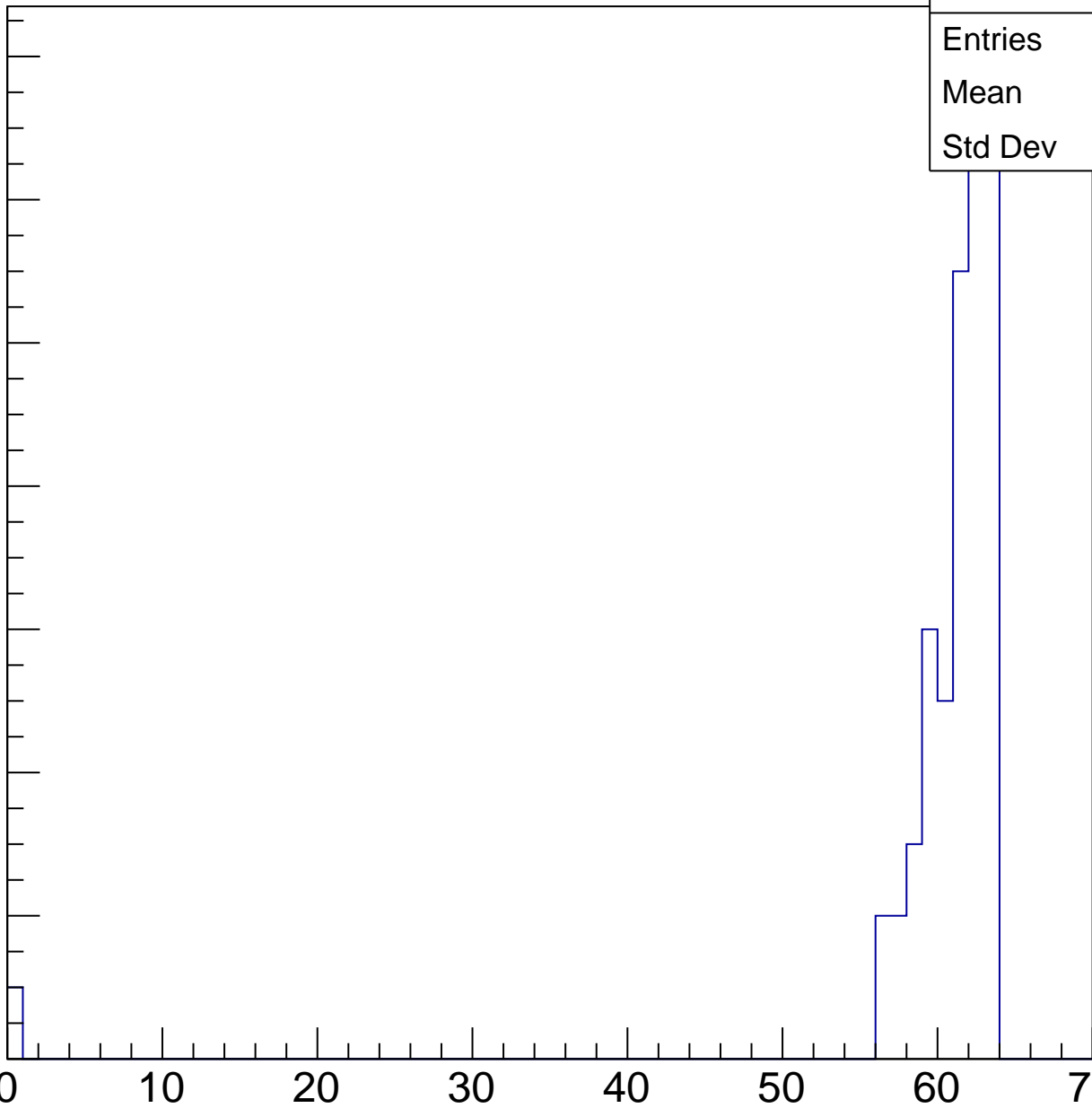
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	57
Mean	59.88
Std Dev	8.225

ampl



B1L001S, U22-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L001S, U22-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch8, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.34
Std Dev	3.973

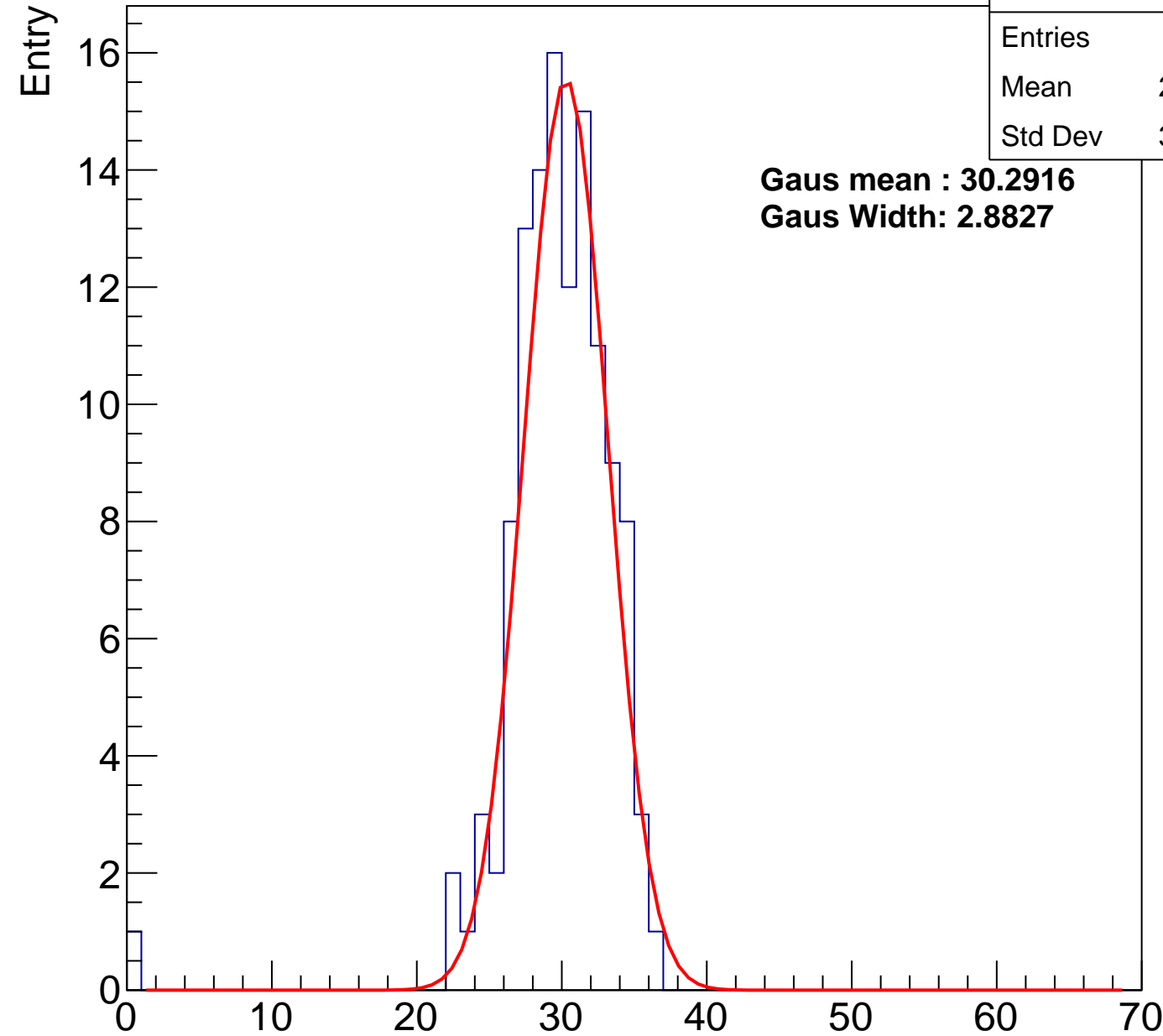
Gaus mean : 30.2916

Gaus Width: 2.8827

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch8, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

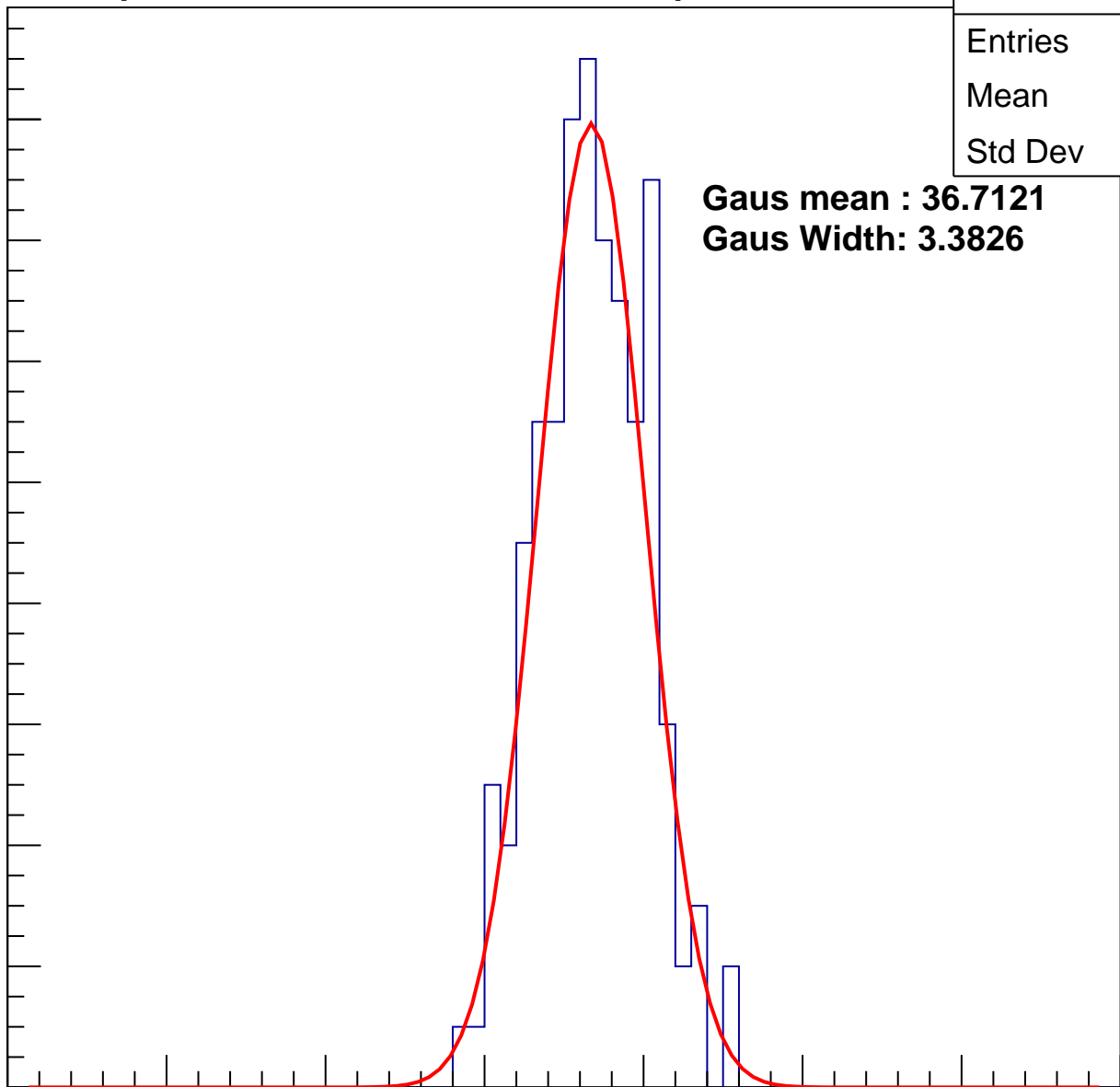
70

ampl

Entries	141
Mean	36.3
Std Dev	3.389

Gaus mean : 36.7121

Gaus Width: 3.3826



B1L001S, U22-ch8, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	43.21
Std Dev	3.654

Gaus mean : 43.6530

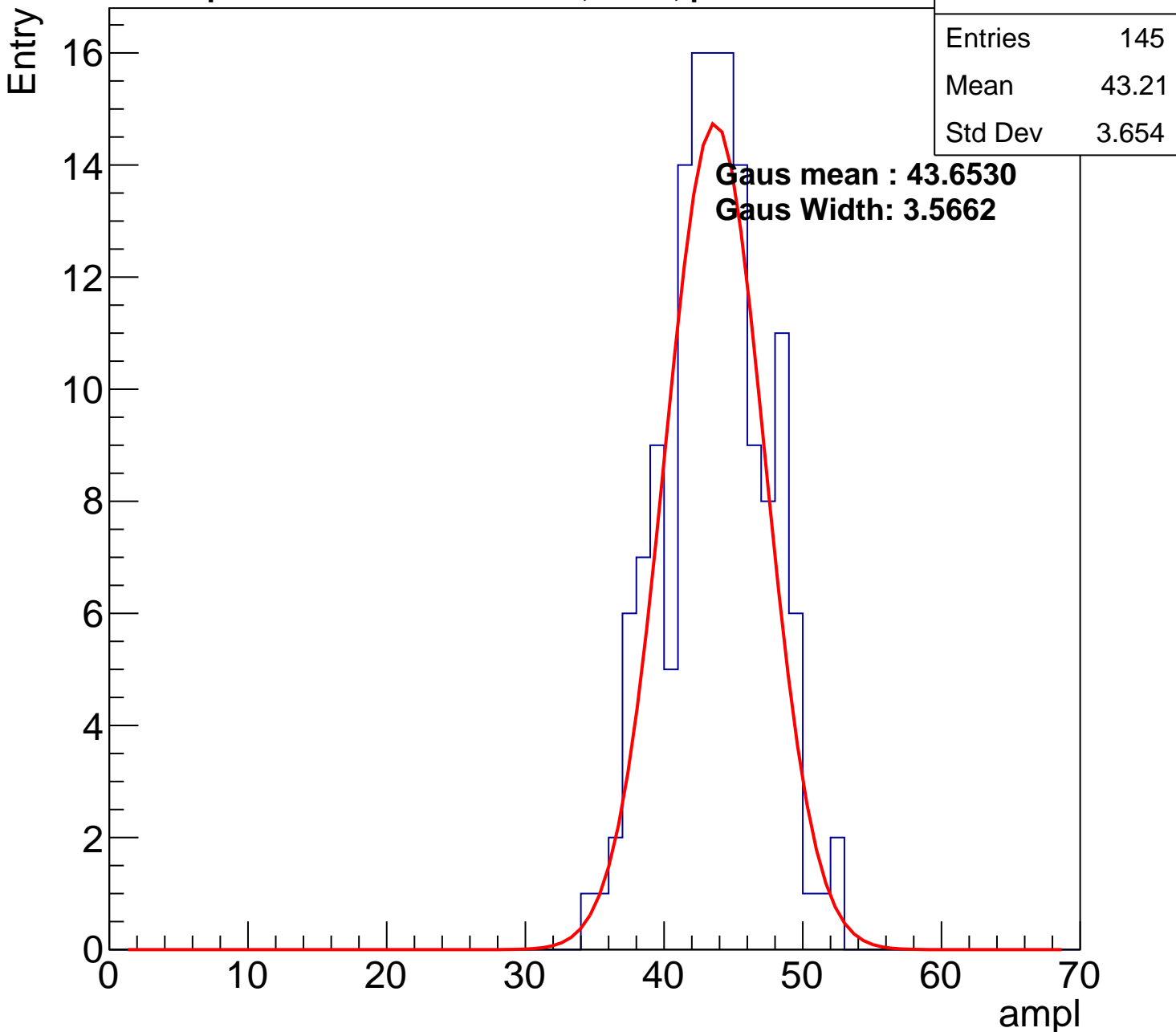
Gaus Width: 3.5662

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

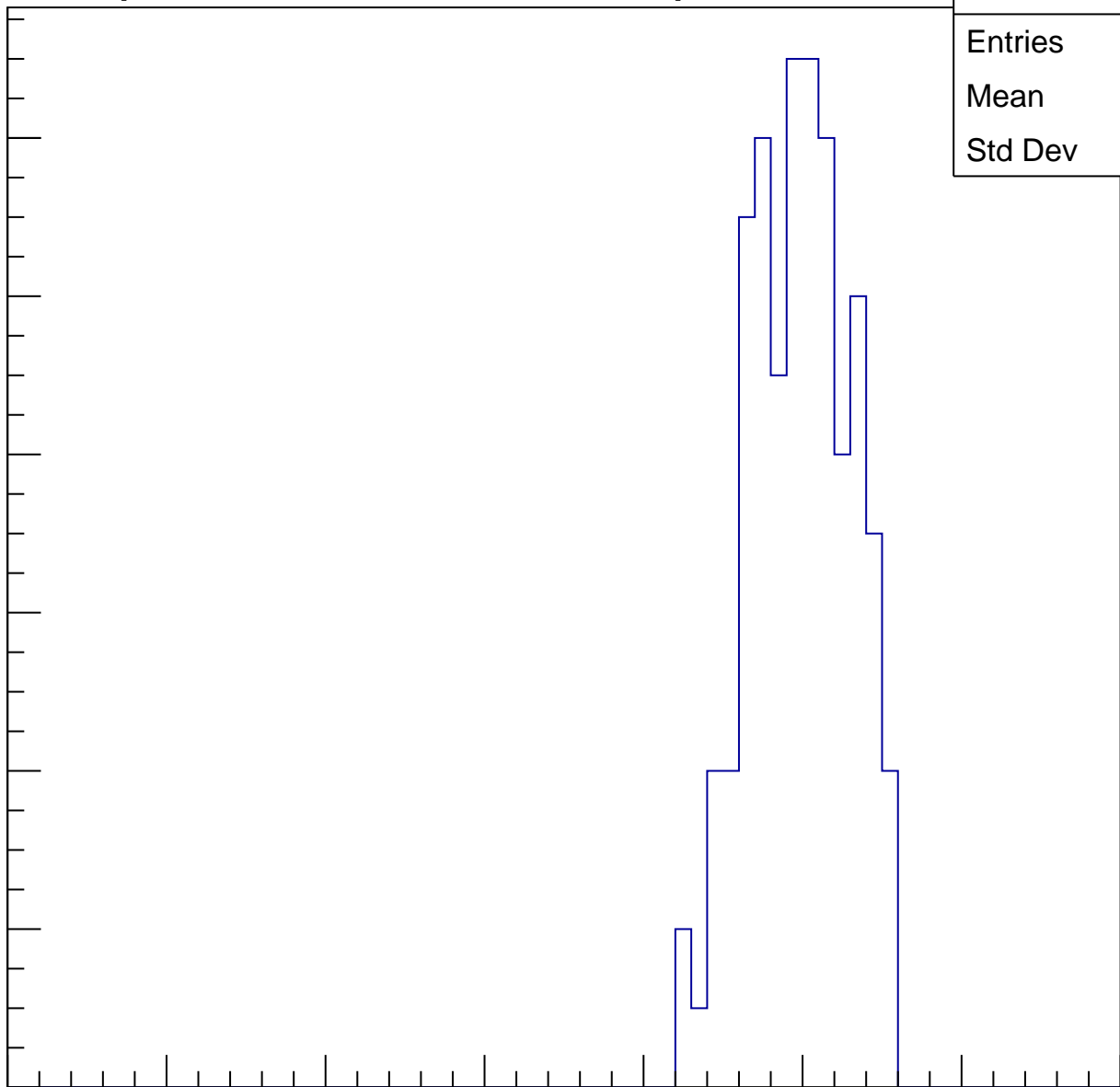
110

Mean

49.35

Std Dev

3.094



B1L001S, U22-ch8, adc4

calib_packv5_042523_0143.root, FC#2, port C2

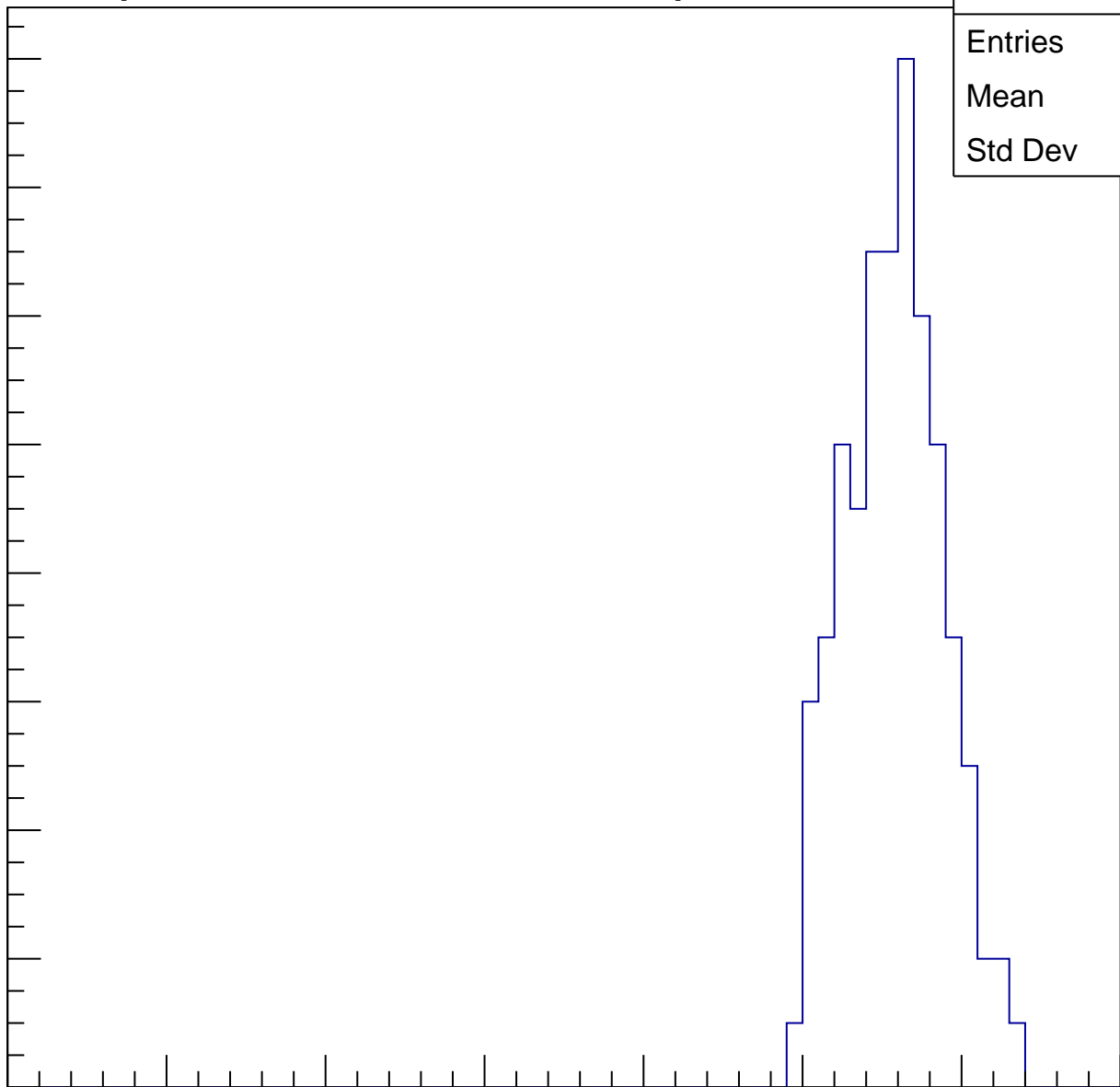
Entries	114
Mean	55.28
Std Dev	3.039

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch8, adc5

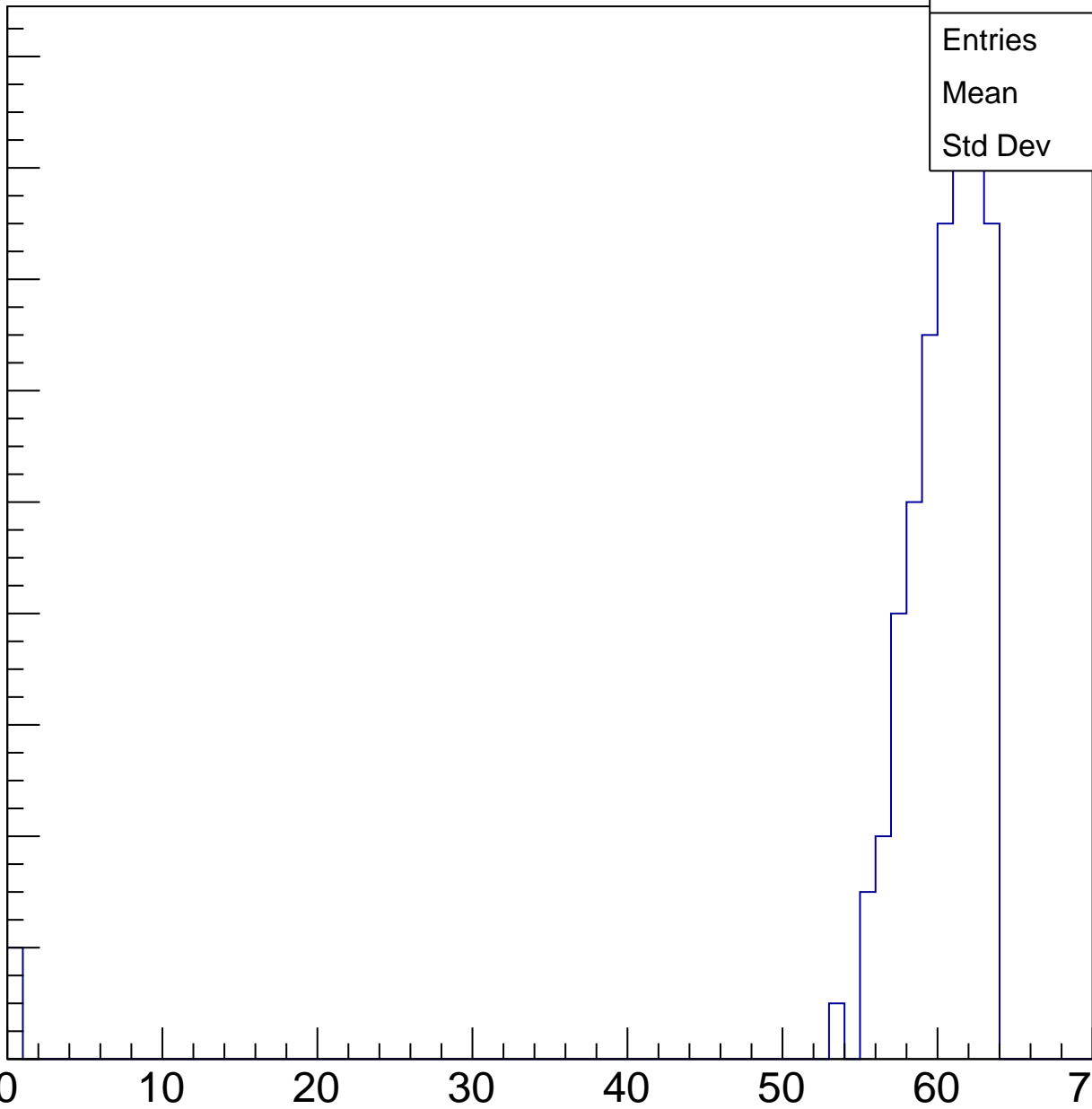
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.92
Std Dev	8.435

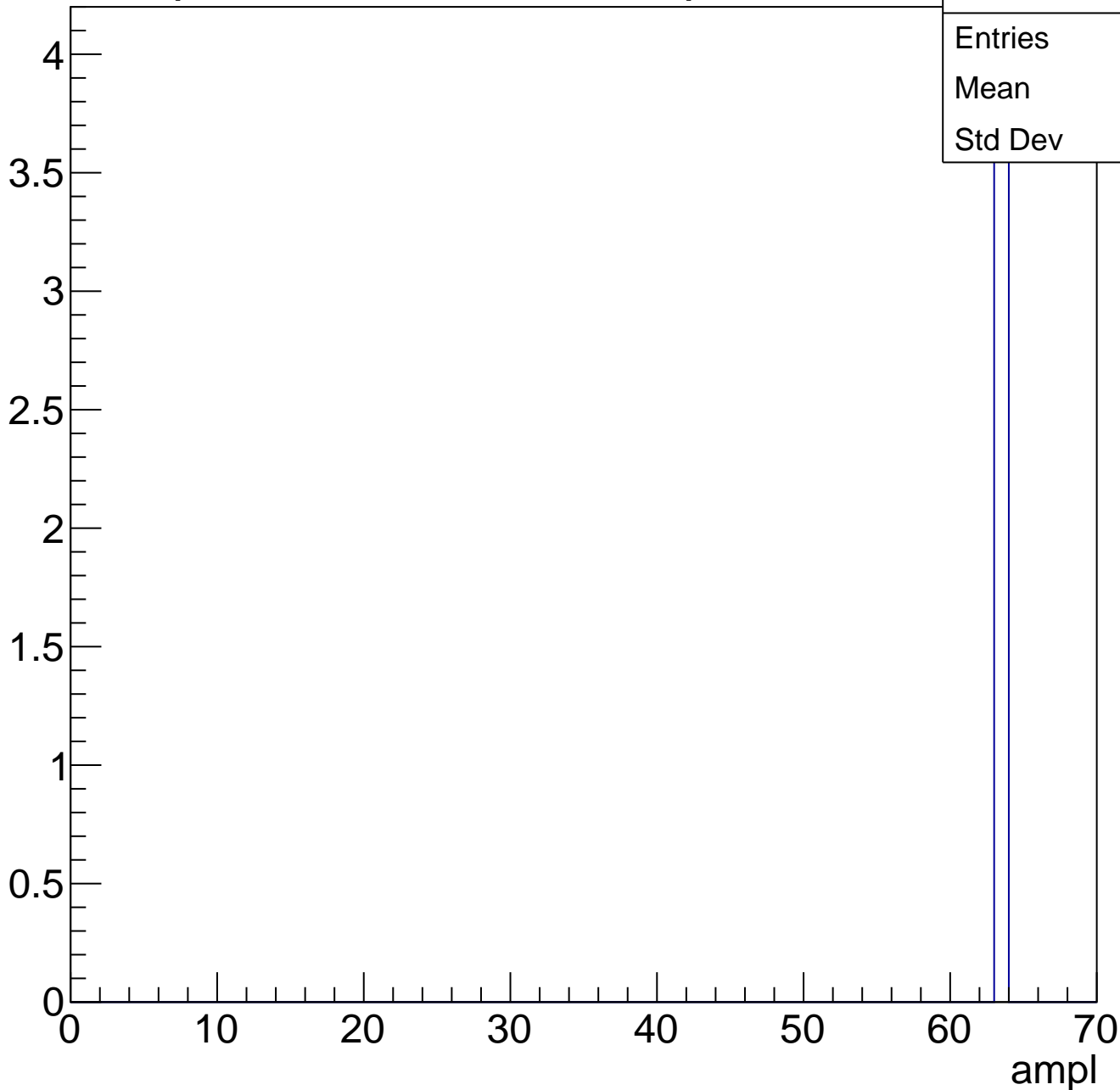
ampl



B1L001S, U22-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



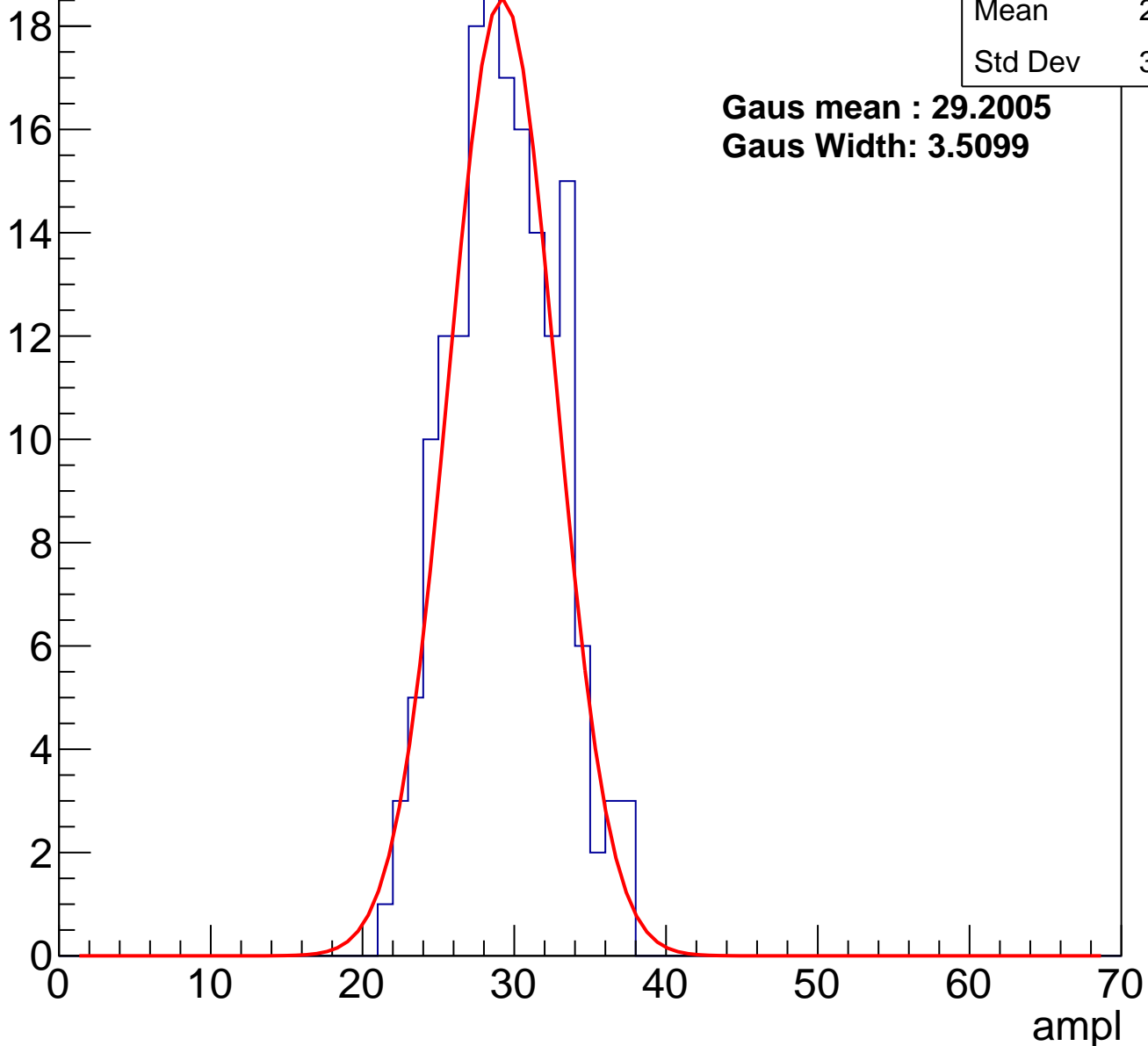
B1L001S, U22-ch9, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	28.88
Std Dev	3.459

Gaus mean : 29.2005
Gaus Width: 3.5099

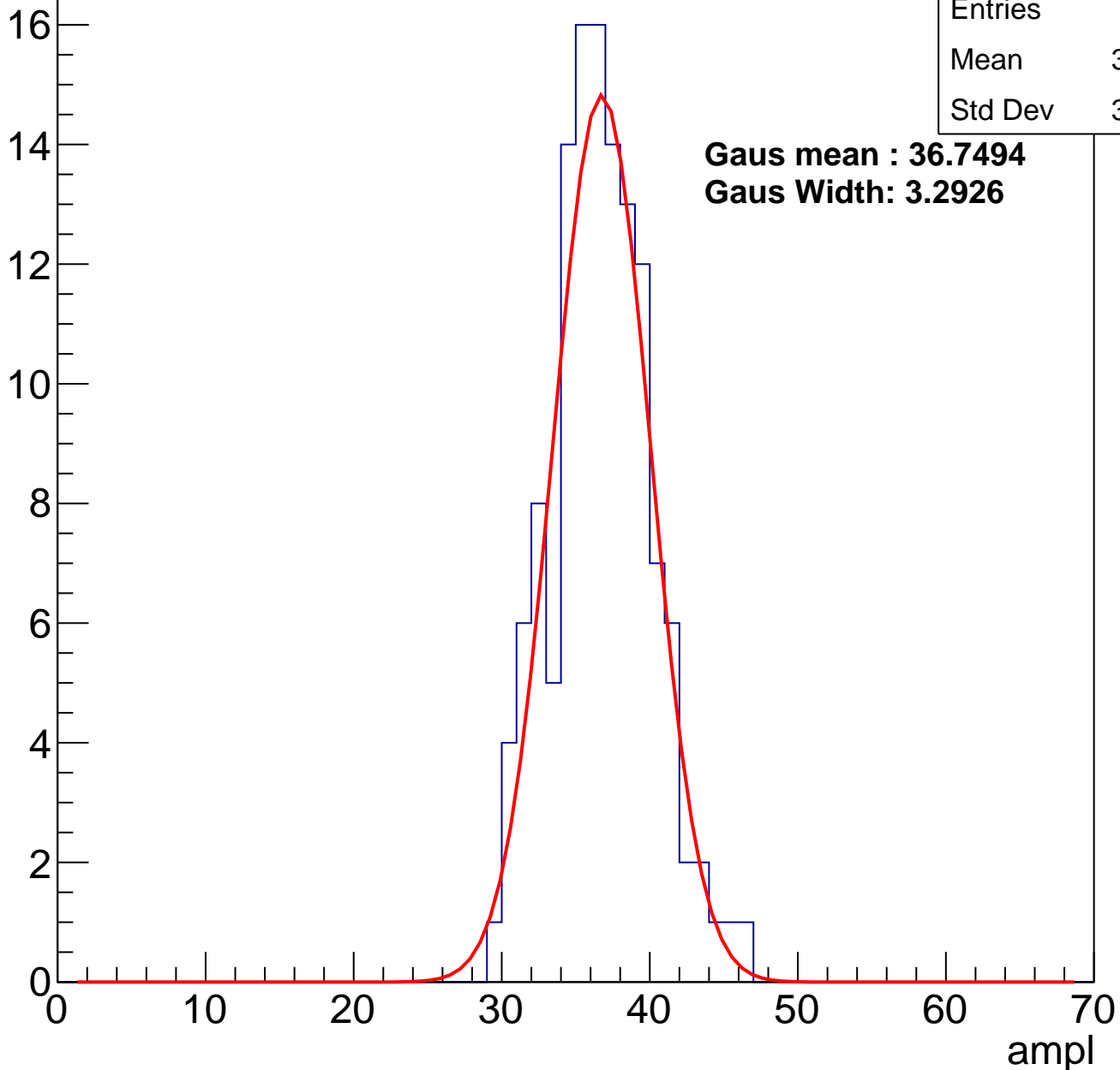
Entry



B1L001S, U22-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.25
Std Dev	2.907

Gaus mean : 42.4907

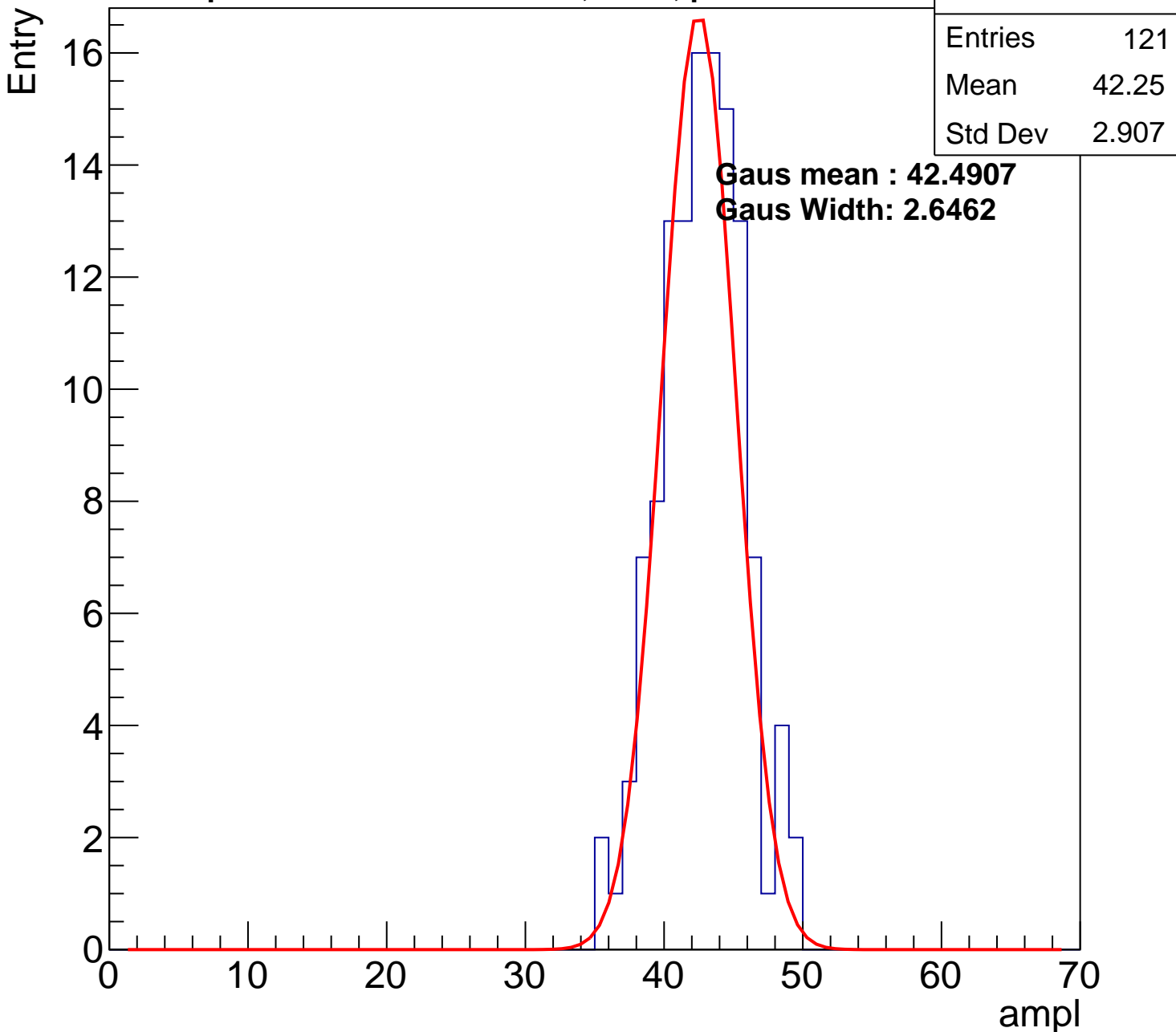
Gaus Width: 2.6462

Entry

16
14
12
10
8
6
4
2
0

ampl

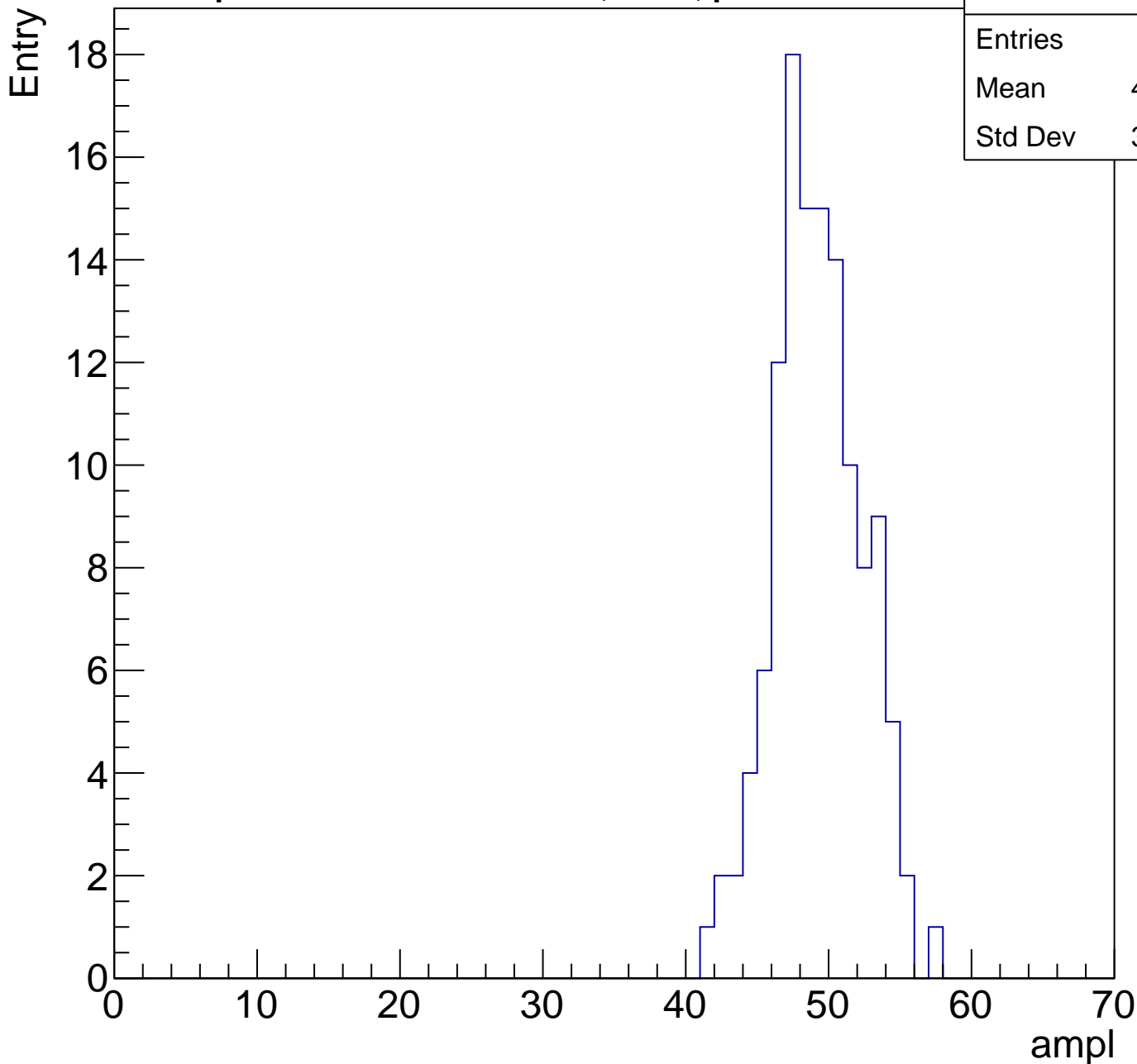
0 10 20 30 40 50 60 70



B1L001S, U22-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

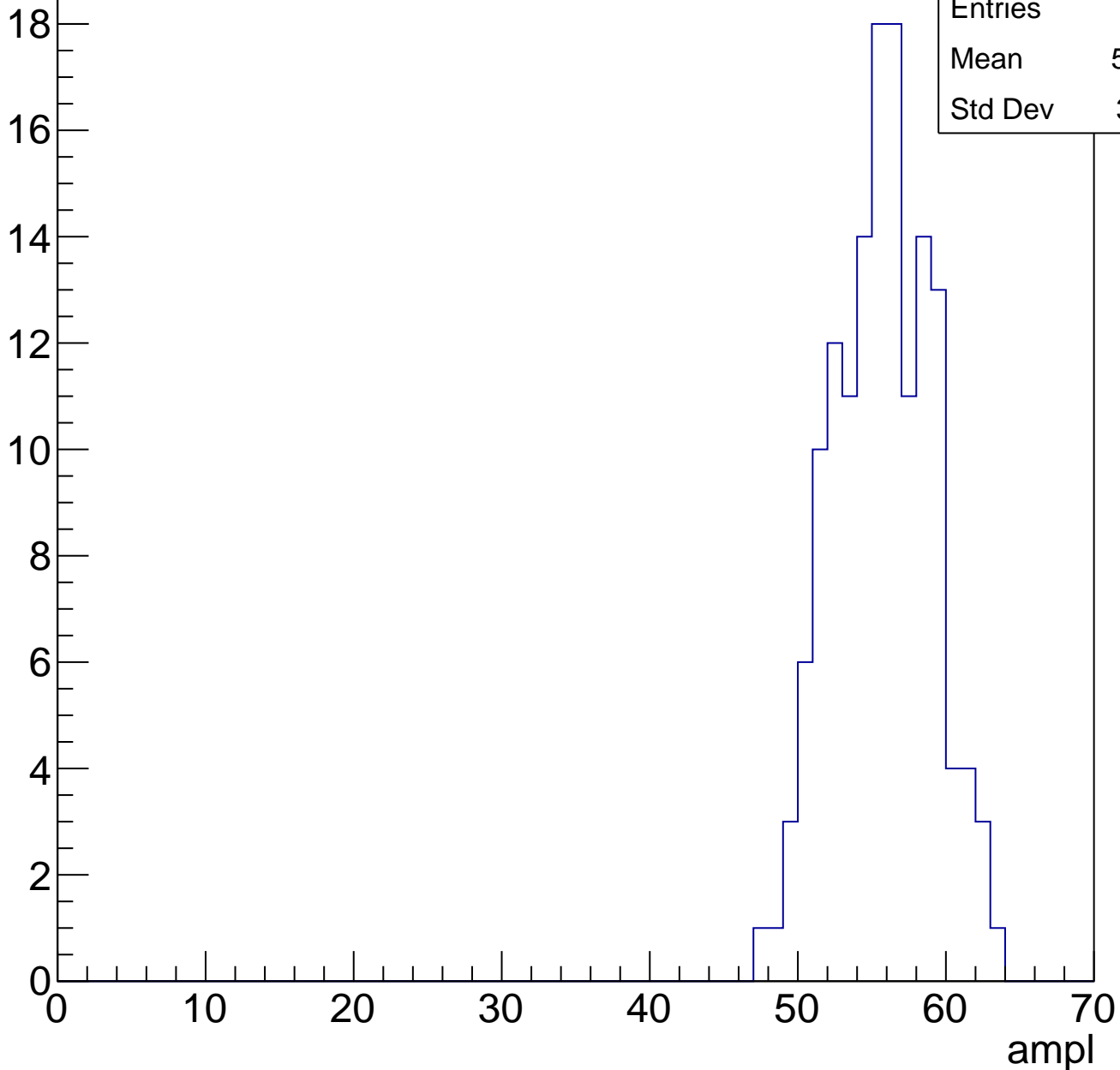
Entries	124
Mean	48.79
Std Dev	3.046



B1L001S, U22-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	144
Mean	55.22
Std Dev	3.271

B1L001S, U22-ch9, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	67
Mean	58.1
Std Dev	10.39

0

2

4

6

8

10

12

B1L001S, U22-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	26
Mean	62.08
Std Dev	1.071

Entry

12

10

8

6

4

2

0

0

10

20

30

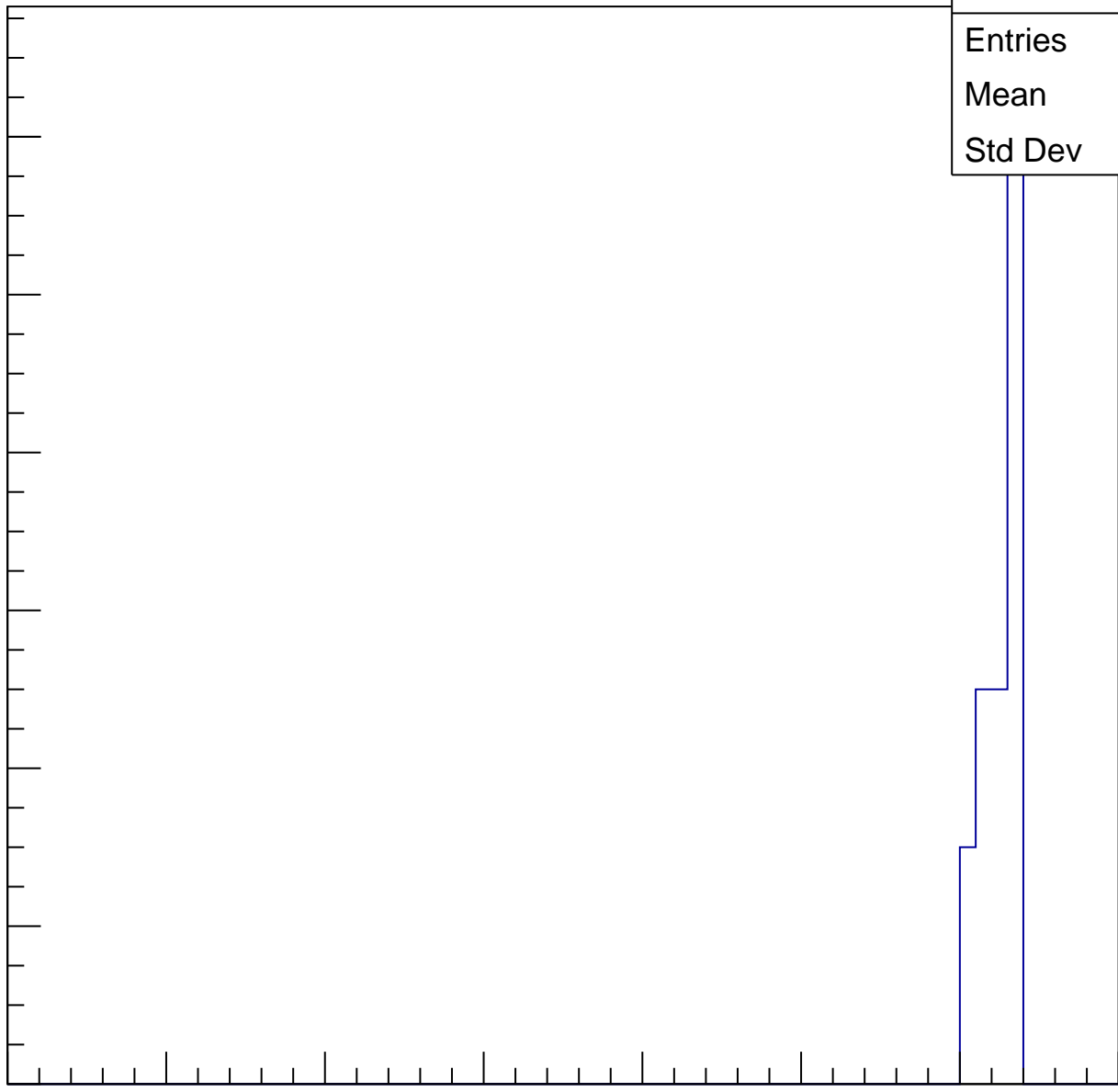
40

50

60

70

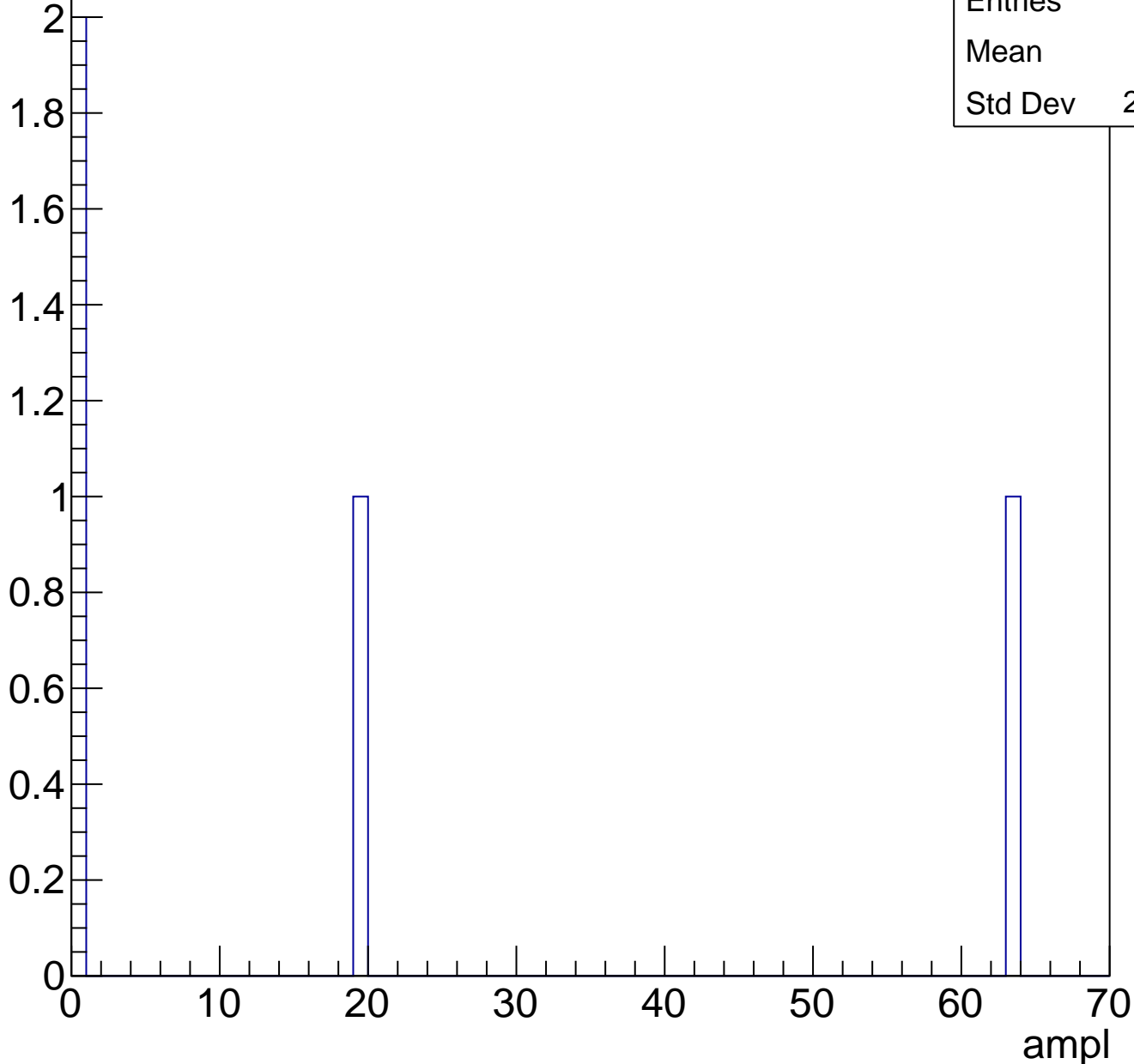
ampl



B1L001S, U22-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	20.5
Std Dev	25.73

B1L001S, U22-ch10, adc0

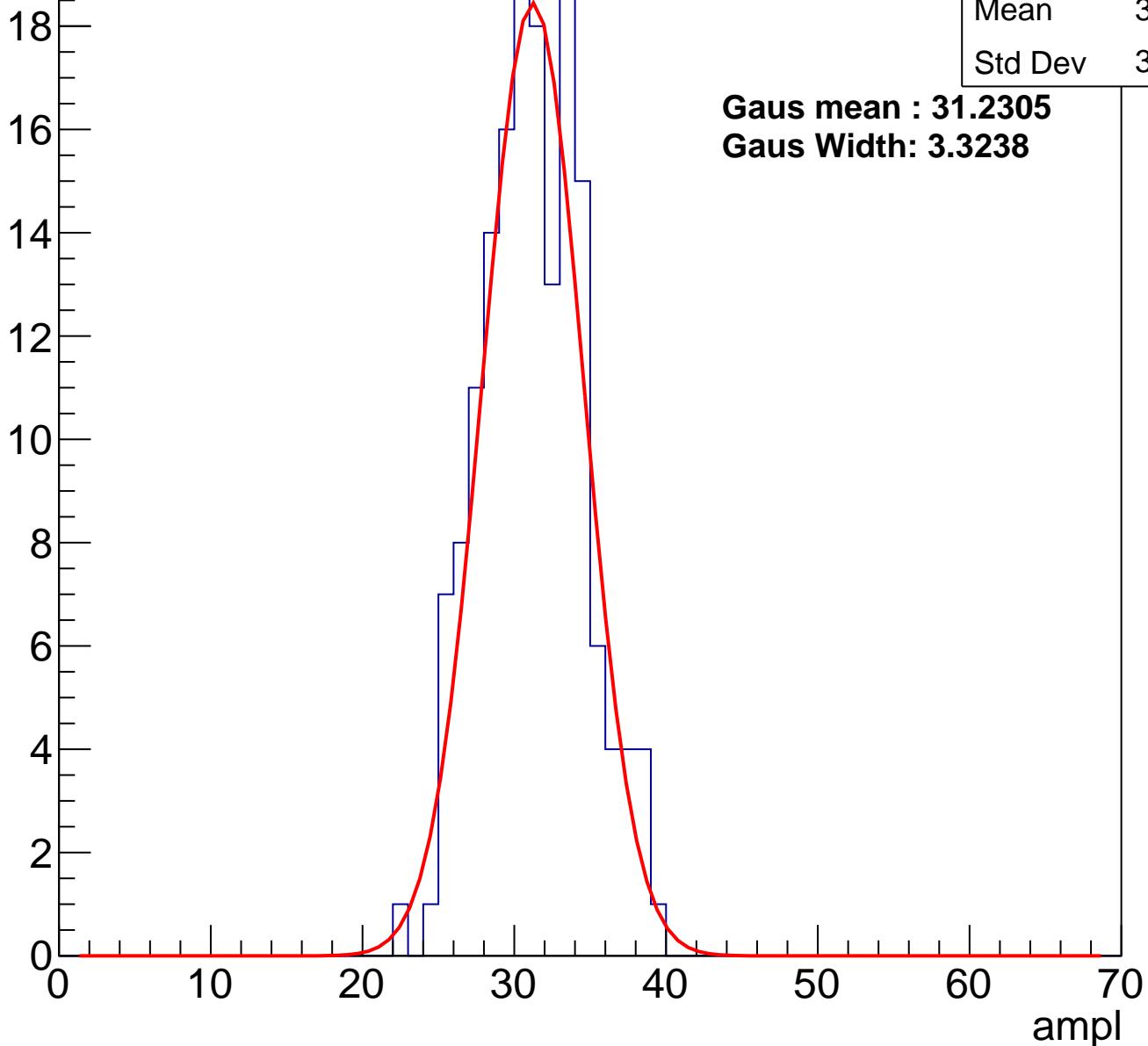
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	30.78
Std Dev	3.328

Gaus mean : 31.2305

Gaus Width: 3.3238

Entry

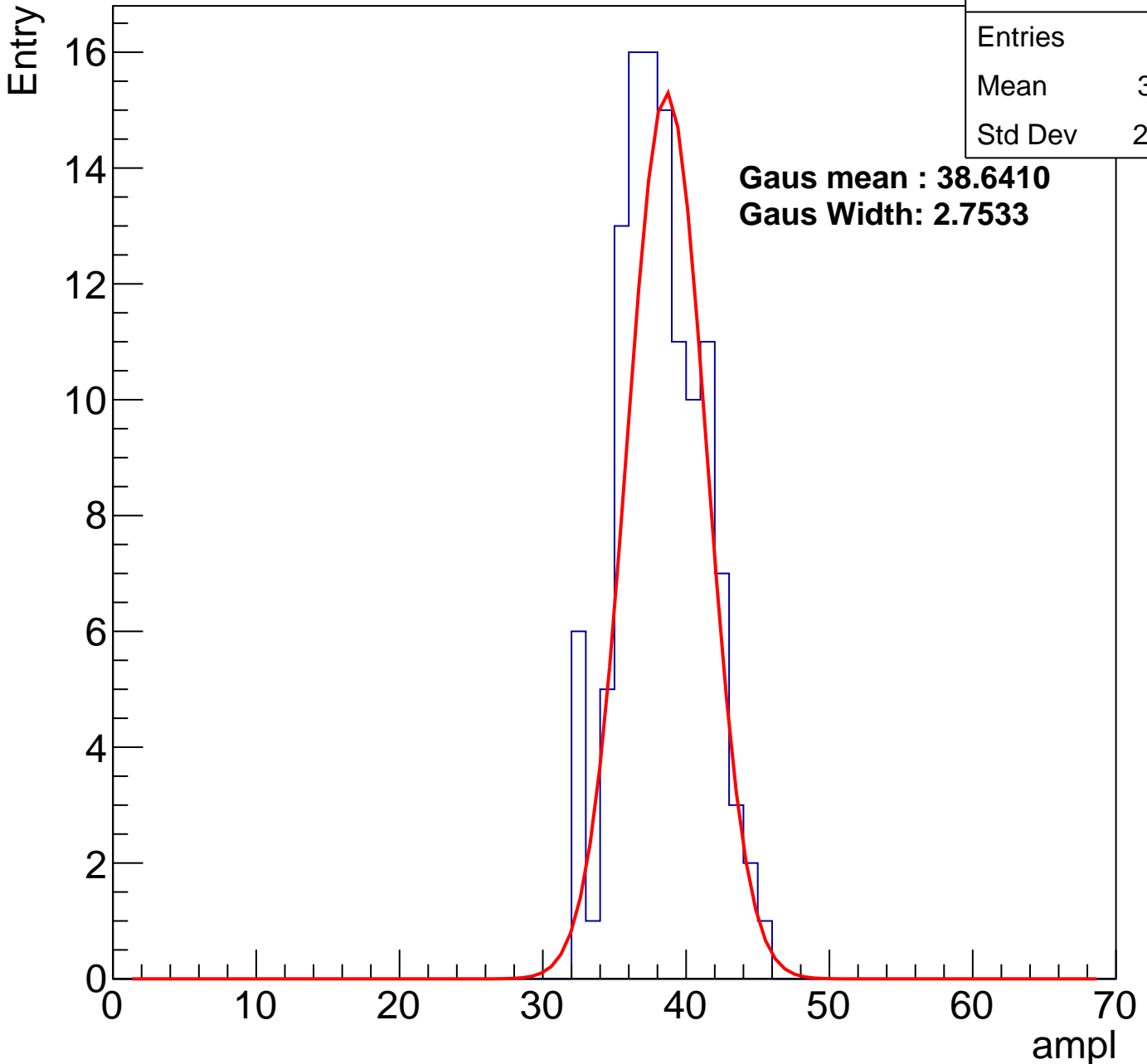


B1L001S, U22-ch10, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	37.81
Std Dev	2.882

Gaus mean : 38.6410
Gaus Width: 2.7533



B1L001S, U22-ch10, adc2

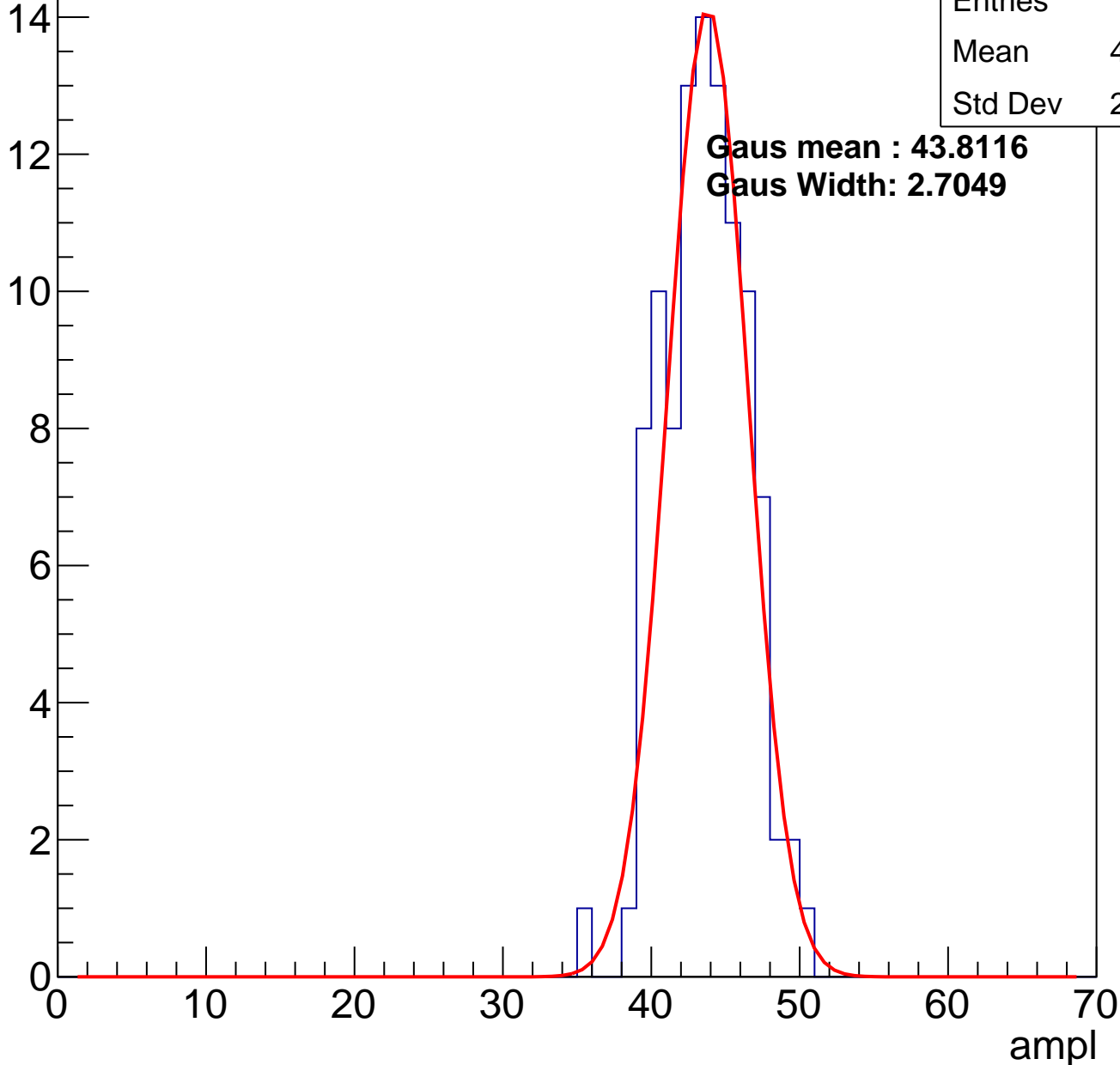
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	101
Mean	43.18
Std Dev	2.777

Gaus mean : 43.8116

Gaus Width: 2.7049

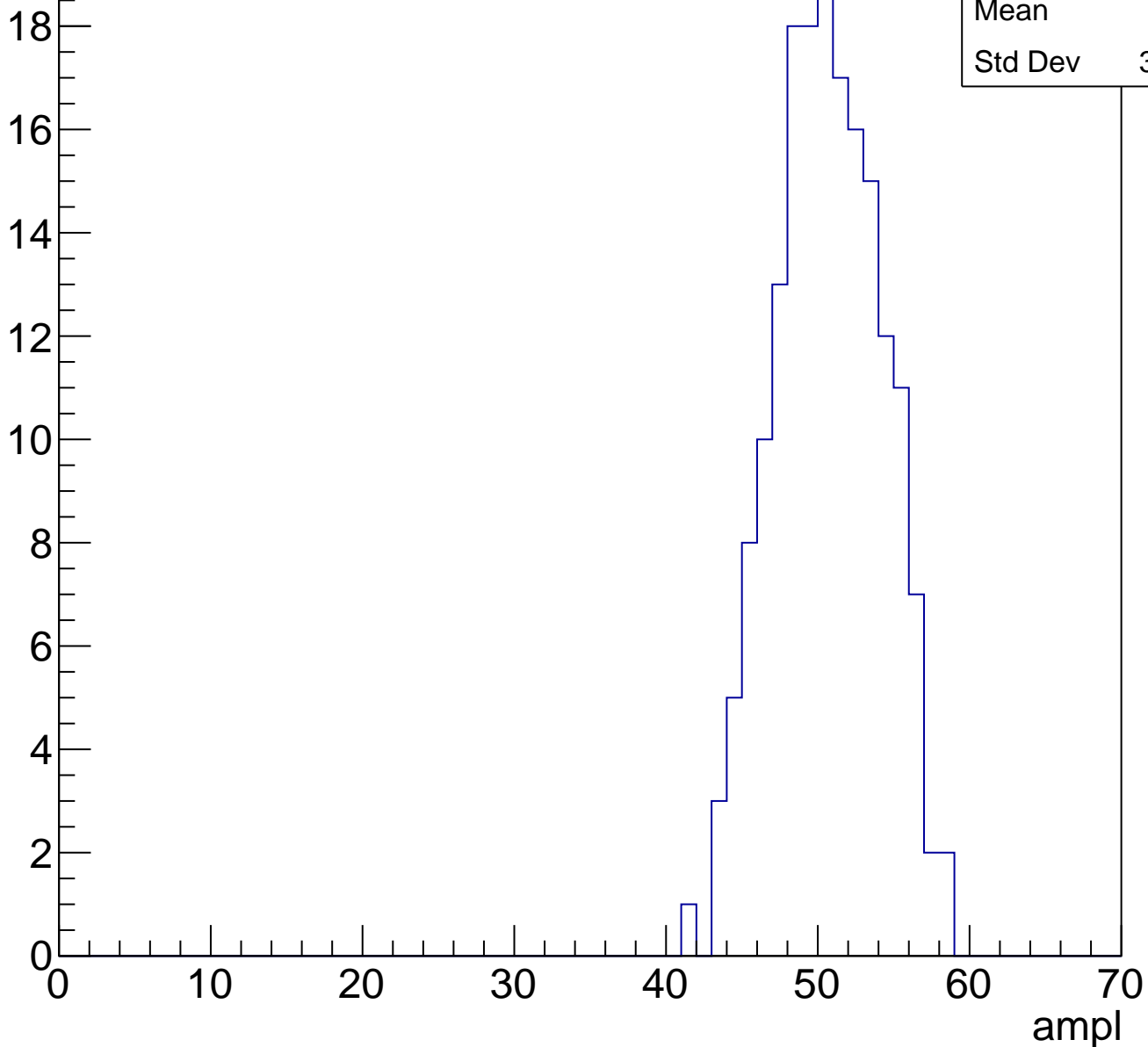


B1L001S, U22-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	177
Mean	50.2
Std Dev	3.468

Entry



B1L001S, U22-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

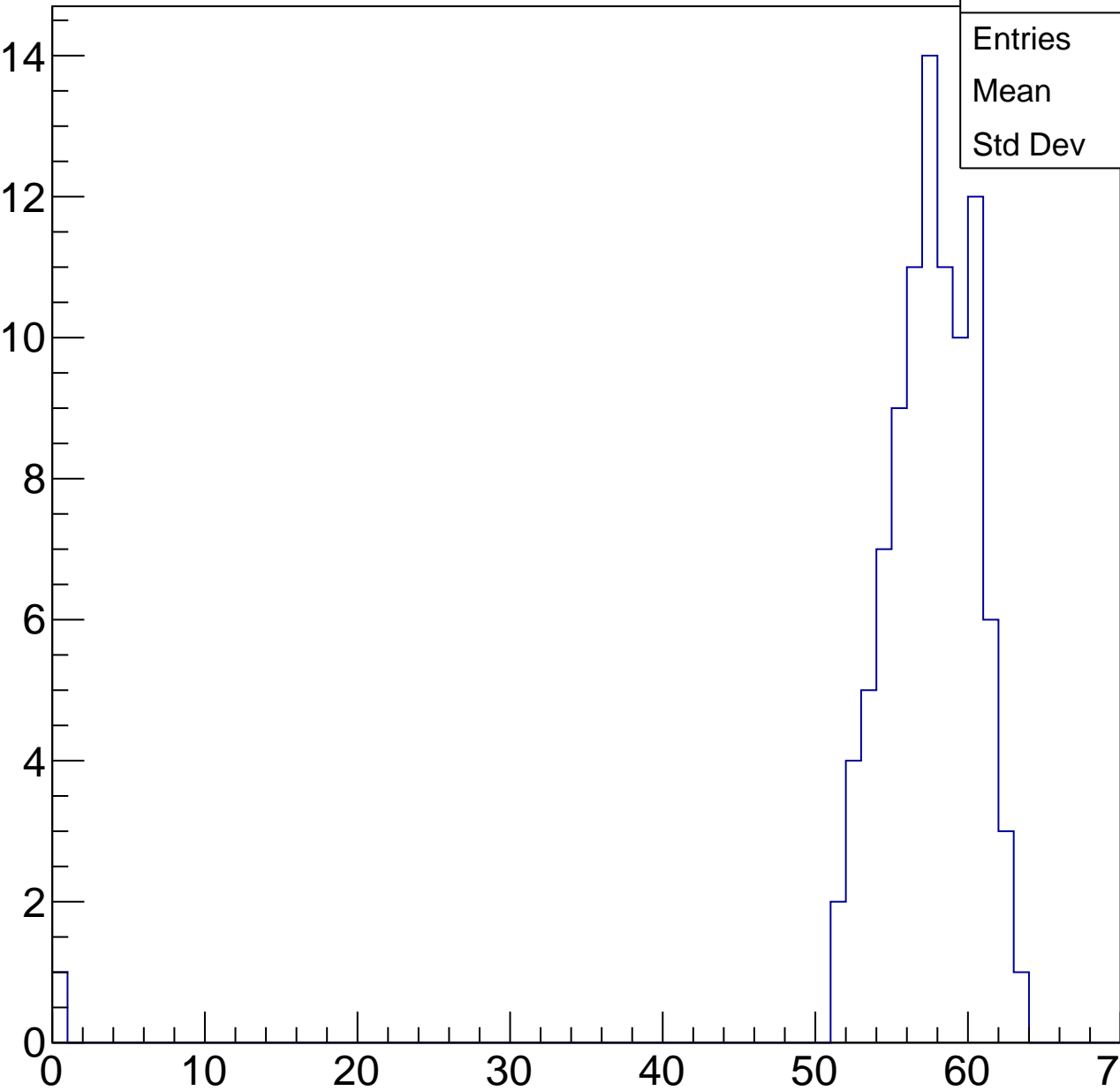
Entries	96
Mean	56.51
Std Dev	6.418

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch10, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

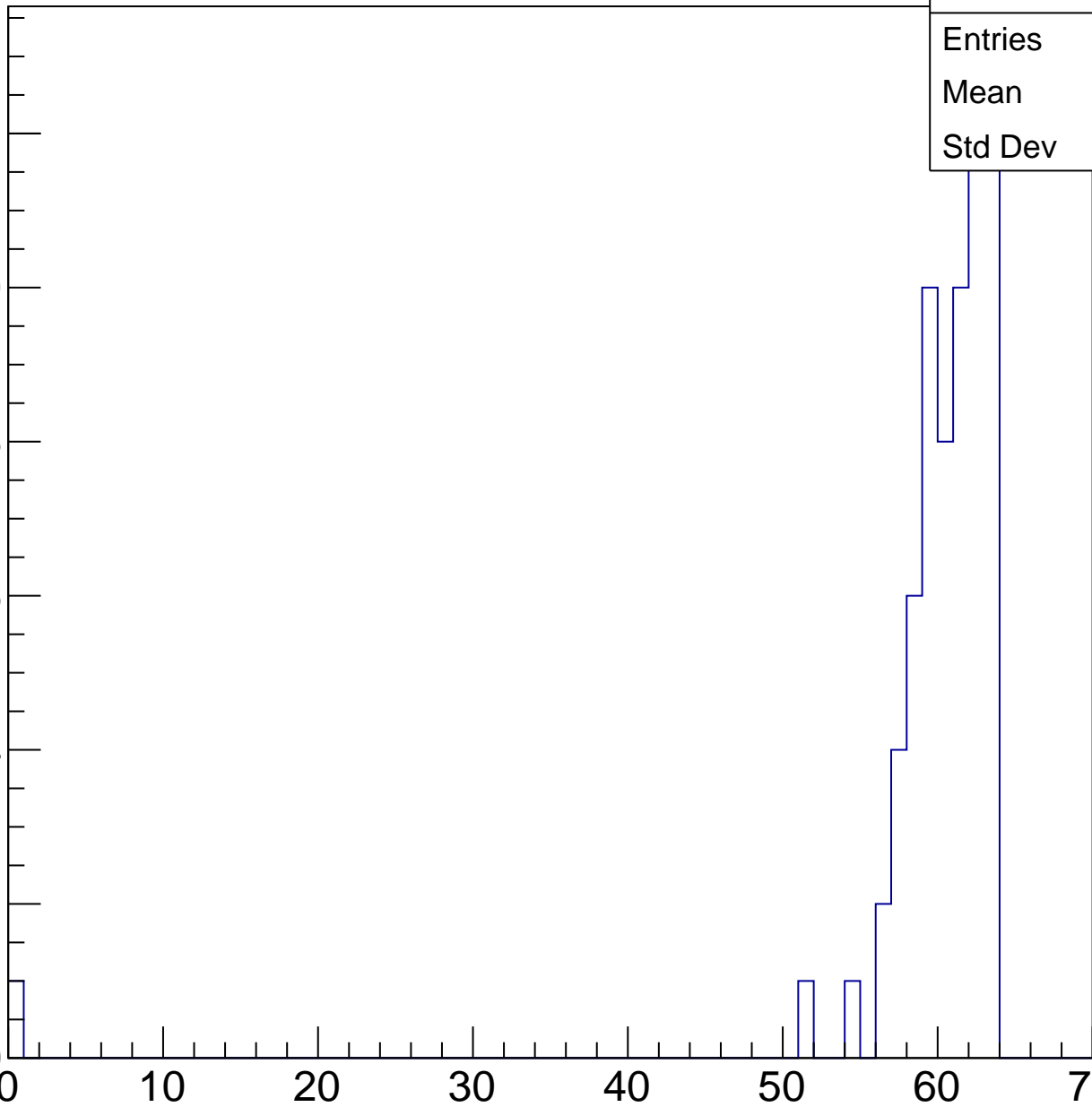
4

2

0

Entries	68
Mean	59.34
Std Dev	7.63

ampl

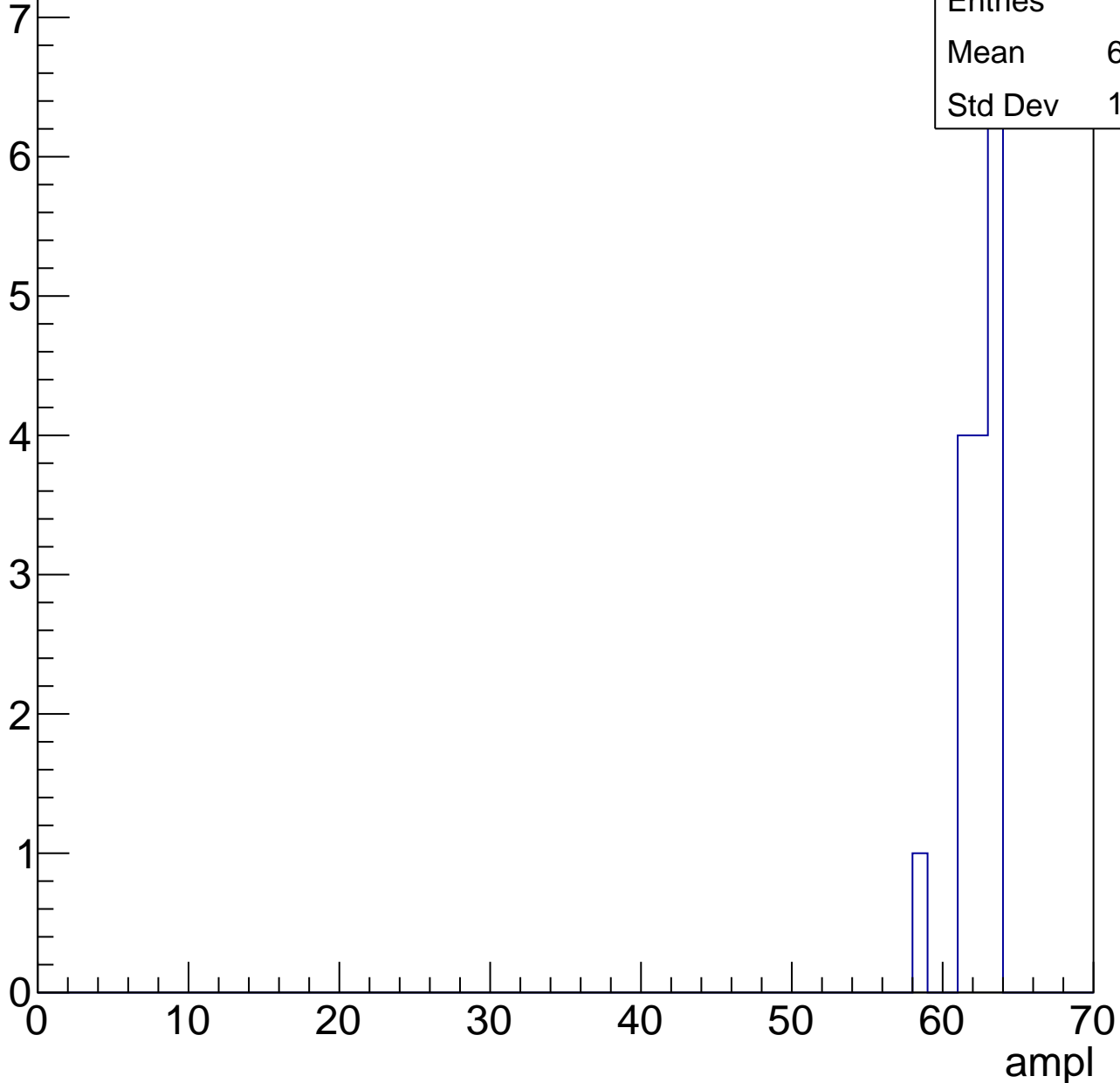


B1L001S, U22-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	61.94
Std Dev	1.298



B1L001S, U22-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

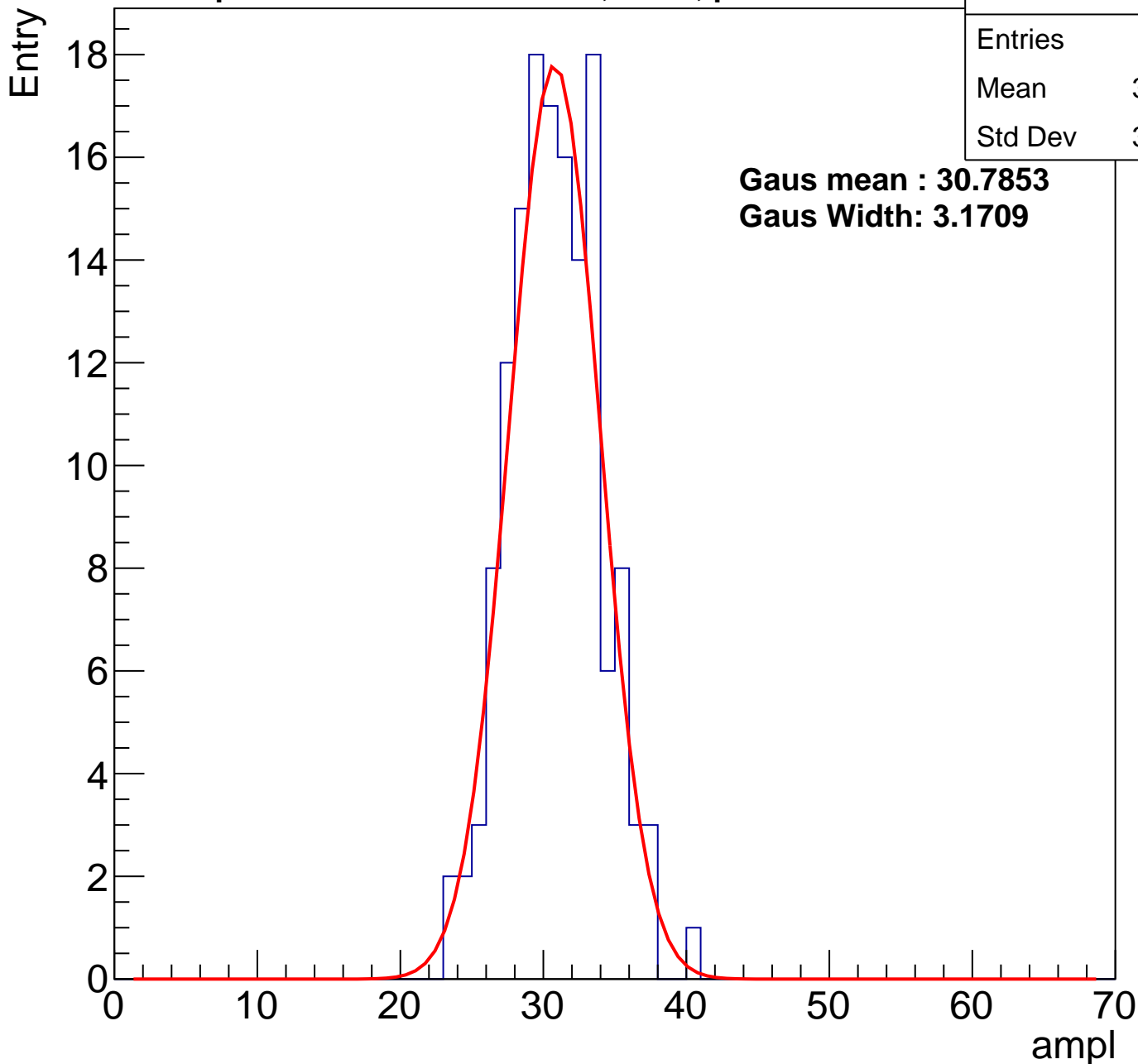
B1L001S, U22-ch11, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	30.37
Std Dev	3.123

Gaus mean : 30.7853

Gaus Width: 3.1709

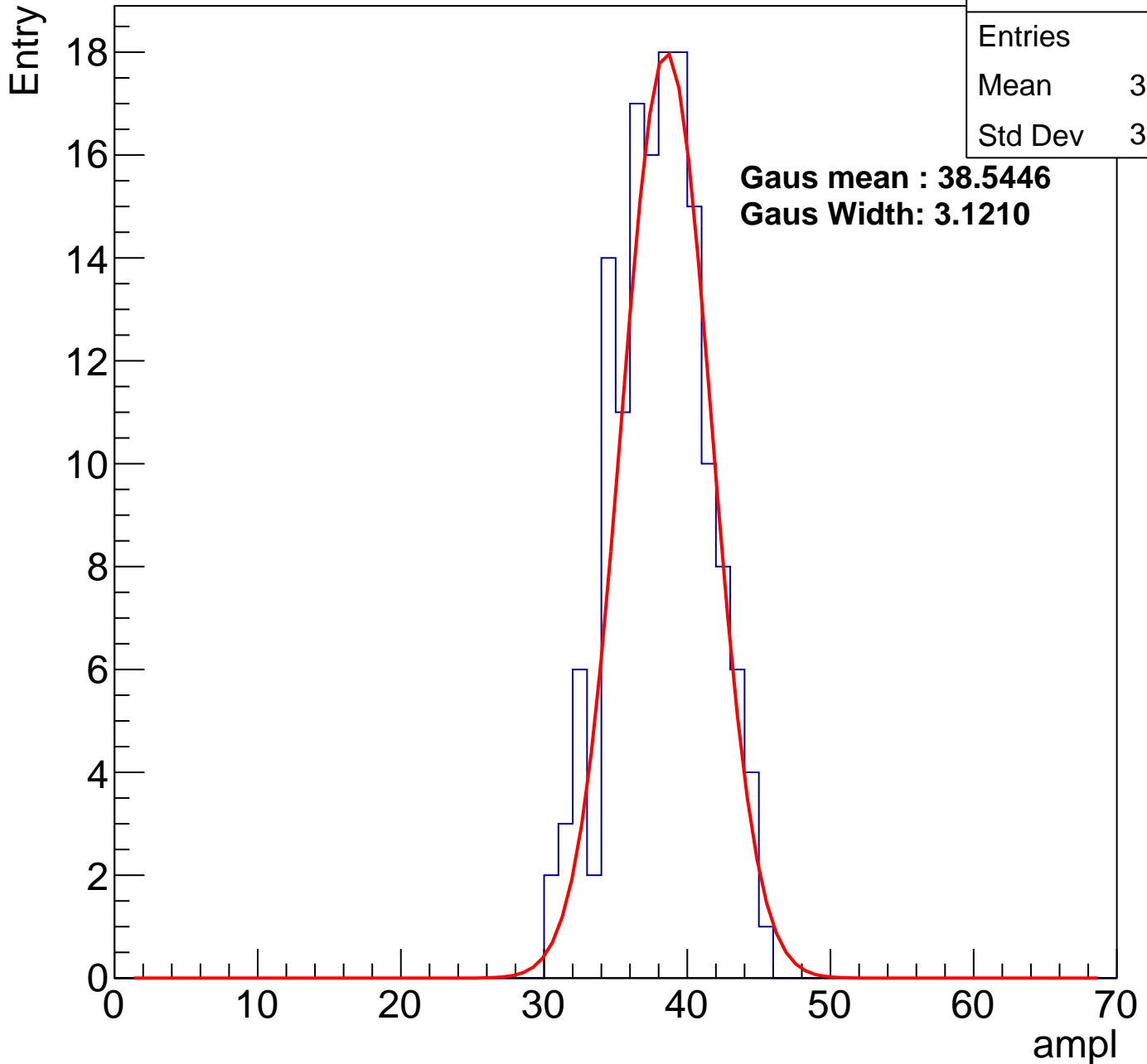


B1L001S, U22-ch11, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	37.66
Std Dev	3.224

Gaus mean : 38.5446
Gaus Width: 3.1210



B1L001S, U22-ch11, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	44.51
Std Dev	3.263

Gaus mean : 44.8895

Gaus Width: 3.0352

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

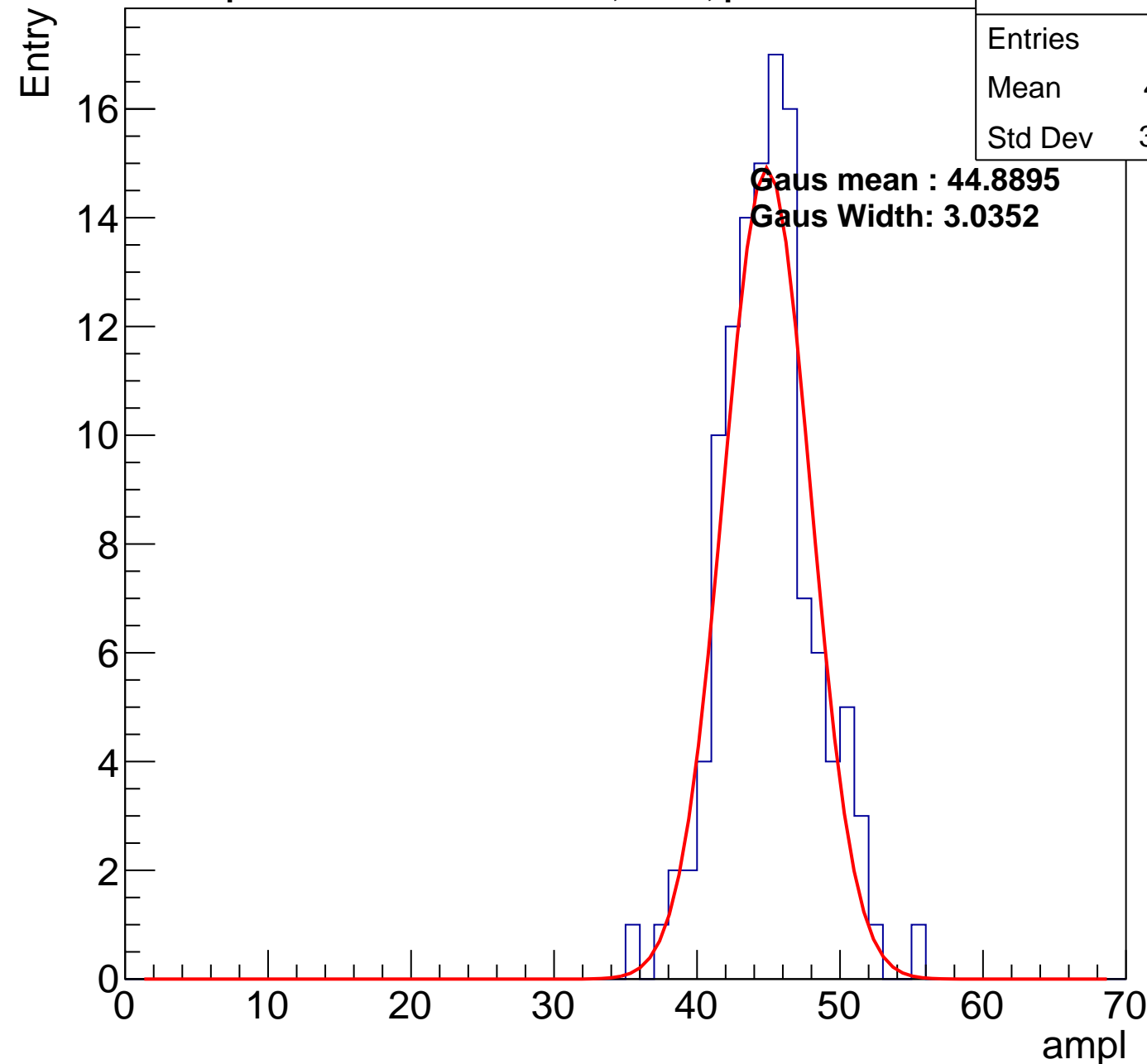
40

50

60

ampl

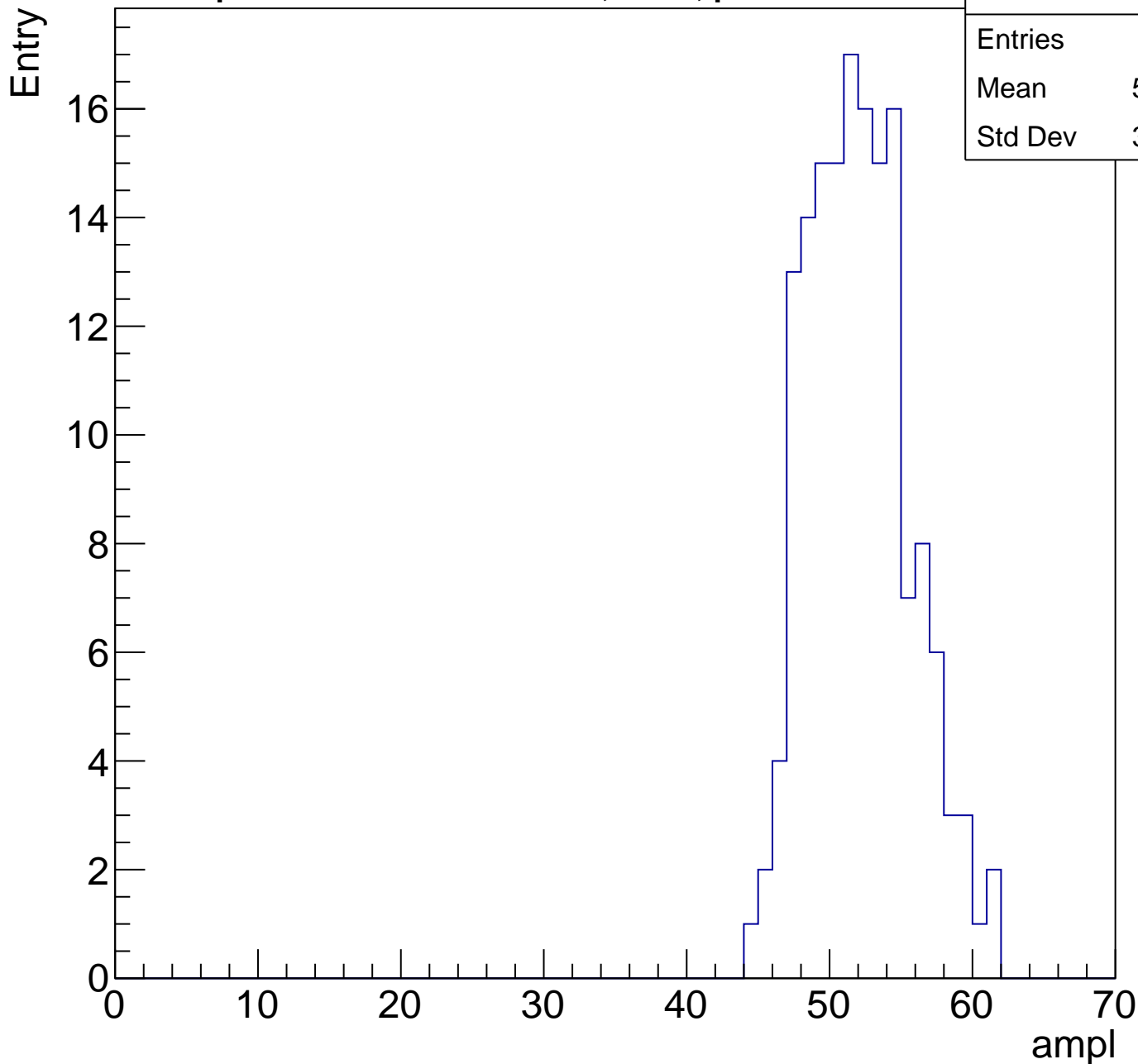
70



B1L001S, U22-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	51.59
Std Dev	3.506



B1L001S, U22-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

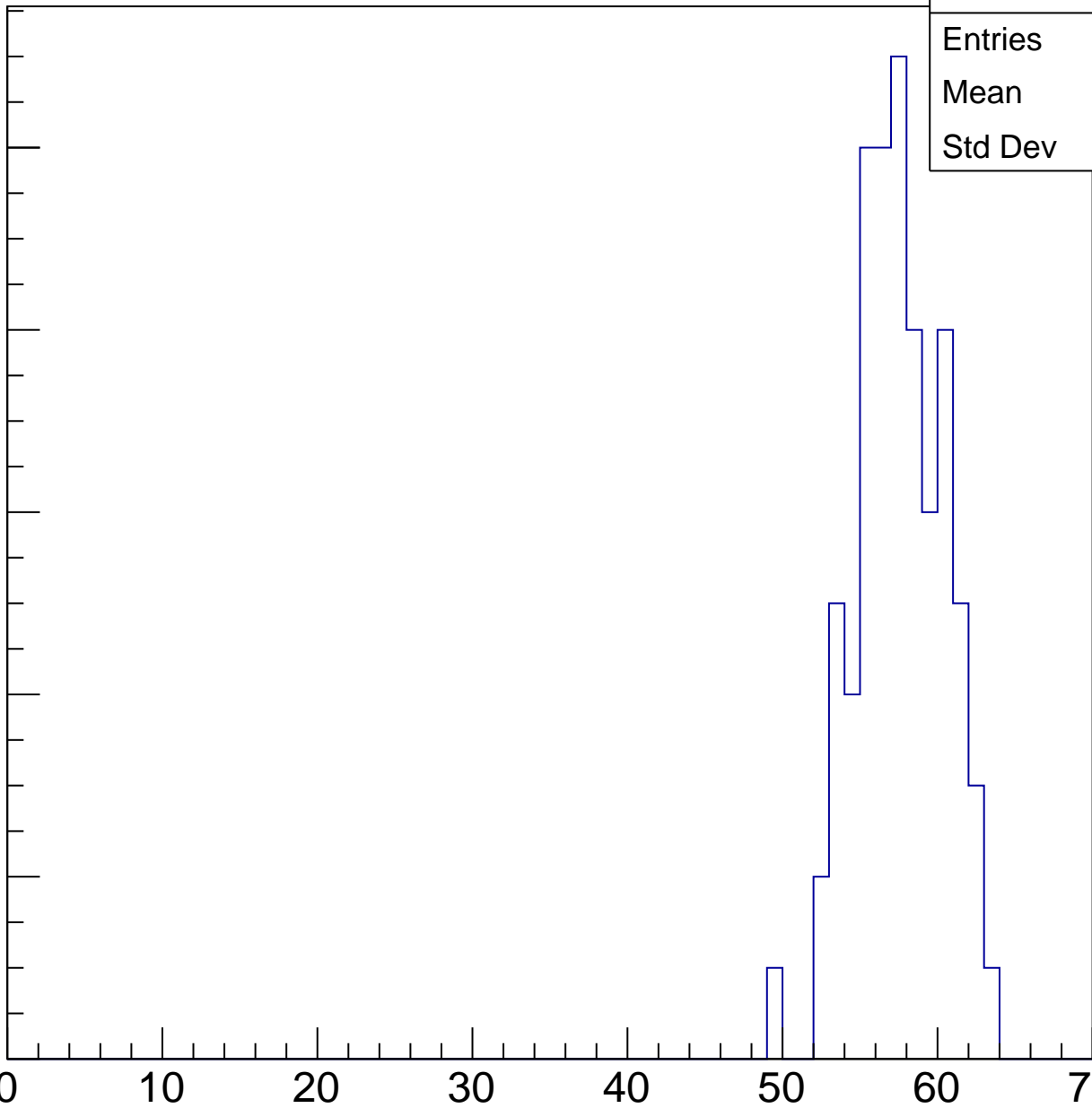
30

40

50

ampl

Entries	74
Mean	57.07
Std Dev	2.787



B1L001S, U22-ch11, adc5

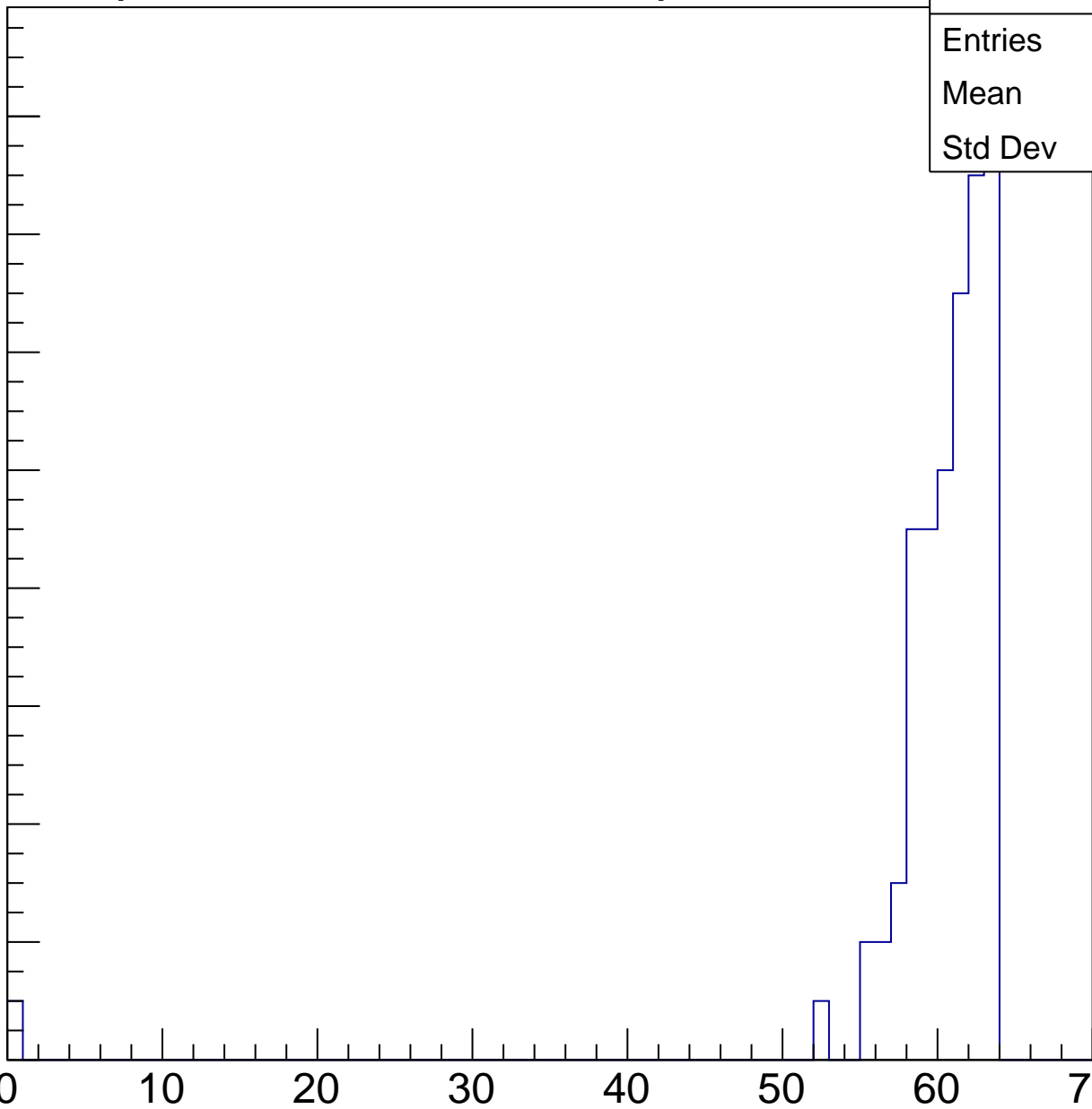
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.66
Std Dev	7.014

ampl



B1L001S, U22-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

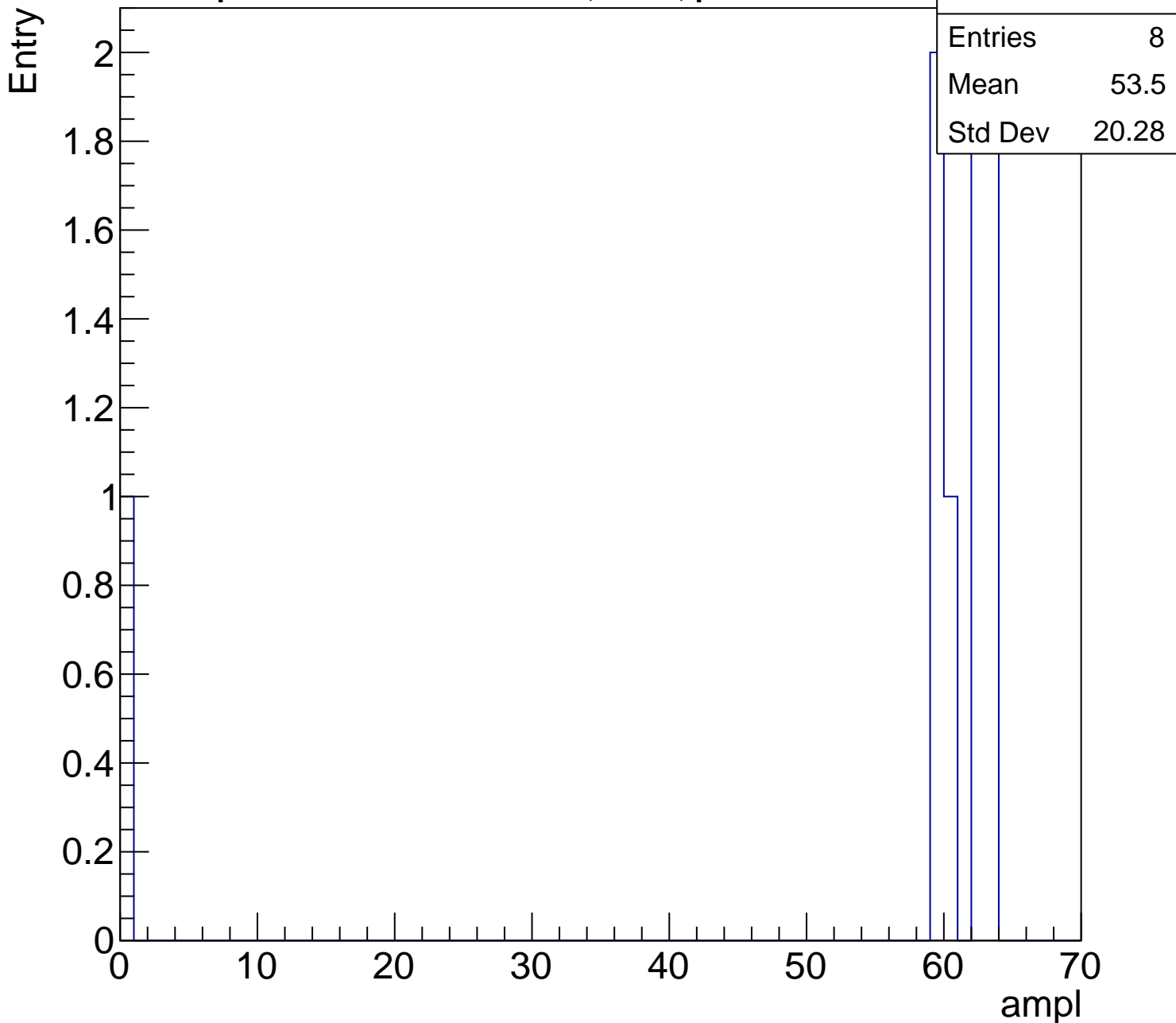
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.5
Std Dev	20.28

0 10 20 30 40 50 60 70

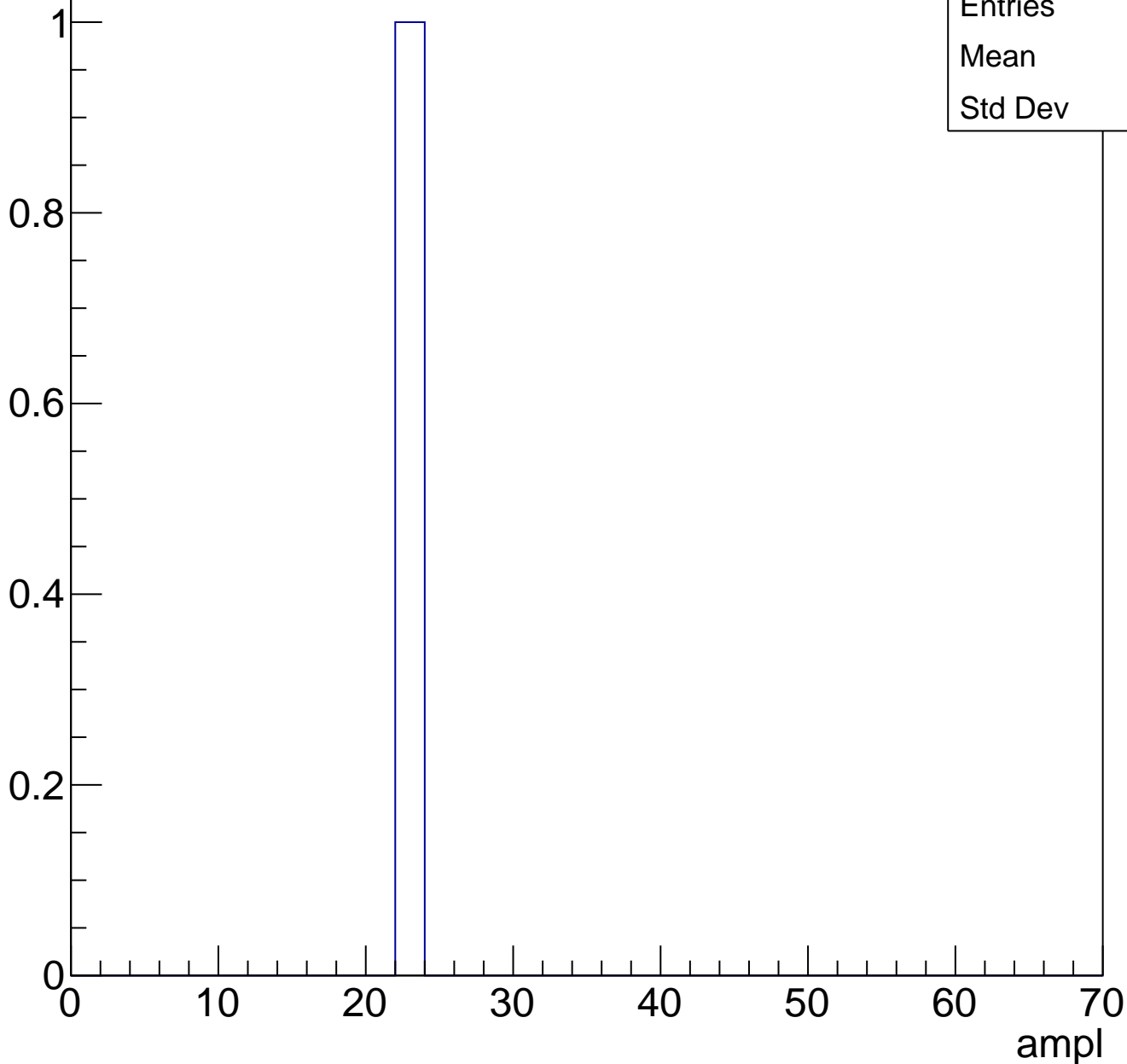
ampl



B1L001S, U22-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch12, adc0

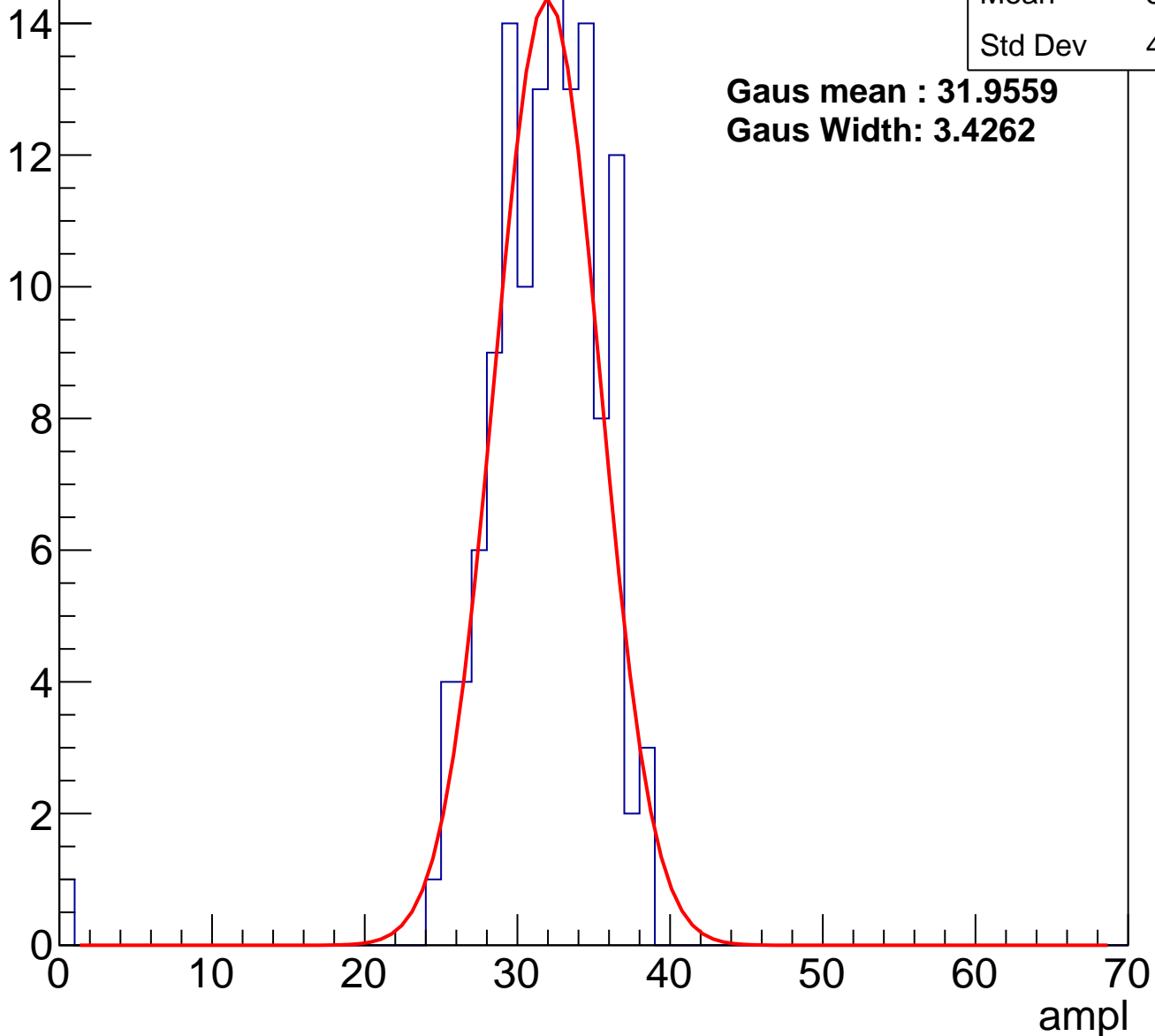
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	31.29
Std Dev	4.247

Gaus mean : 31.9559

Gaus Width: 3.4262

Entry



B1L001S, U22-ch12, adc1

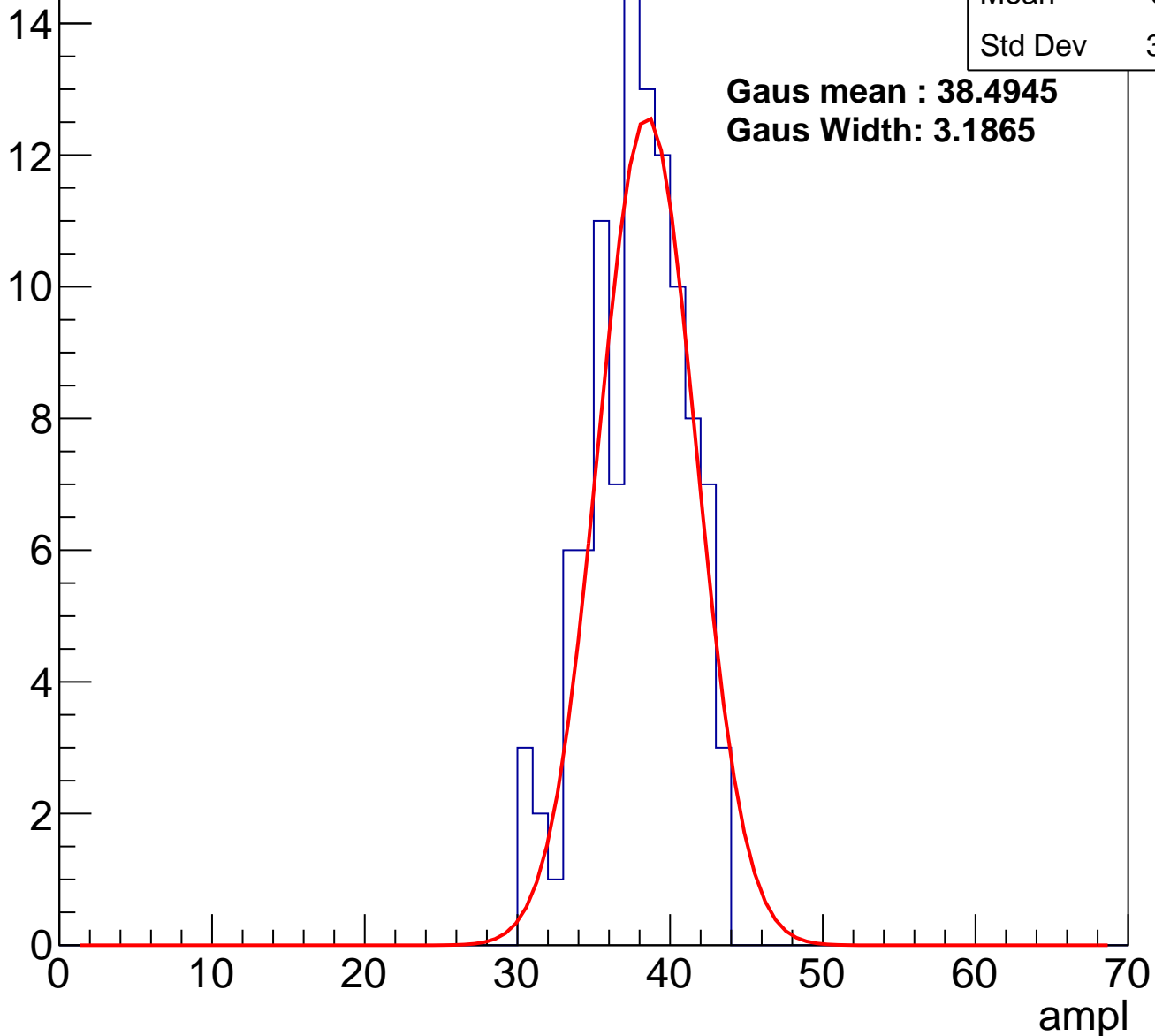
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	37.41
Std Dev	3.084

Gaus mean : 38.4945

Gaus Width: 3.1865

Entry



B1L001S, U22-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	43.64
Std Dev	3.291

Gaus mean : 44.0028

Gaus Width: 3.4153

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

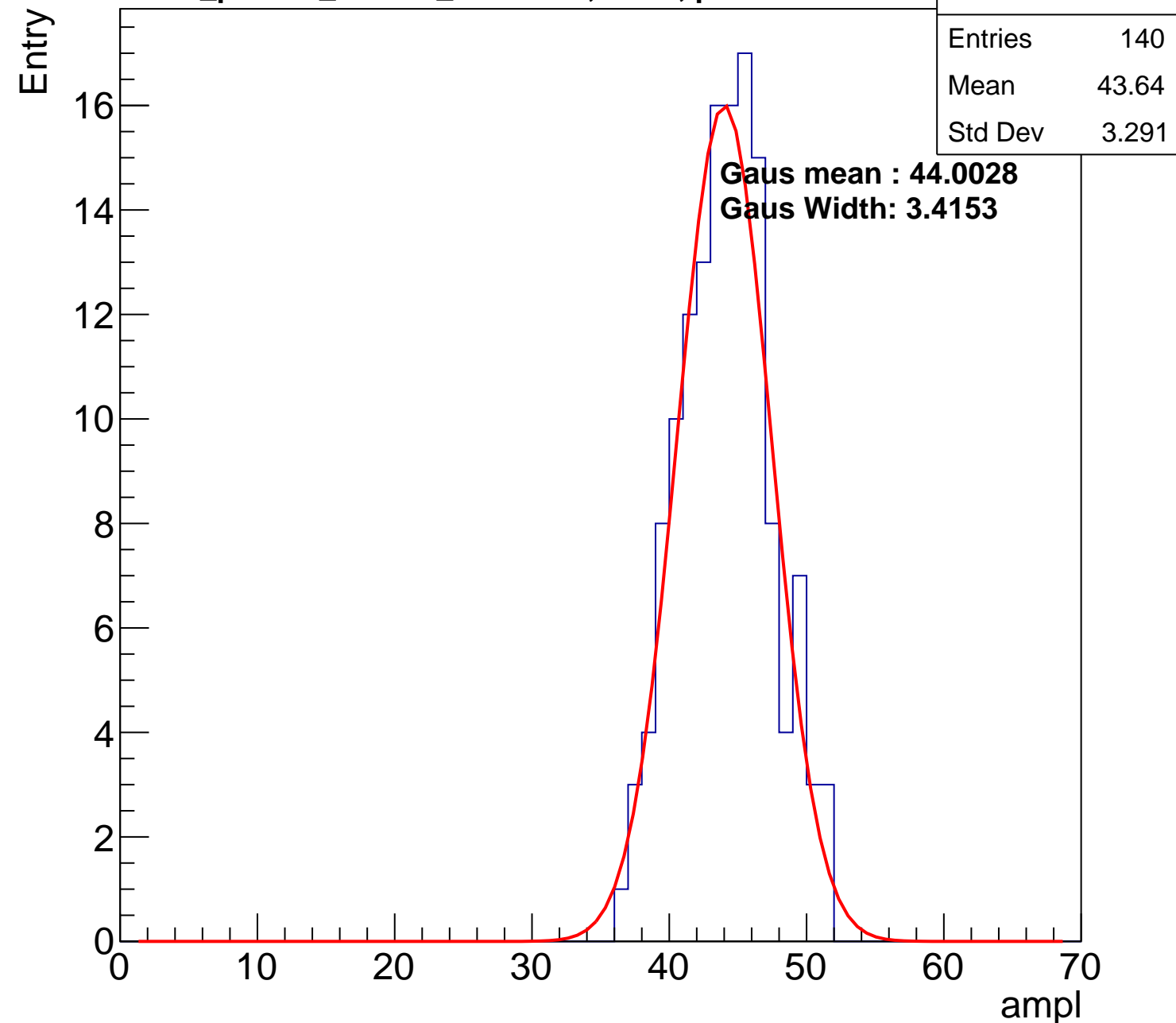
40

50

60

70

ampl

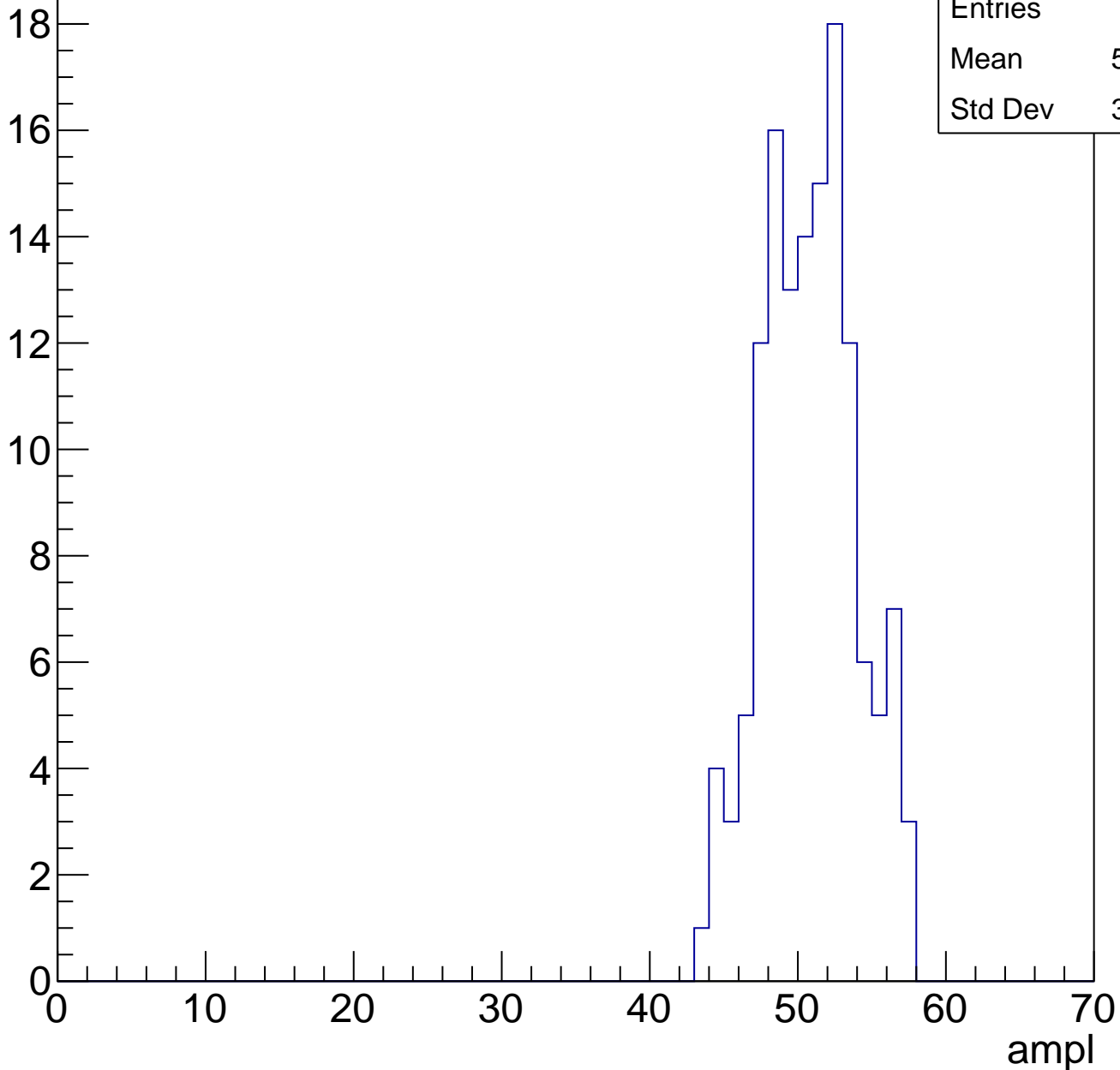


B1L001S, U22-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	50.39
Std Dev	3.148

Entry



B1L001S, U22-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

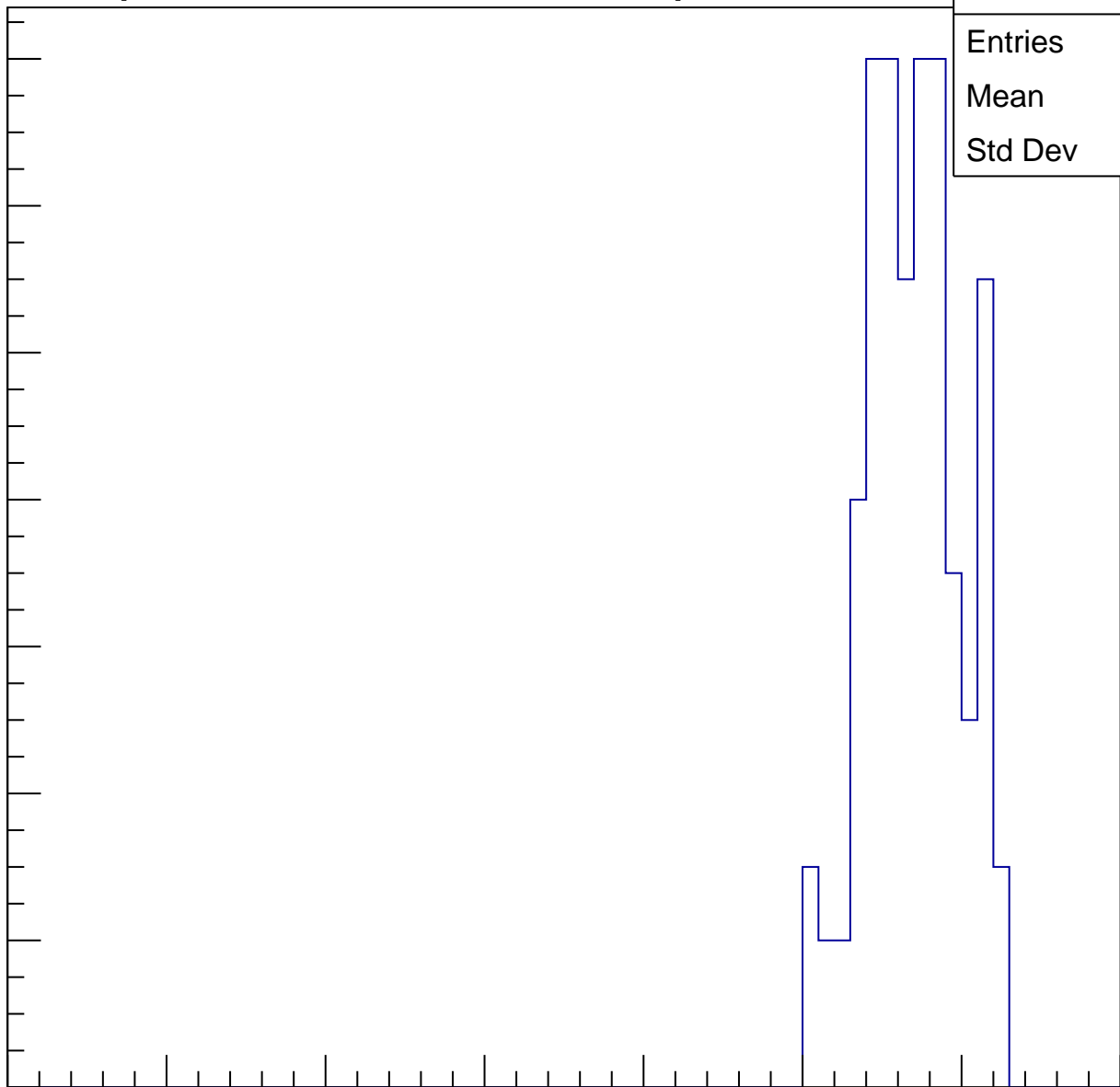
Entries	108
Mean	56.5
Std Dev	2.888

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch12, adc5

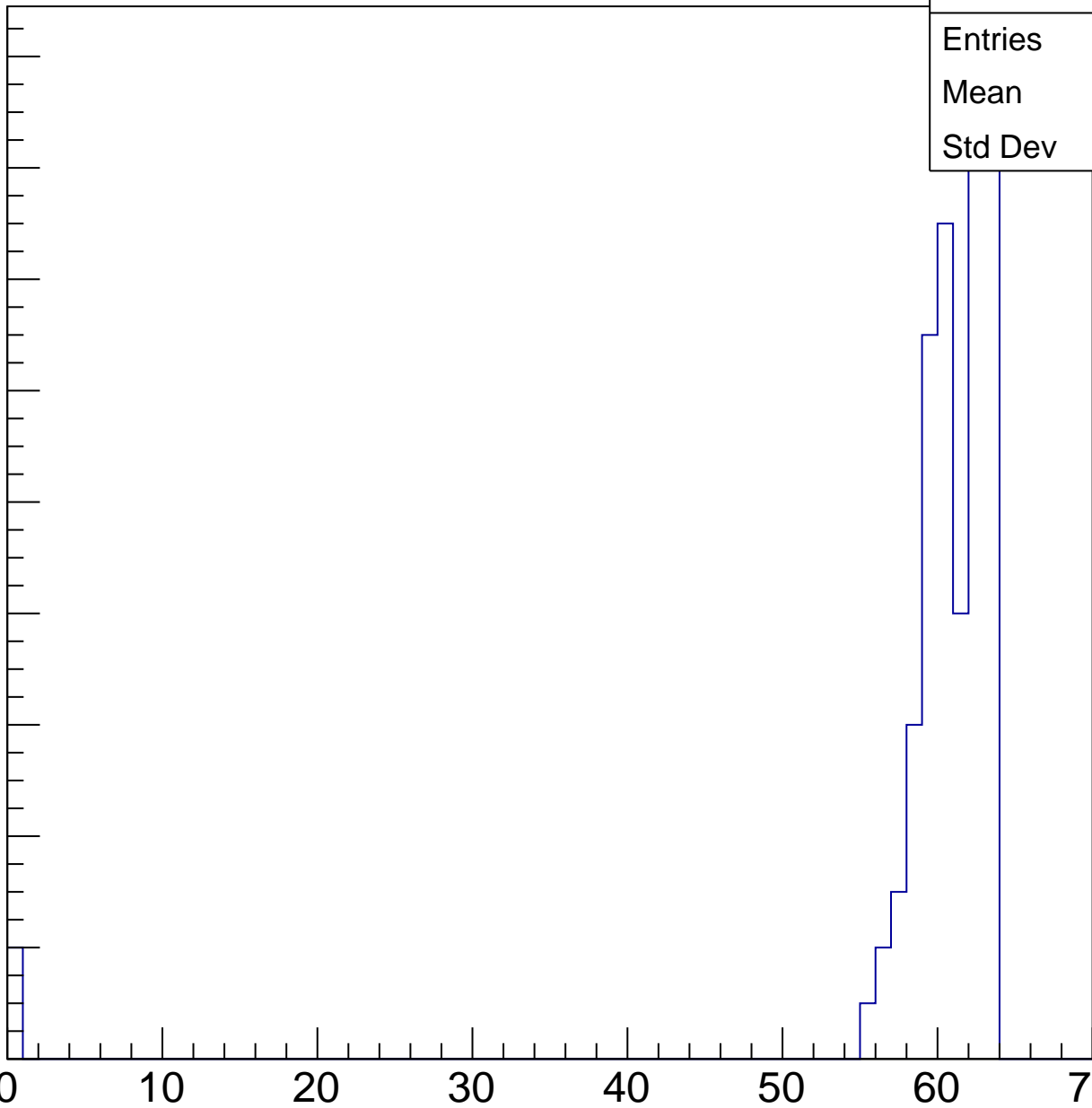
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.13
Std Dev	9.444

ampl



B1L001S, U22-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

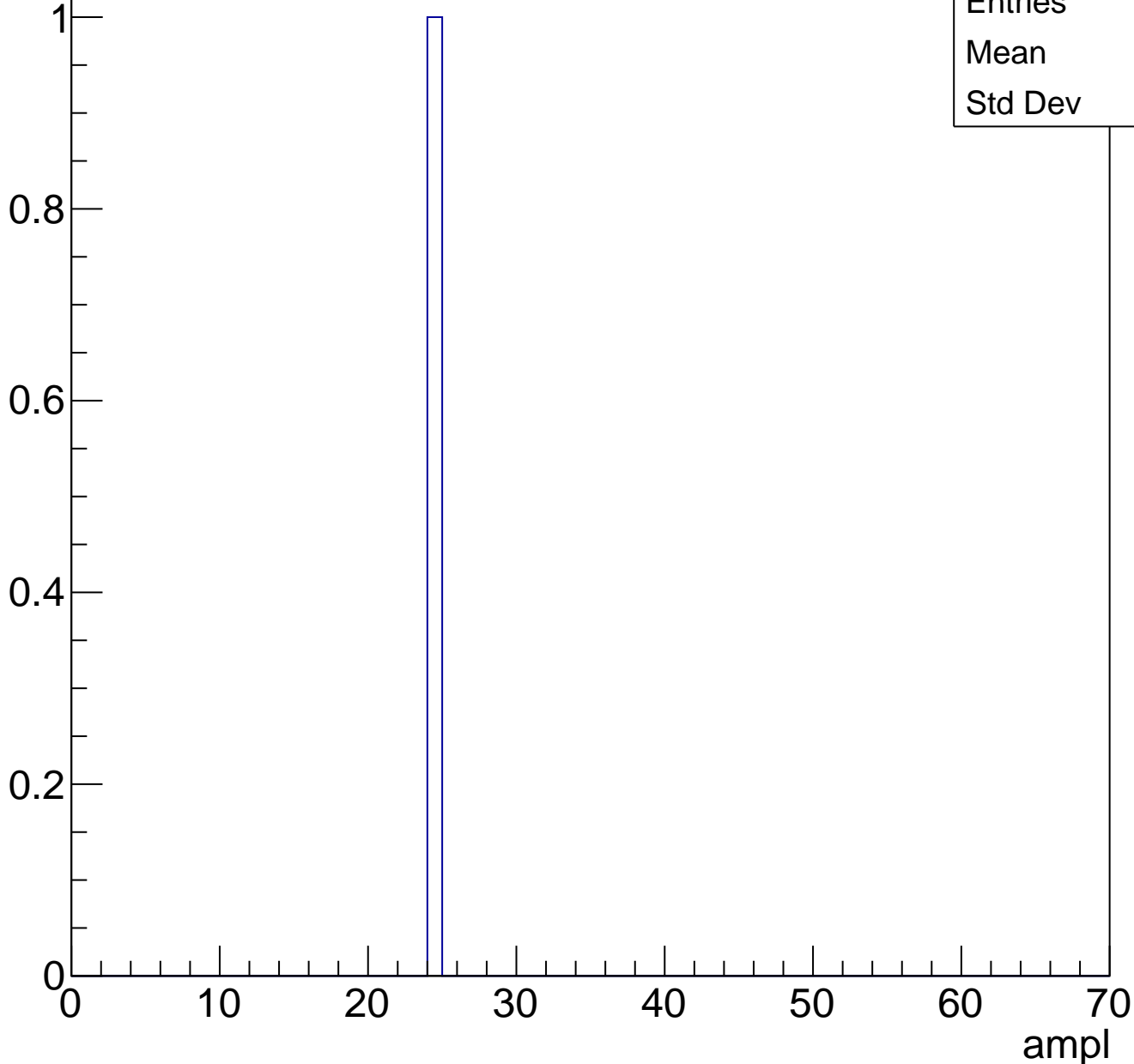
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch13, adc0

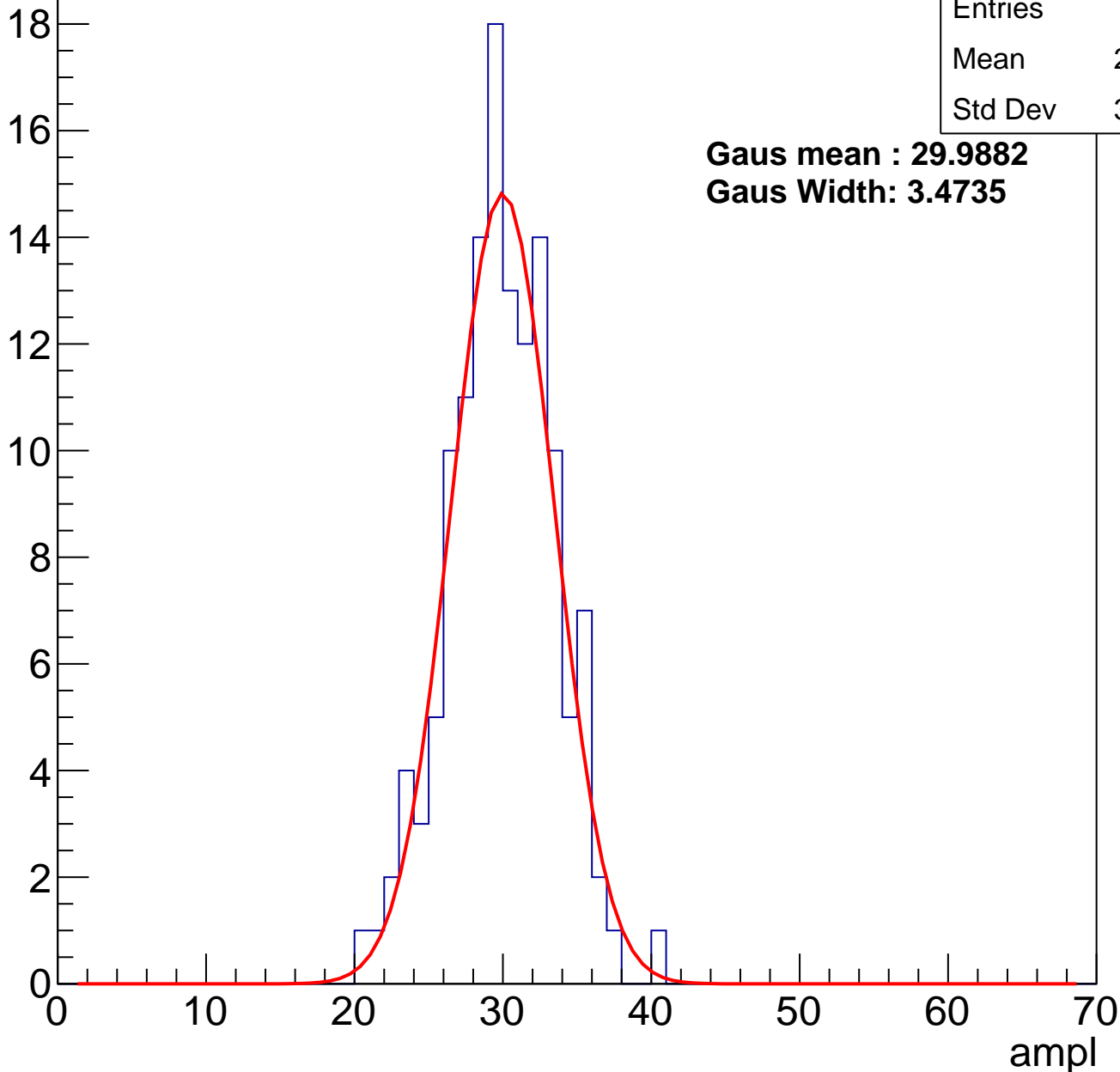
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.47
Std Dev	3.527

Gaus mean : 29.9882

Gaus Width: 3.4735

Entry

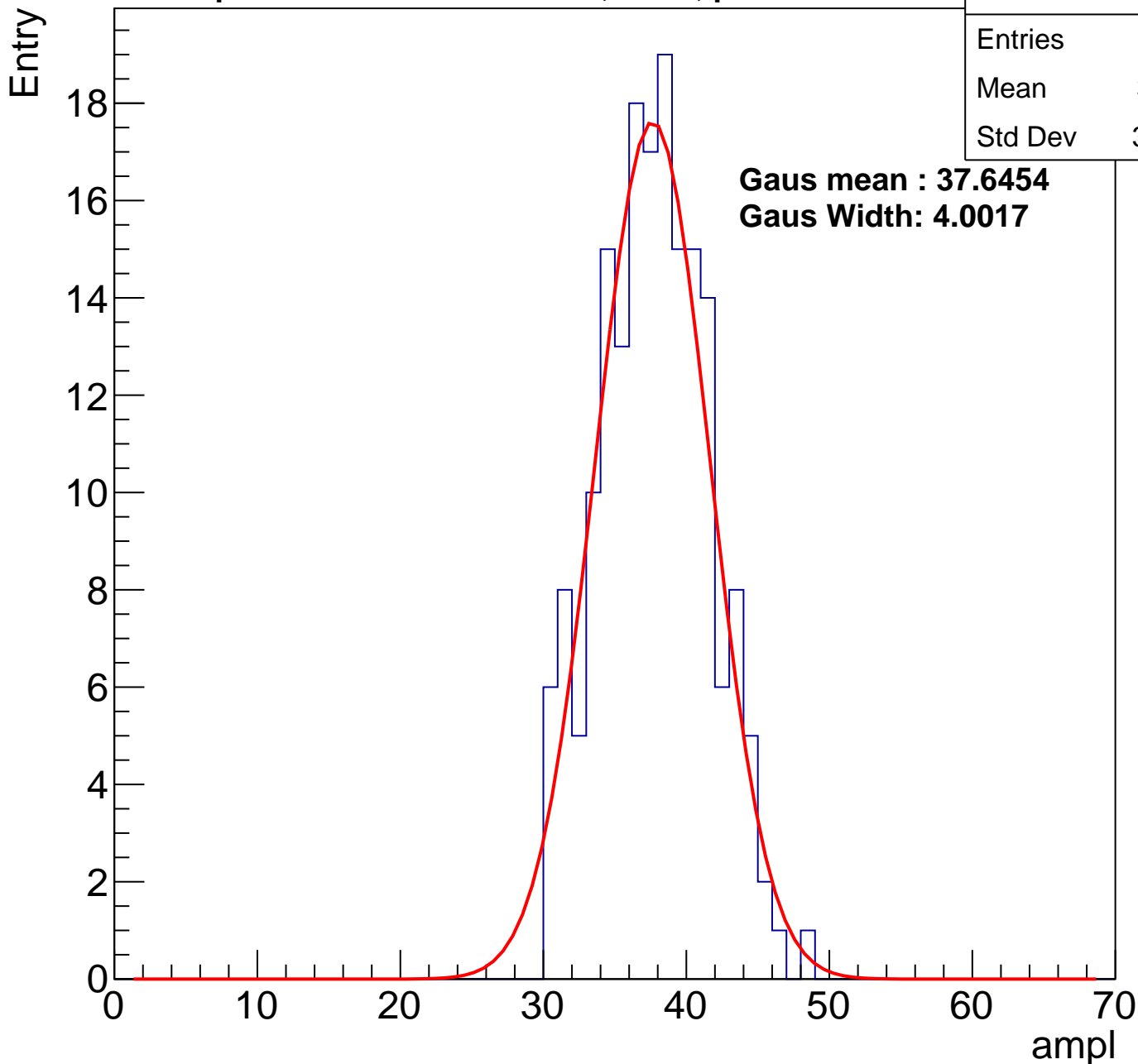


B1L001S, U22-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	178
Mean	37.31
Std Dev	3.752

Gaus mean : 37.6454
Gaus Width: 4.0017



B1L001S, U22-ch13, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

113

Mean

44.33

Std Dev

2.922

Gaus mean : 44.8549

Gaus Width: 2.8138

0

10

20

30

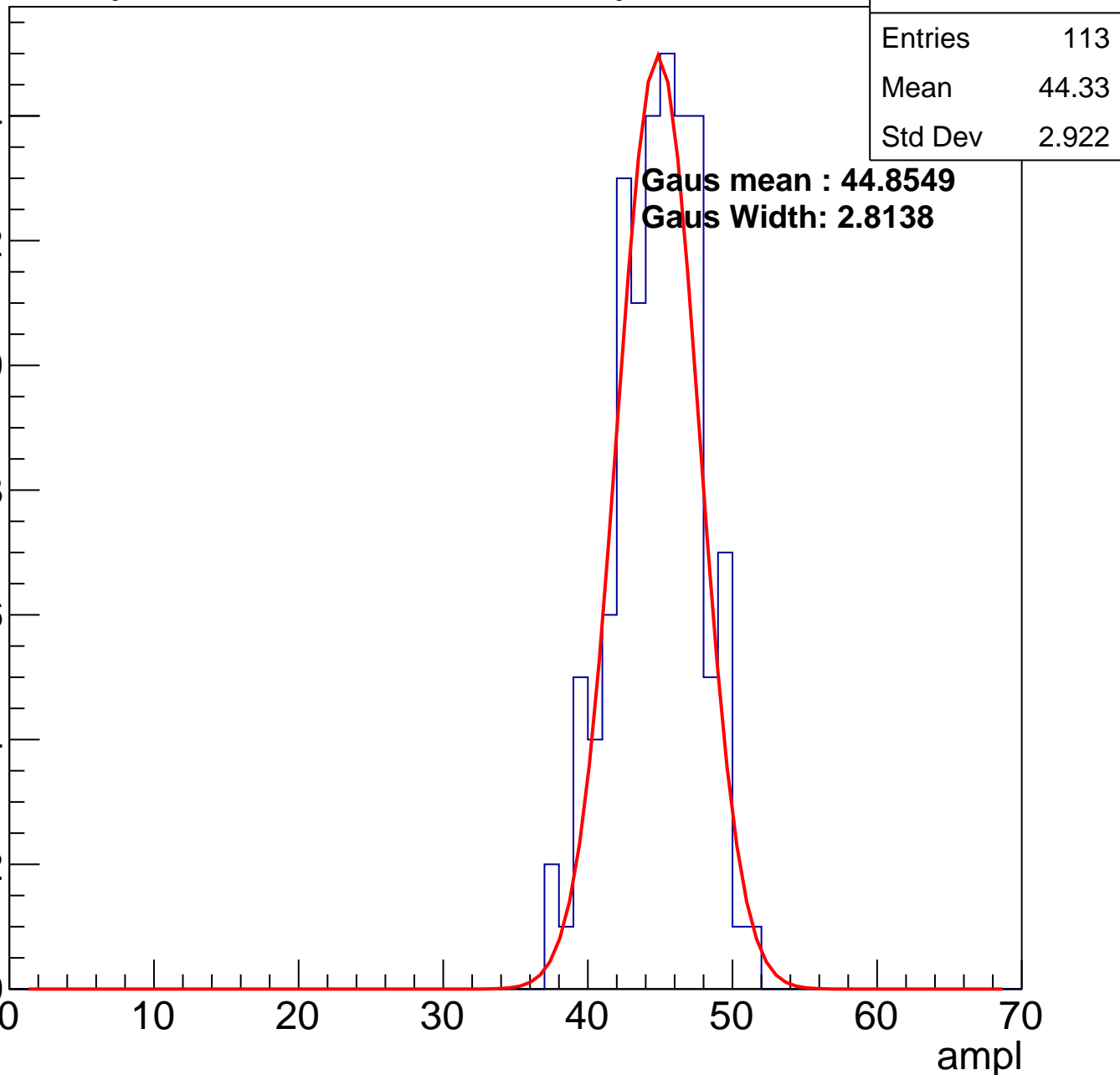
40

50

60

70

ampl



B1L001S, U22-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

50.94

Std Dev

3.155

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

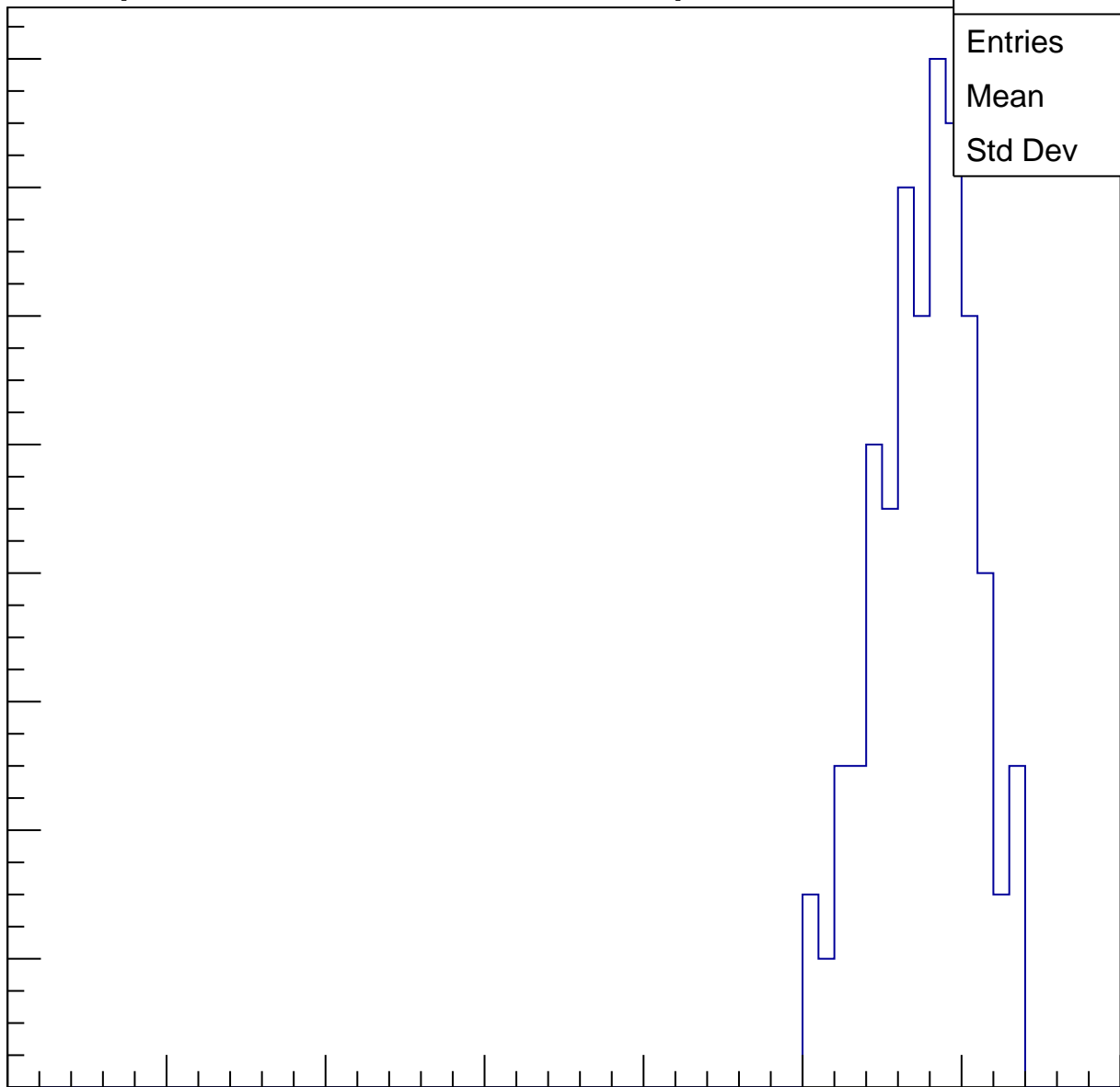
Entries	119
Mean	57.16
Std Dev	3.079

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch13, adc5

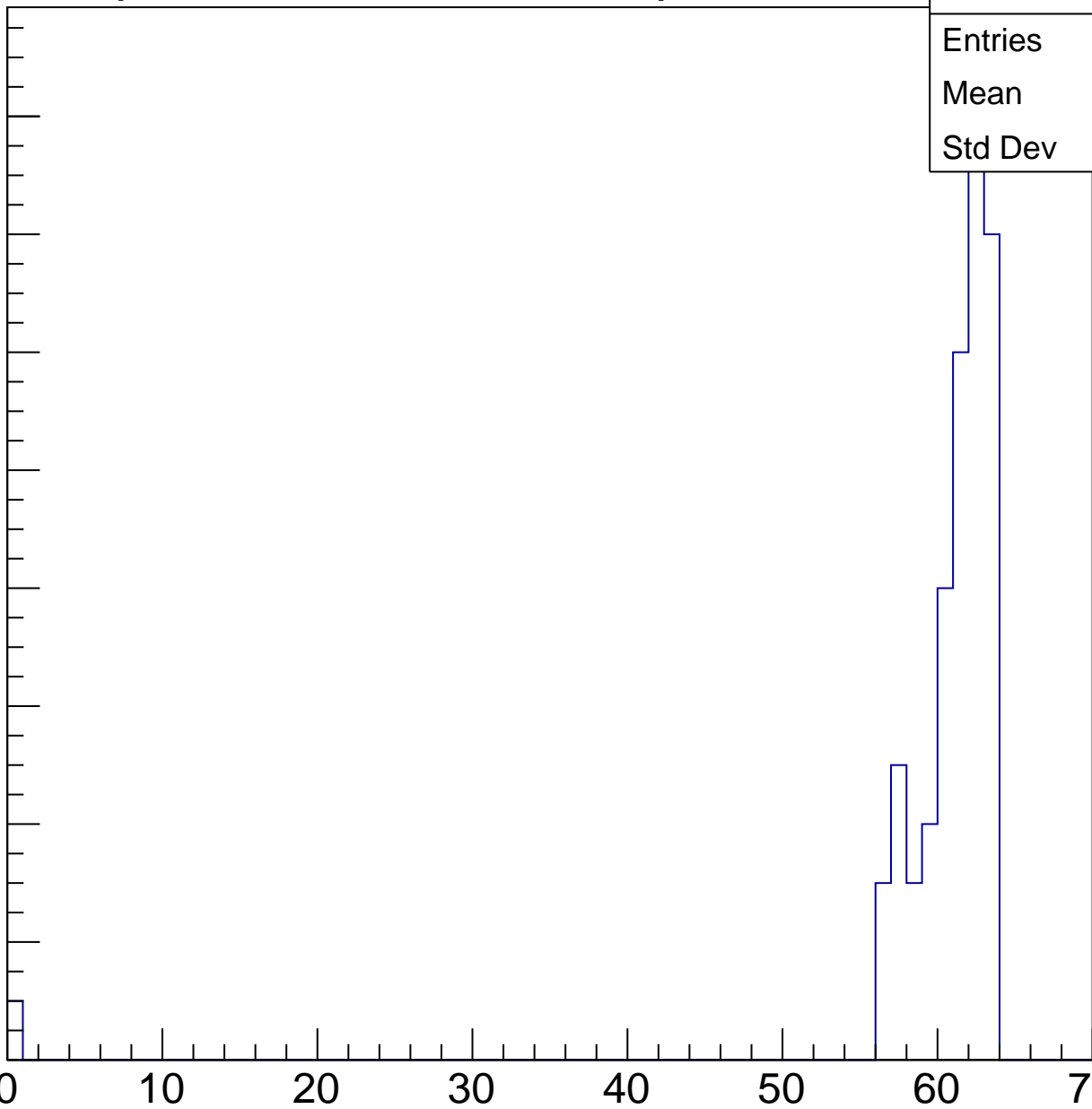
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	59.87
Std Dev	7.643

ampl



B1L001S, U22-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U22-ch14, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

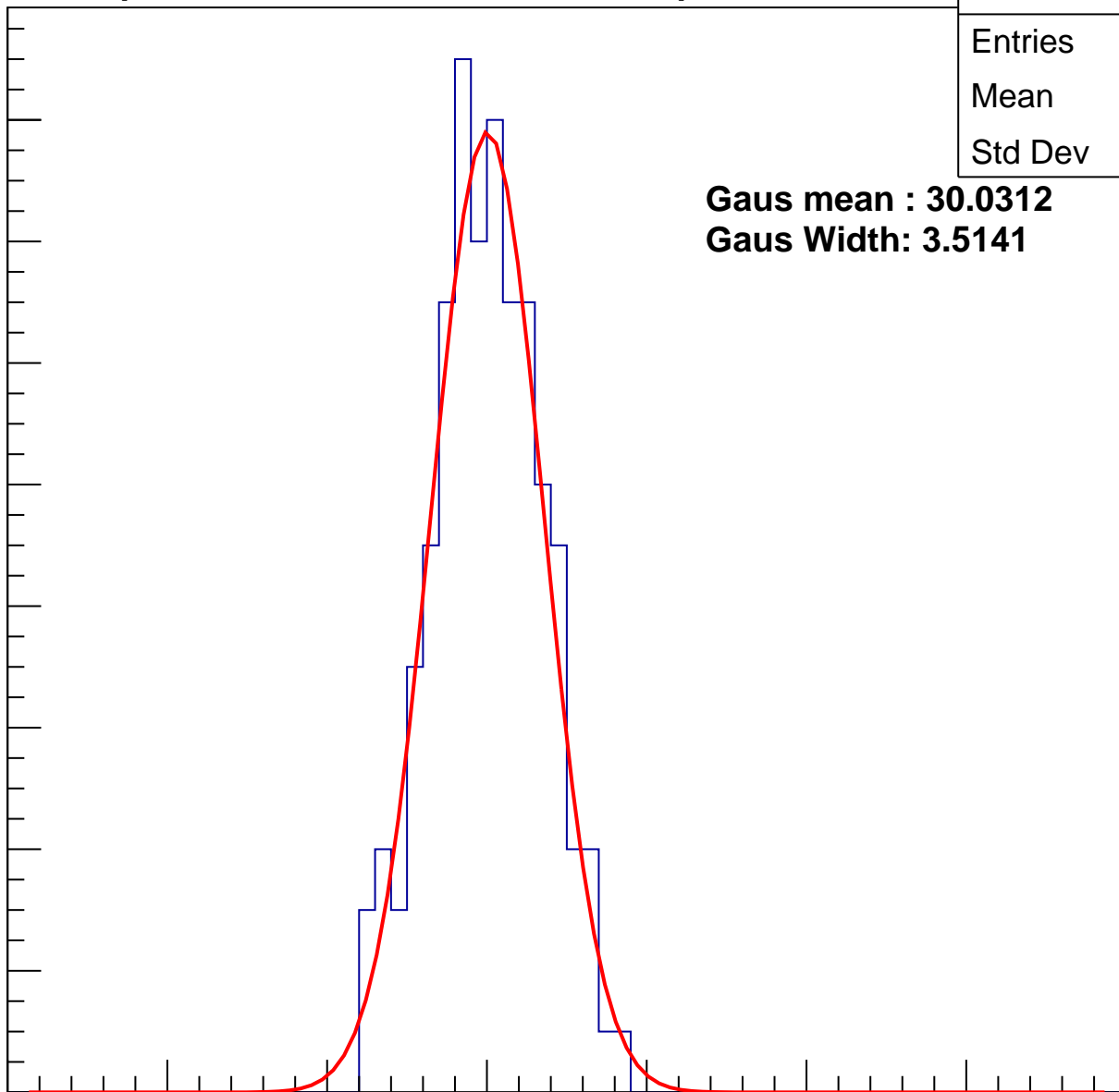
70

ampl

Entries	141
Mean	29.55
Std Dev	3.411

Gaus mean : 30.0312

Gaus Width: 3.5141



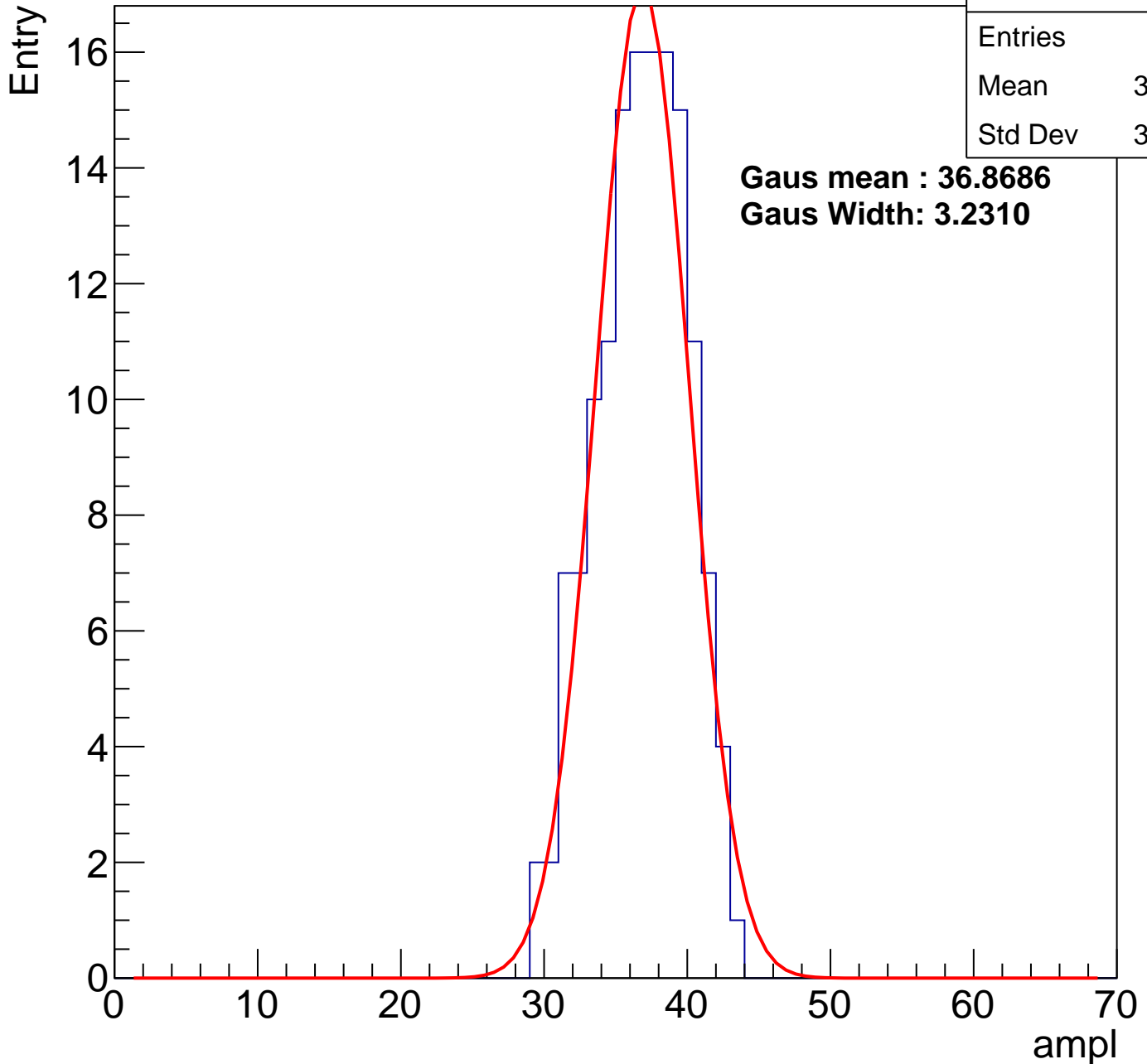
B1L001S, U22-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.34
Std Dev	3.104

Gaus mean : 36.8686

Gaus Width: 3.2310



B1L001S, U22-ch14, adc2

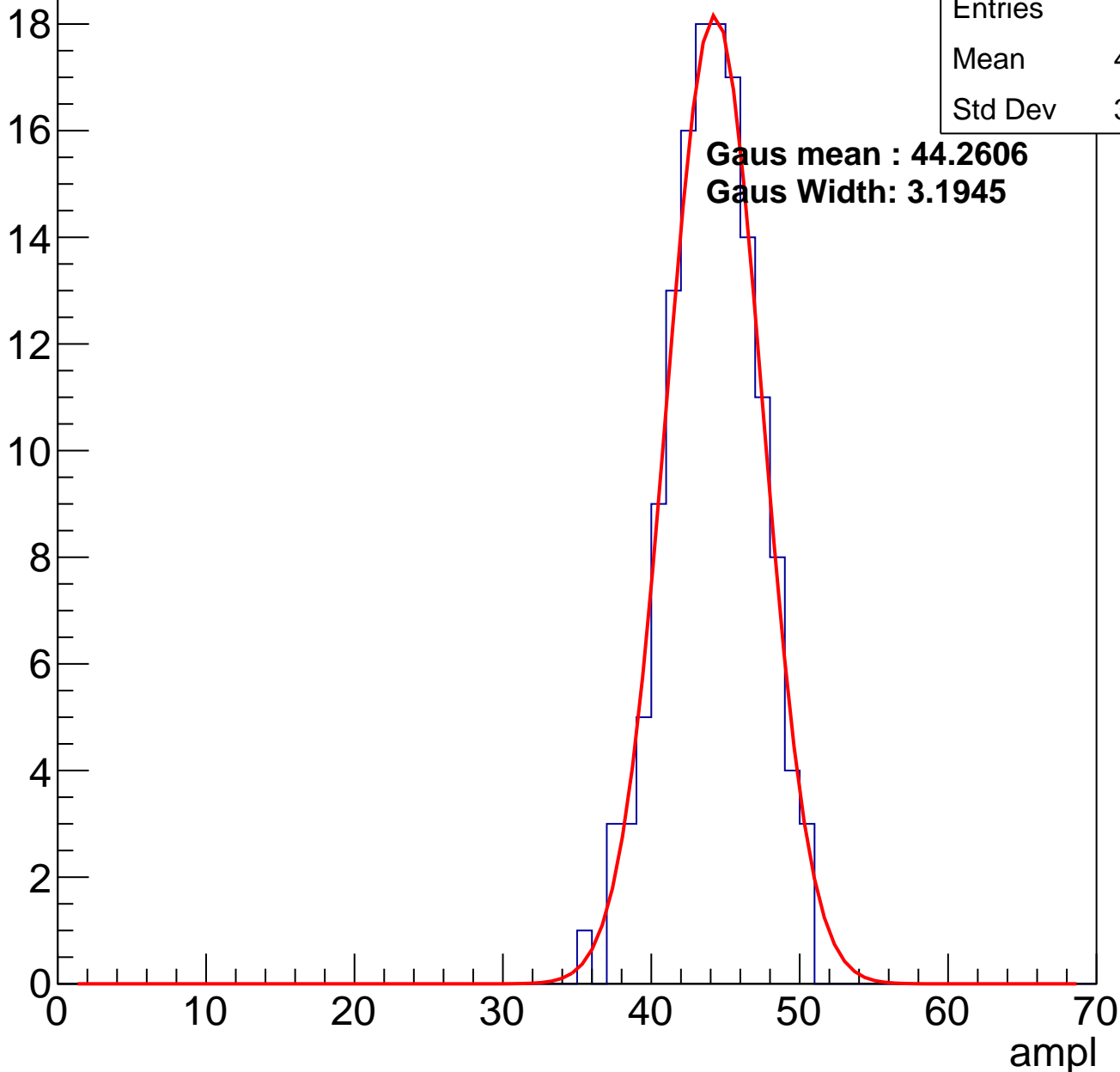
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	43.65
Std Dev	3.009

Gaus mean : 44.2606

Gaus Width: 3.1945

Entry



B1L001S, U22-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

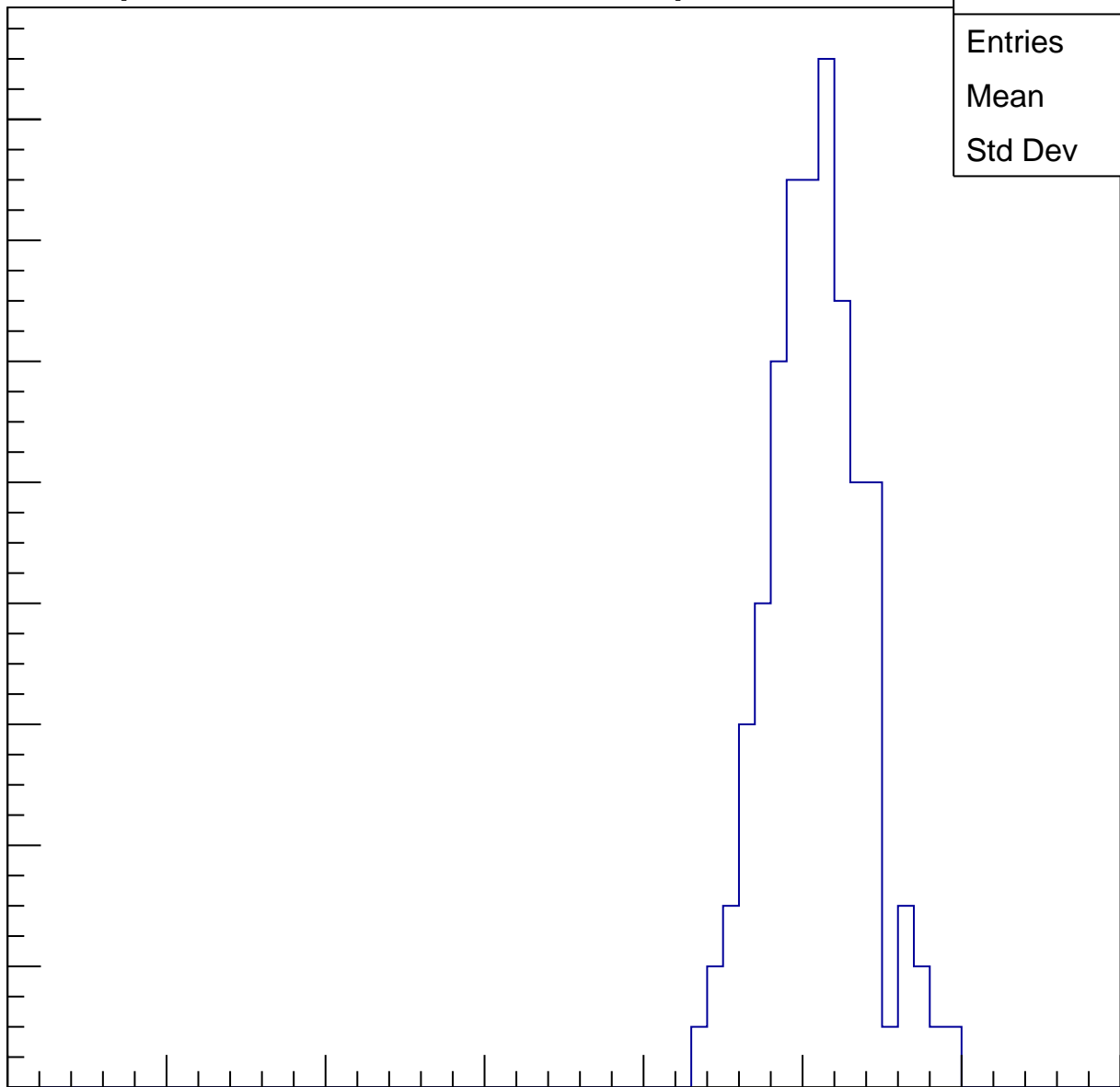
Entries	120
Mean	50.38
Std Dev	3.034

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	55.68
Std Dev	2.891

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

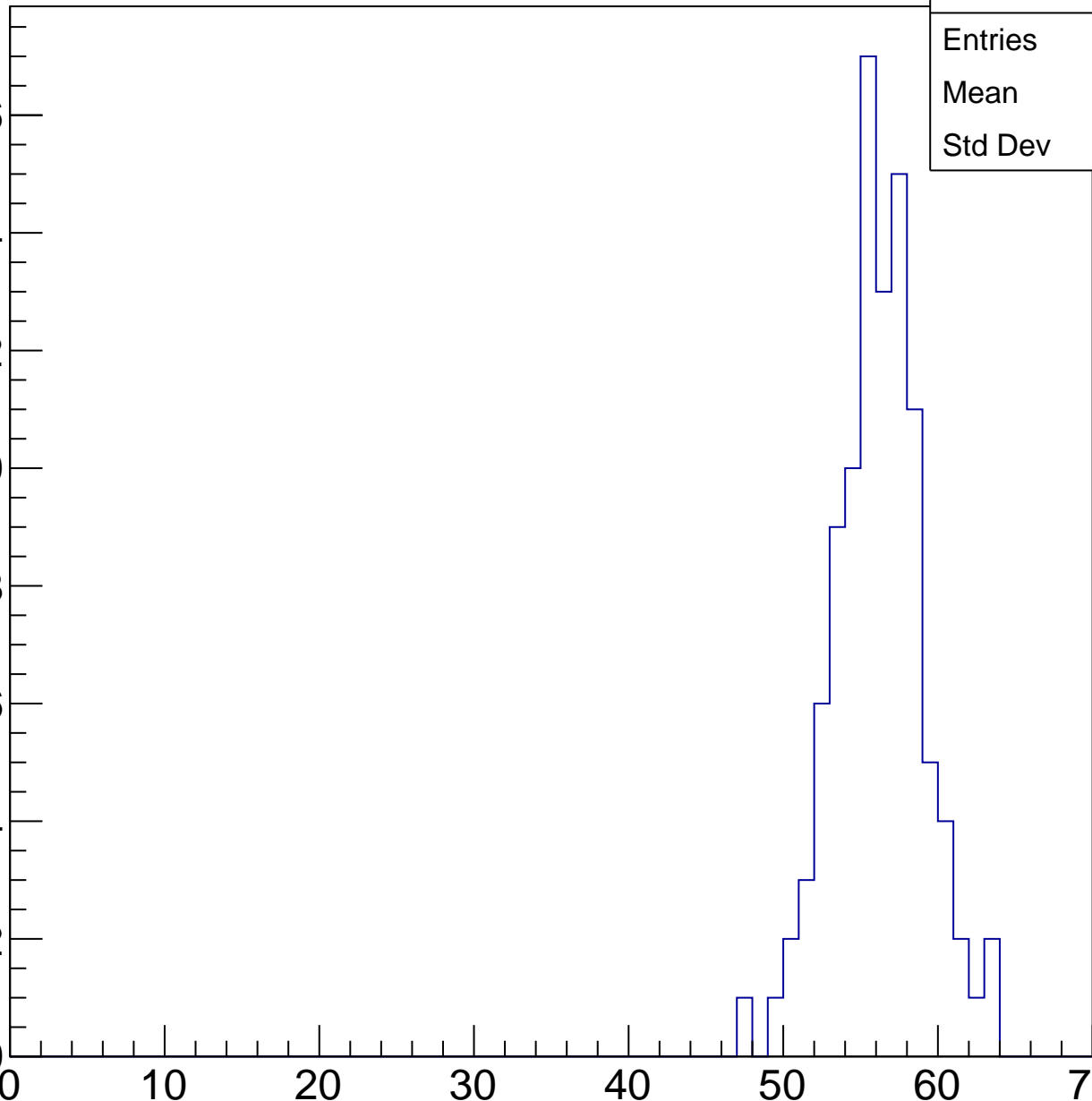
40

50

60

70

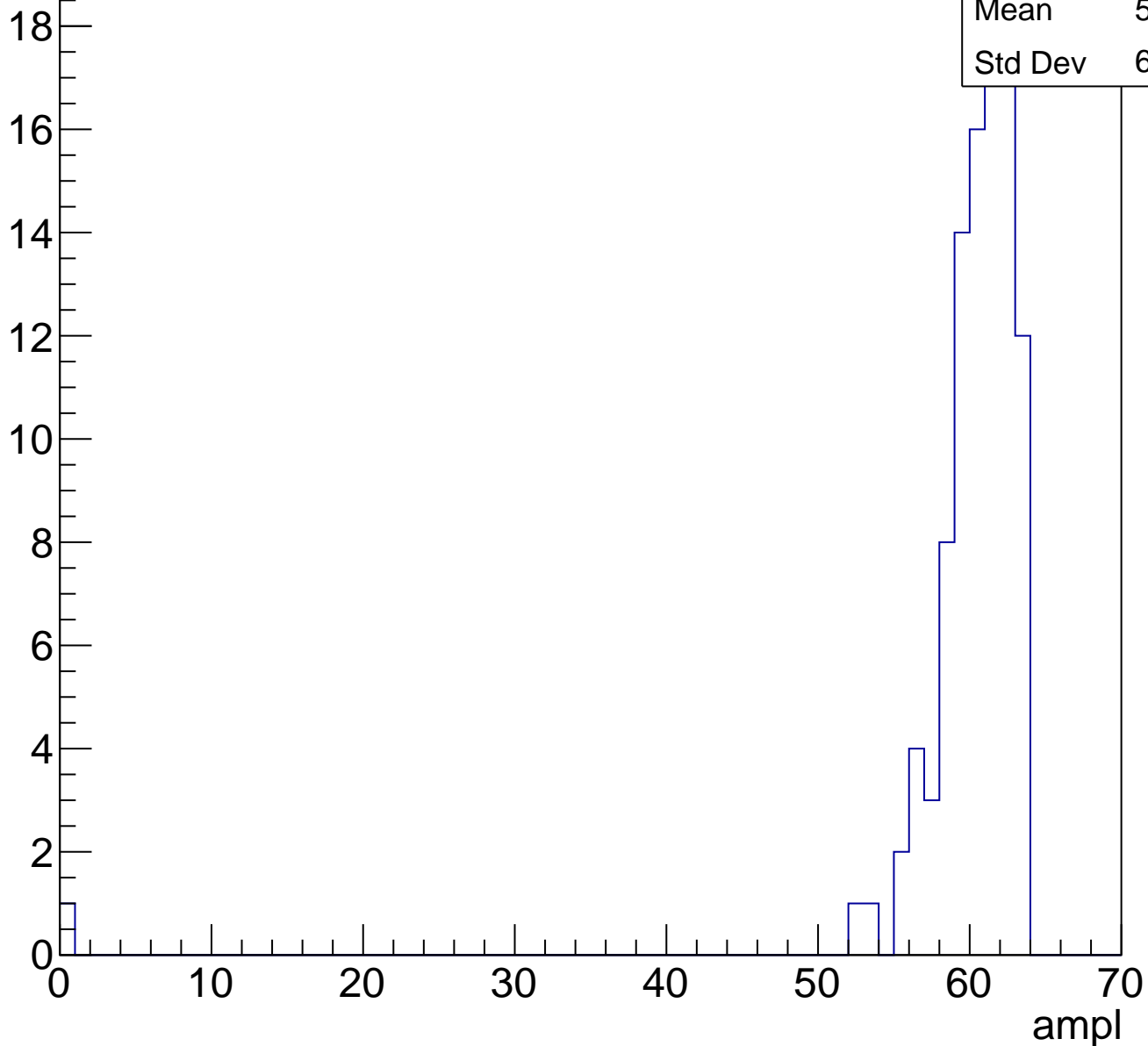
ampl



B1L001S, U22-ch14, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

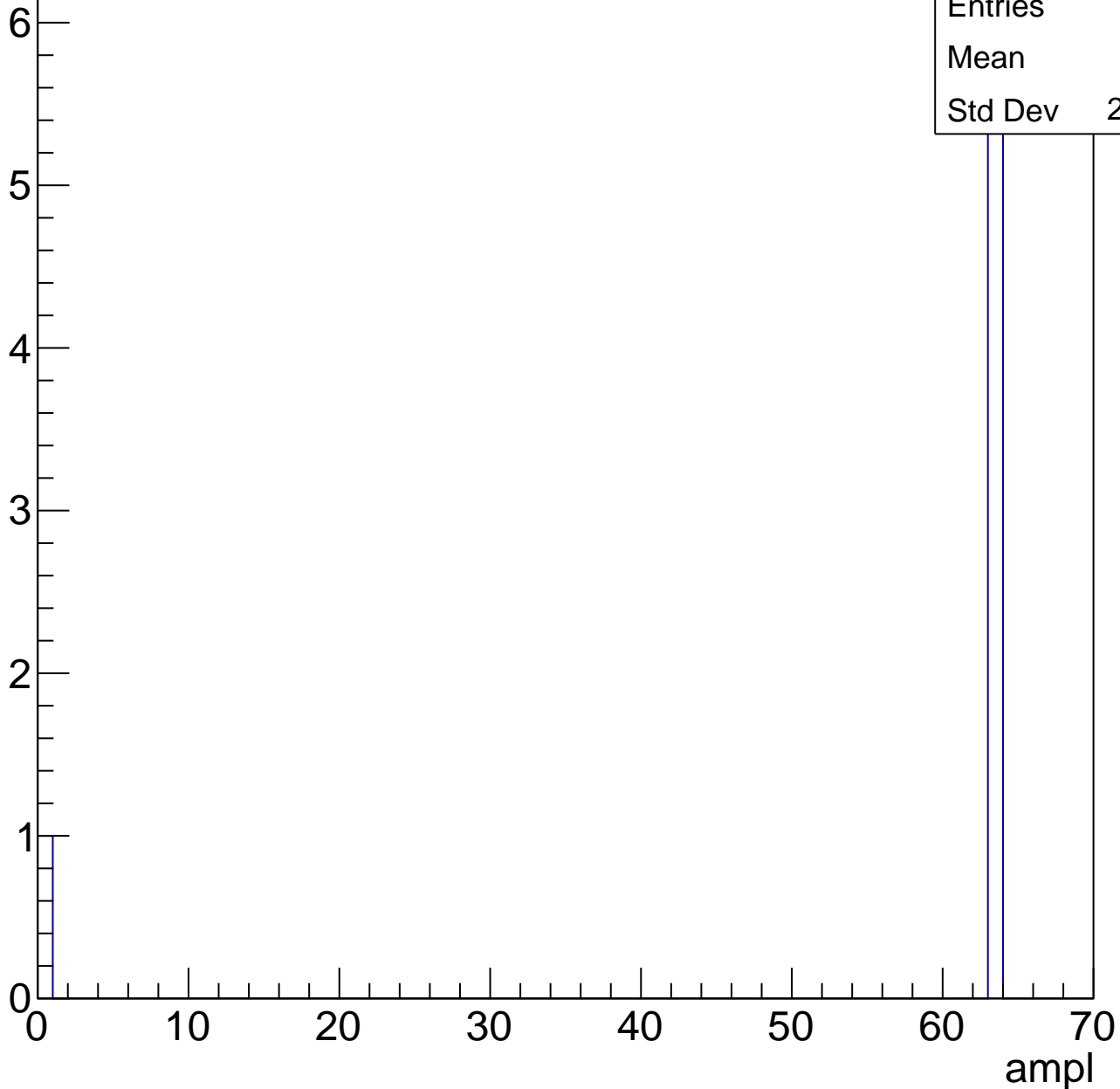


B1L001S, U22-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	54
Std Dev	22.05



B1L001S, U22-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch15, adc0

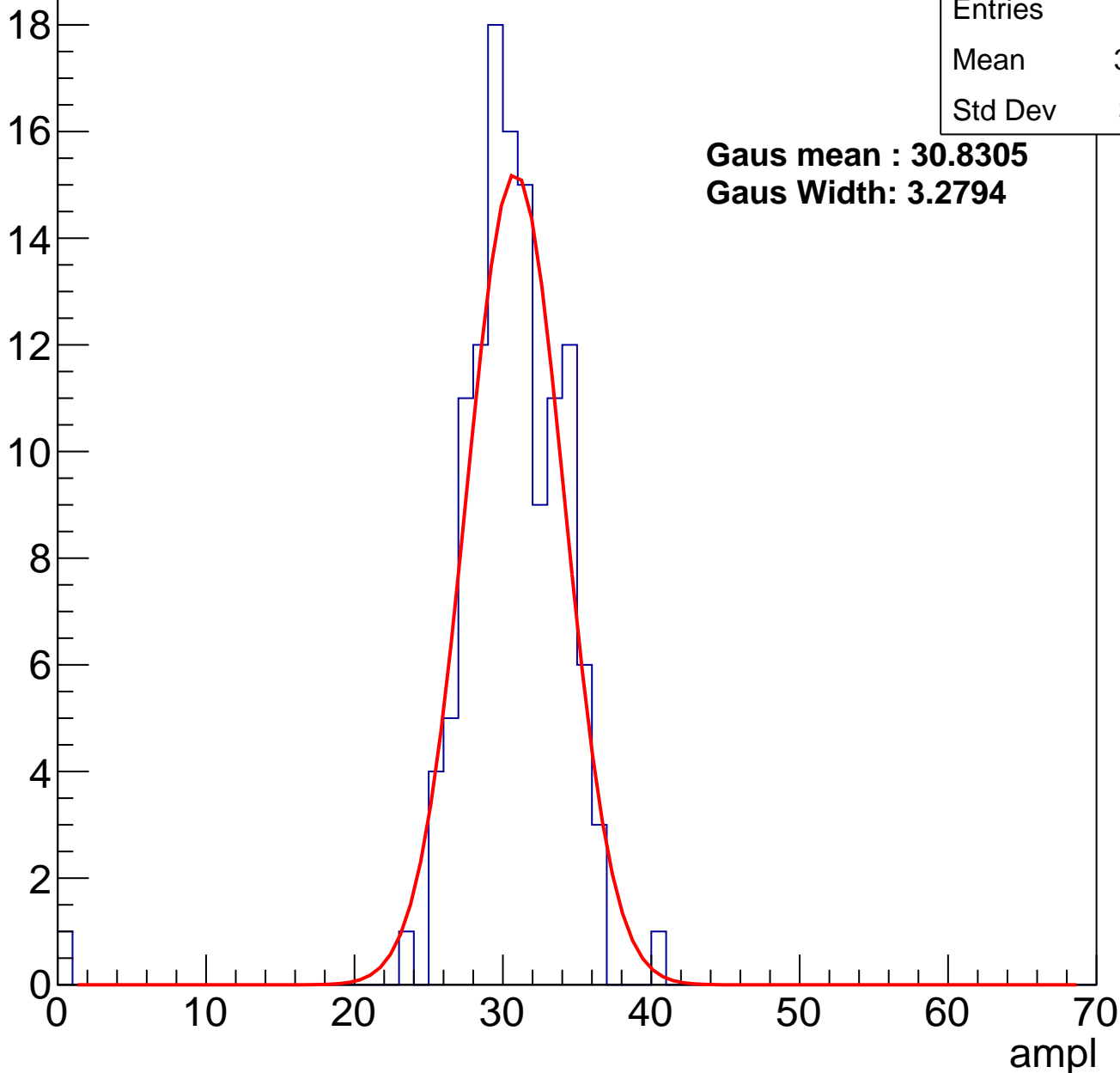
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.16
Std Dev	3.991

Gaus mean : 30.8305

Gaus Width: 3.2794

Entry



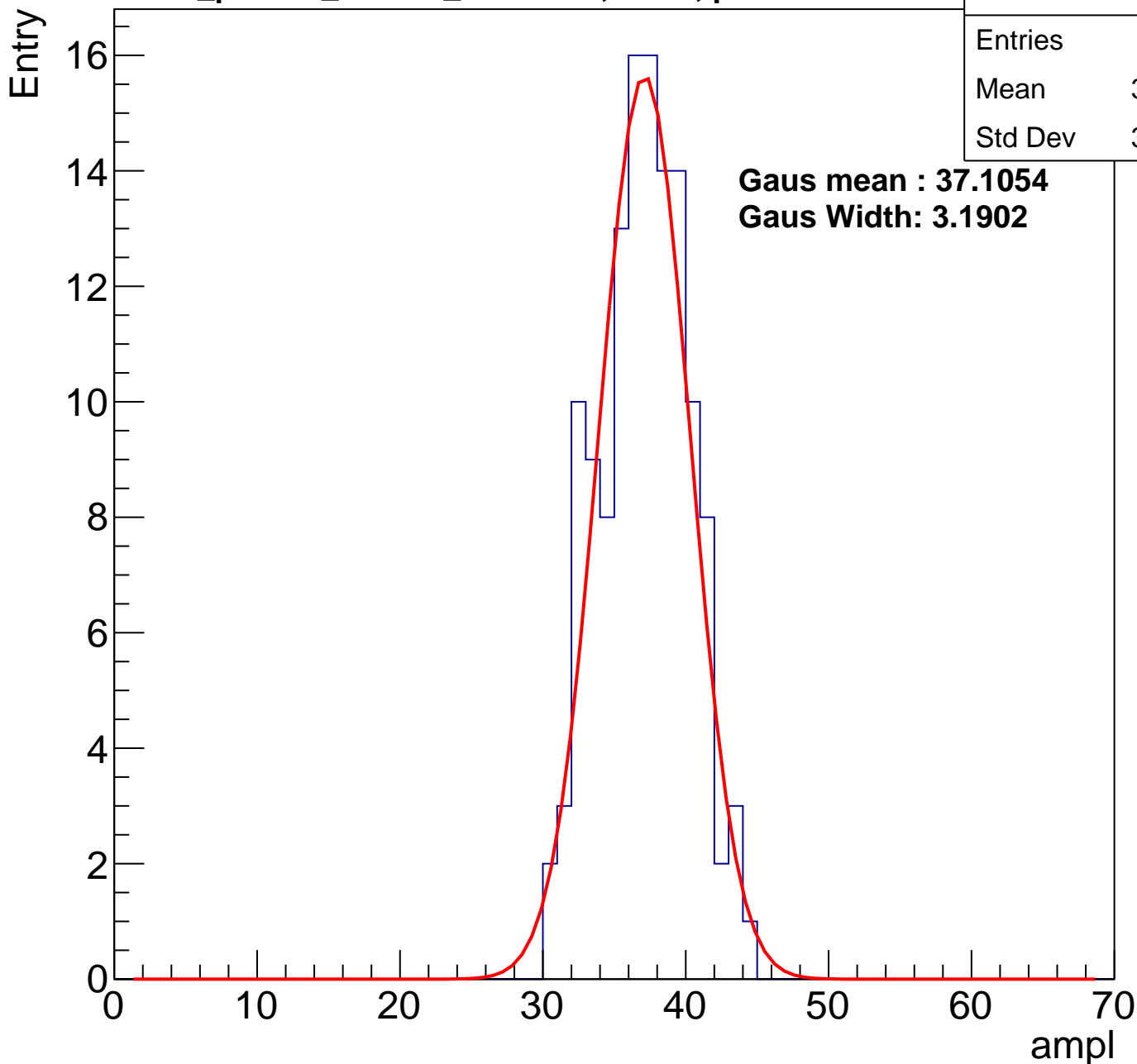
B1L001S, U22-ch15, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.65
Std Dev	3.074

Gaus mean : 37.1054

Gaus Width: 3.1902



B1L001S, U22-ch15, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	43.14
Std Dev	3.307

Gaus mean : 43.7055

Gaus Width: 3.4834

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

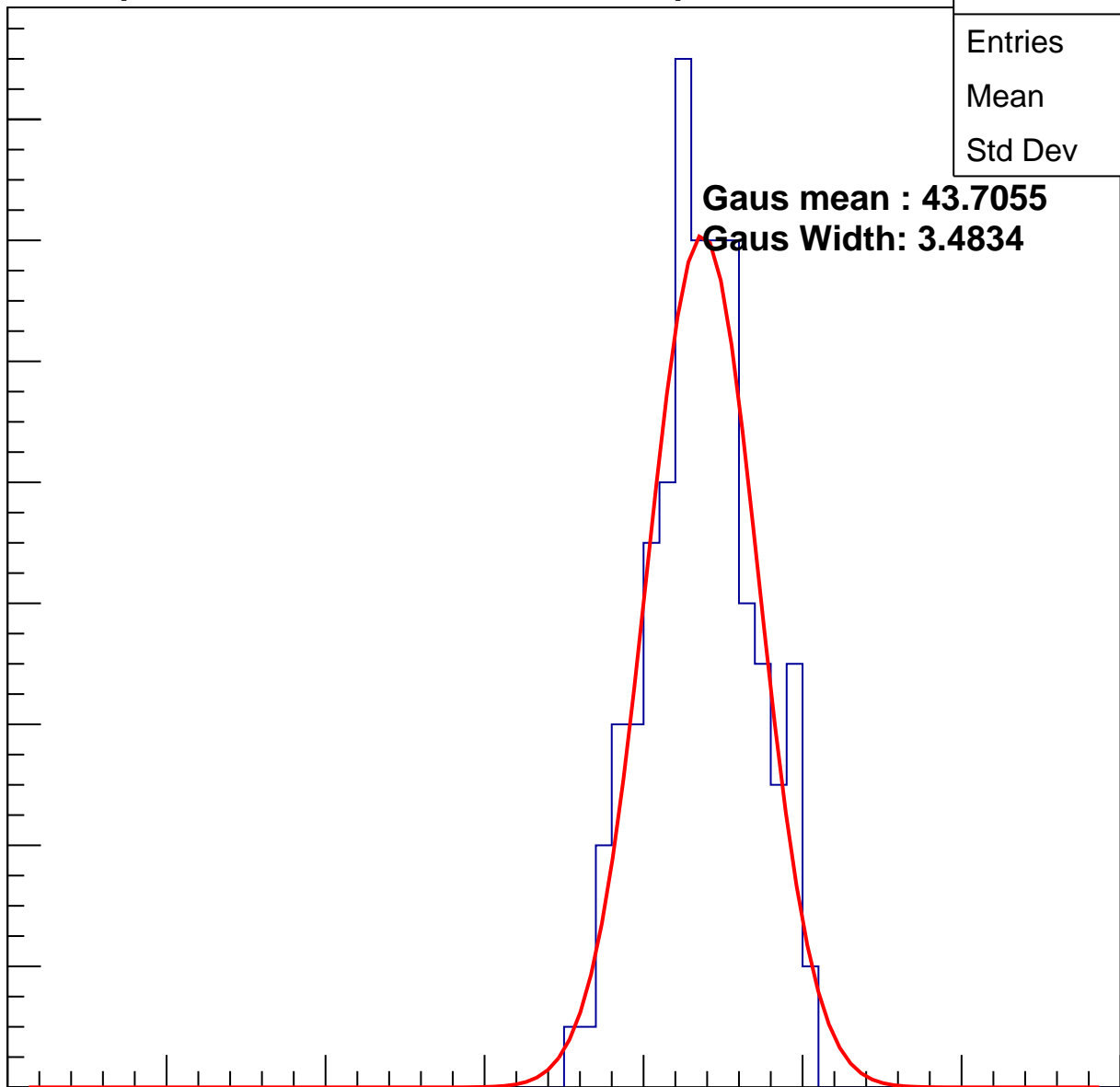
40

50

60

70

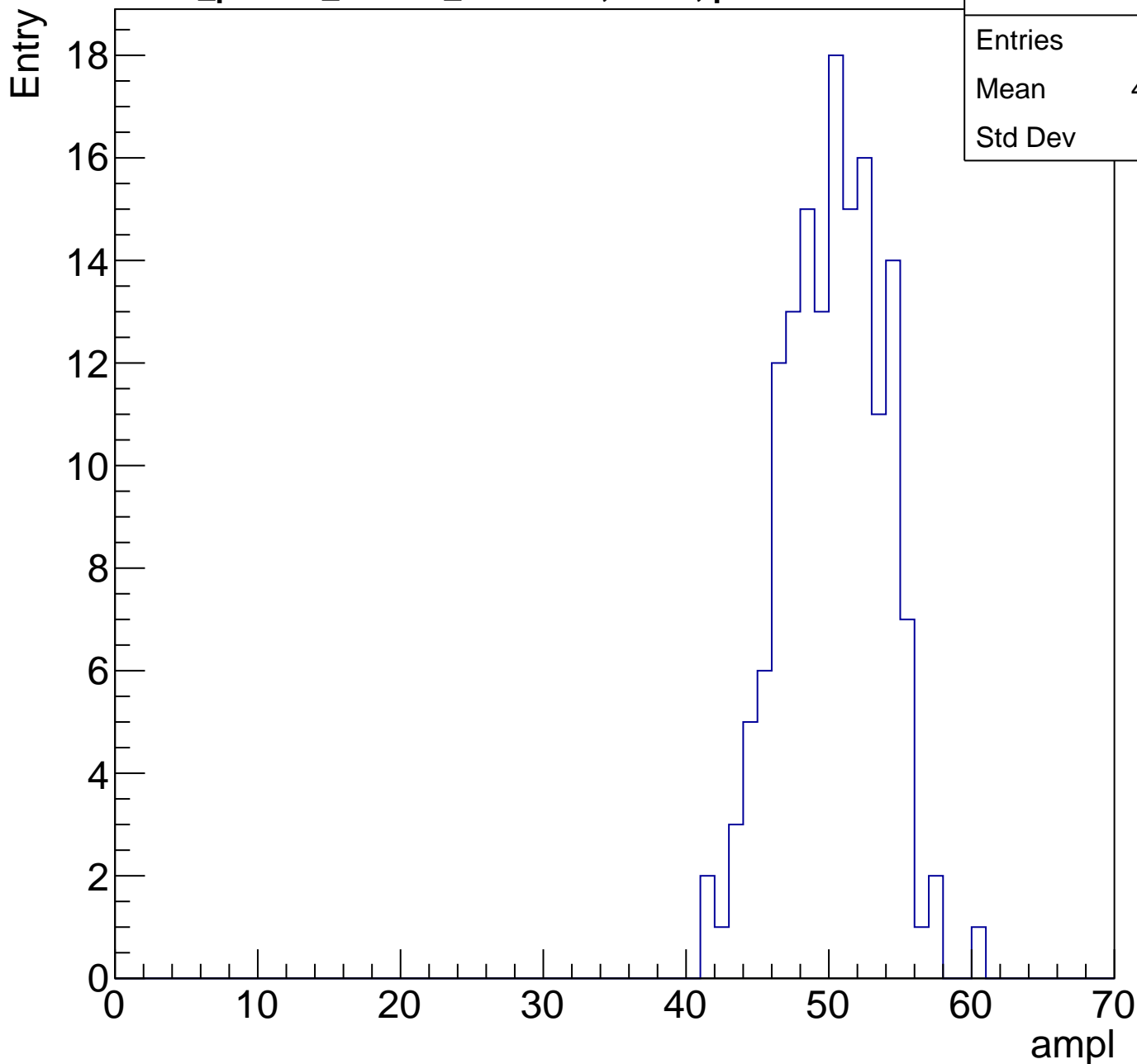
ampl



B1L001S, U22-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	49.77
Std Dev	3.49



B1L001S, U22-ch15, adc4

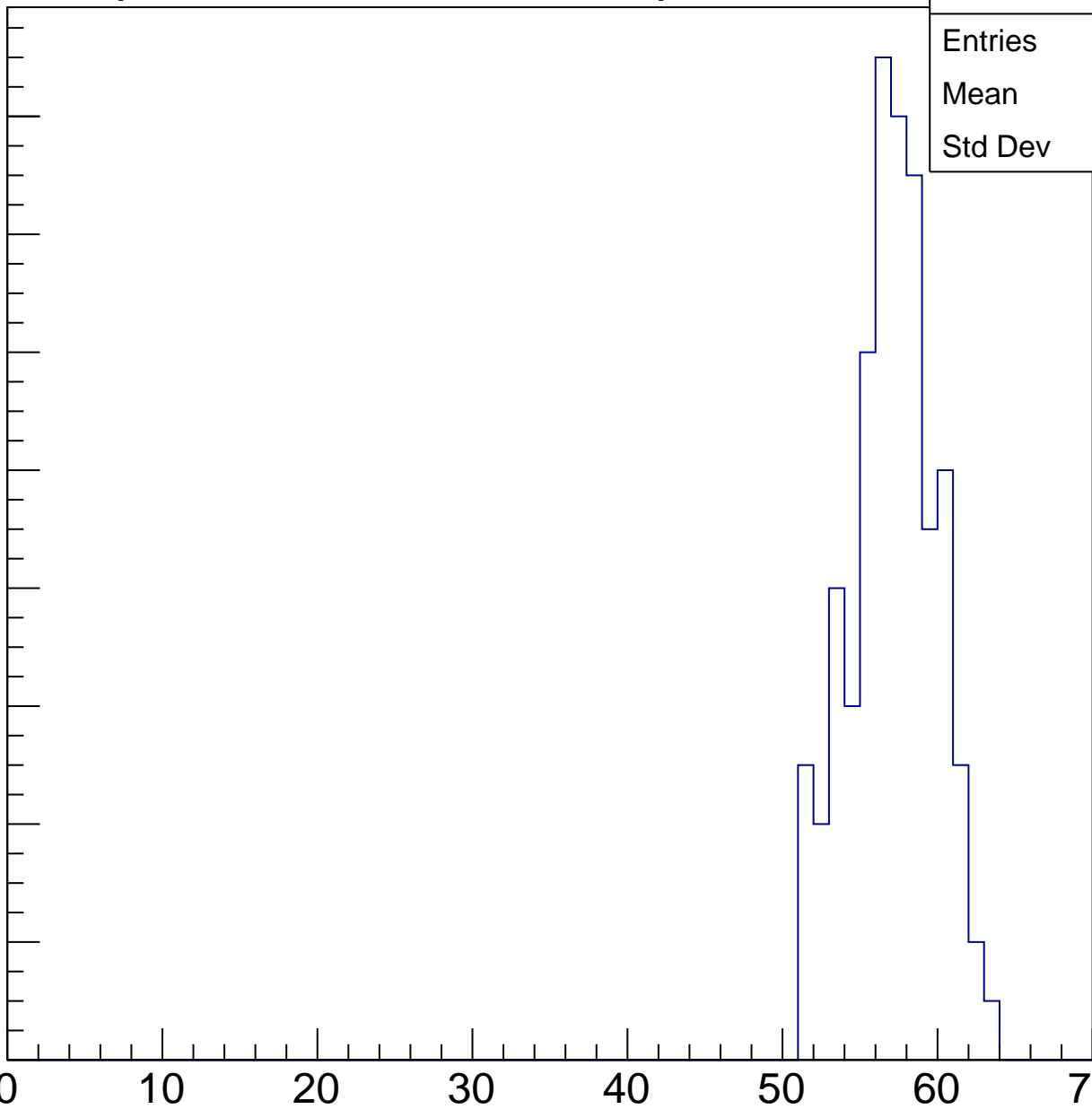
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	56.62
Std Dev	2.727

ampl



B1L001S, U22-ch15, adc5

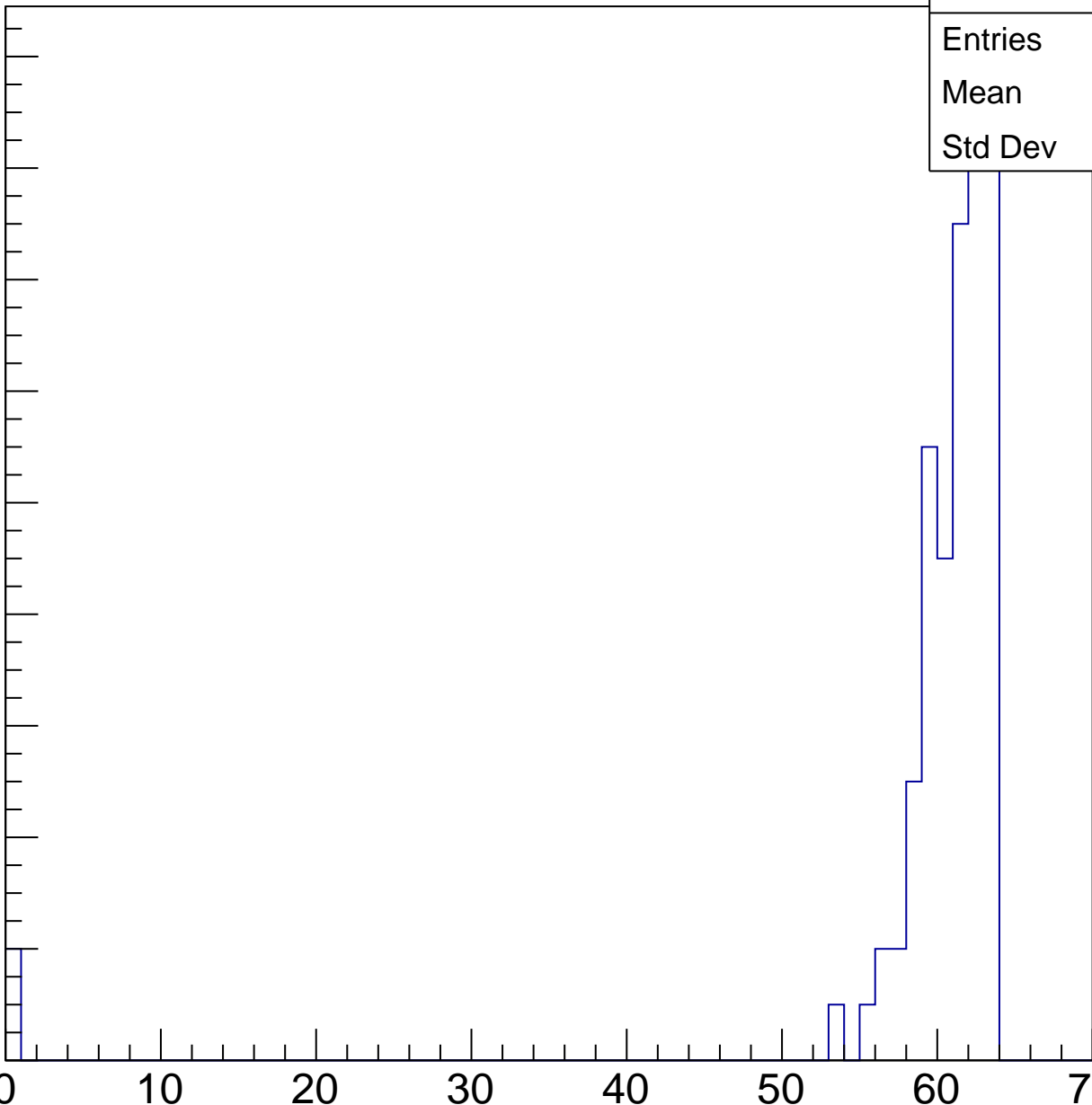
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.22
Std Dev	9.534

ampl



B1L001S, U22-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

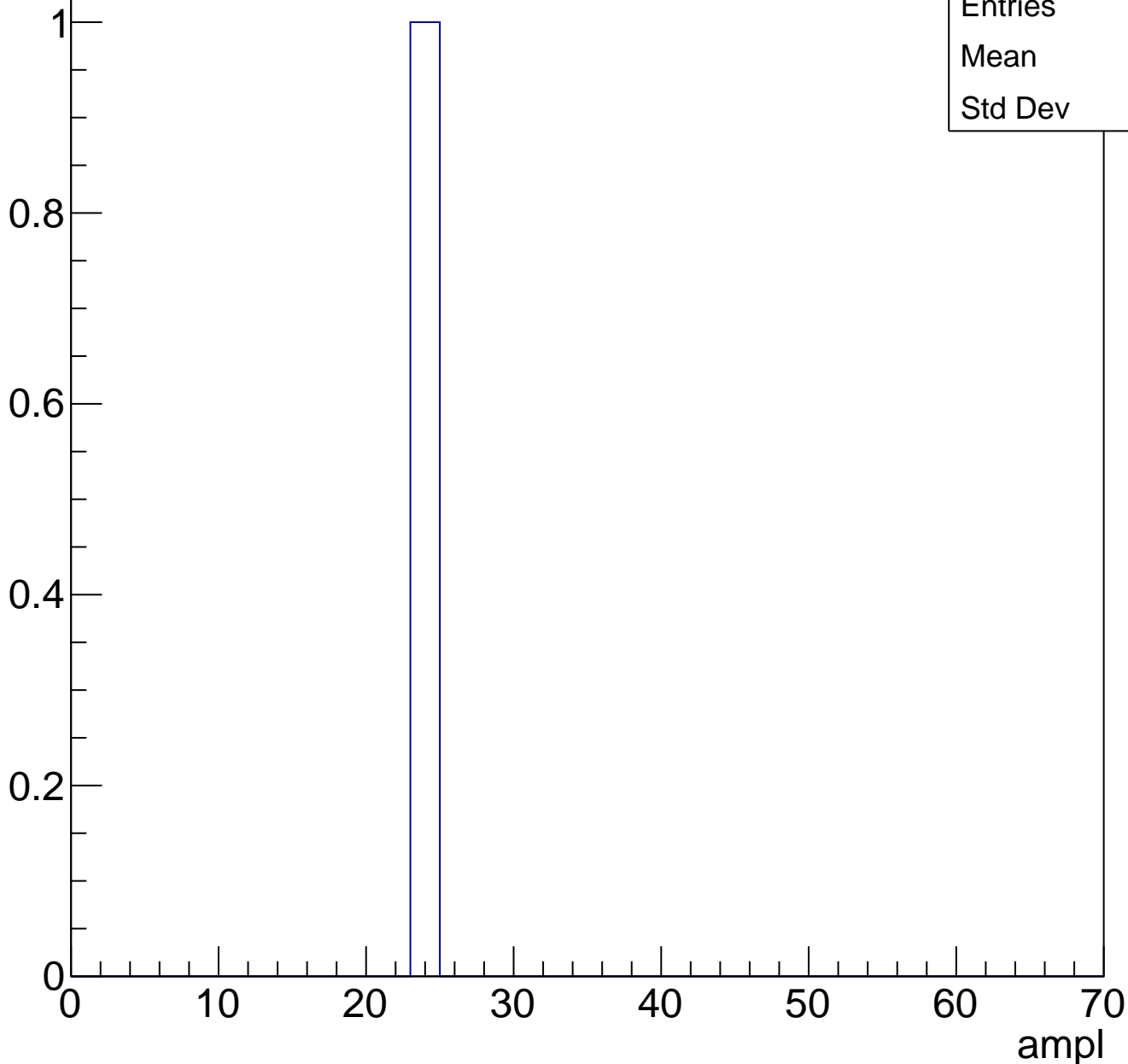
Entry



B1L001S, U22-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch16, adc0

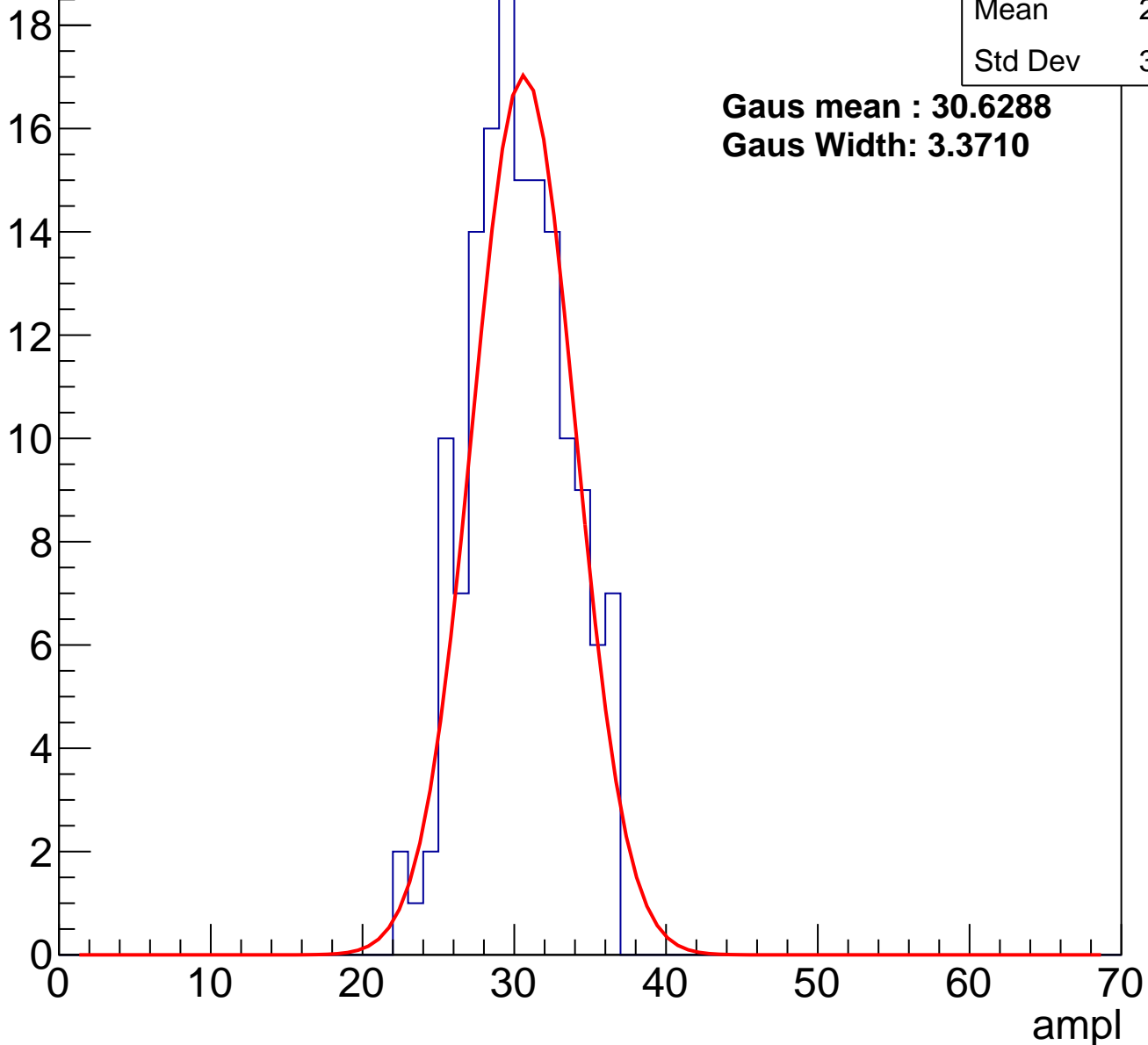
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.83
Std Dev	3.219

Gaus mean : 30.6288

Gaus Width: 3.3710

Entry



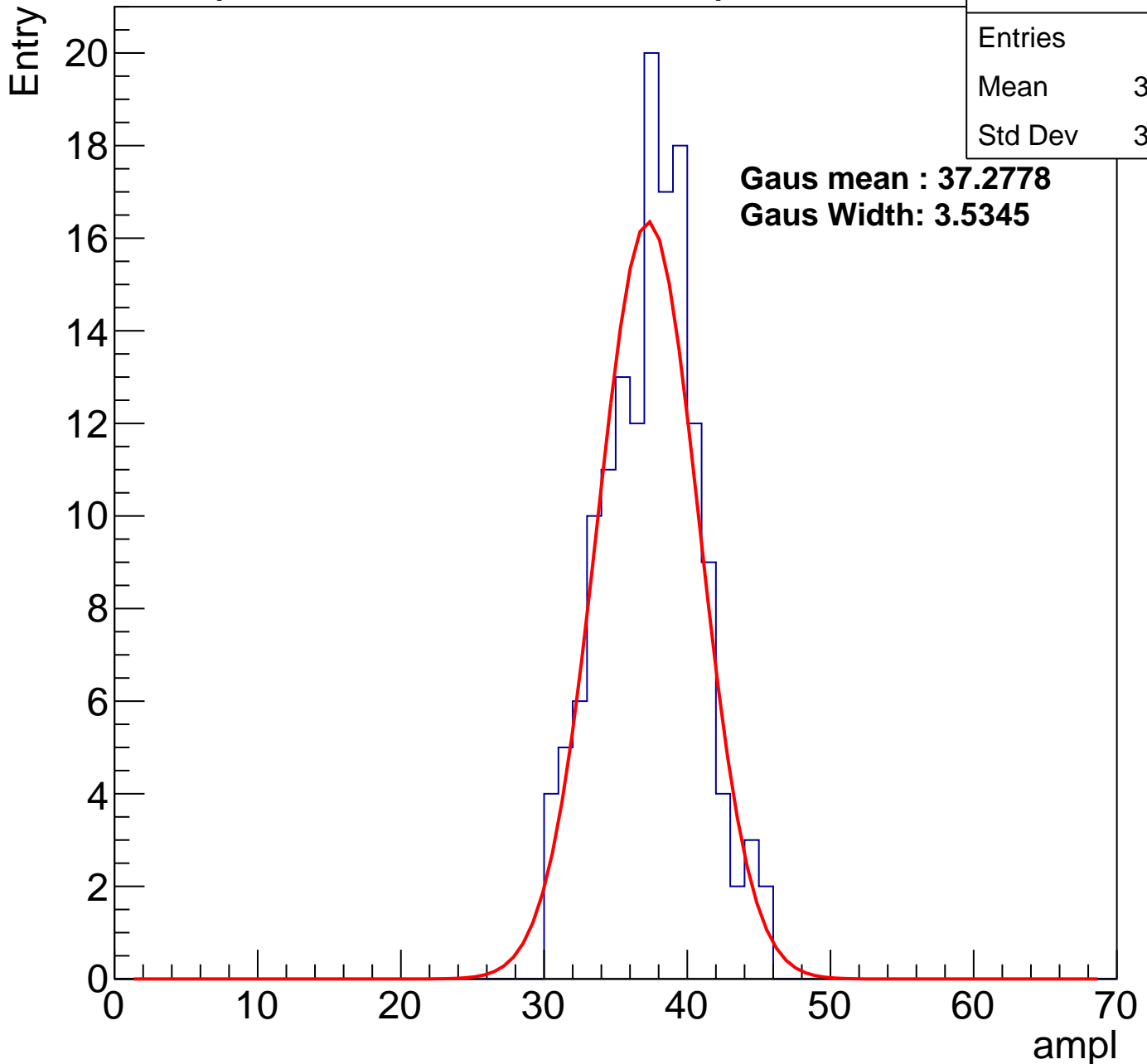
B1L001S, U22-ch16, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.97
Std Dev	3.326

Gaus mean : 37.2778

Gaus Width: 3.5345



B1L001S, U22-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

44.12

Std Dev

3.056

Gaus mean : 44.7294

Gaus Width: 3.0371

0

10

20

30

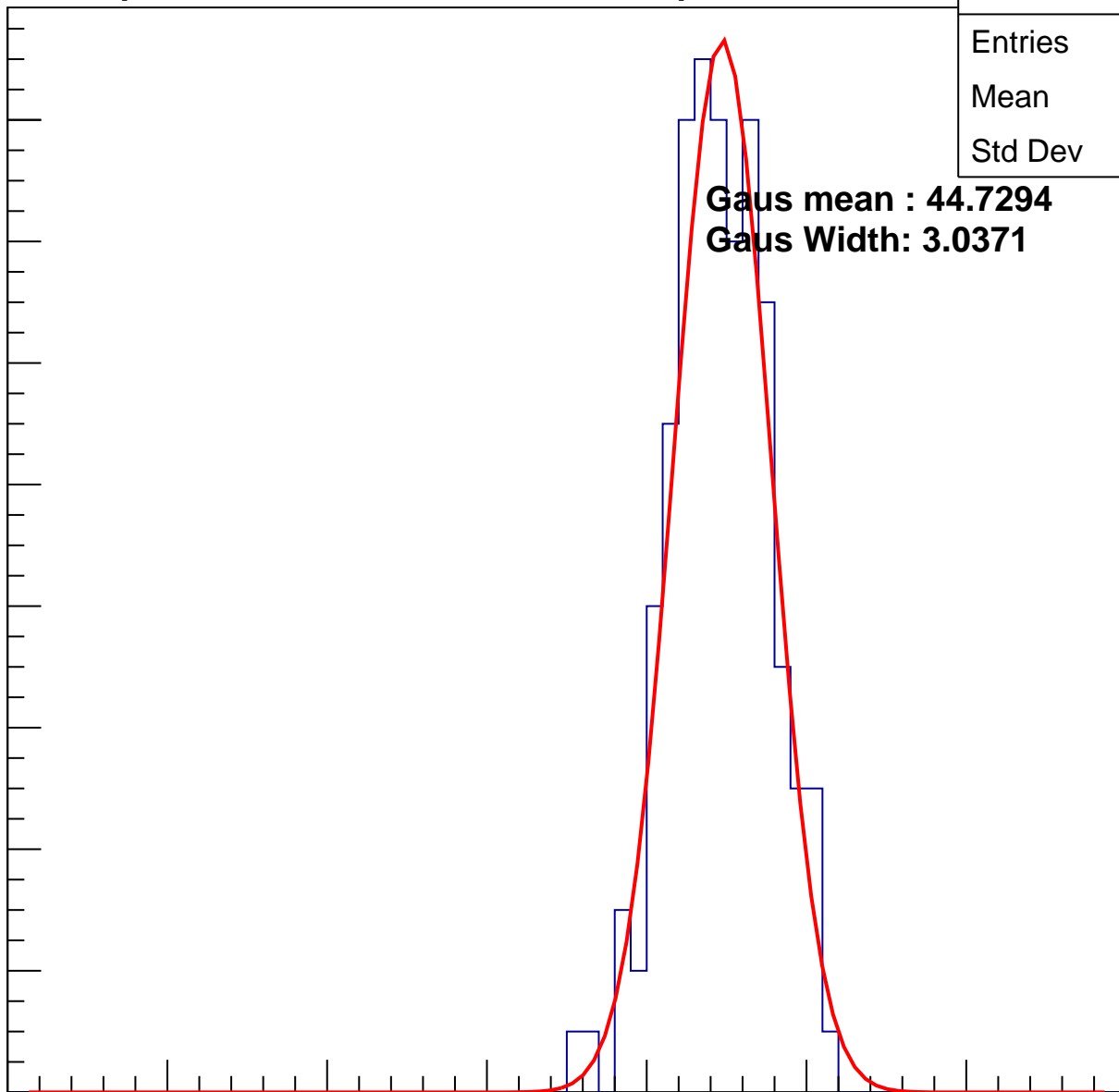
40

50

60

70

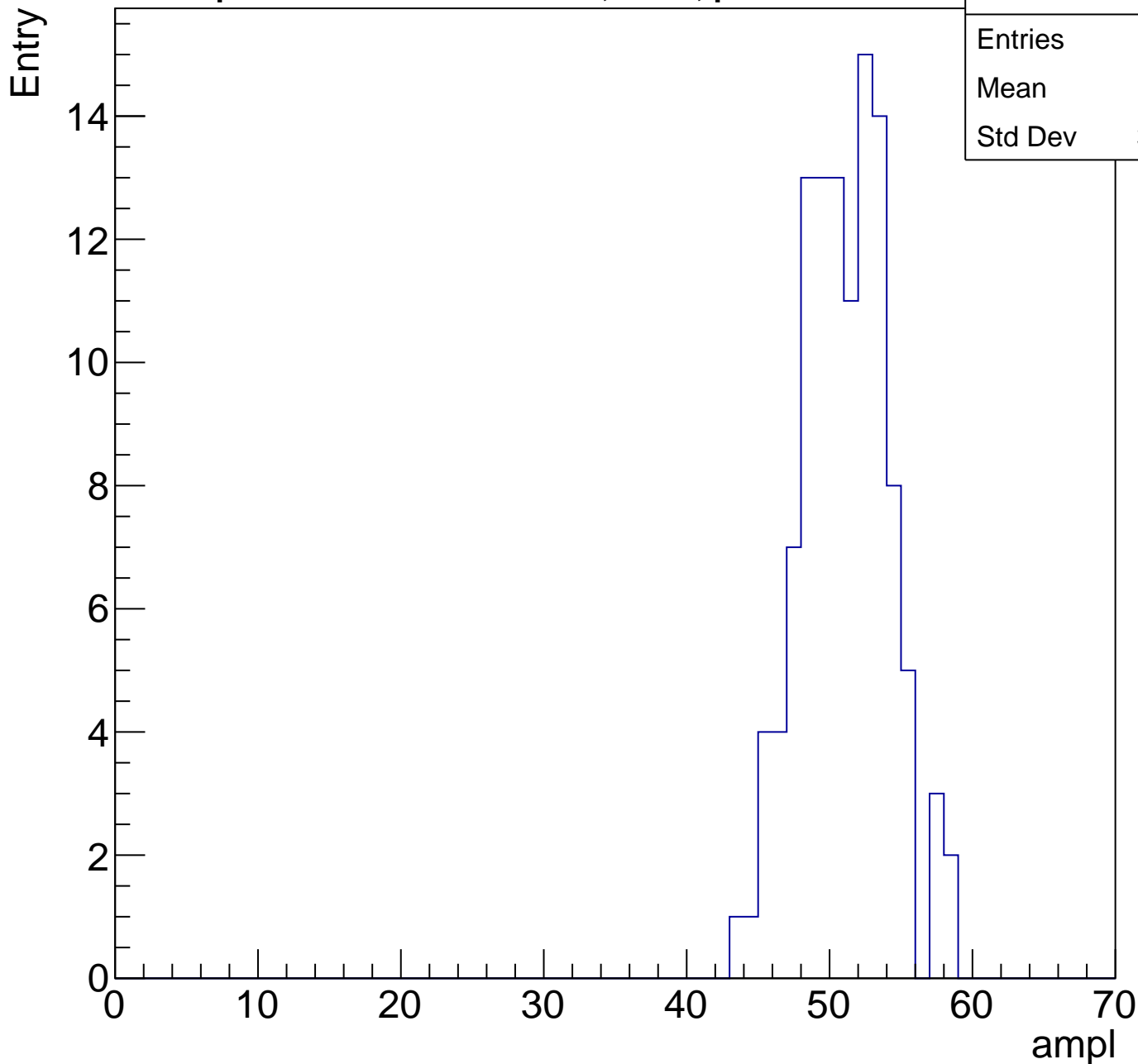
ampl



B1L001S, U22-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	50.6
Std Dev	3.051



B1L001S, U22-ch16, adc4

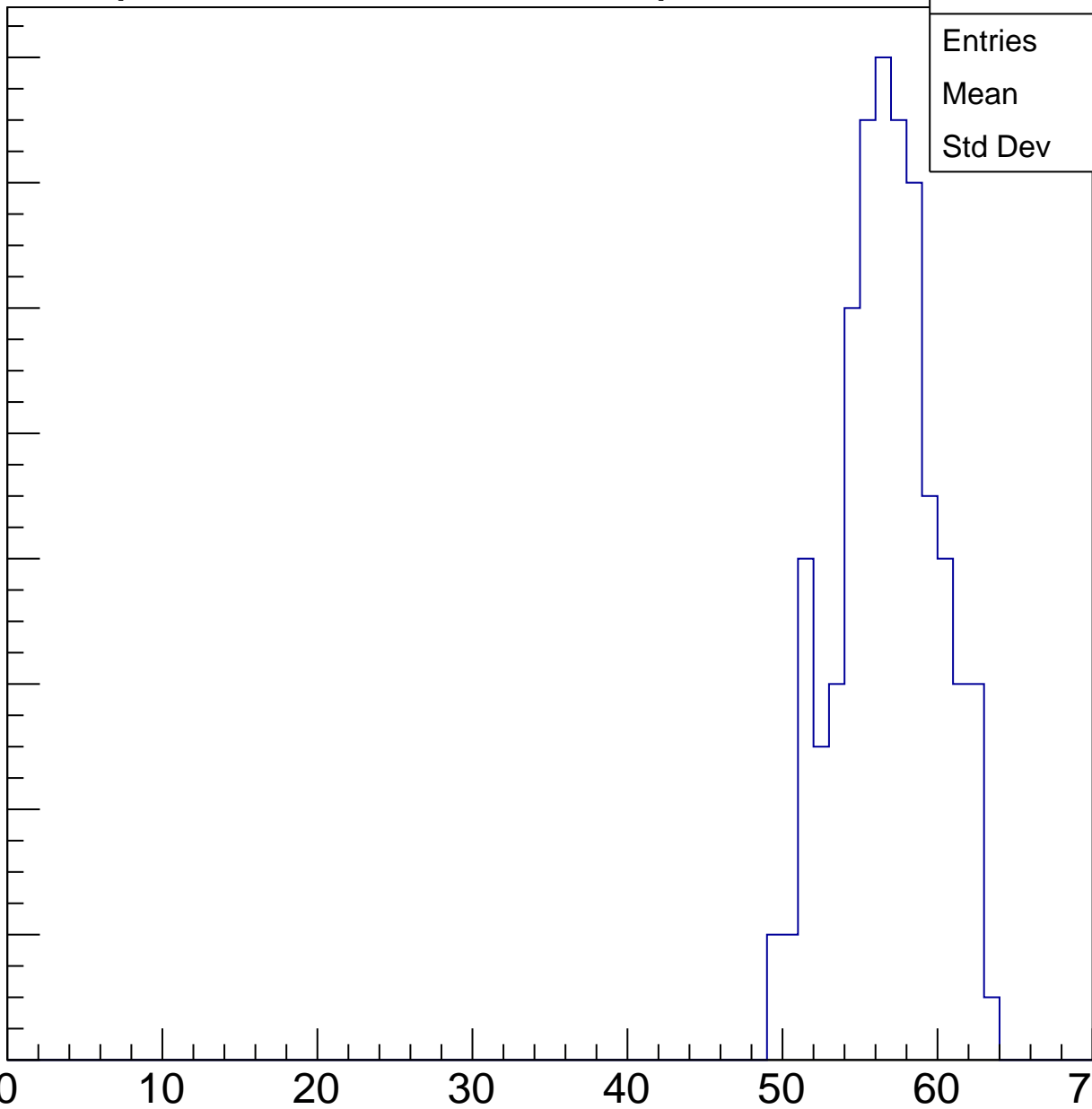
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	56.26
Std Dev	3.167

ampl



B1L001S, U22-ch16, adc5

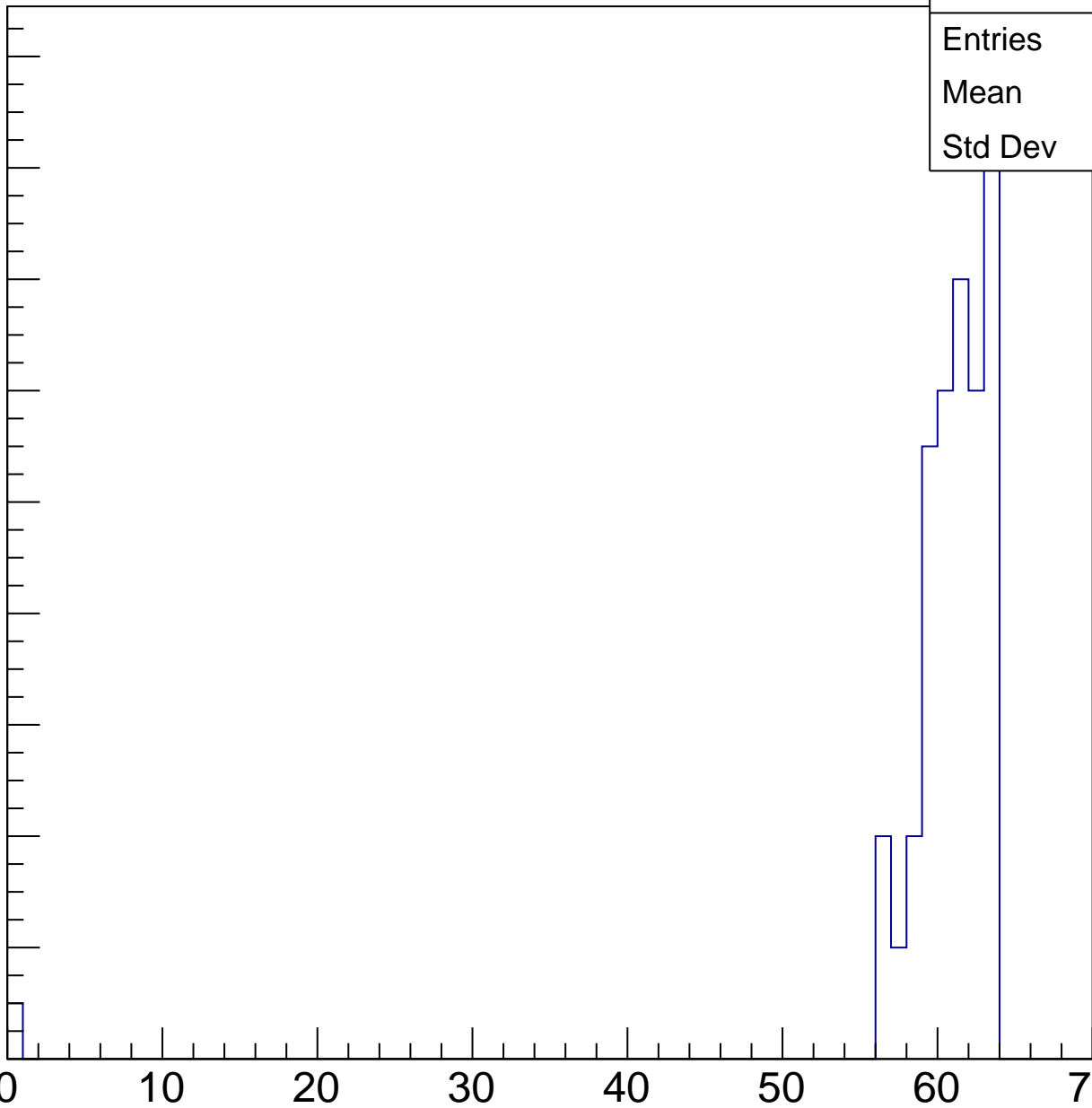
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.88
Std Dev	7.1

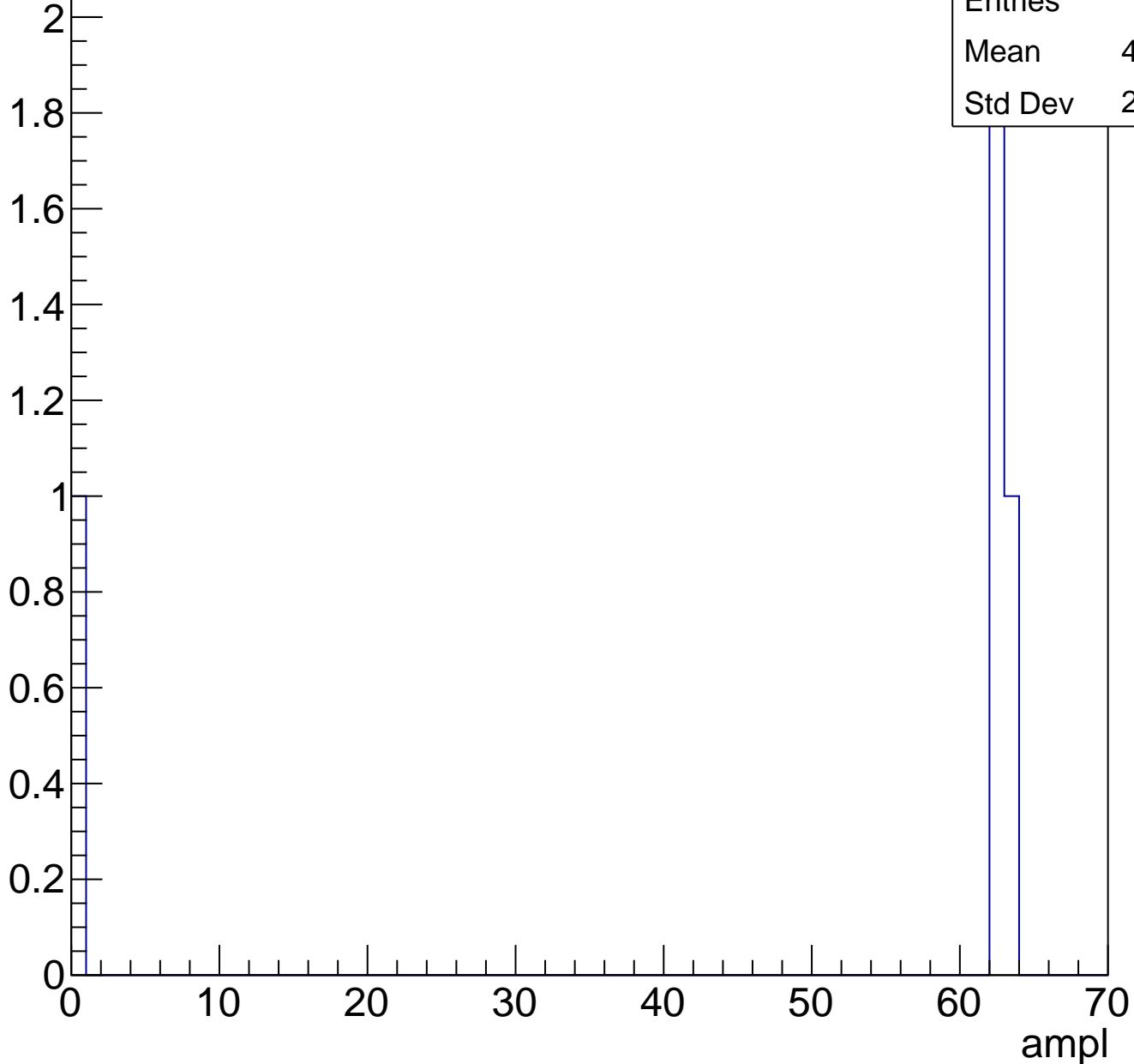
ampl



B1L001S, U22-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

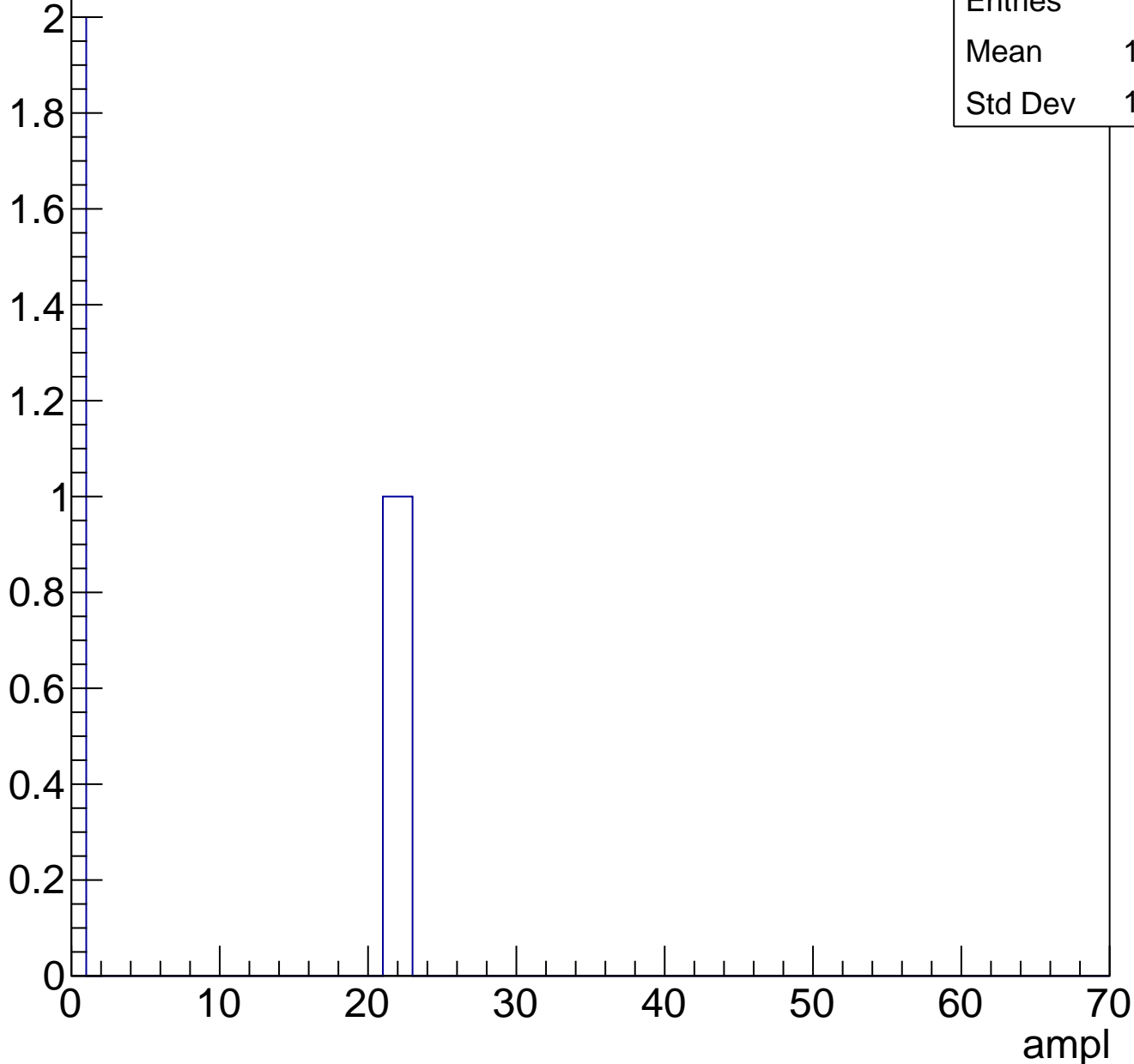
Entry



B1L001S, U22-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	10.75
Std Dev	10.76

B1L001S, U22-ch17, adc0

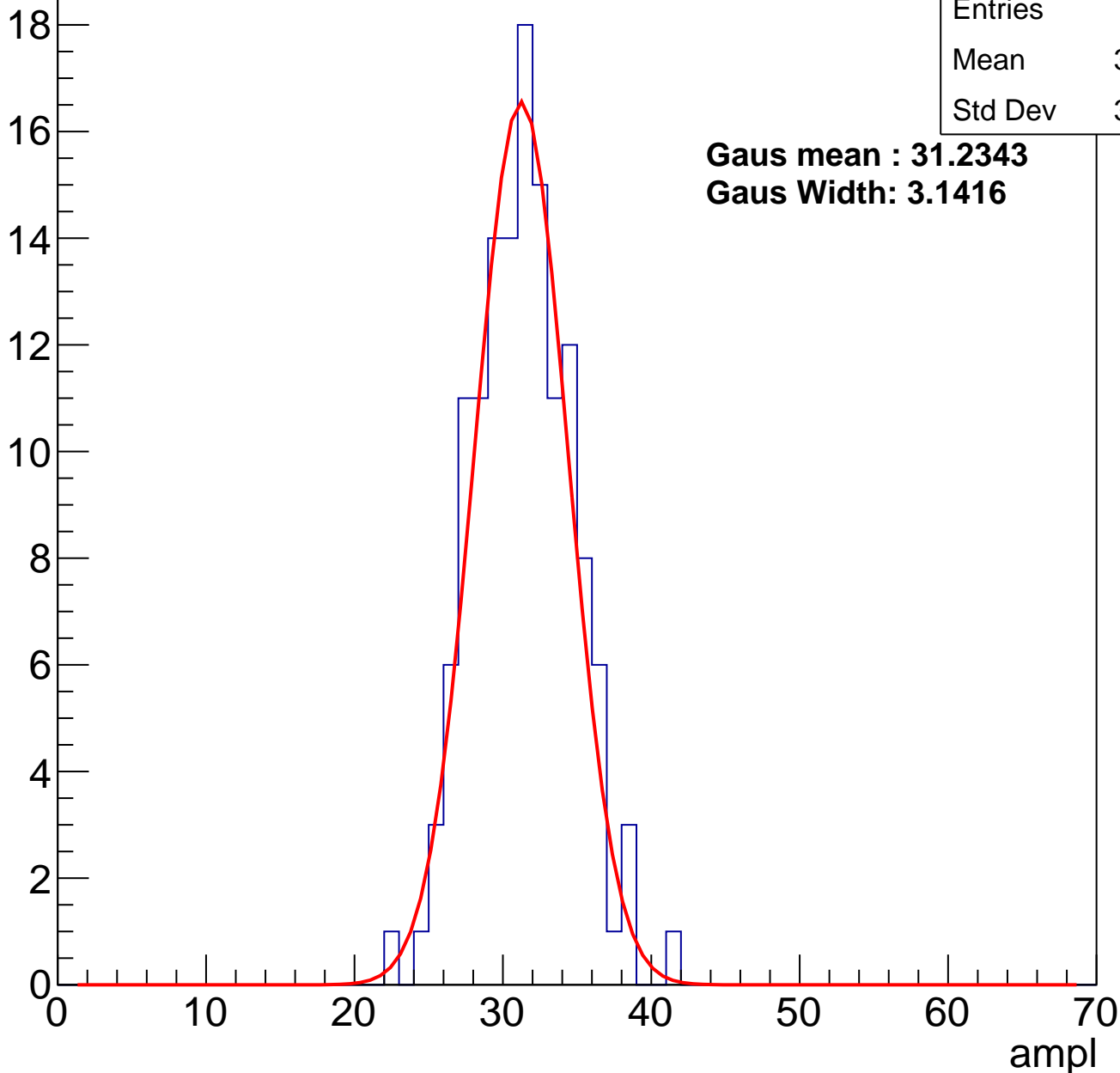
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.92
Std Dev	3.277

Gaus mean : 31.2343

Gaus Width: 3.1416

Entry

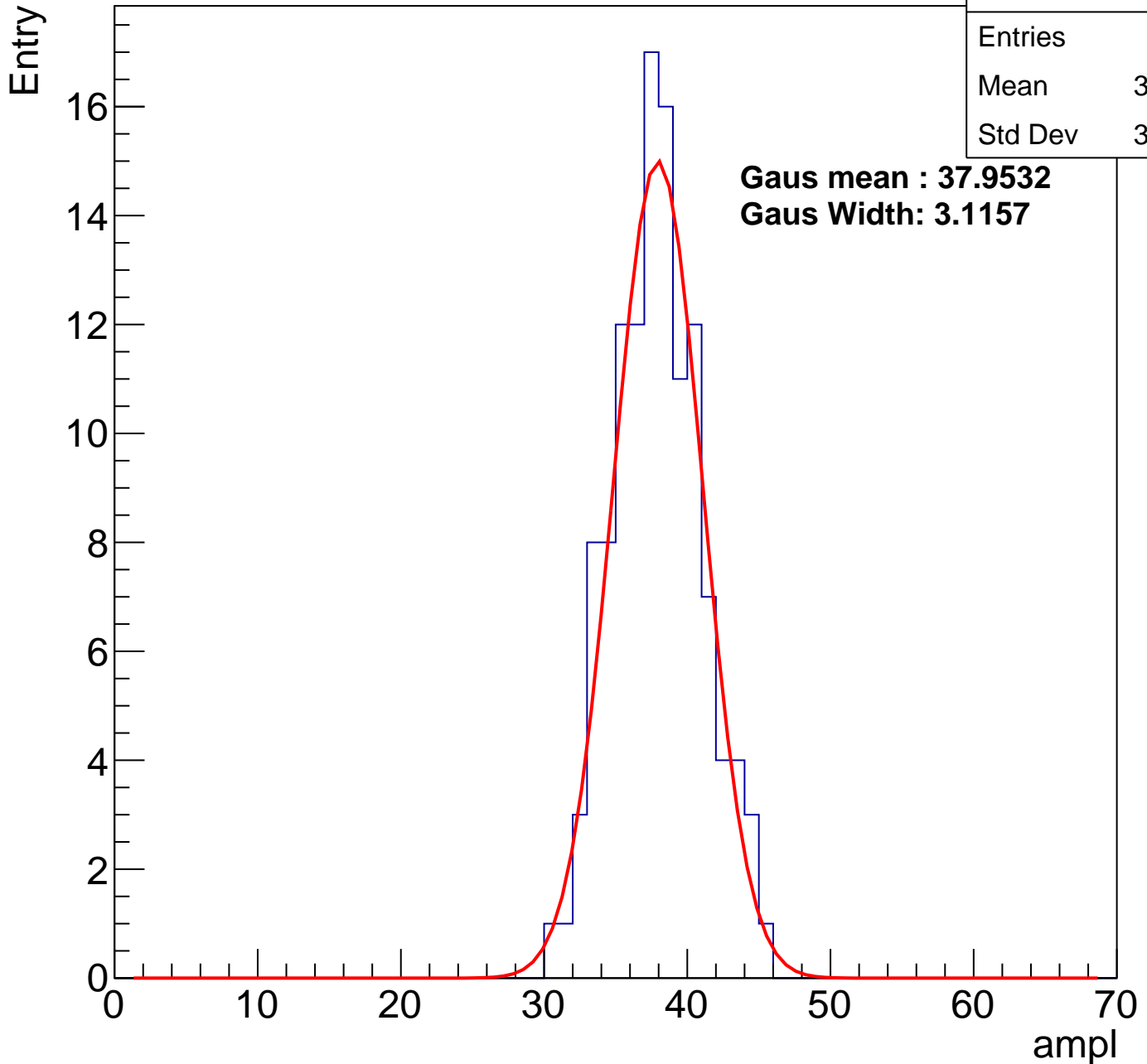


B1L001S, U22-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	37.46
Std Dev	3.068

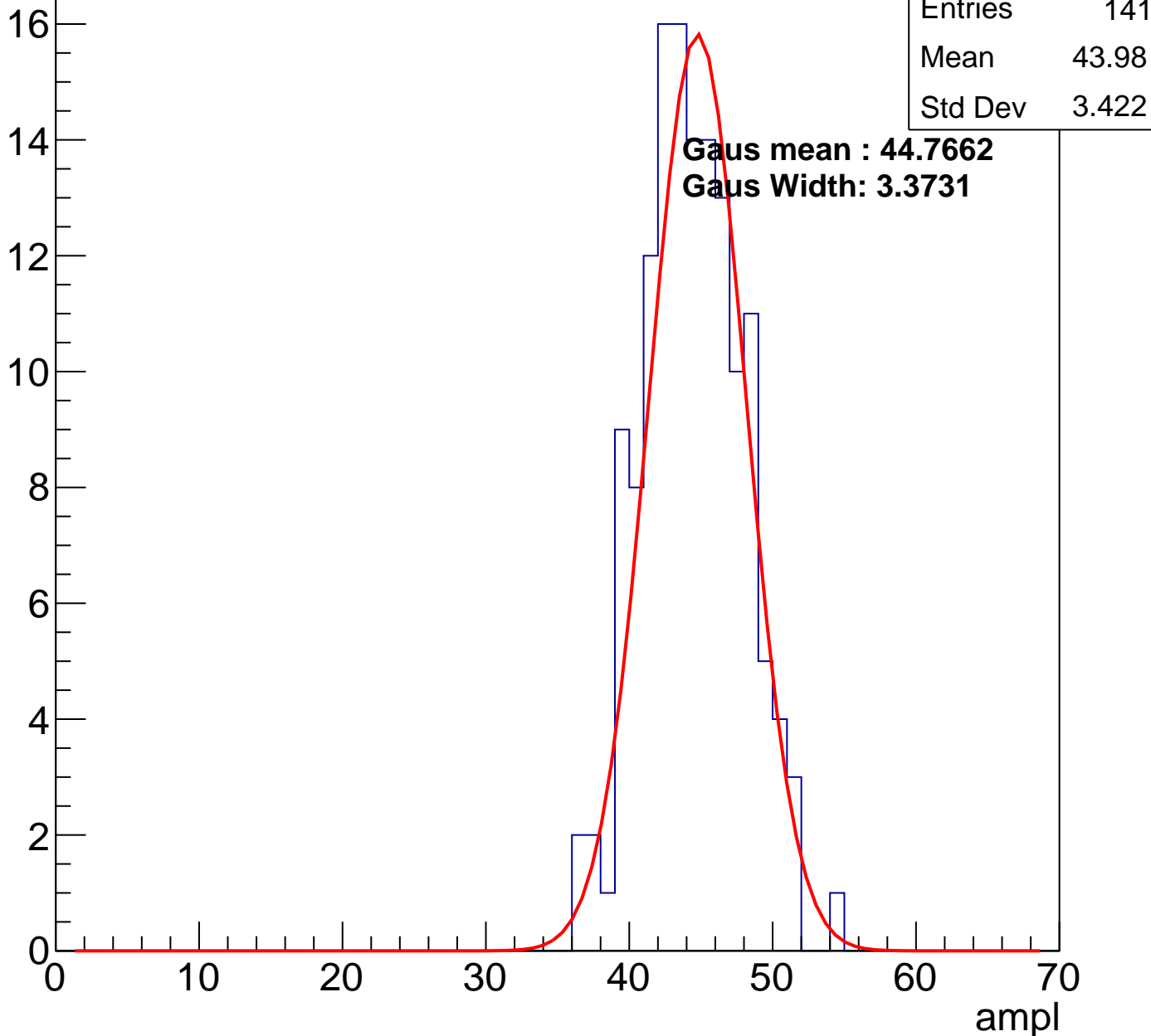
Gaus mean : 37.9532
Gaus Width: 3.1157



B1L001S, U22-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

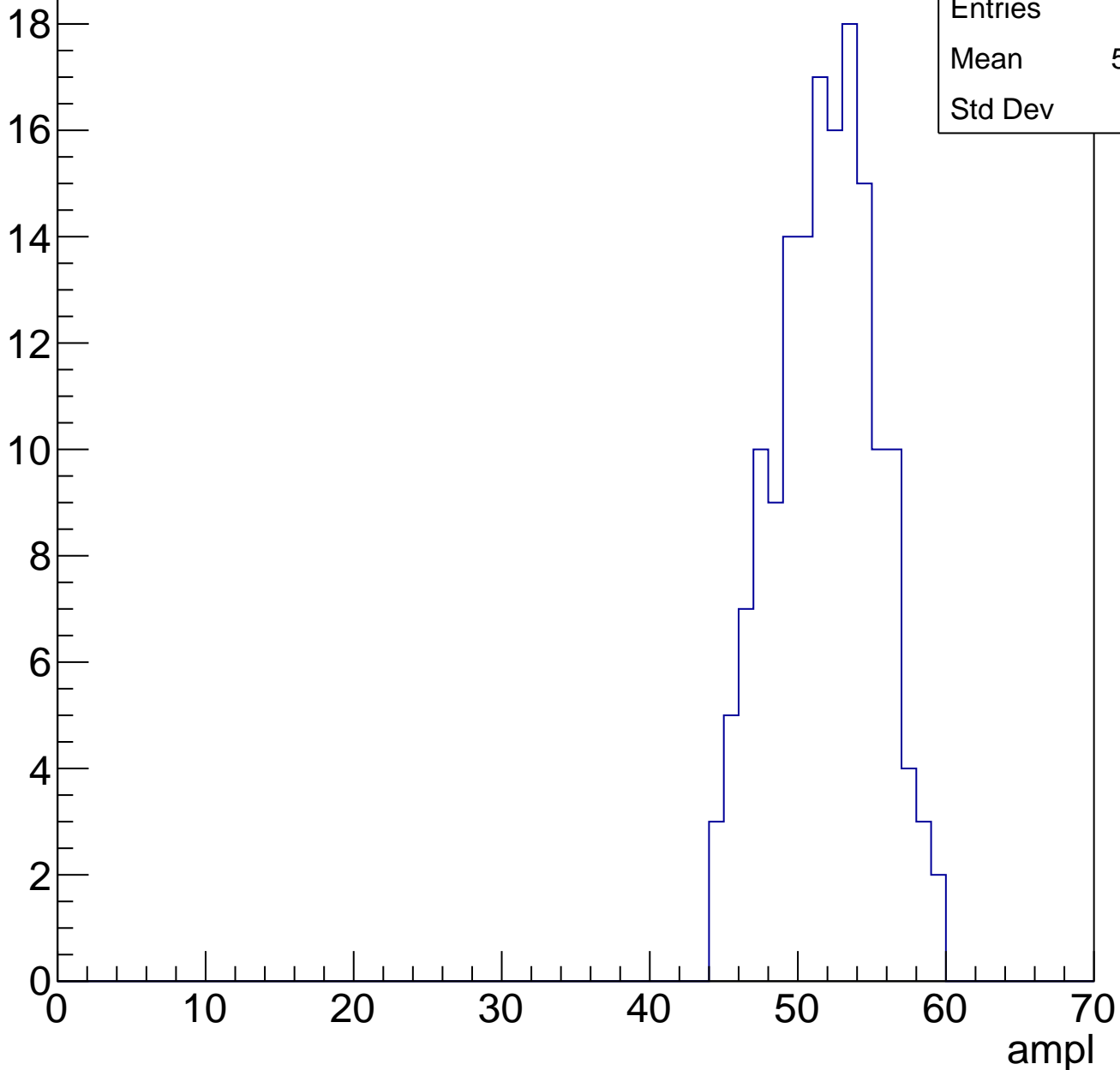
Entry



B1L001S, U22-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

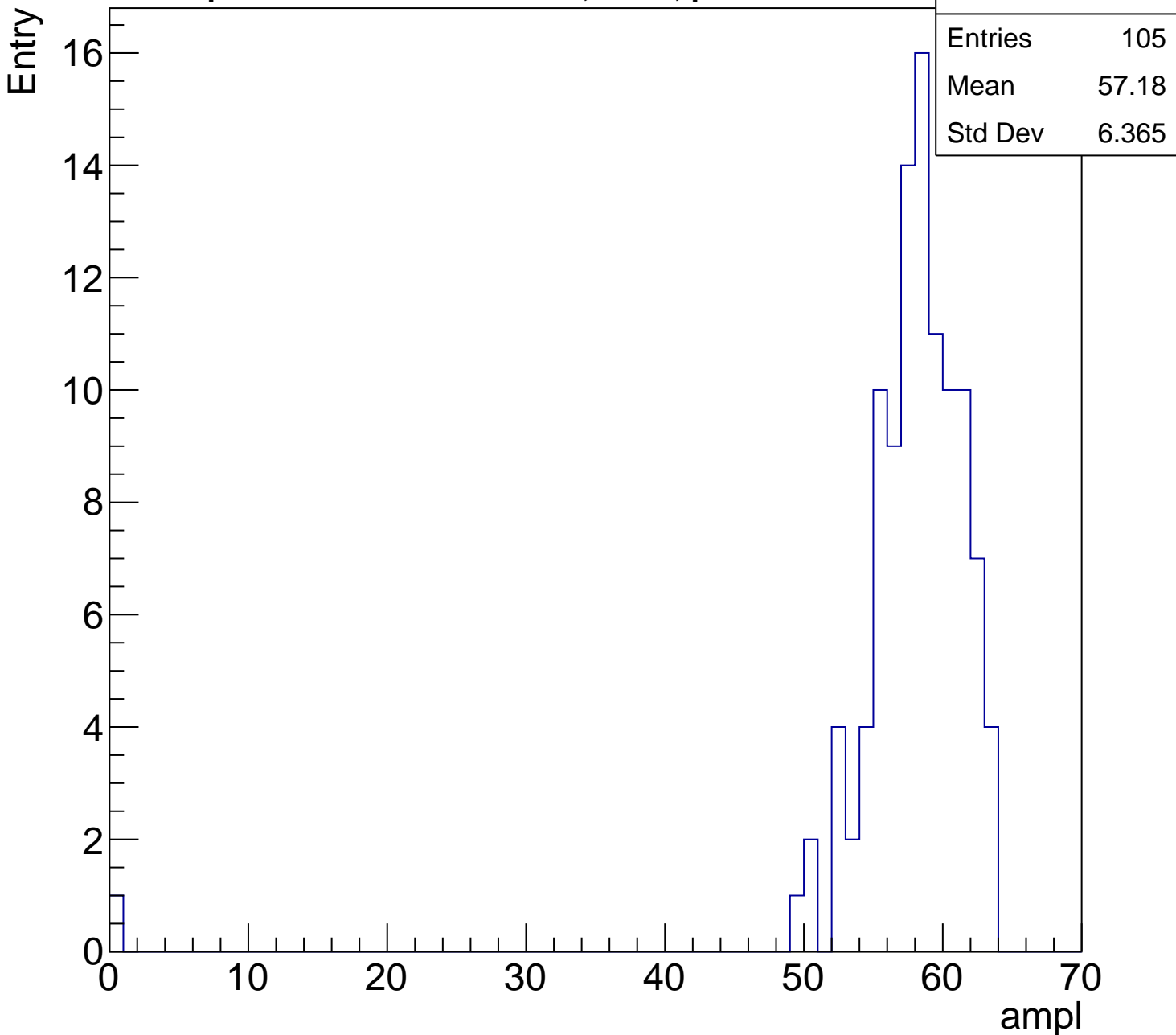
Entries	105
Mean	57.18
Std Dev	6.365

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch17, adc5

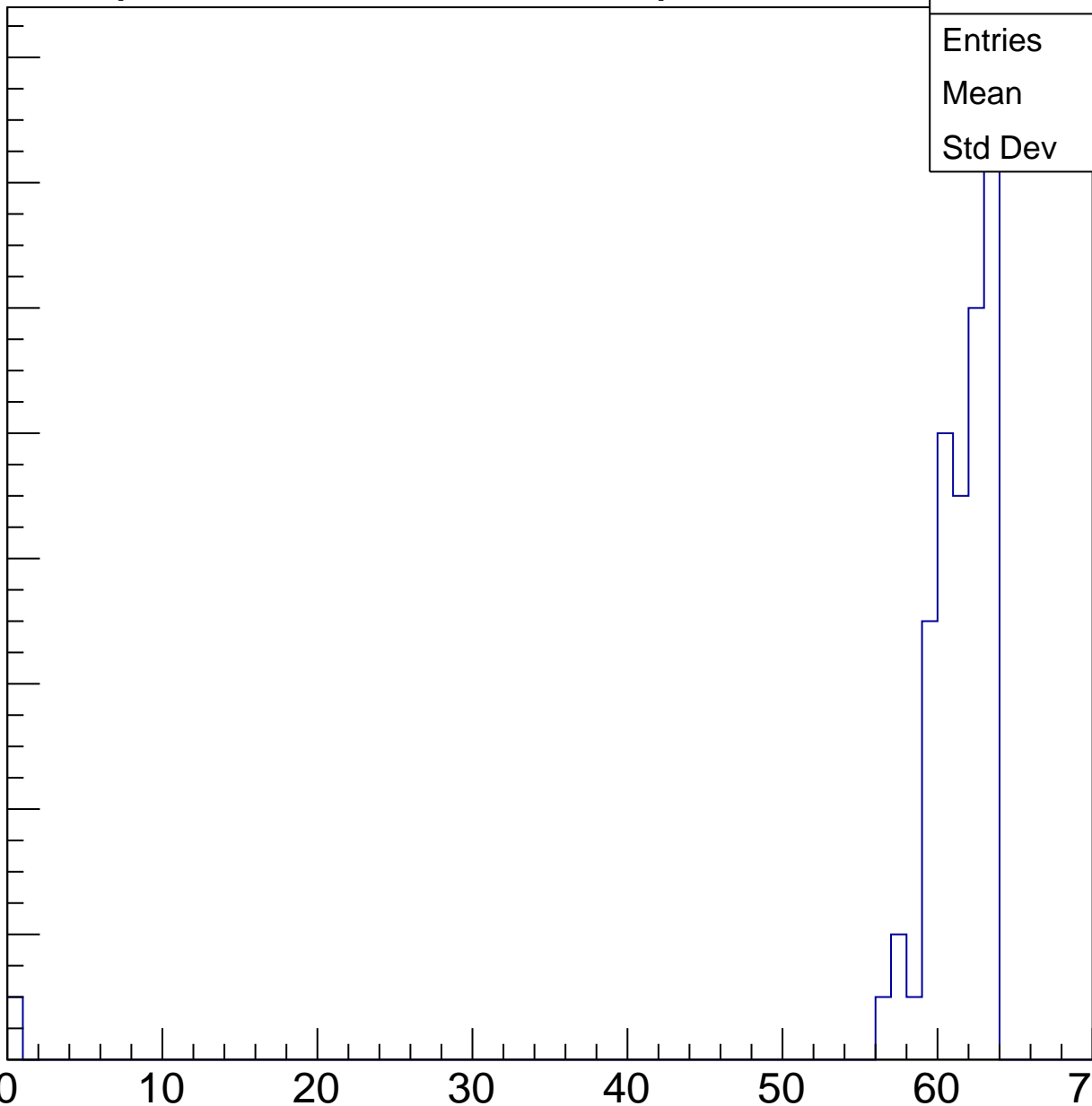
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	60.03
Std Dev	8.074

ampl



B1L001S, U22-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch18, adc0

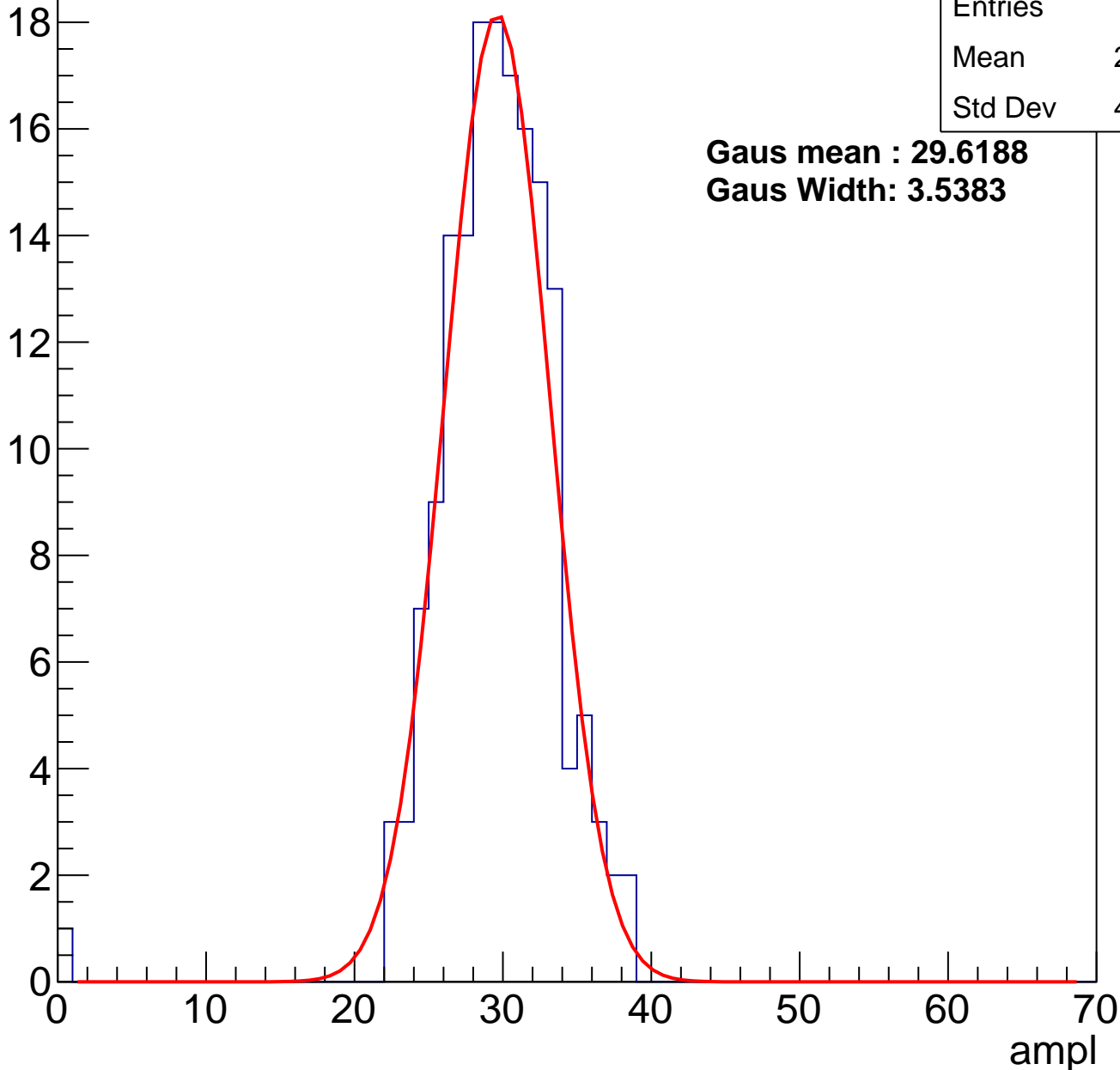
calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	29.15
Std Dev	4.097

Gaus mean : 29.6188

Gaus Width: 3.5383

Entry



B1L001S, U22-ch18, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.29
Std Dev	3.199

Gaus mean : 37.7017

Gaus Width: 3.3745

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

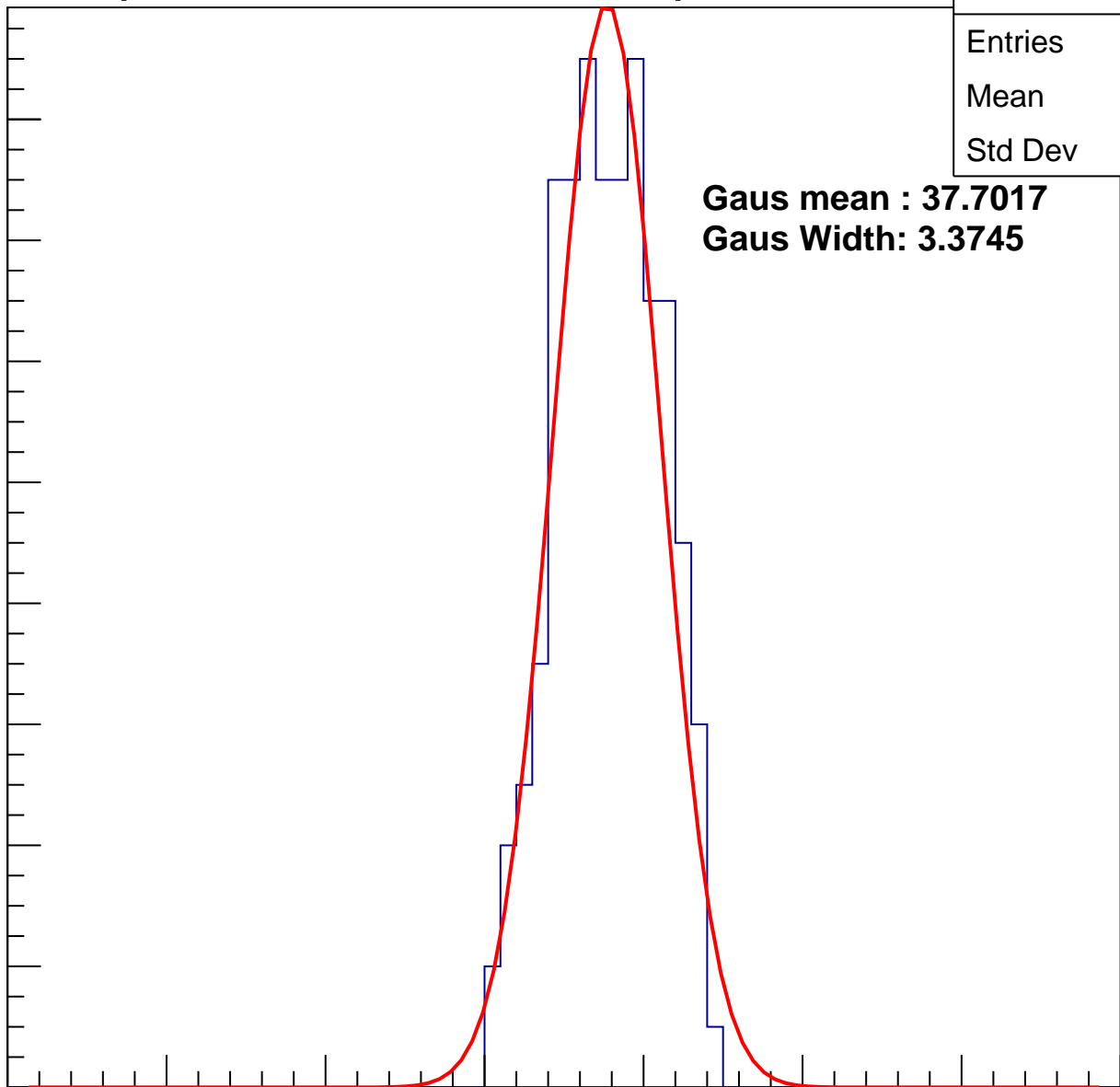
40

50

60

70

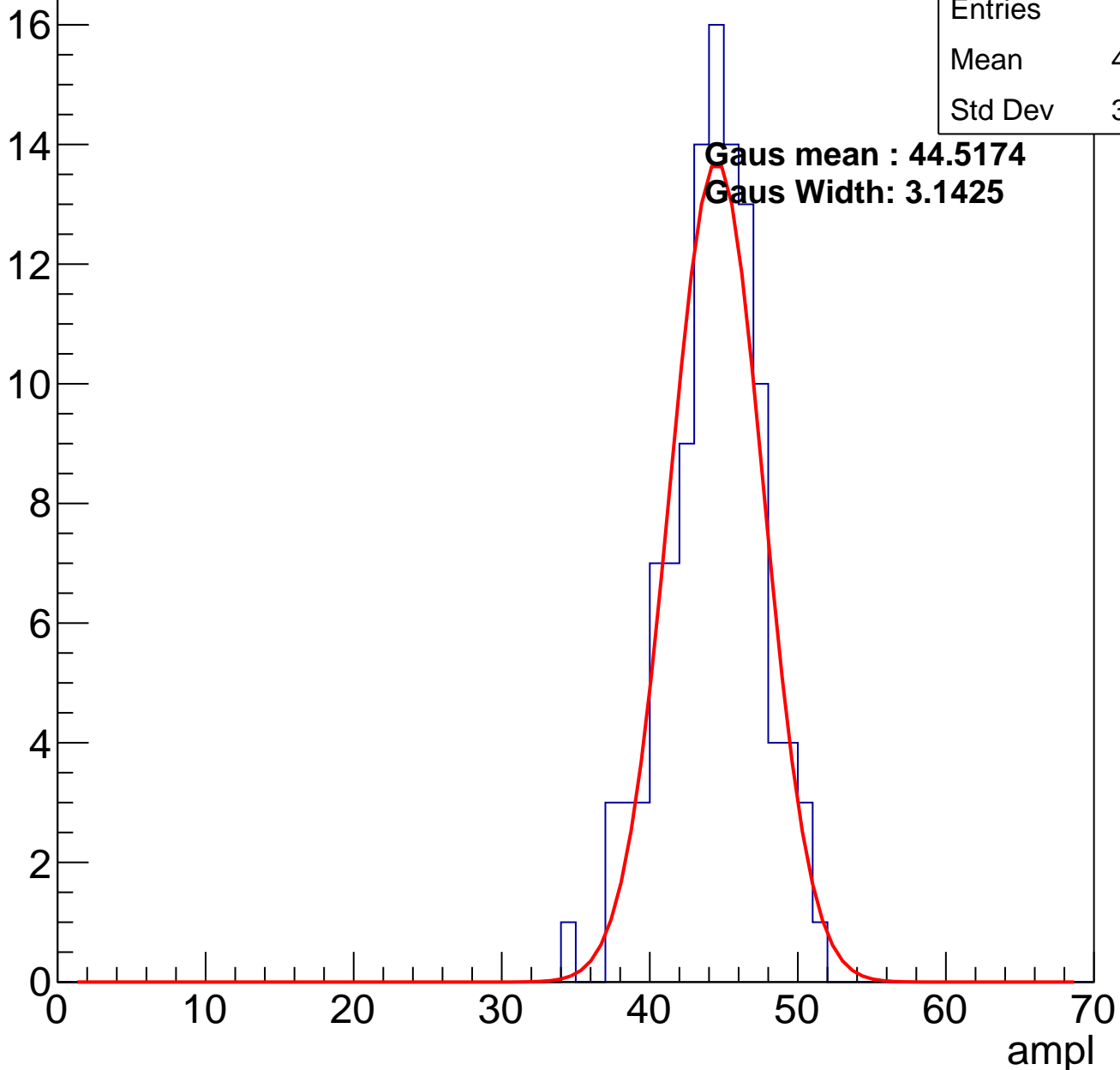
ampl



B1L001S, U22-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

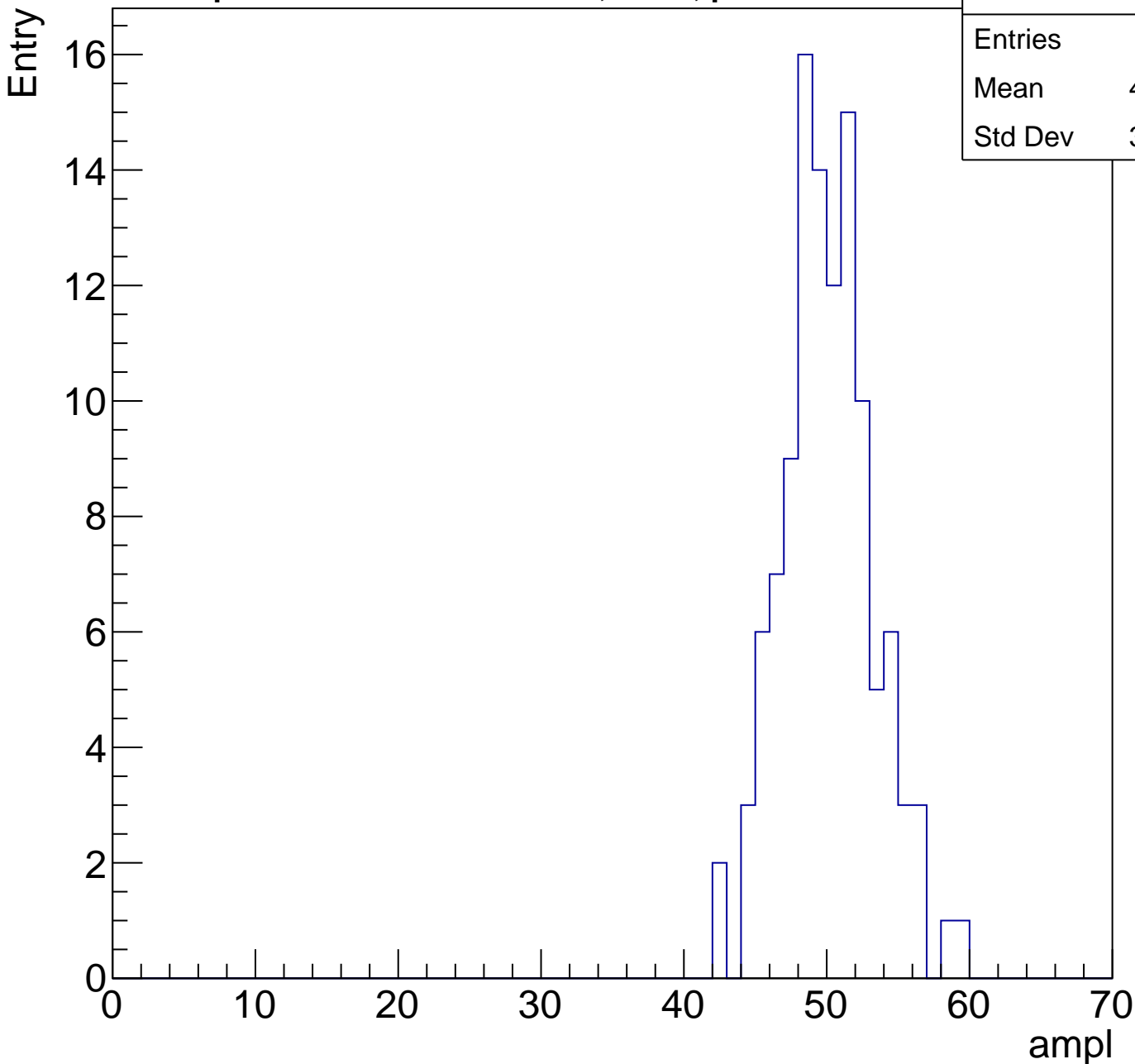
Entry



B1L001S, U22-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	49.64
Std Dev	3.212

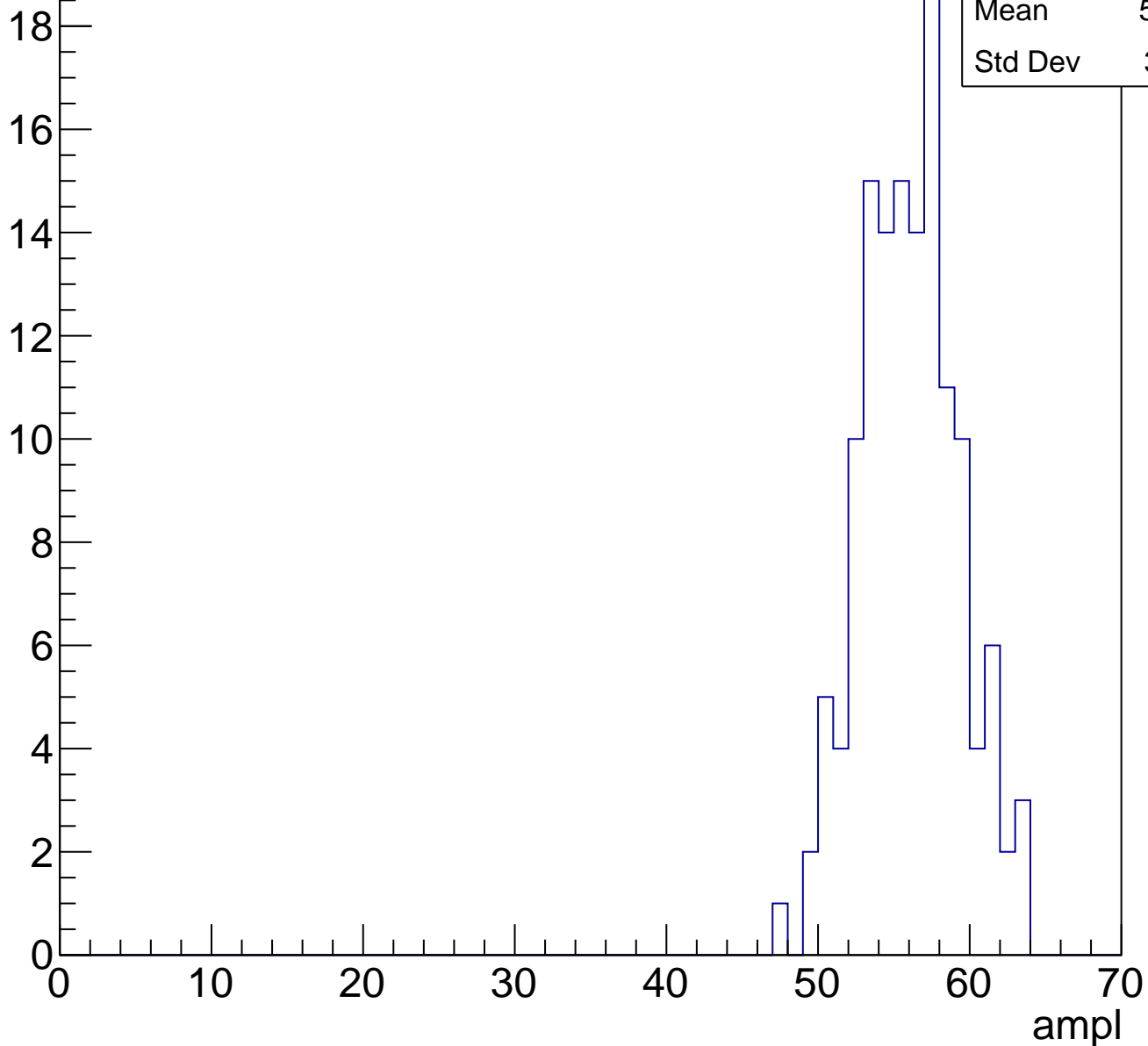


B1L001S, U22-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.62
Std Dev	3.211

Entry



B1L001S, U22-ch18, adc5

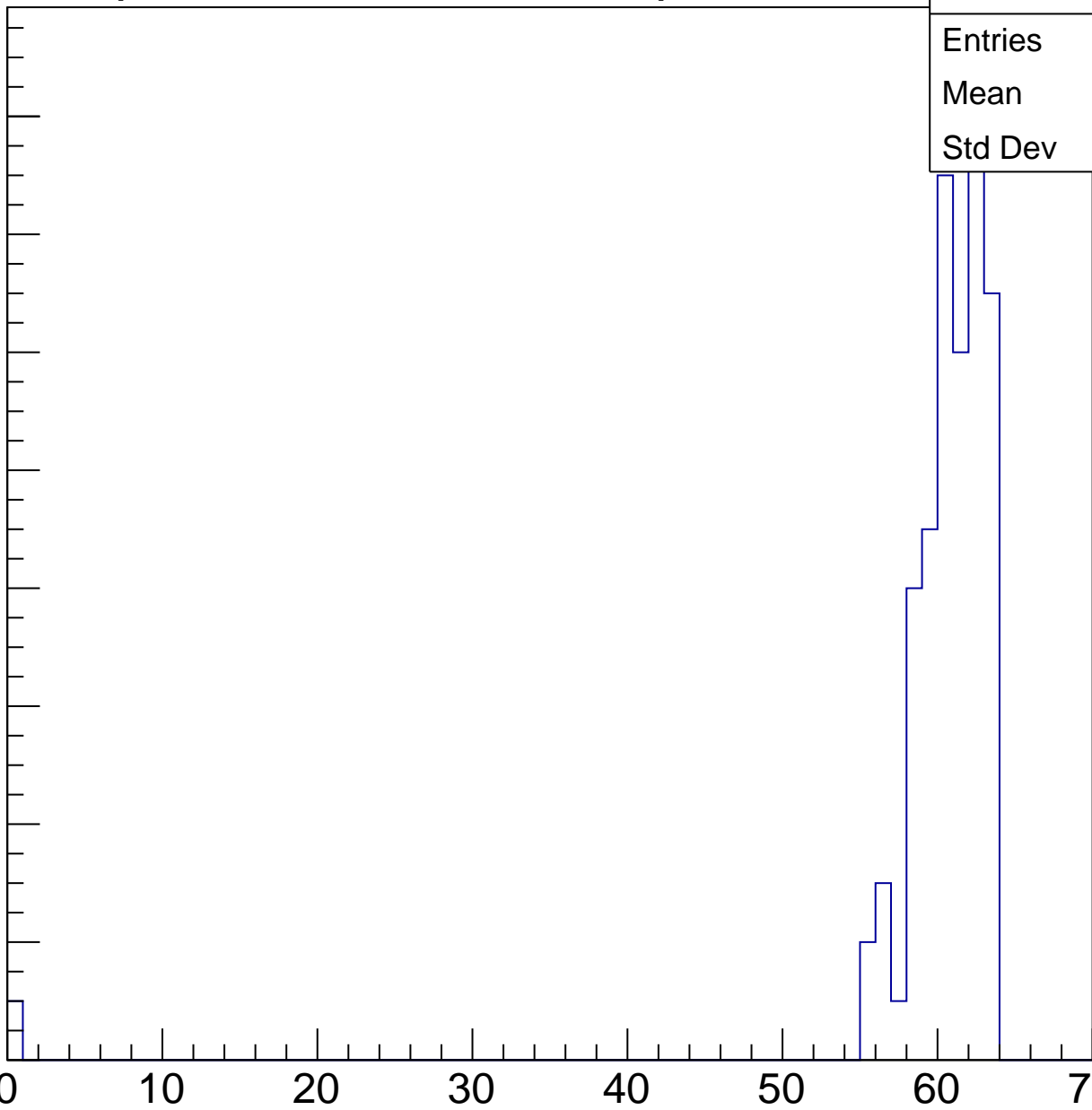
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.69
Std Dev	6.972

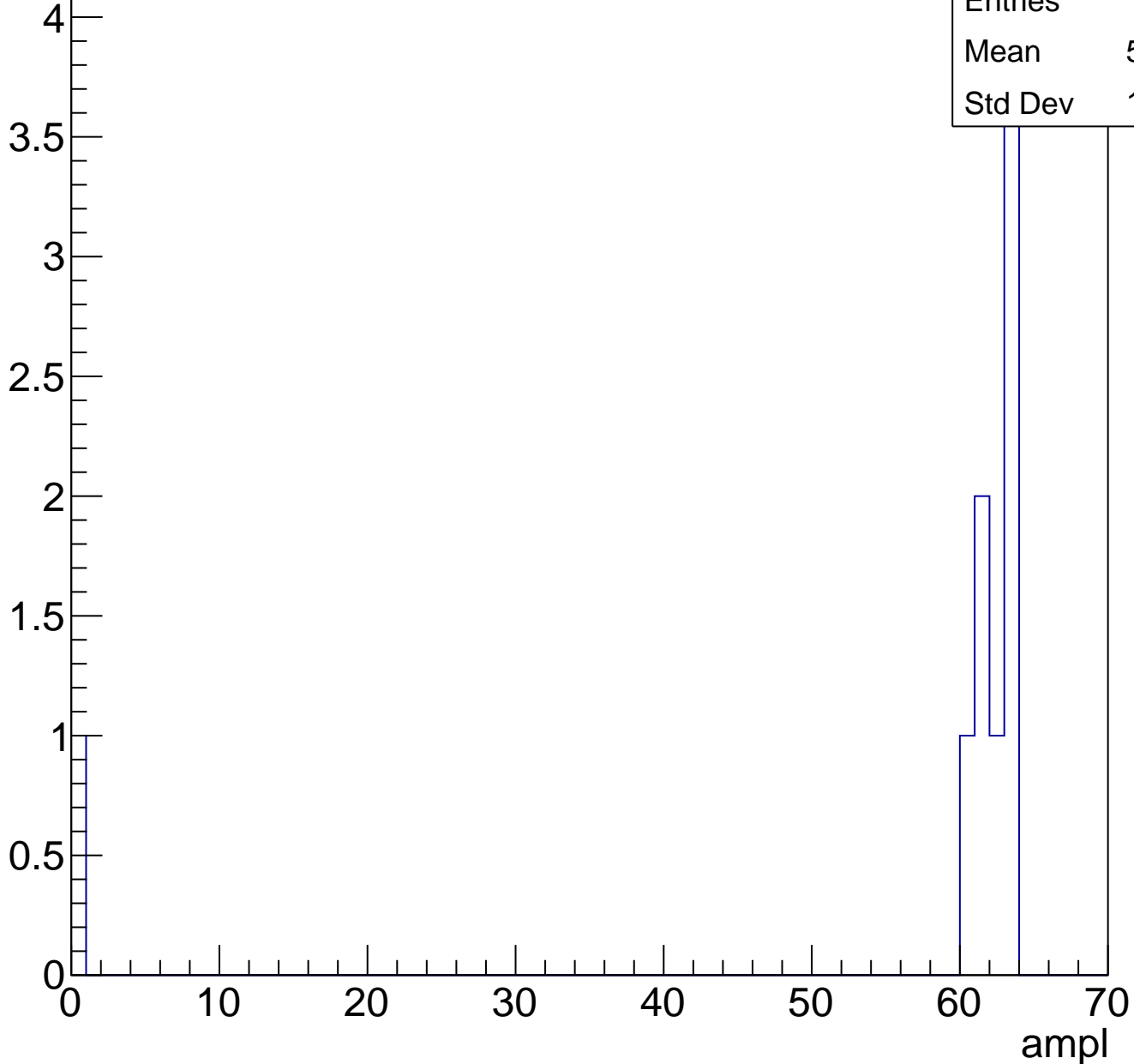
ampl



B1L001S, U22-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch18, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	28.96
Std Dev	3.134

Gaus mean : 29.4411

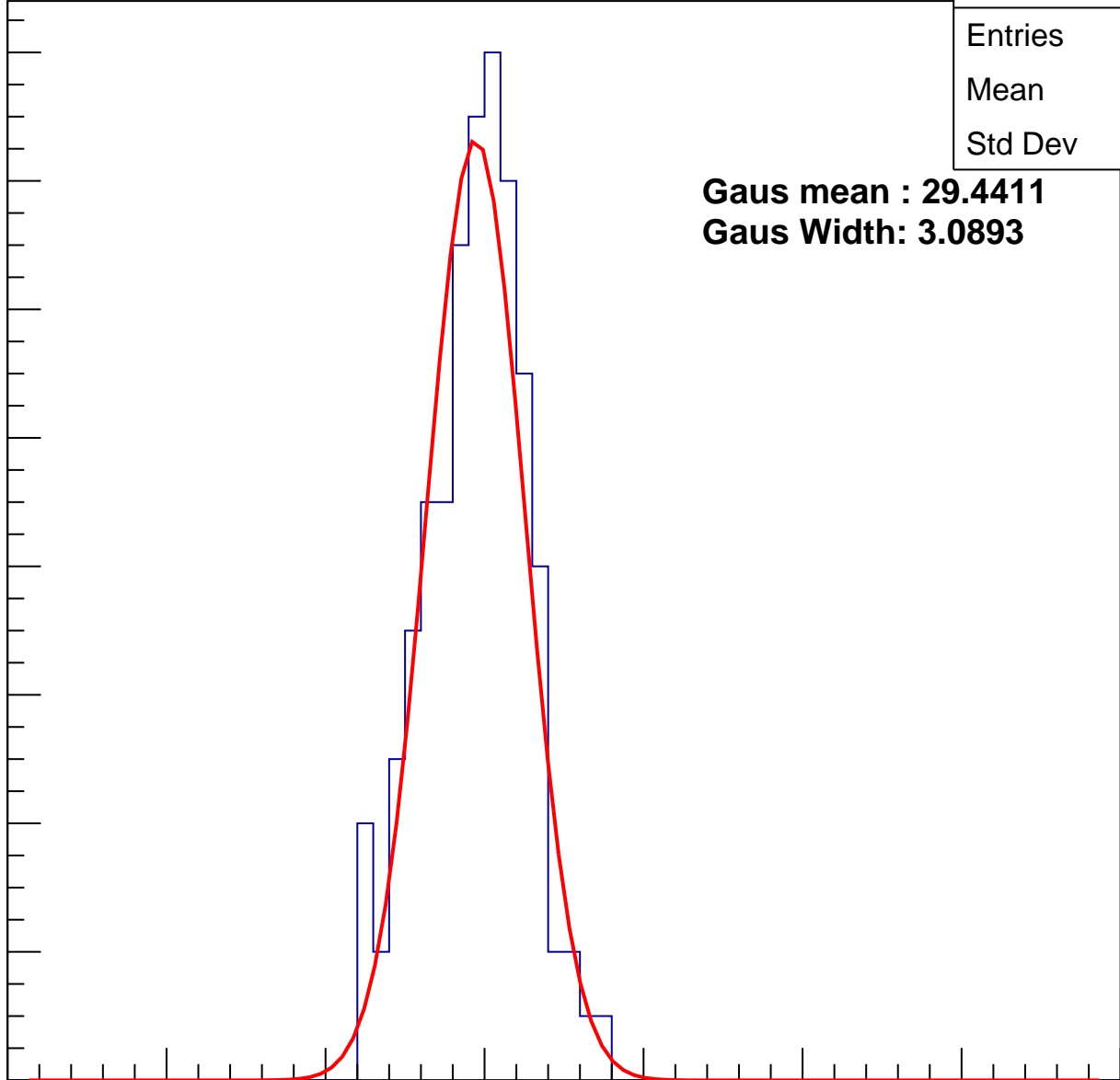
Gaus Width: 3.0893

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch19, adc1

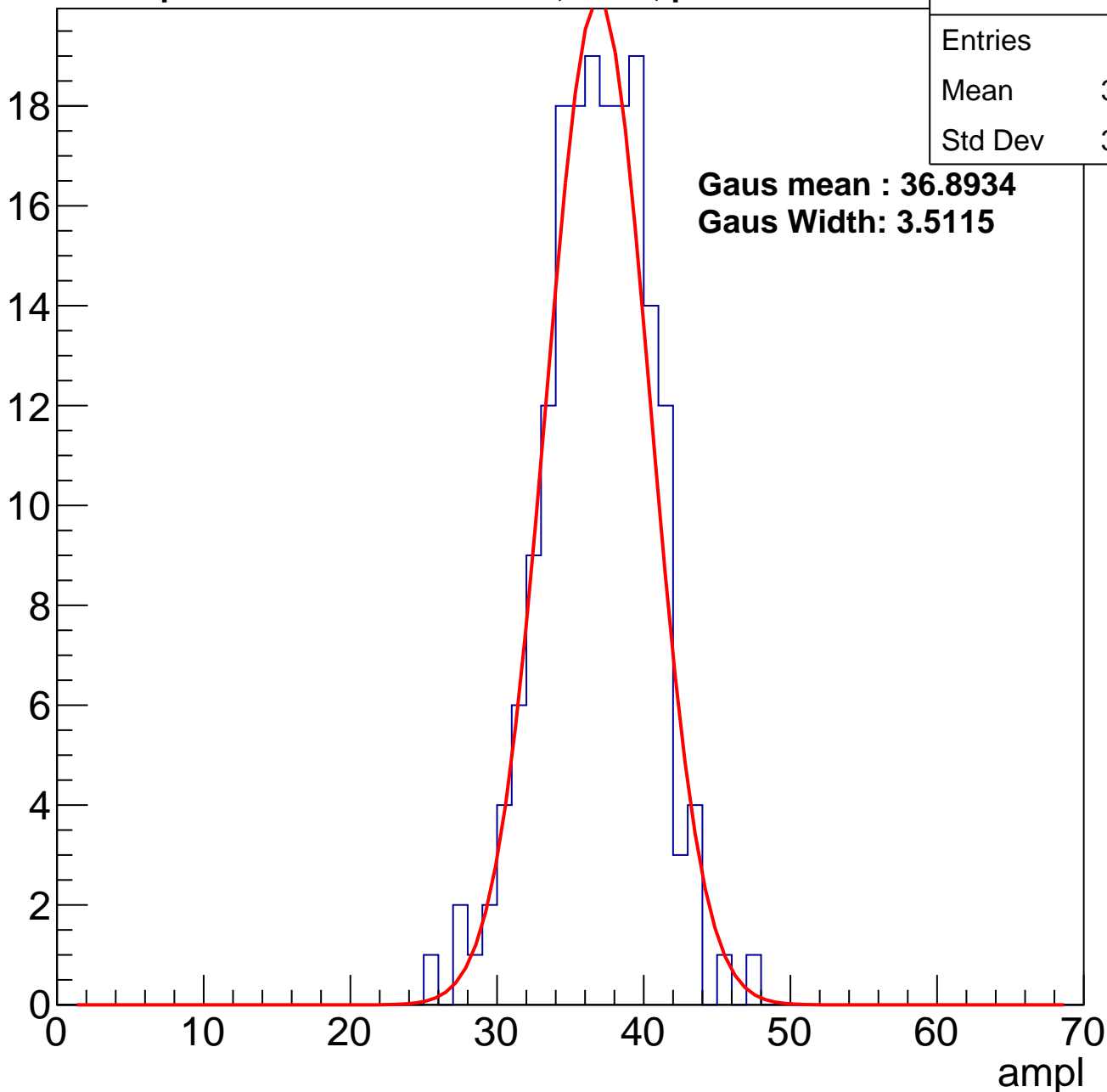
calib_packv5_042523_0143.root, FC#2, port C2

Entries	182
Mean	36.34
Std Dev	3.594

Gaus mean : 36.8934

Gaus Width: 3.5115

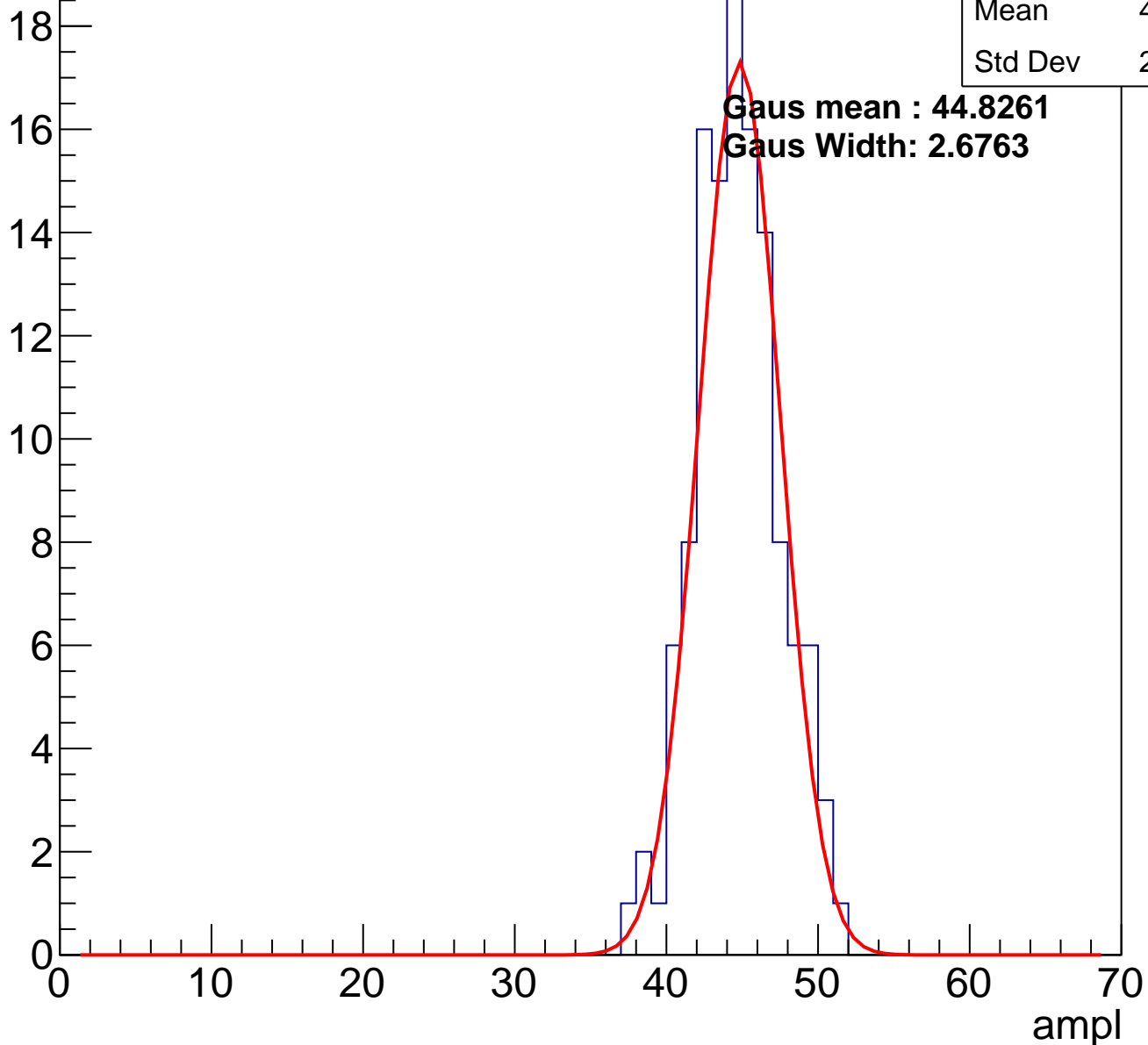
Entry



B1L001S, U22-ch19, adc2

calib_packv5_042523_0143.root, FC#2, port C2

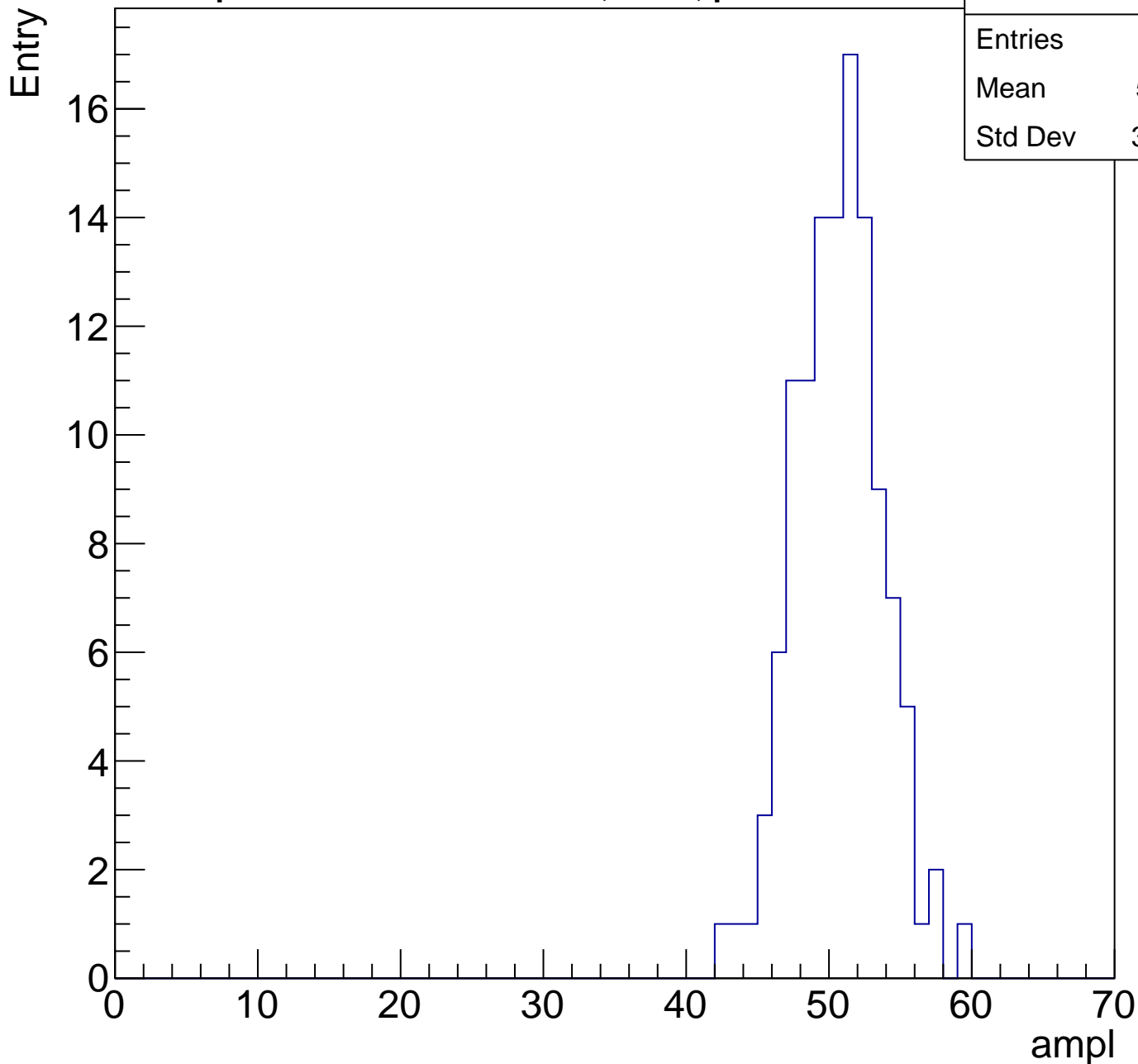
Entry



B1L001S, U22-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

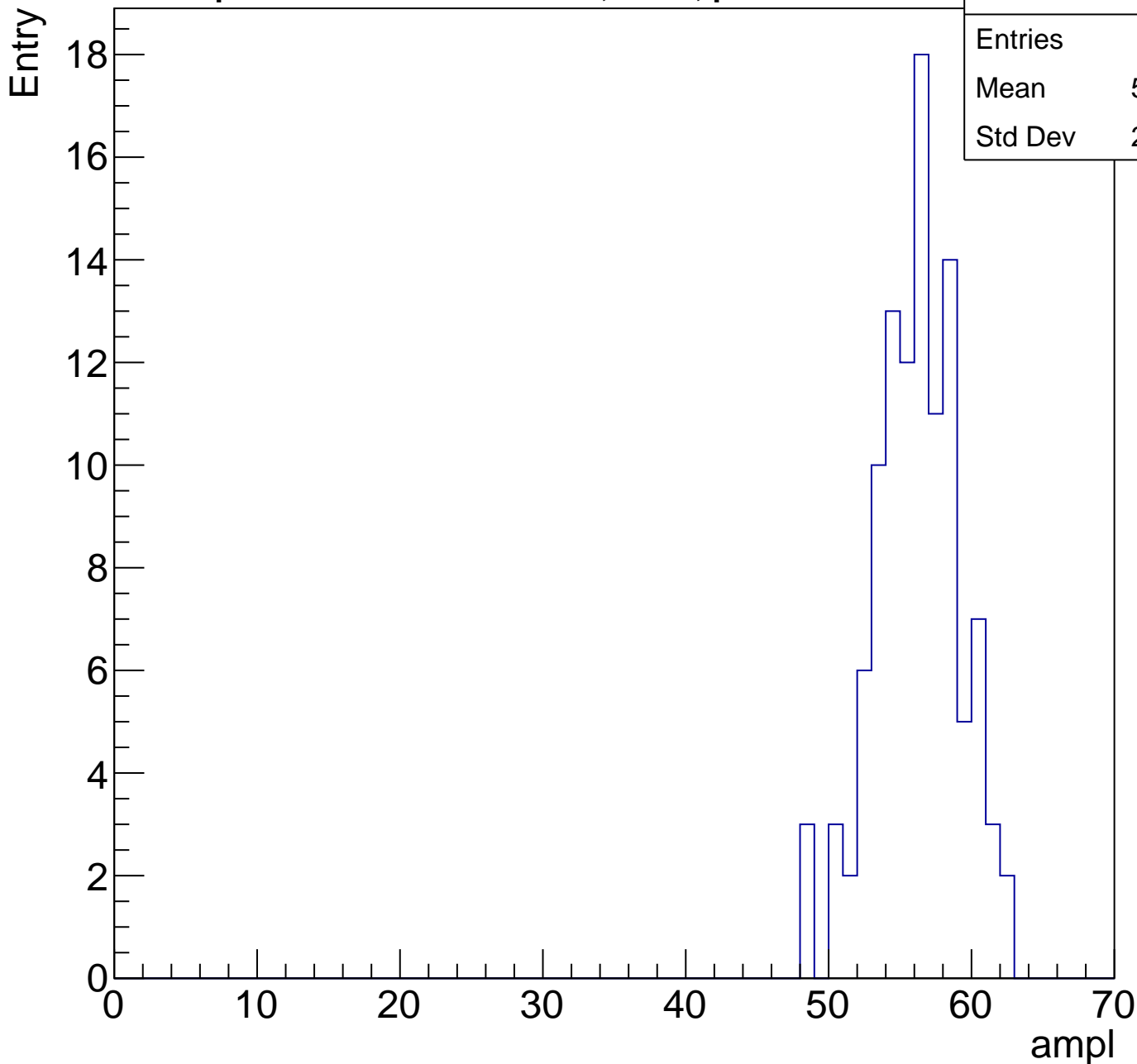
Entries	118
Mean	50.21
Std Dev	3.022



B1L001S, U22-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	55.68
Std Dev	2.967



B1L001S, U22-ch19, adc5

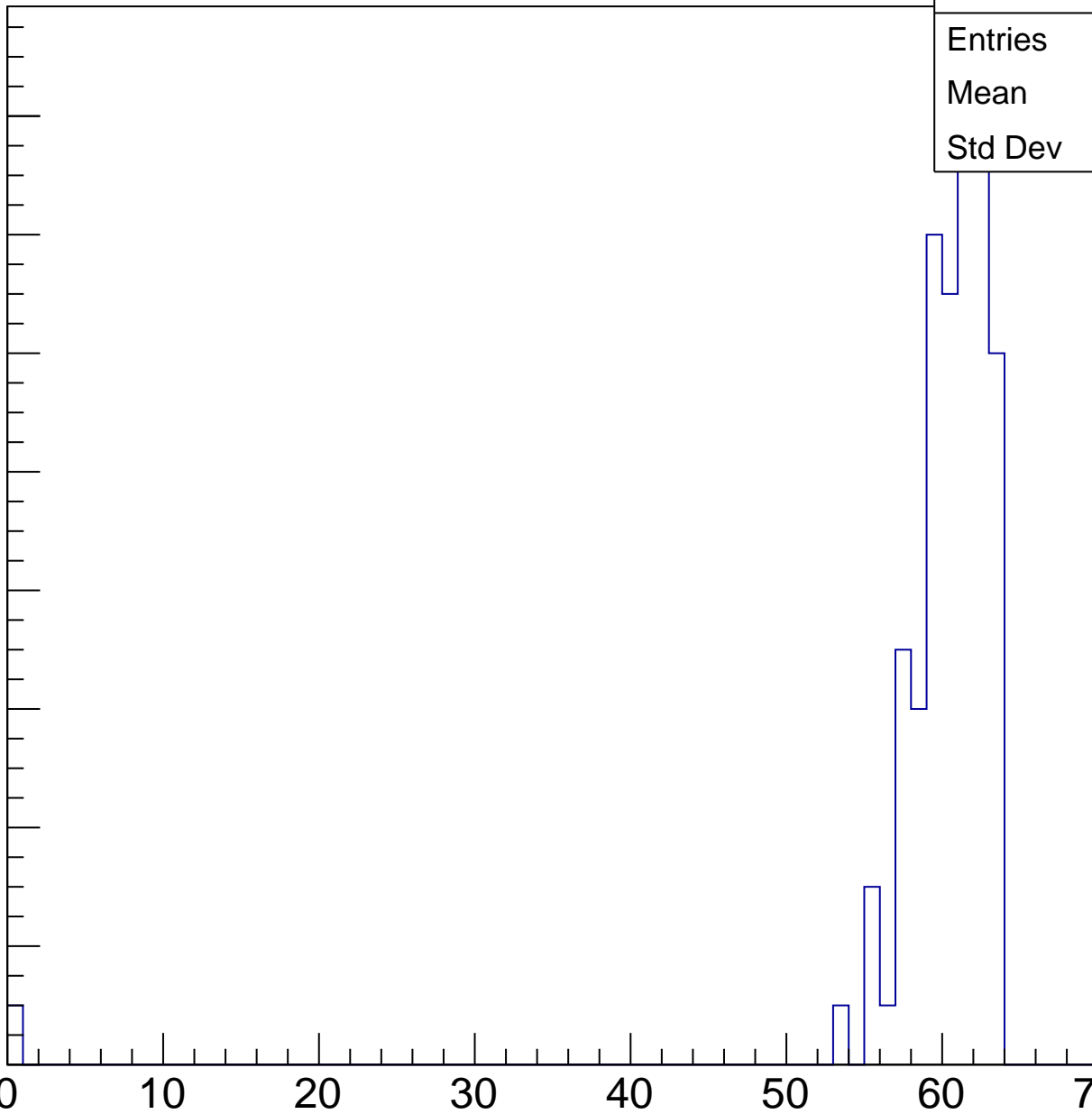
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.48
Std Dev	6.637

ampl

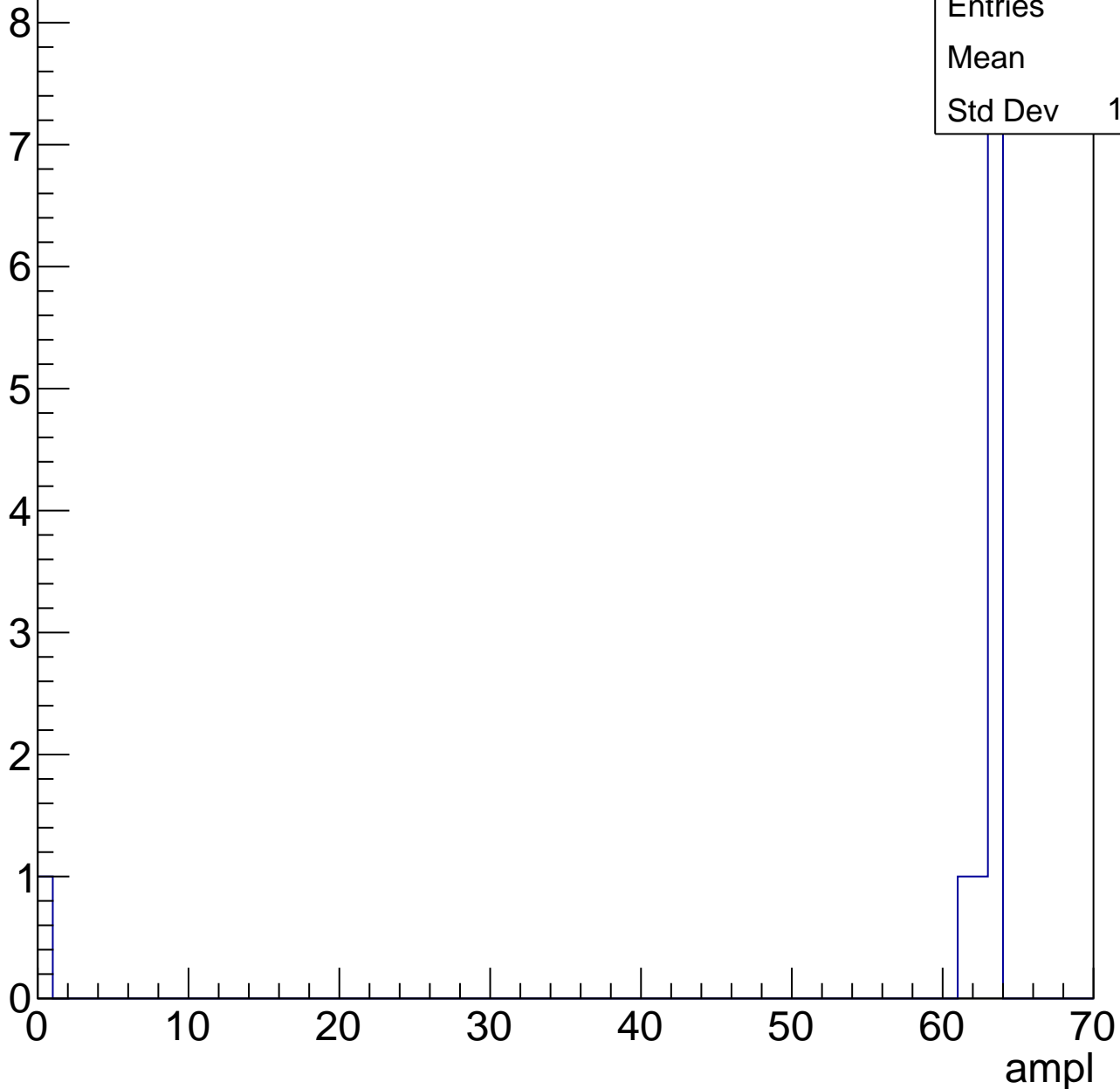


B1L001S, U22-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	57
Std Dev	18.04



B1L001S, U22-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch20, adc0

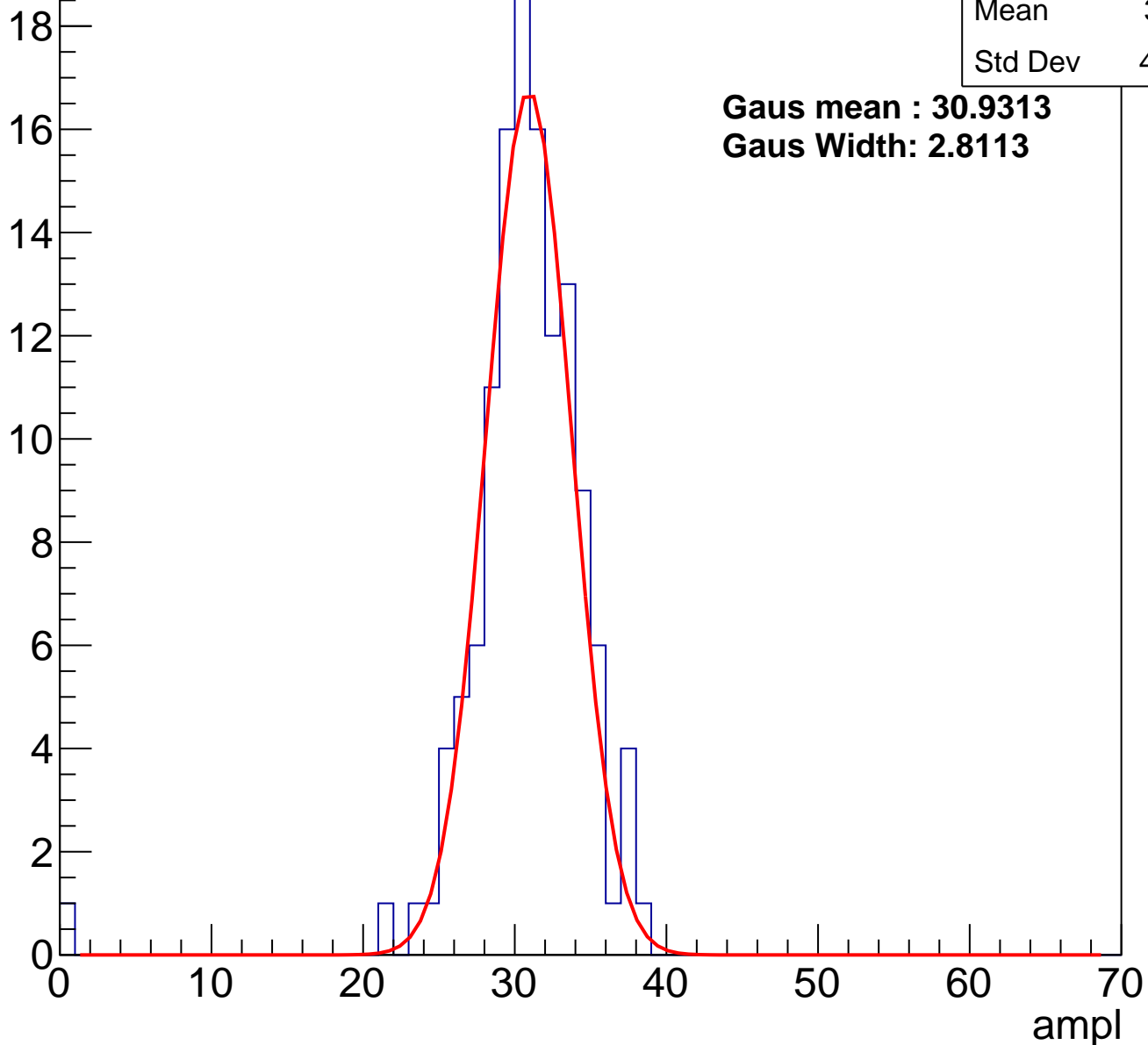
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	30.31
Std Dev	4.085

Gaus mean : 30.9313

Gaus Width: 2.8113

Entry

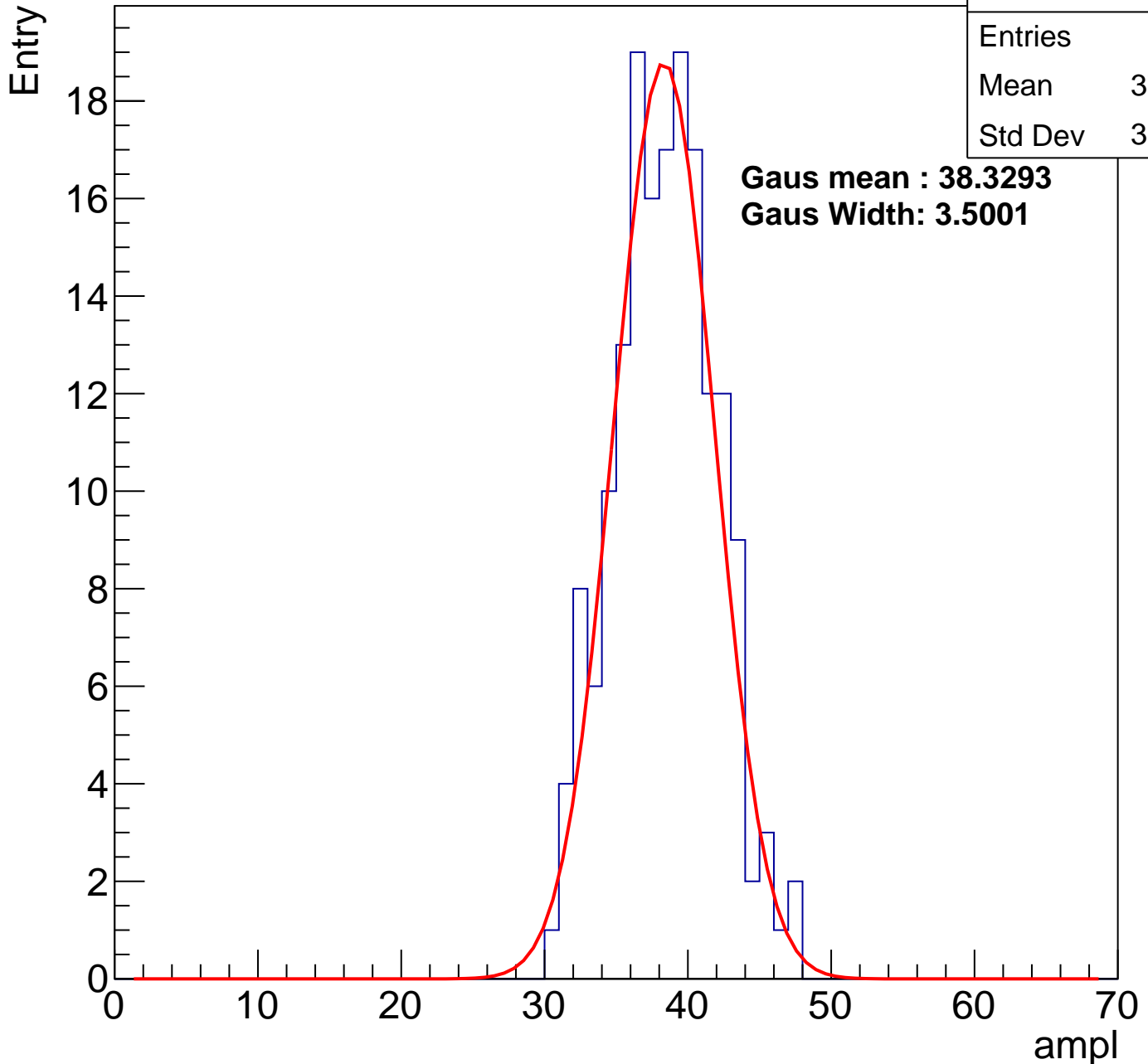


B1L001S, U22-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	37.96
Std Dev	3.524

Gaus mean : 38.3293
Gaus Width: 3.5001



B1L001S, U22-ch20, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	44.74
Std Dev	2.783

Gaus mean : 45.2189

Gaus Width: 2.6210

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

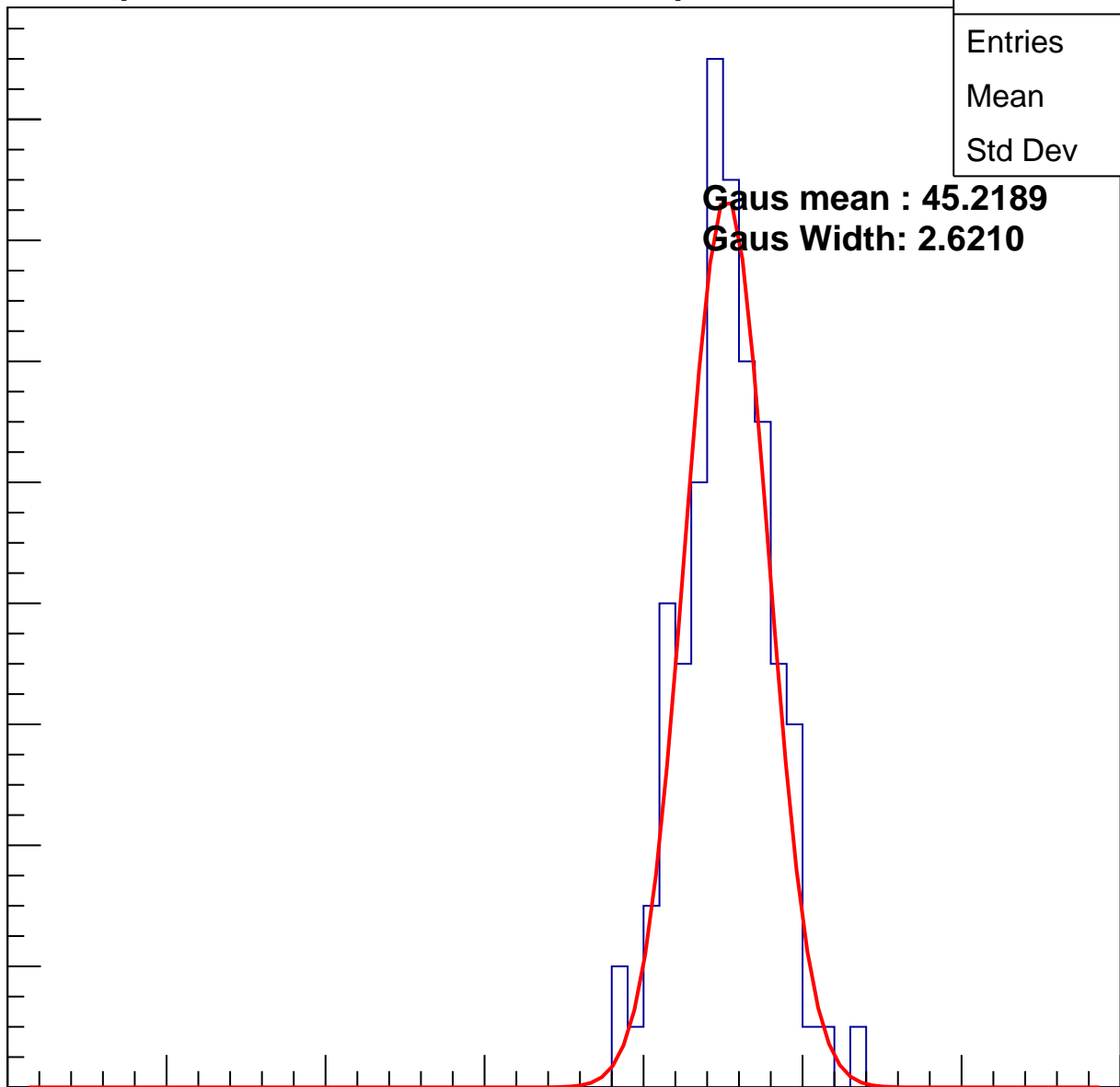
40

50

60

70

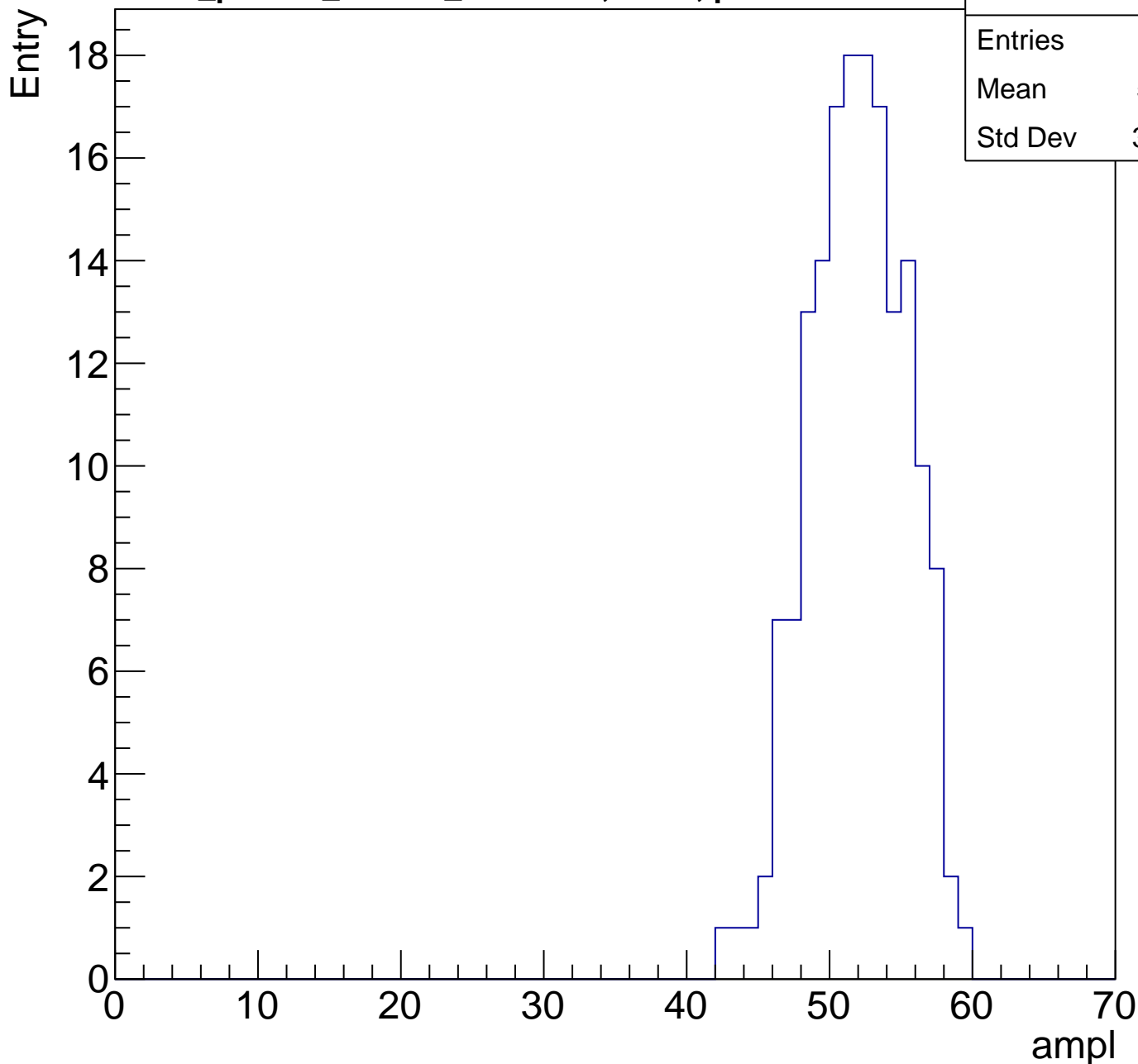
ampl



B1L001S, U22-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

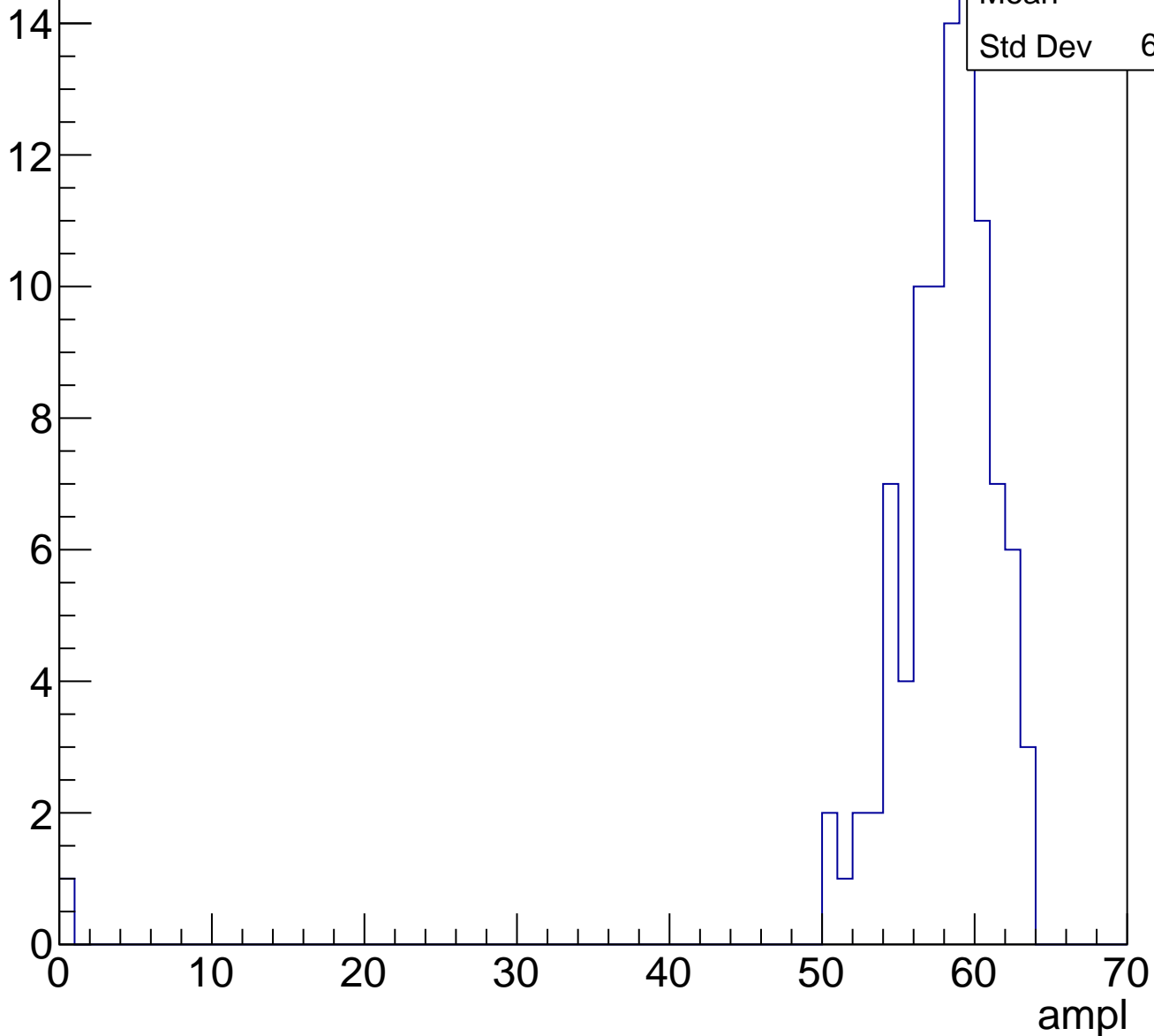
Entries	164
Mean	51.51
Std Dev	3.338



B1L001S, U22-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch20, adc5

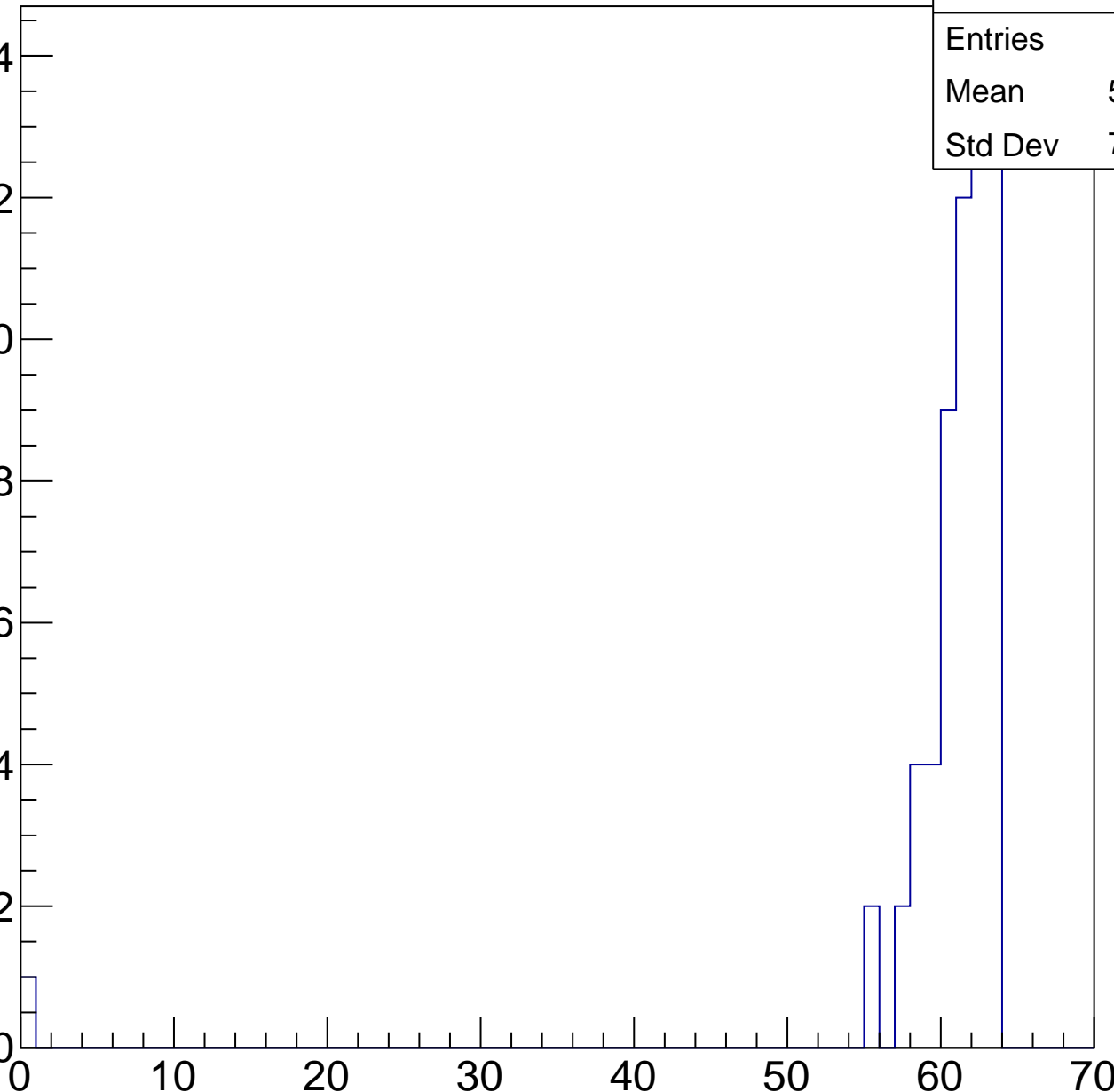
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	59.85
Std Dev	7.967

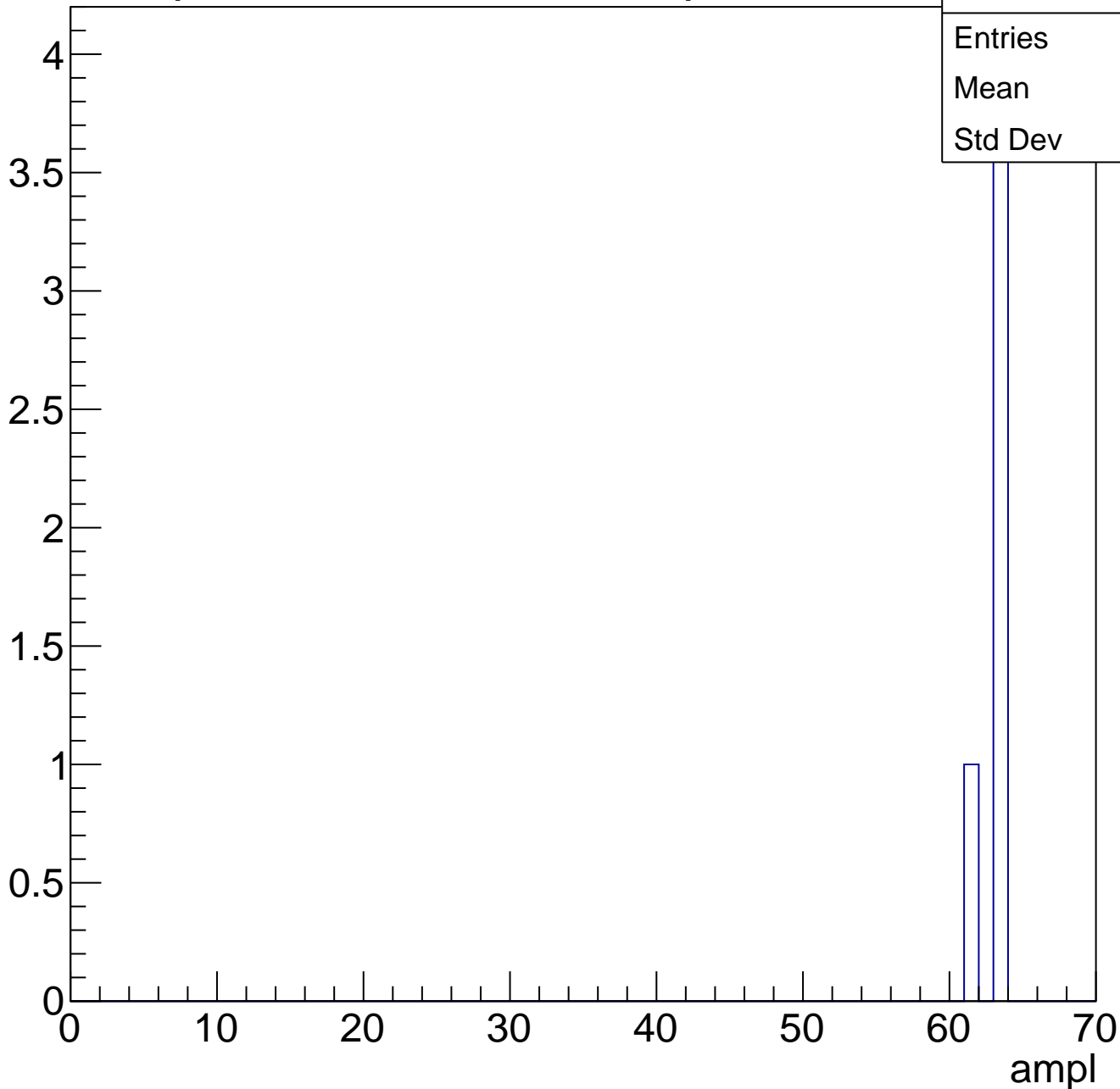
ampl



B1L001S, U22-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

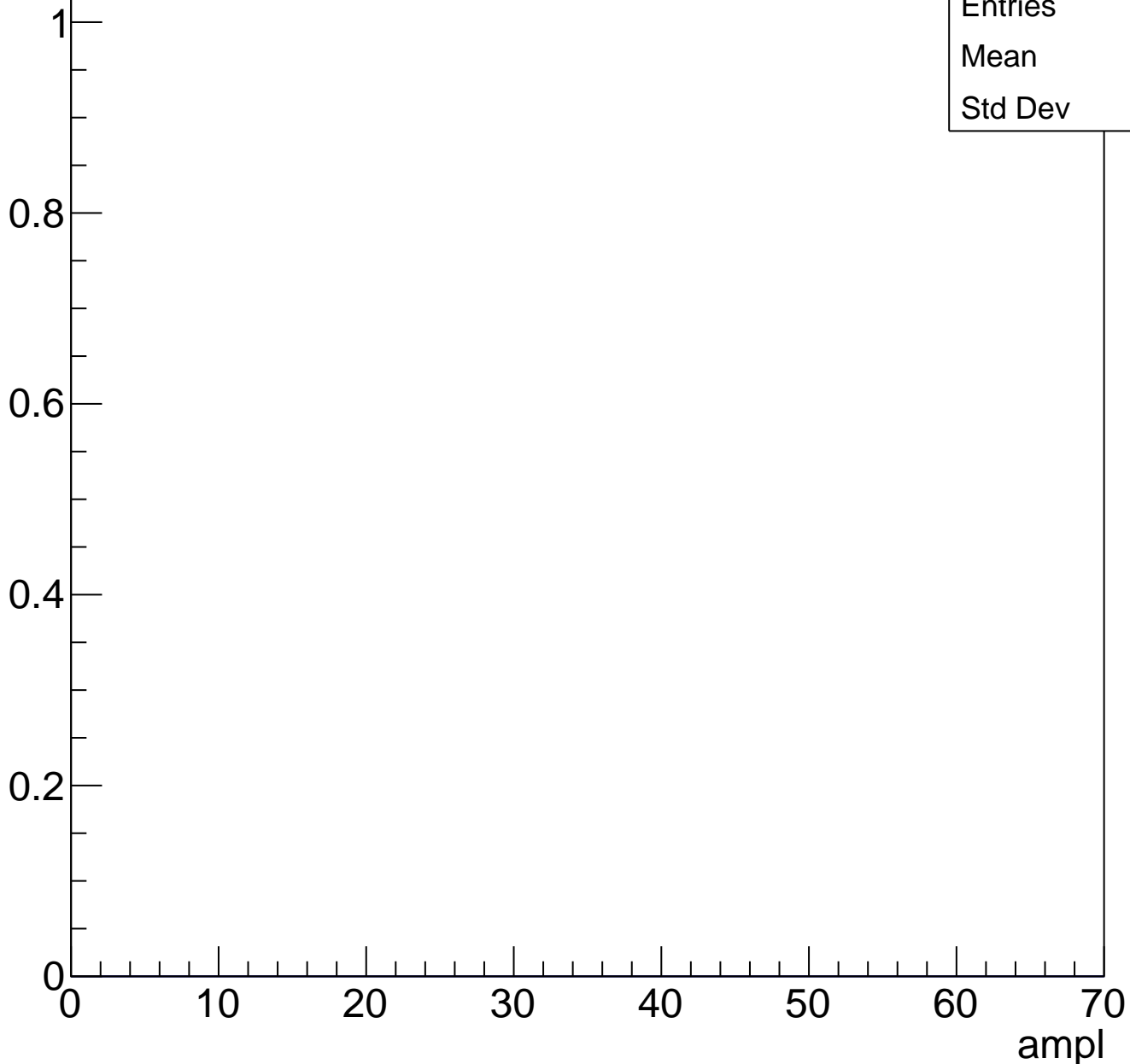
Entry



B1L001S, U22-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch21, adc0

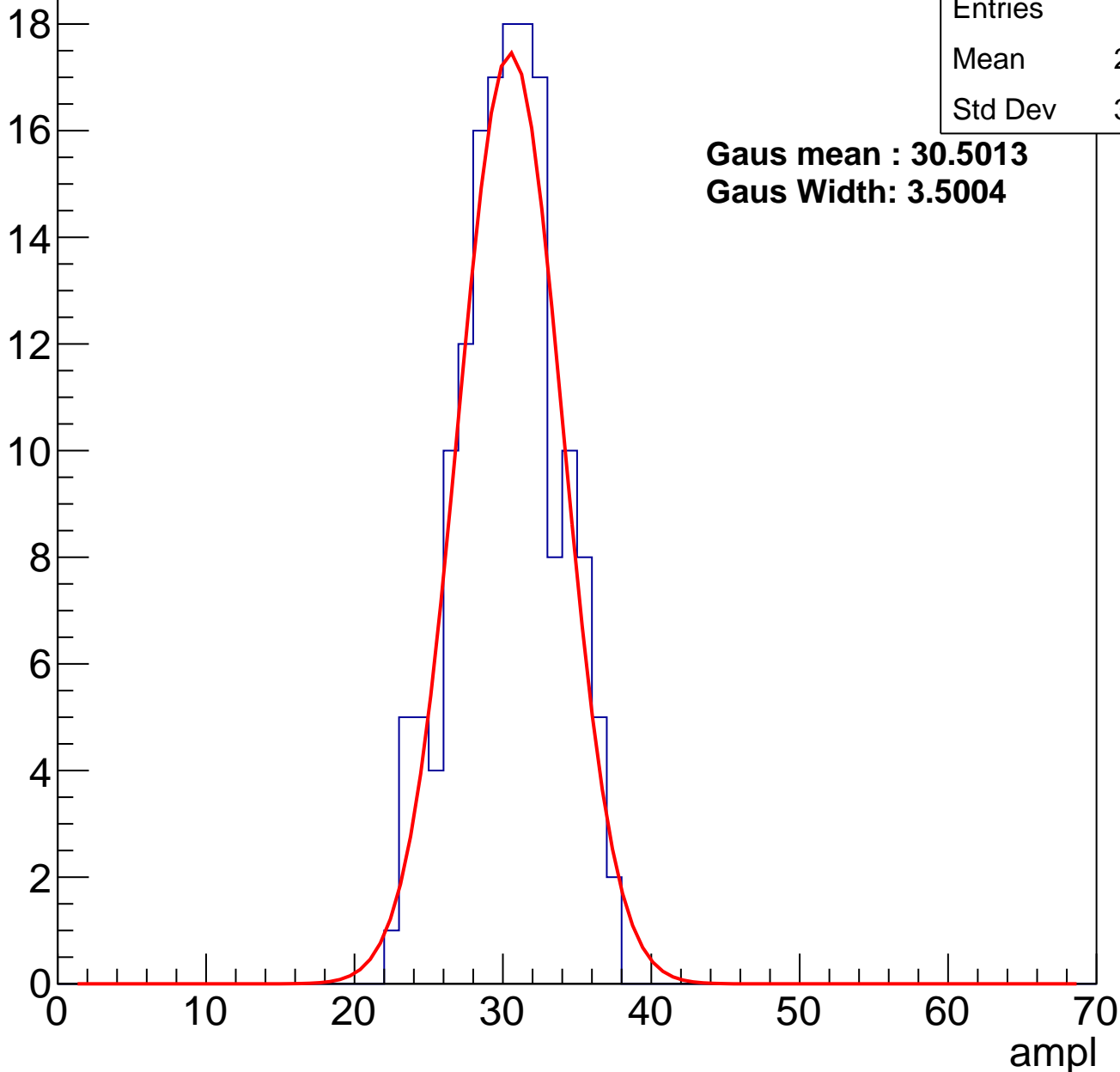
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	29.88
Std Dev	3.332

Gaus mean : 30.5013

Gaus Width: 3.5004

Entry

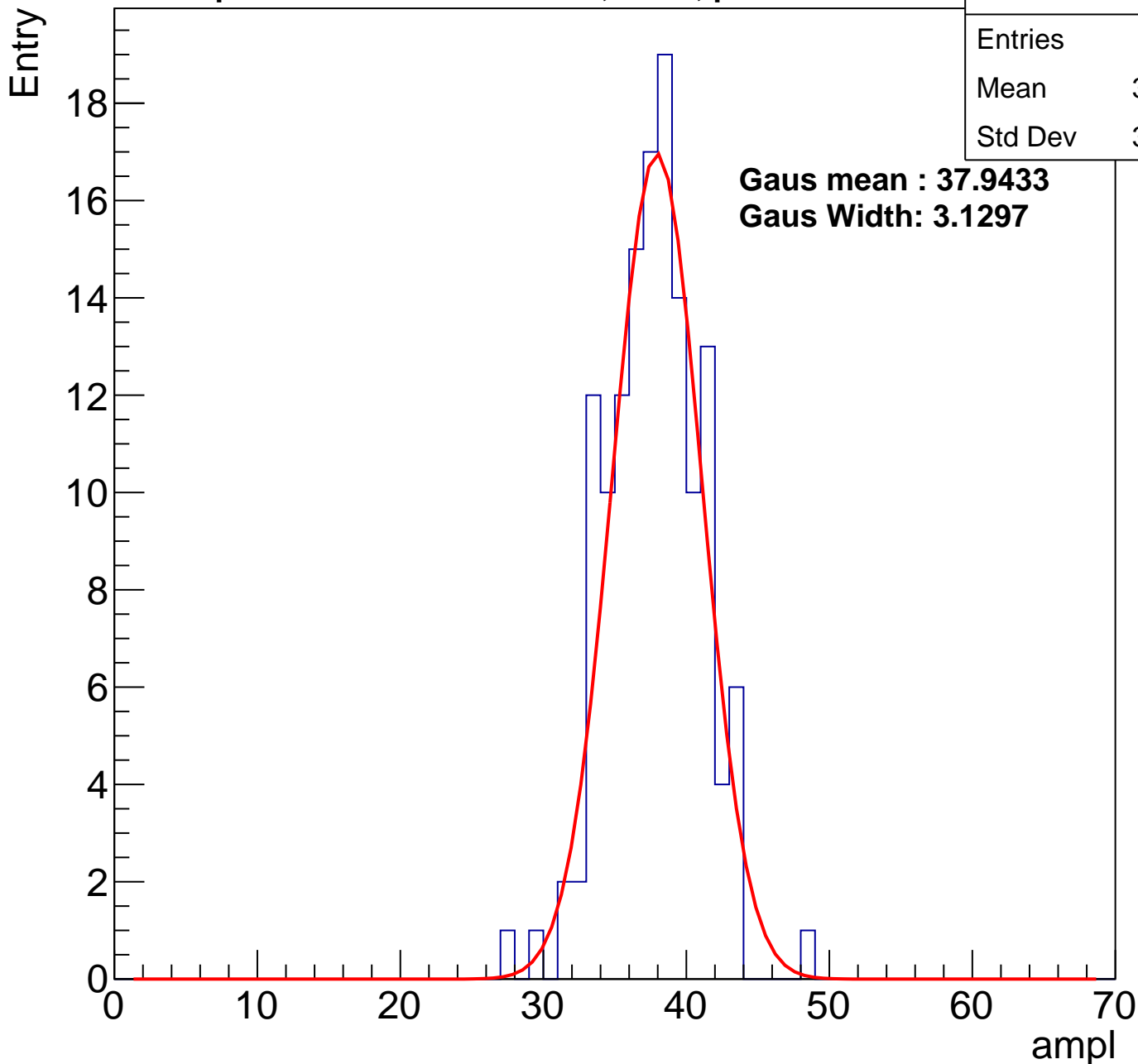


B1L001S, U22-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	37.28
Std Dev	3.208

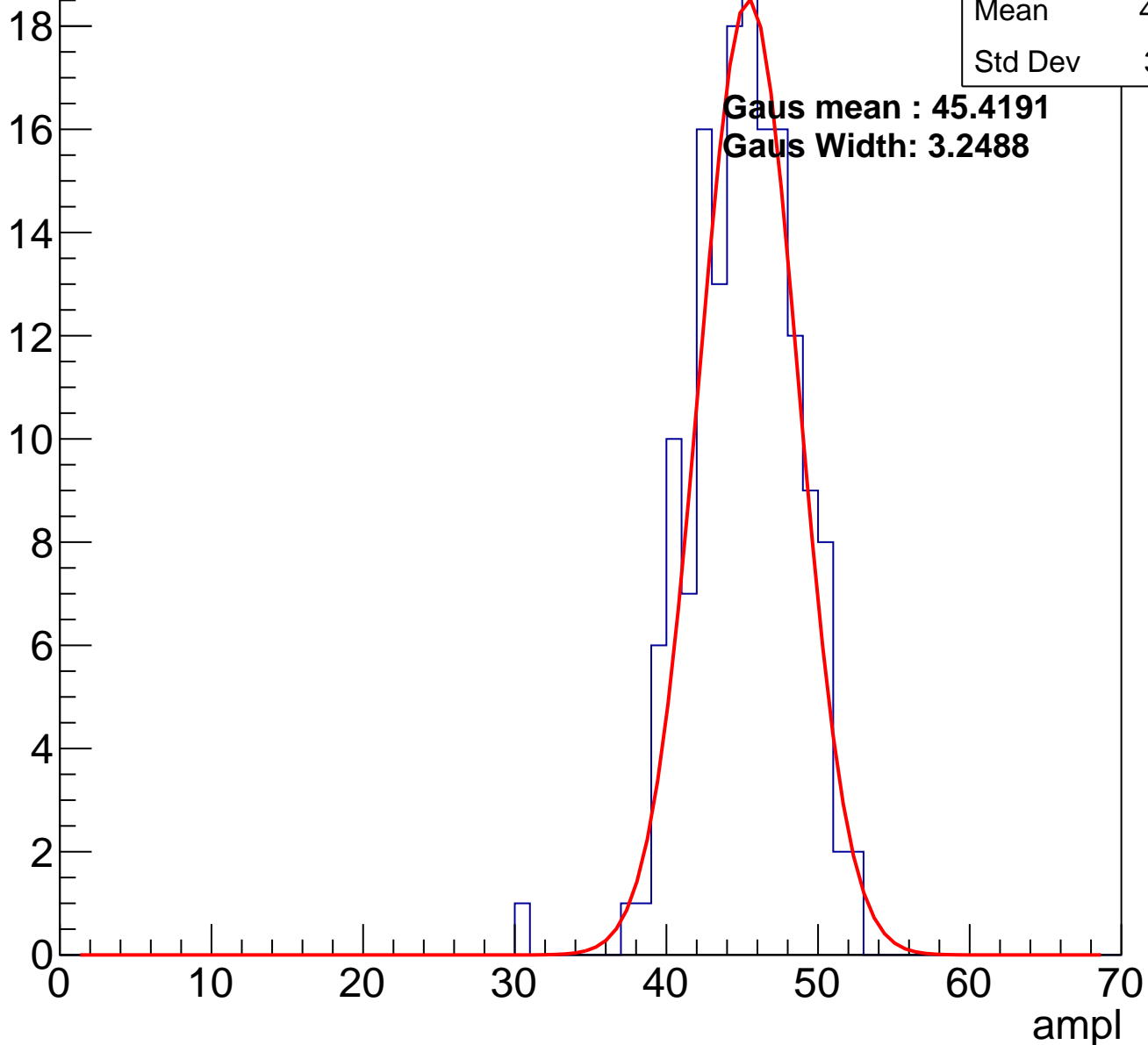
Gaus mean : 37.9433
Gaus Width: 3.1297



B1L001S, U22-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	157
Mean	44.68
Std Dev	3.401

B1L001S, U22-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

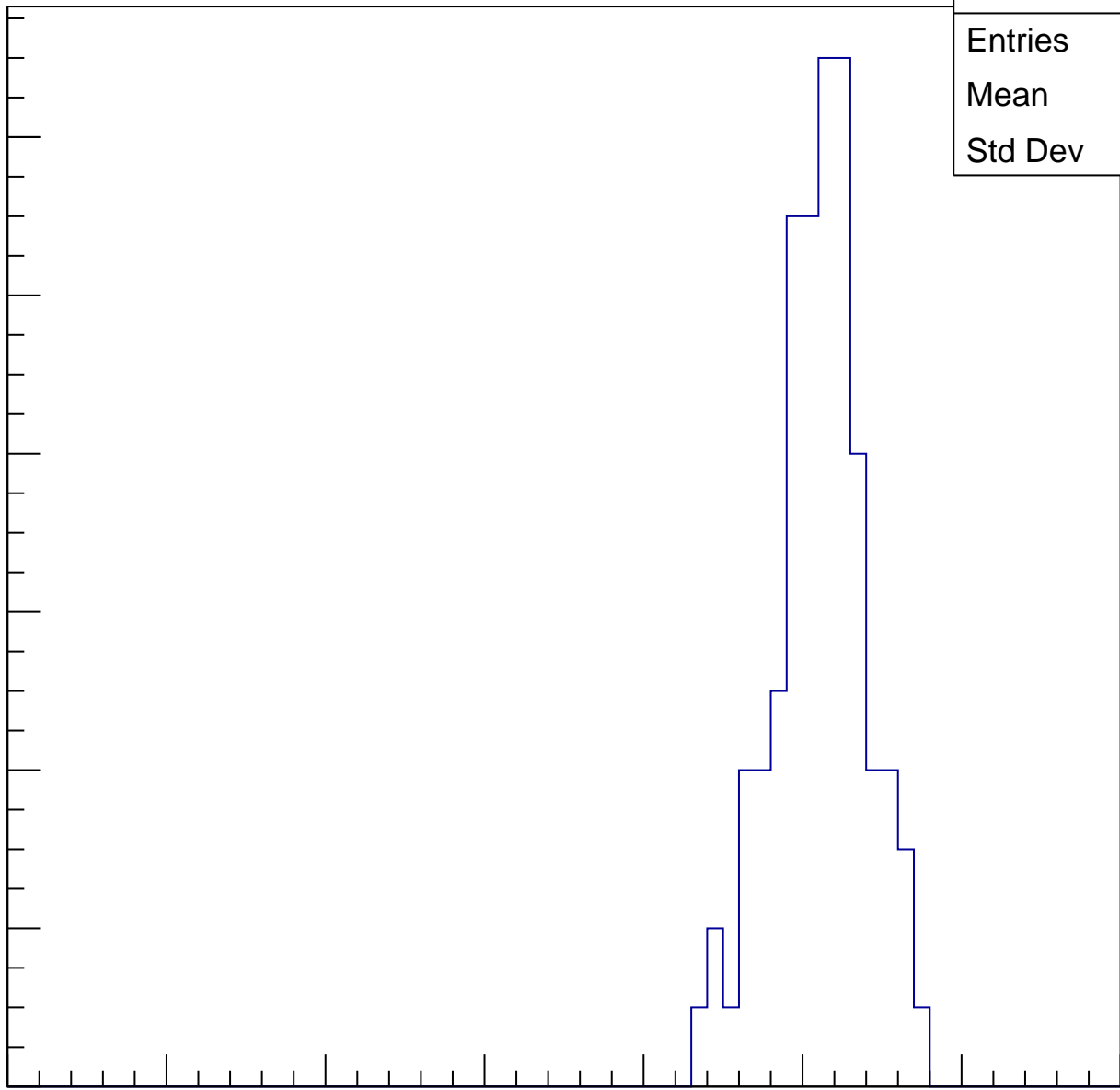
2

0

Entries	85
Mean	50.6
Std Dev	2.879

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch21, adc4

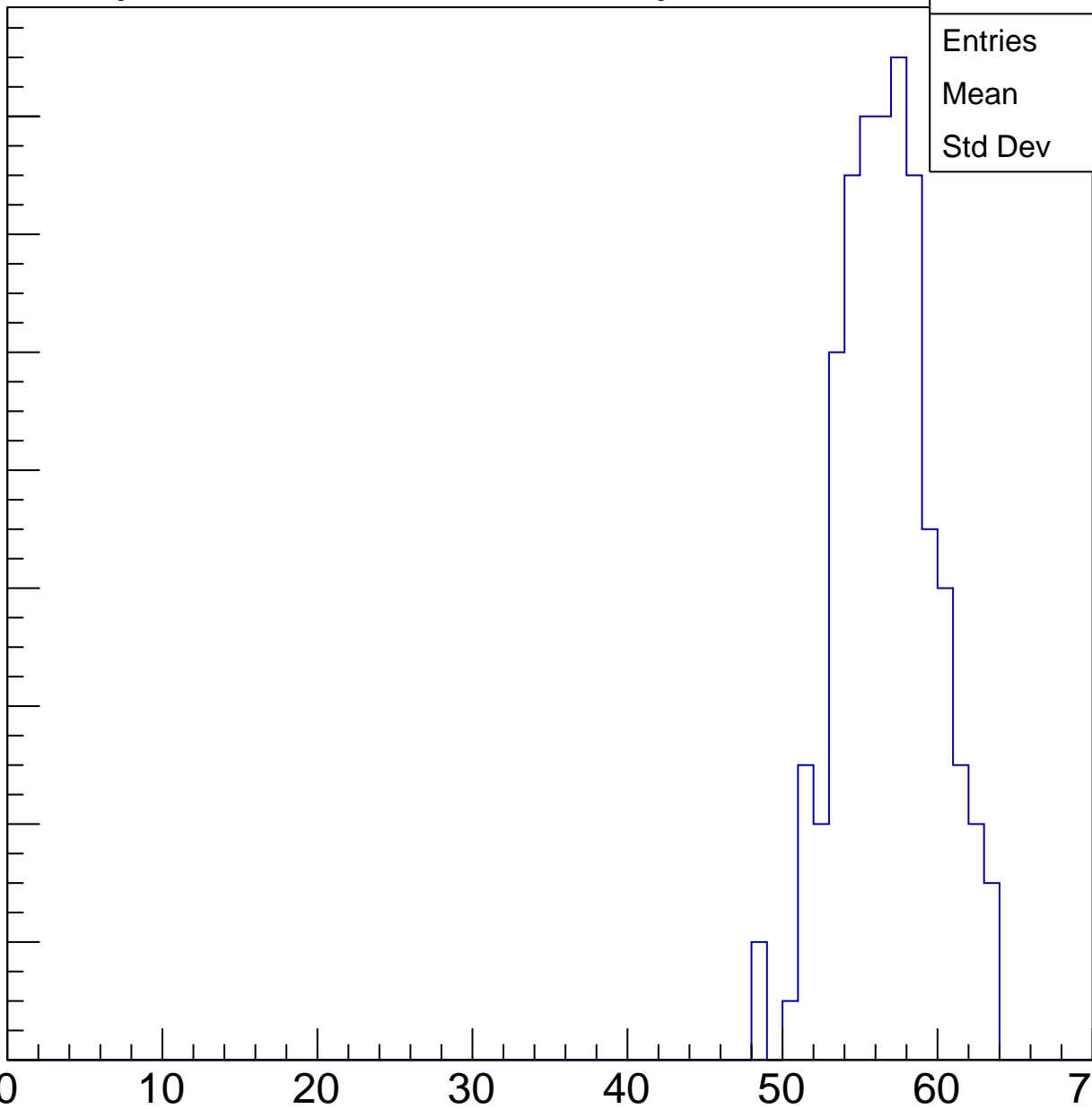
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	56.23
Std Dev	3.055

ampl



B1L001S, U22-ch21, adc5

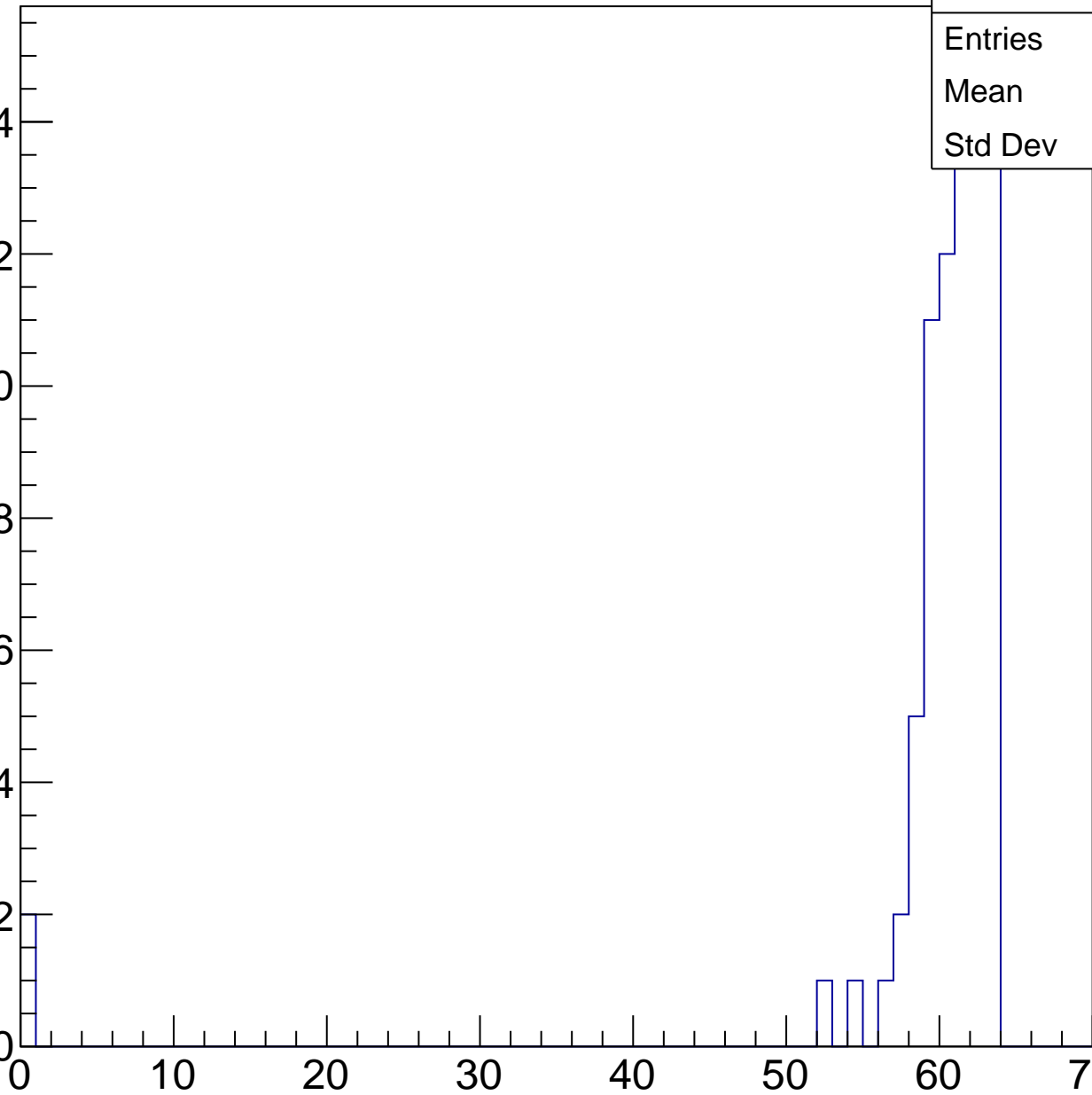
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.06
Std Dev	9.685

ampl



B1L001S, U22-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

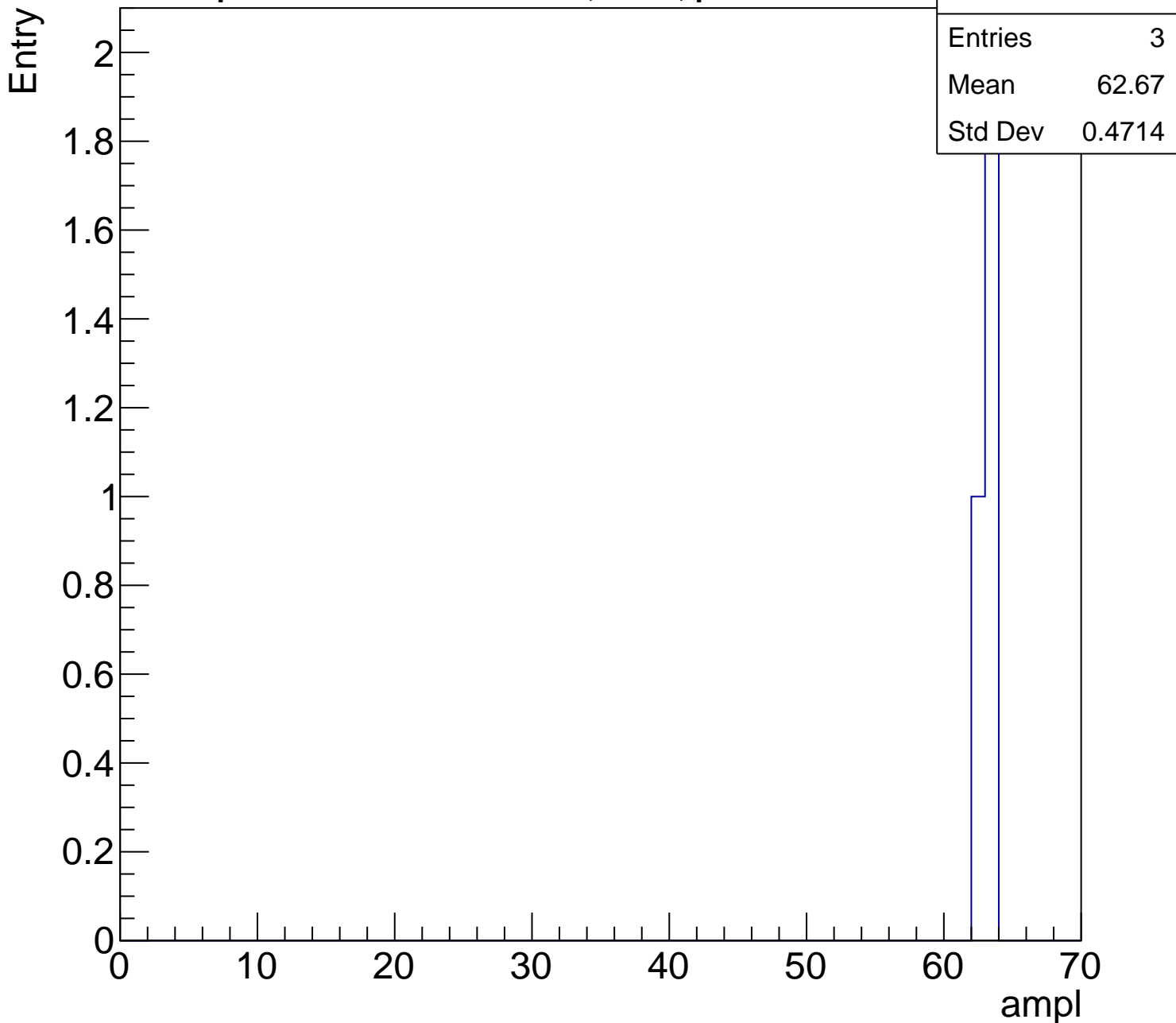
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

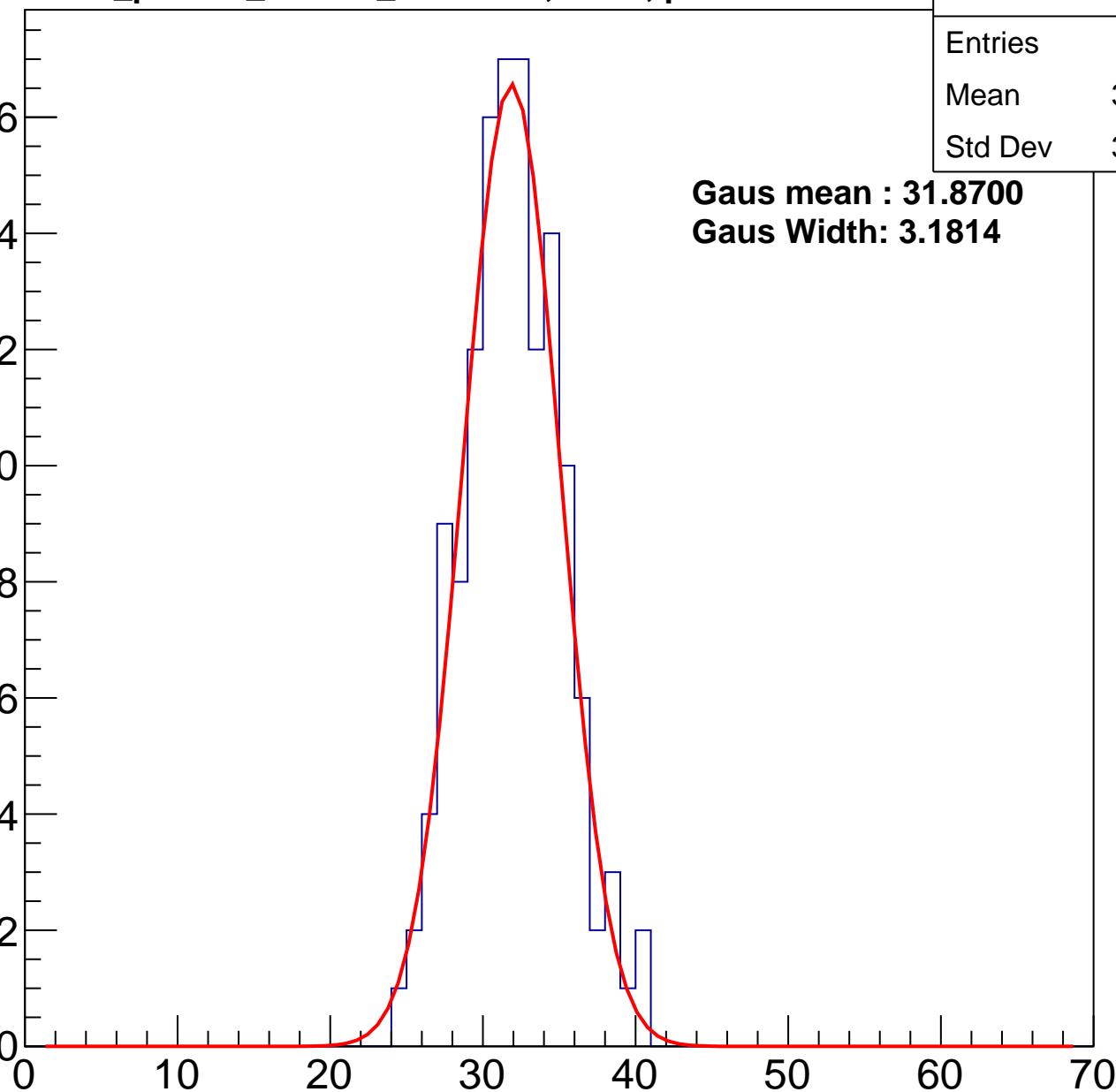
Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	31.54
Std Dev	3.217

Gaus mean : 31.8700

Gaus Width: 3.1814



ampl

B1L001S, U22-ch22, adc1

calib_packv5_042523_0143.root, FC#2, port C2

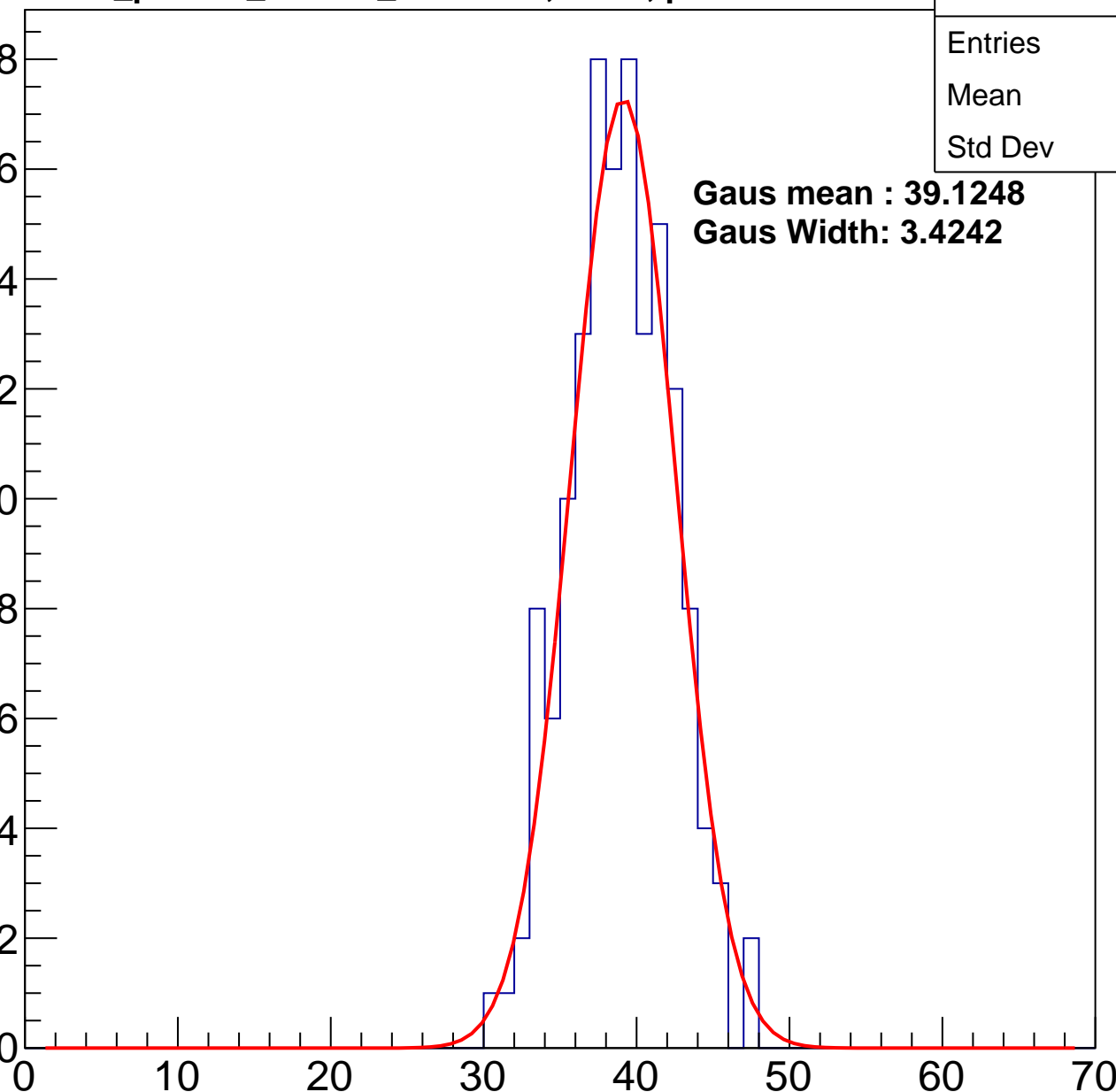
Entry

18
16
14
12
10
8
6
4
2
0

Entries	150
Mean	38.5
Std Dev	3.32

Gaus mean : 39.1248

Gaus Width: 3.4242



ampl

B1L001S, U22-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	45.39
Std Dev	3.042

Gaus mean : 46.2036

Gaus Width: 3.2100

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

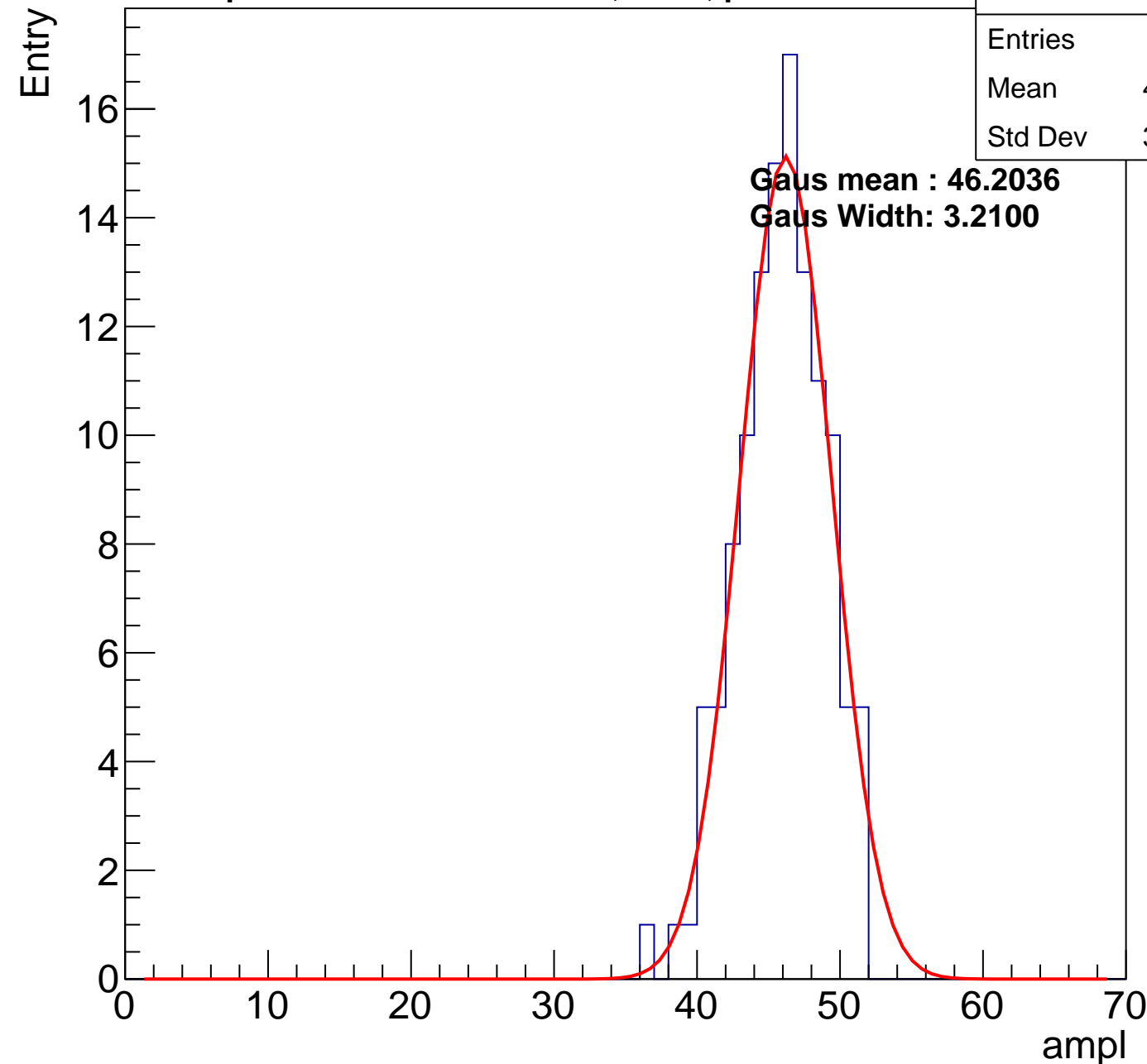
40

50

60

ampl

70



B1L001S, U22-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

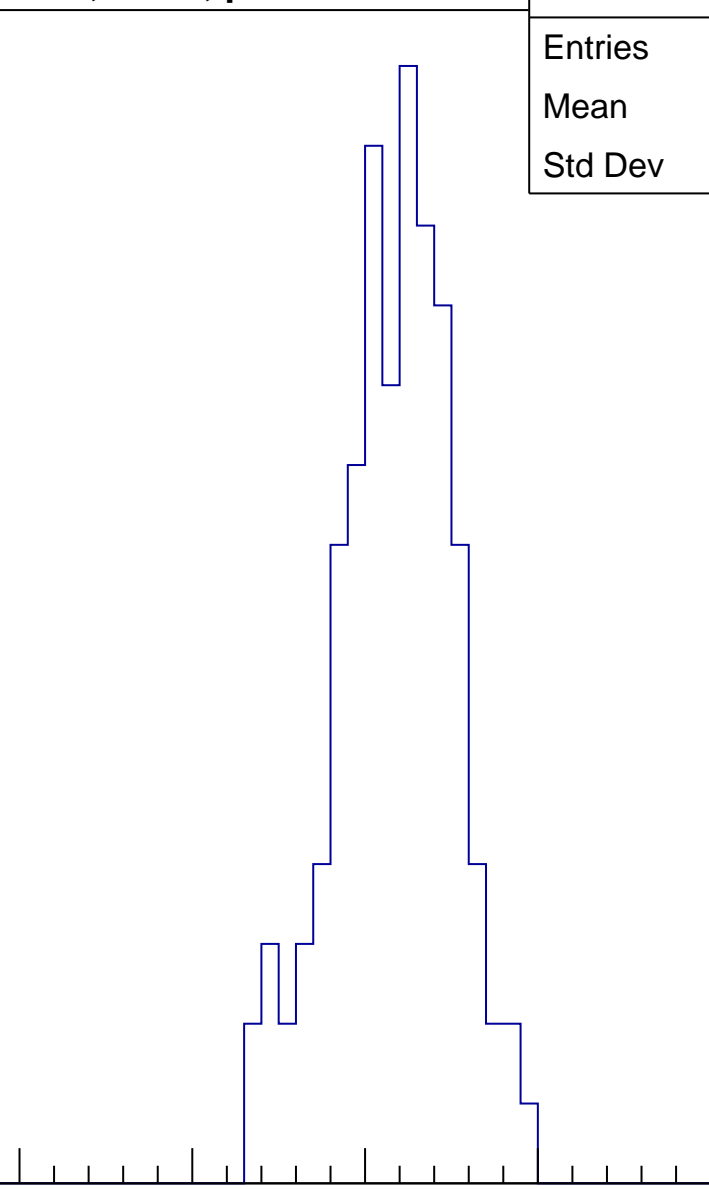
Entries	108
Mean	51.2
Std Dev	3.369

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

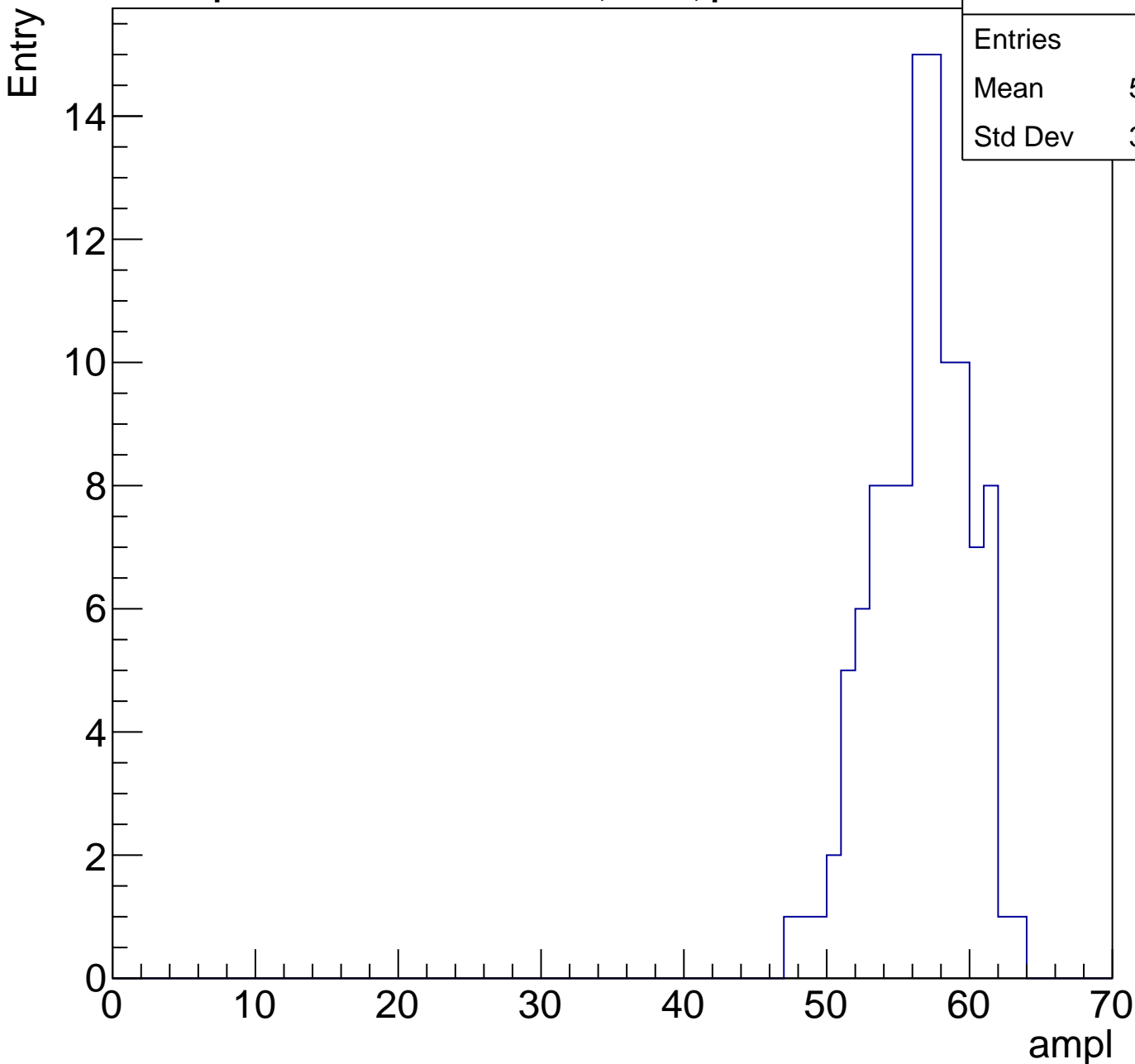
ampl



B1L001S, U22-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	56.12
Std Dev	3.263



B1L001S, U22-ch22, adc5

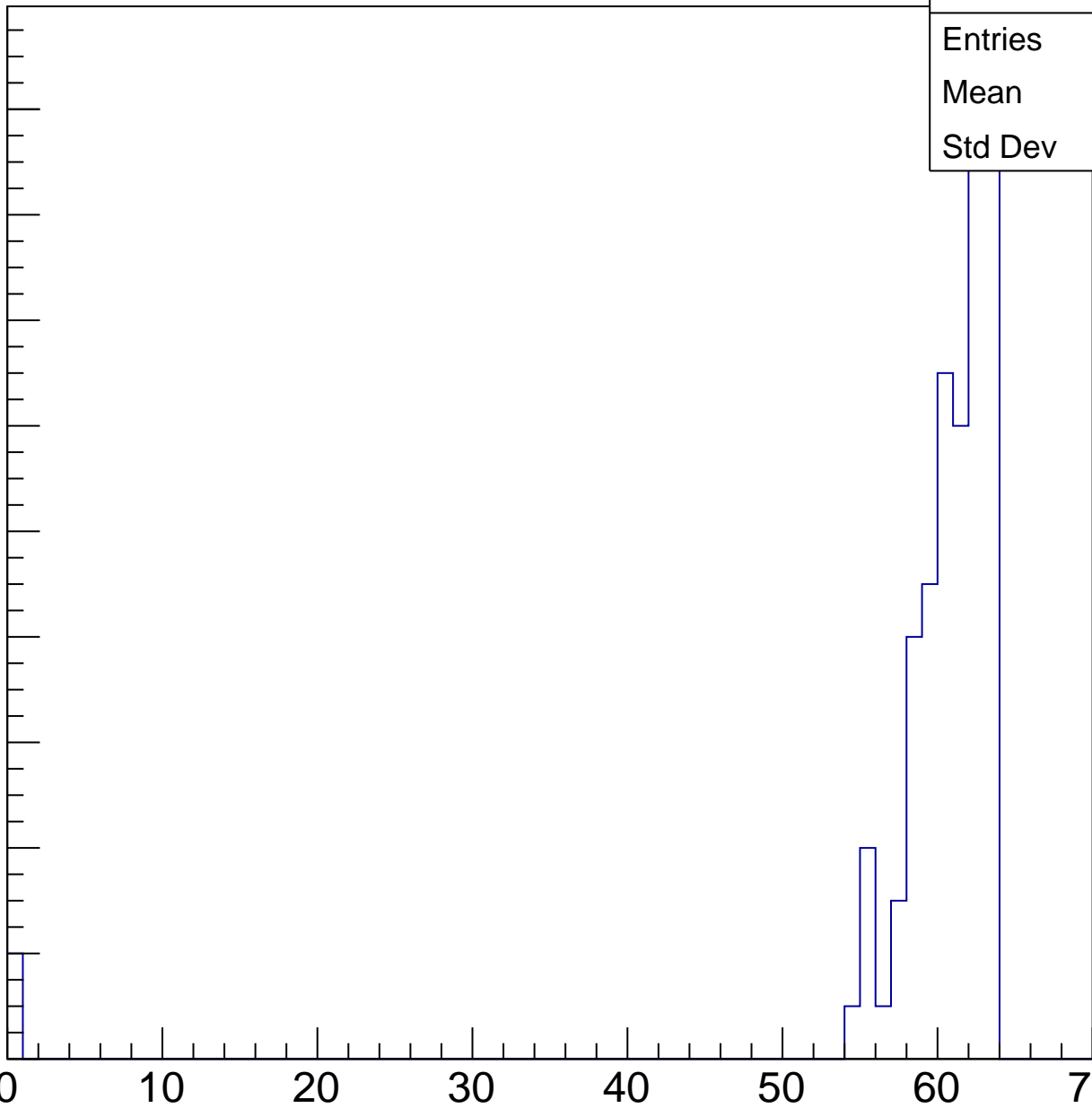
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.15
Std Dev	9.147

ampl



B1L001S, U22-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U22-ch23, adc0

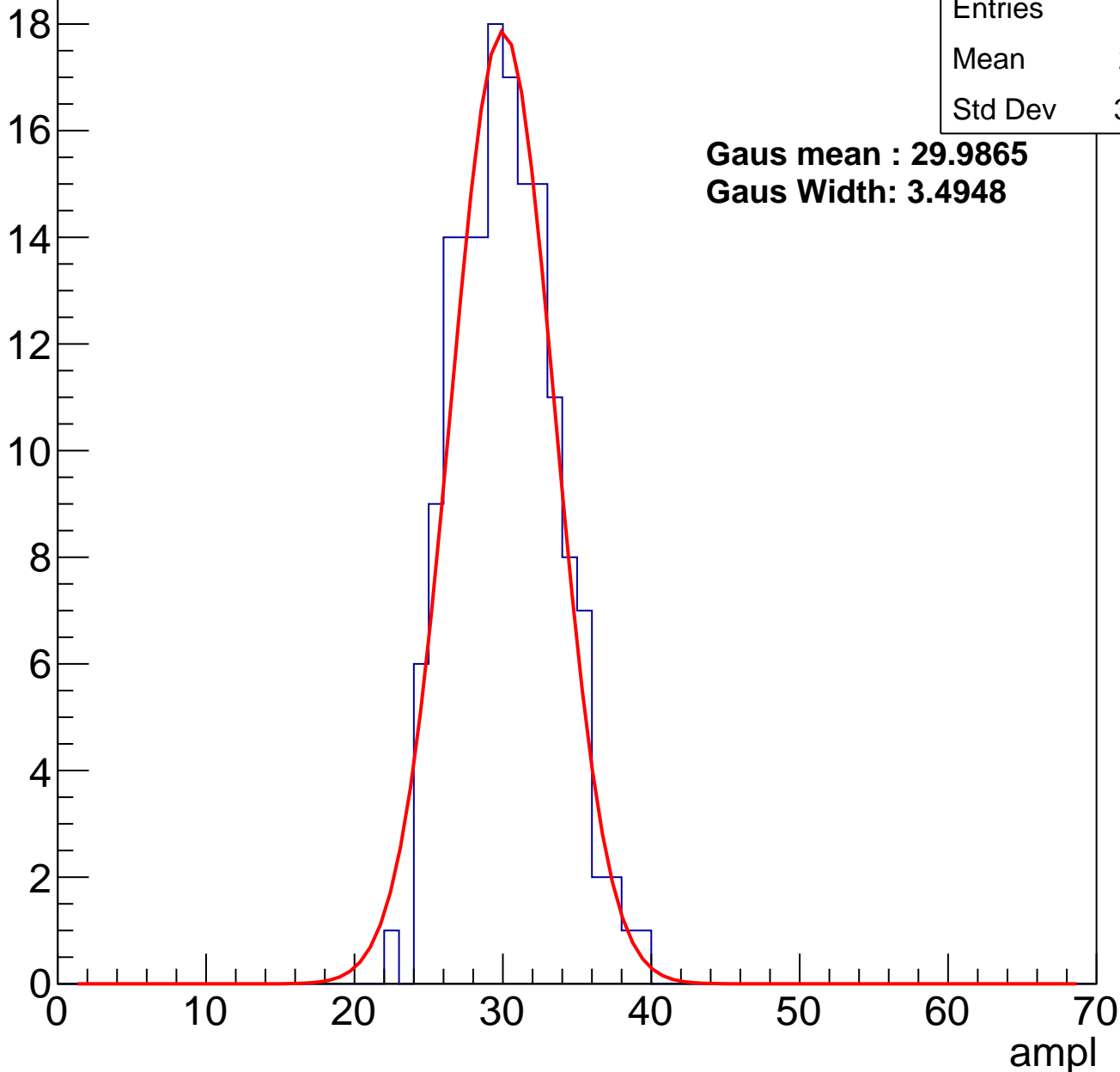
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	29.71
Std Dev	3.319

Gaus mean : 29.9865

Gaus Width: 3.4948

Entry



B1L001S, U22-ch23, adc1

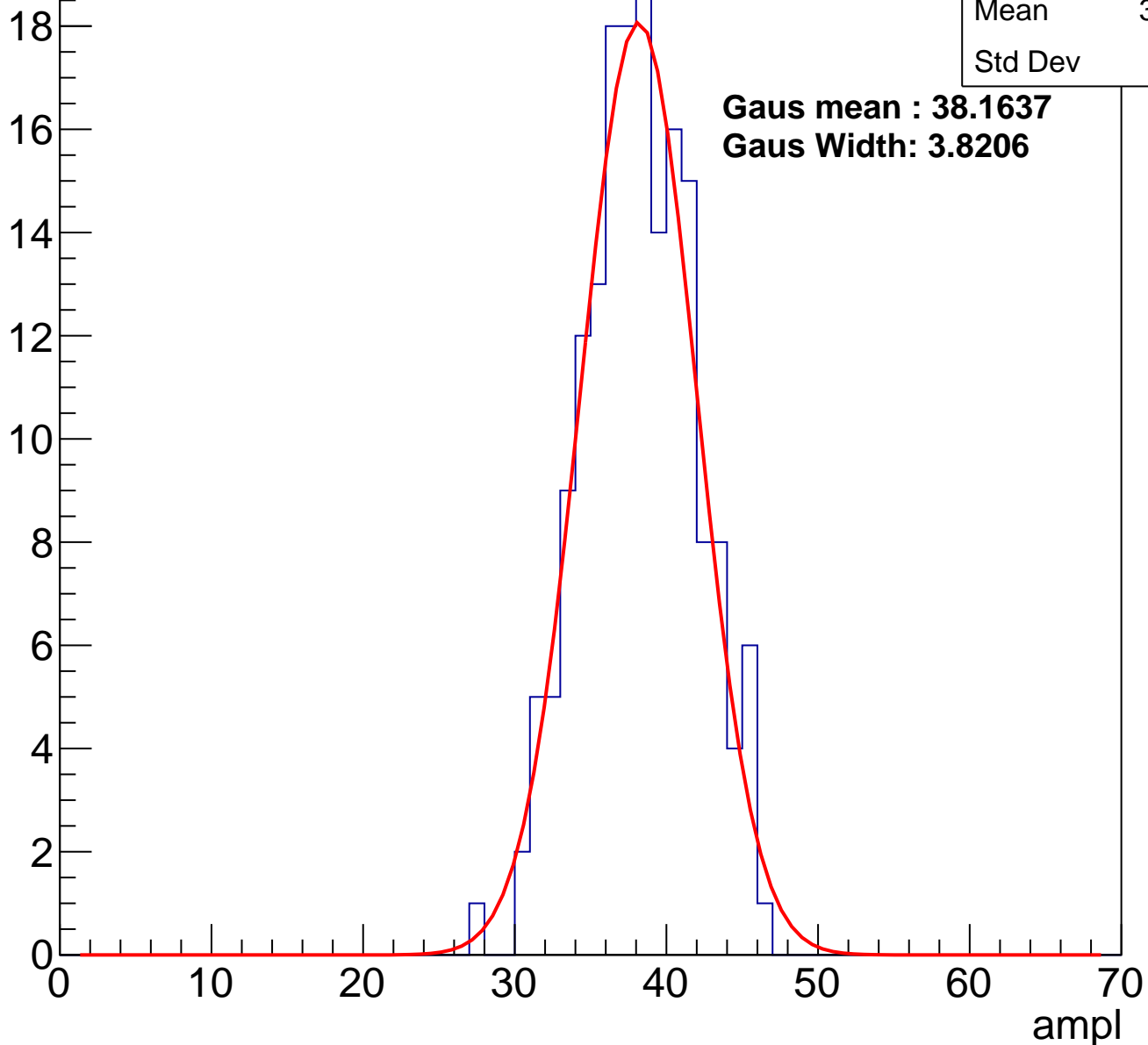
calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	37.76
Std Dev	3.66

Gaus mean : 38.1637

Gaus Width: 3.8206

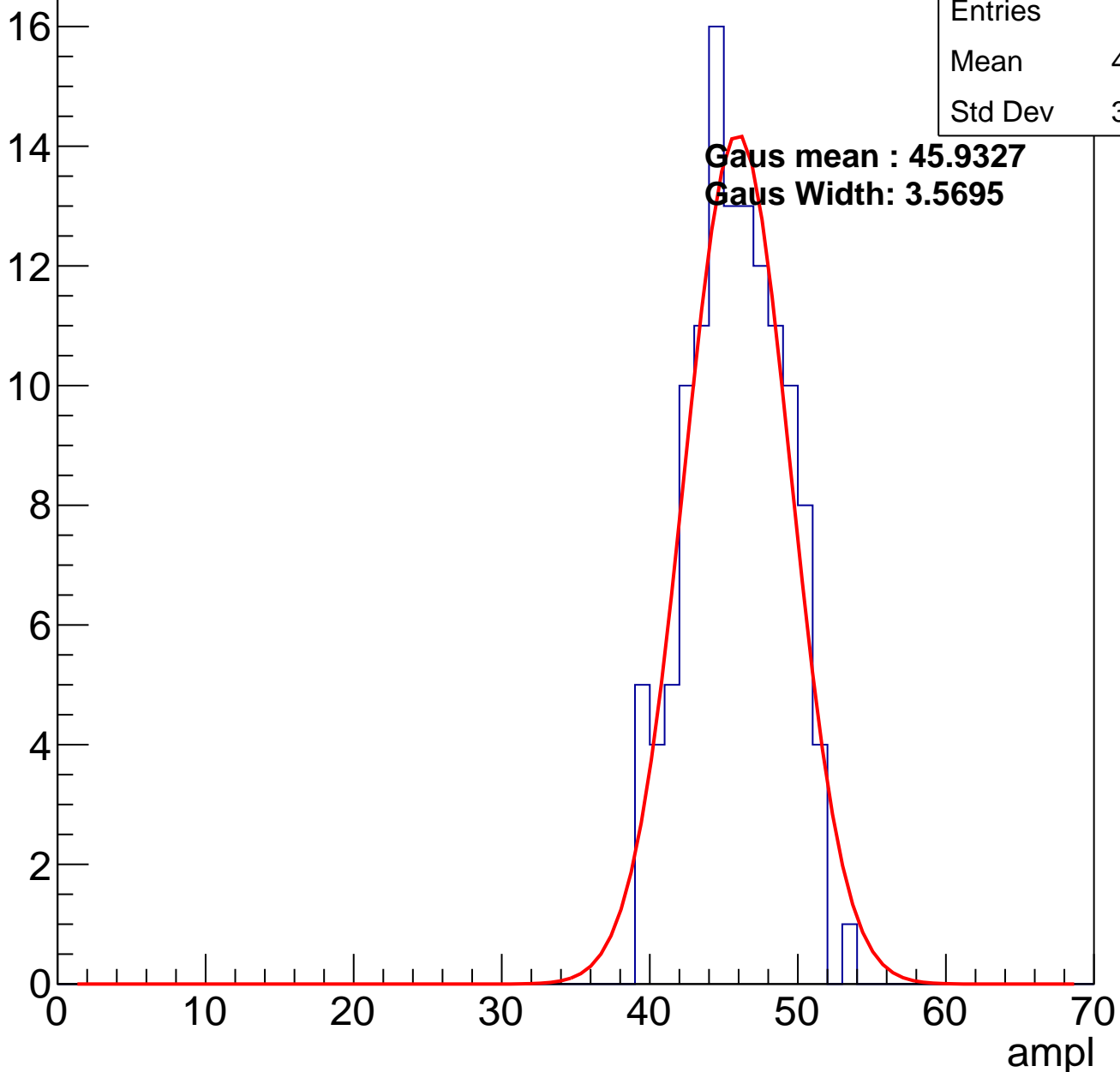
Entry



B1L001S, U22-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

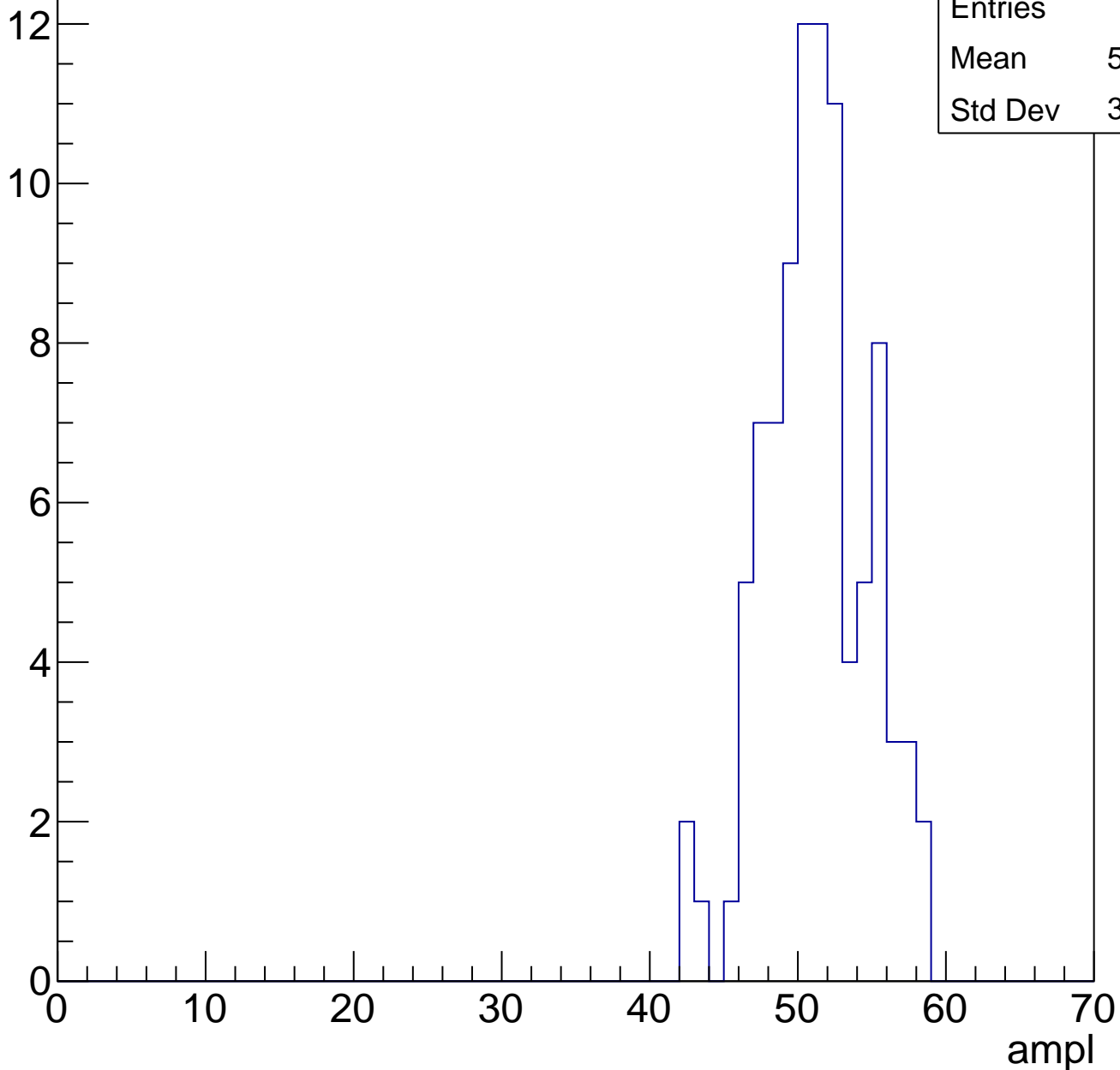


B1L001S, U22-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	92
Mean	50.75
Std Dev	3.435

Entry



B1L001S, U22-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

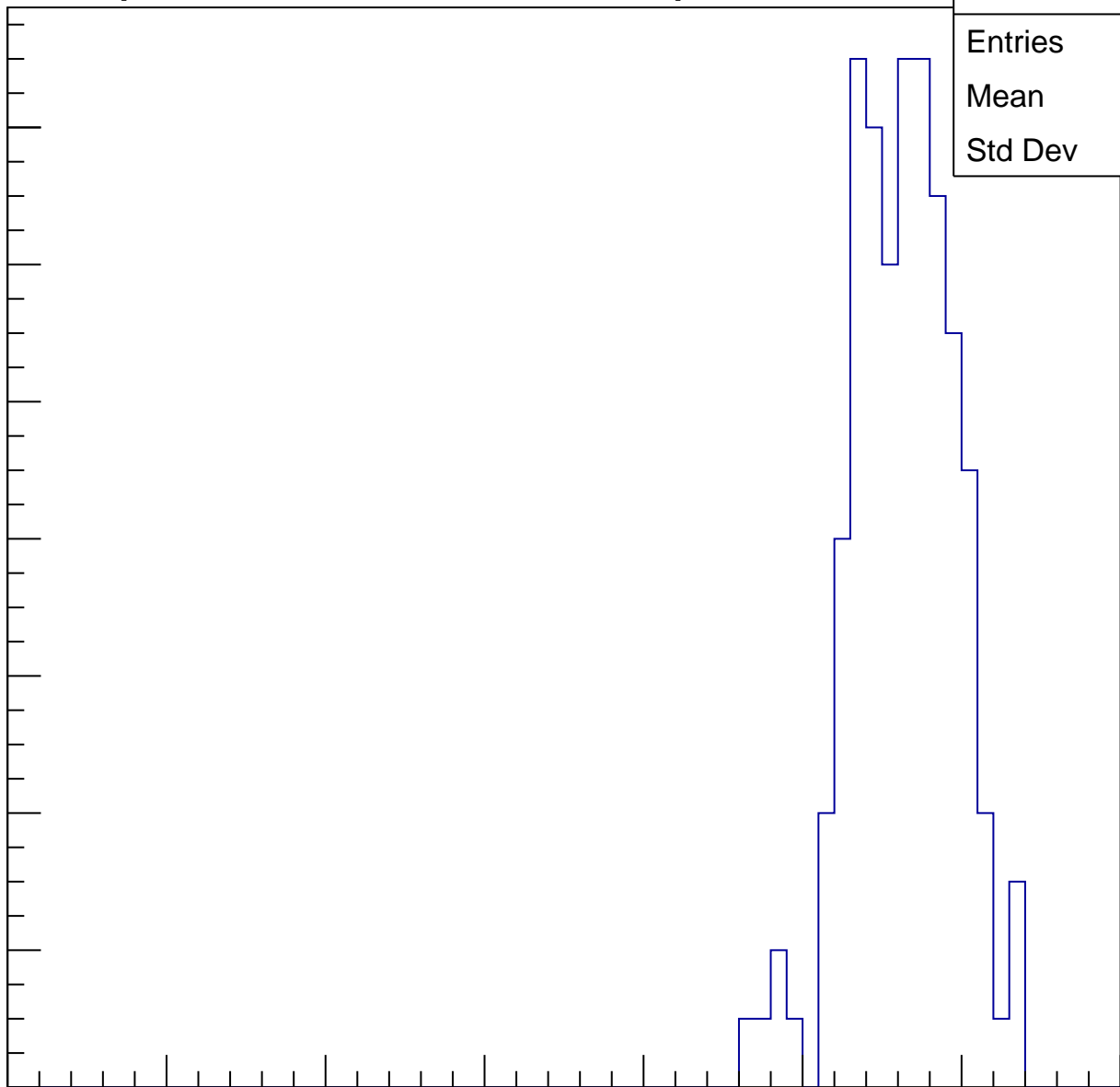
40

50

60

ampl

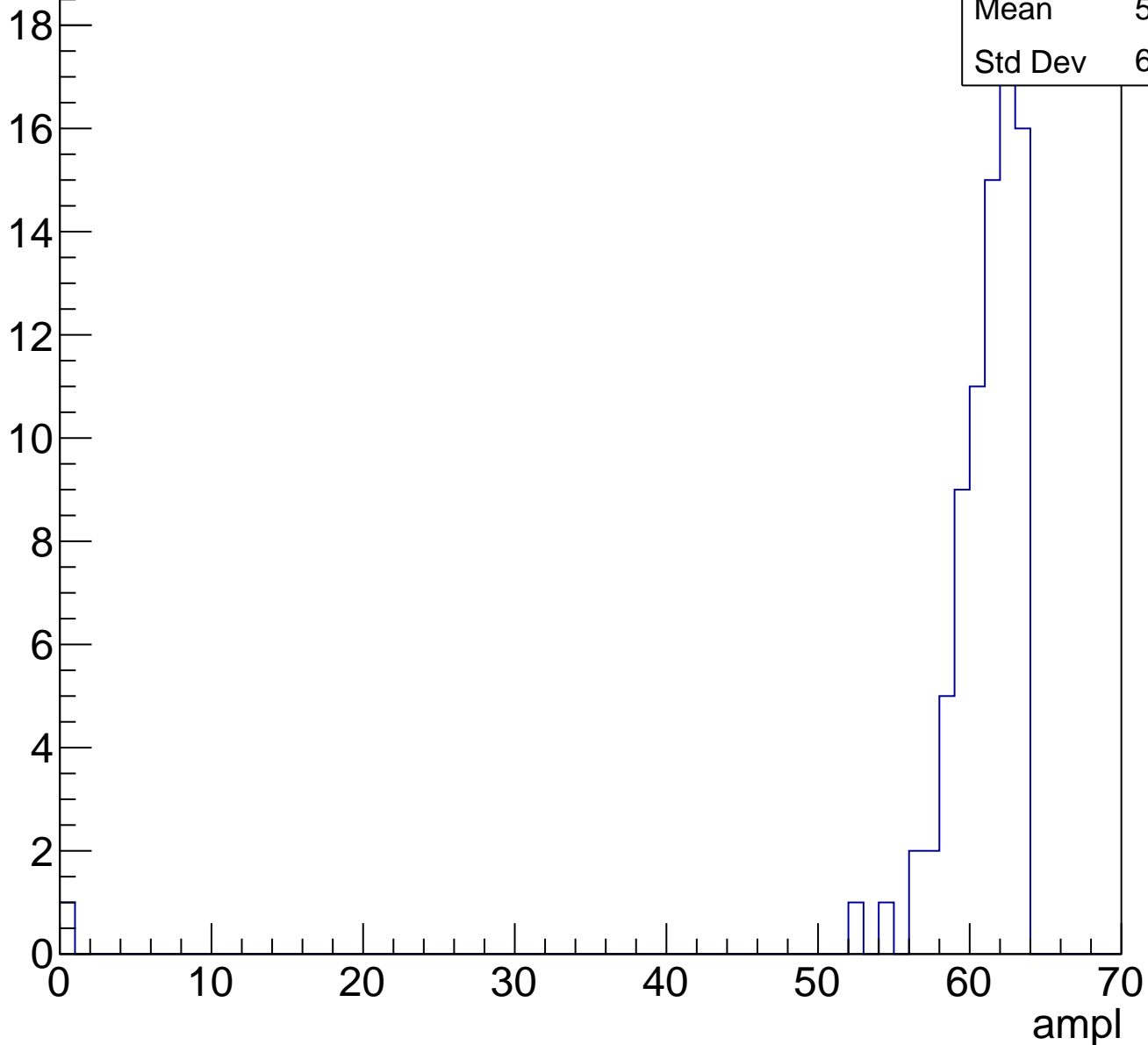
Entries	129
Mean	55.83
Std Dev	3.269



B1L001S, U22-ch23, adc5

calib_packv5_042523_0143.root, FC#2, port C2

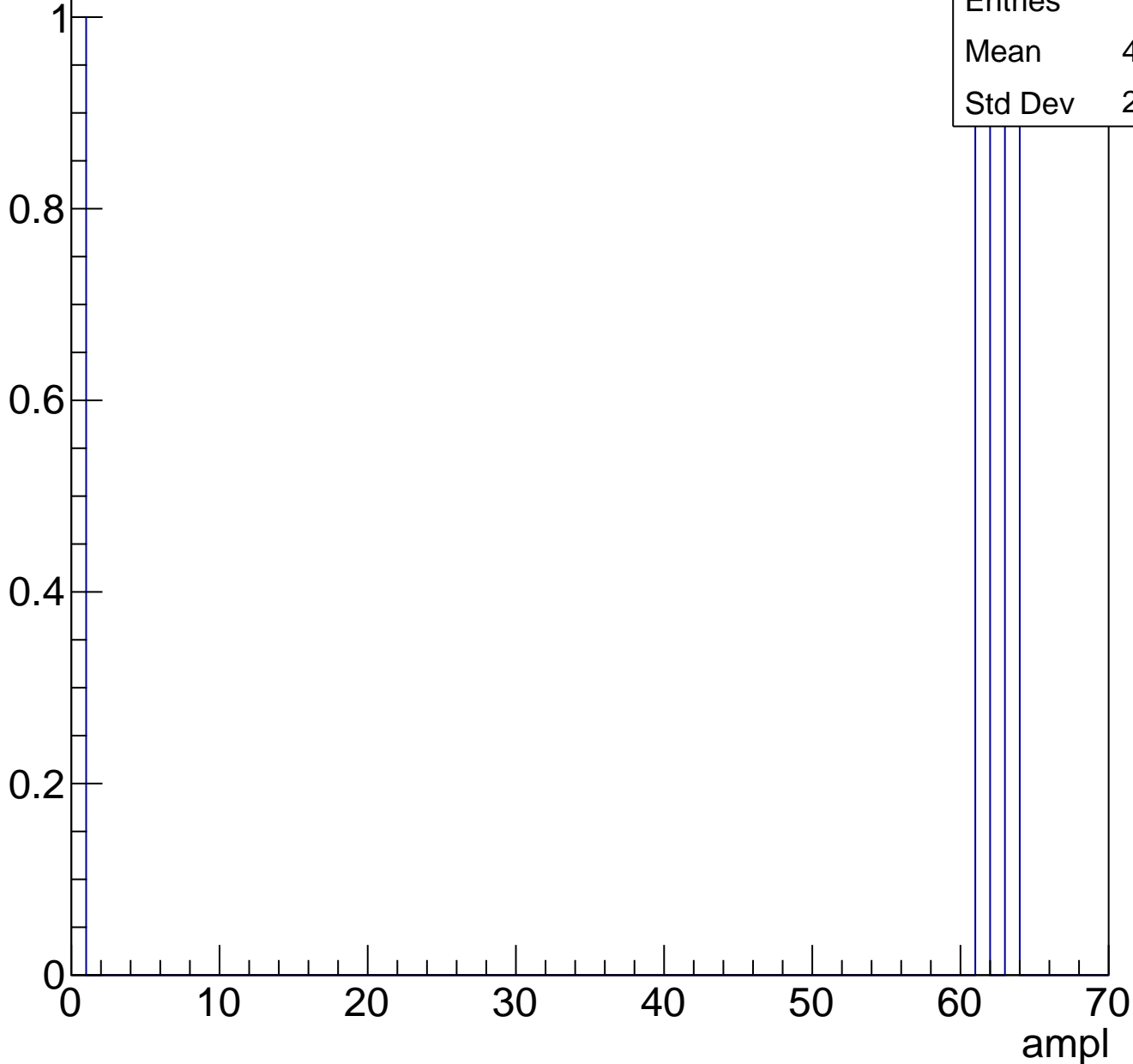
Entry



B1L001S, U22-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

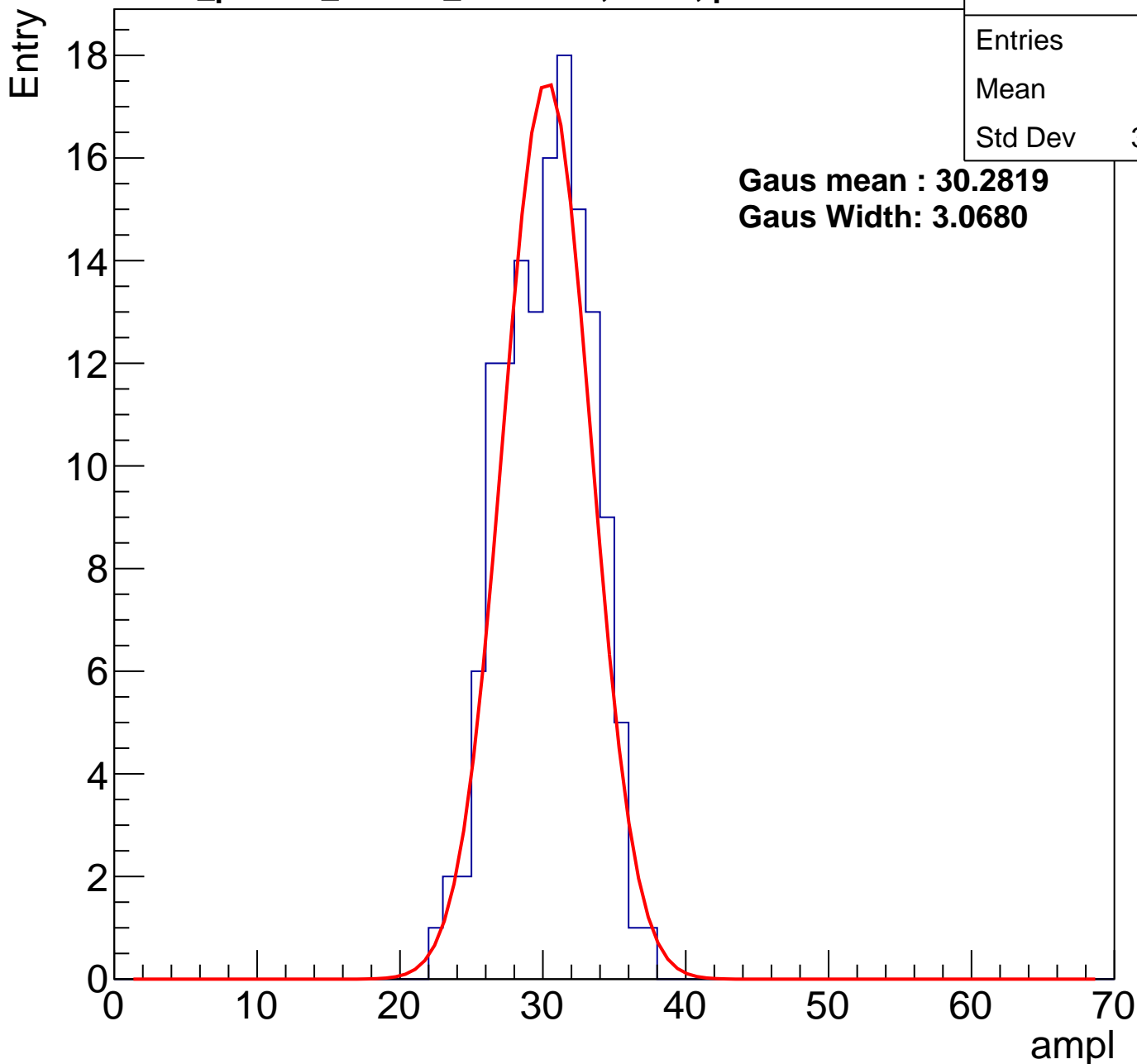
B1L001S, U22-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.8
Std Dev	3.043

Gaus mean : 30.2819

Gaus Width: 3.0680



B1L001S, U22-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.97
Std Dev	3.385

Gaus mean : 37.3051

Gaus Width: 3.0410

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

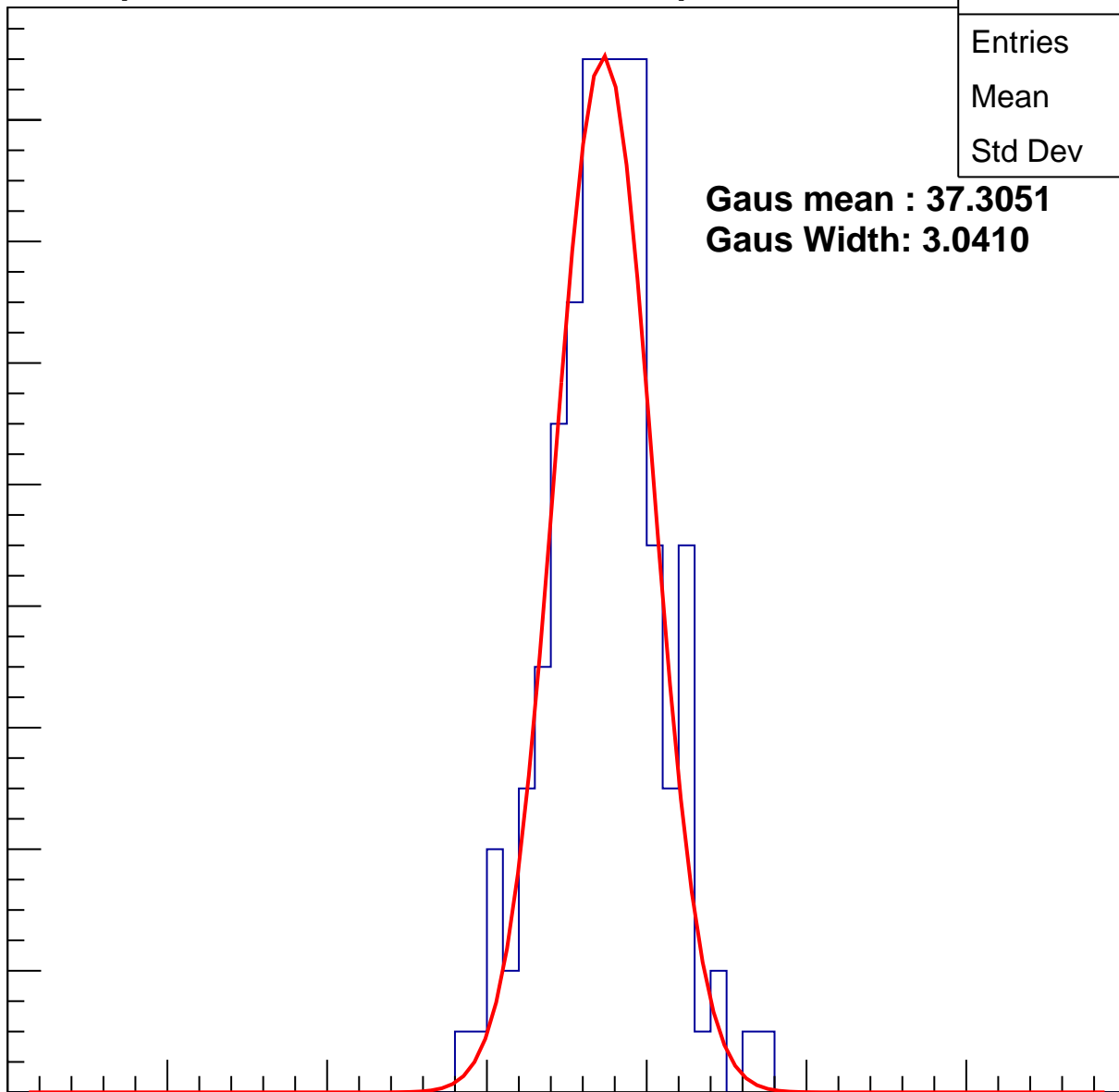
40

50

60

70

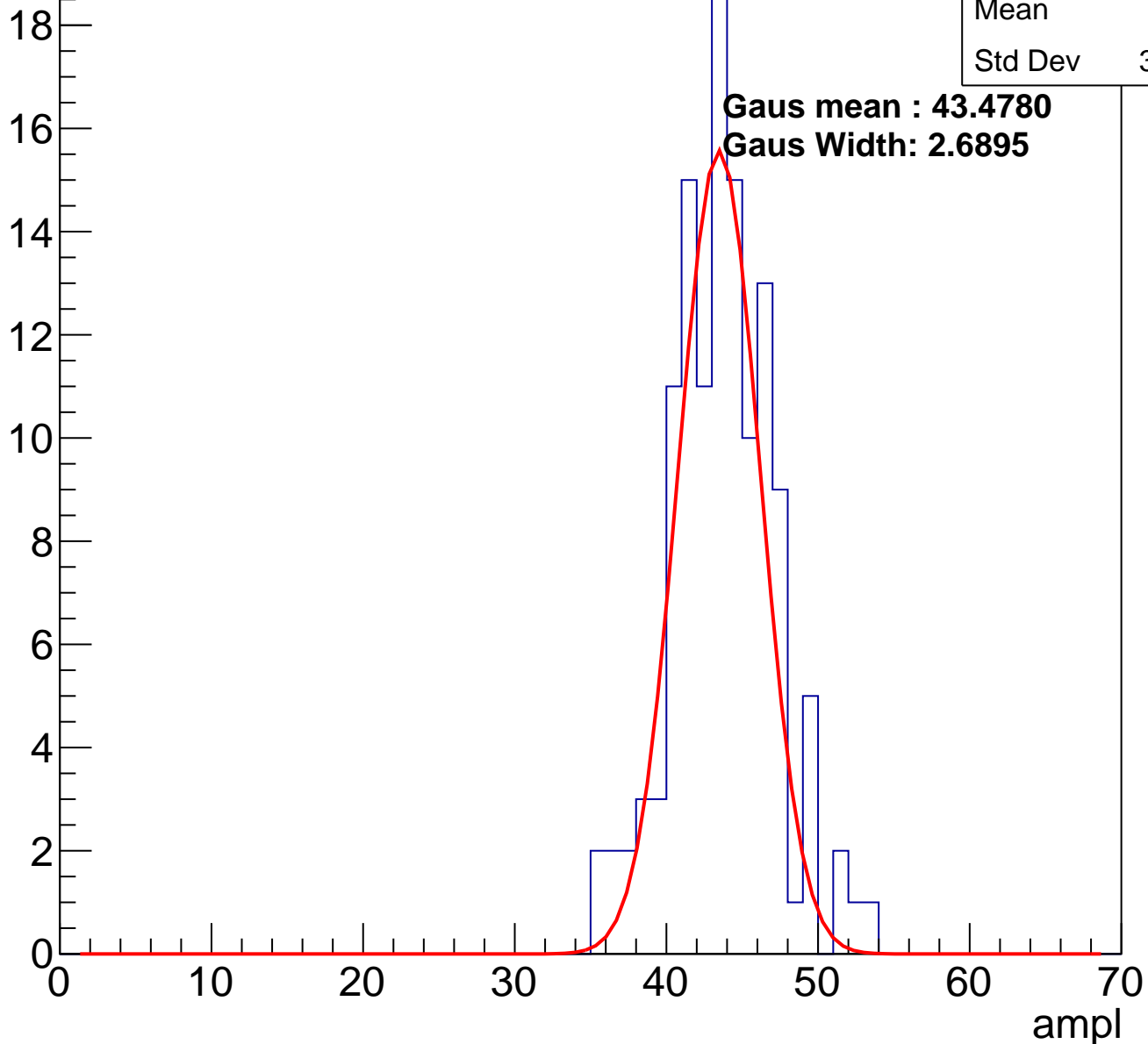
ampl



B1L001S, U22-ch24, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	125
Mean	43.3
Std Dev	3.359

Gaus mean : 43.4780

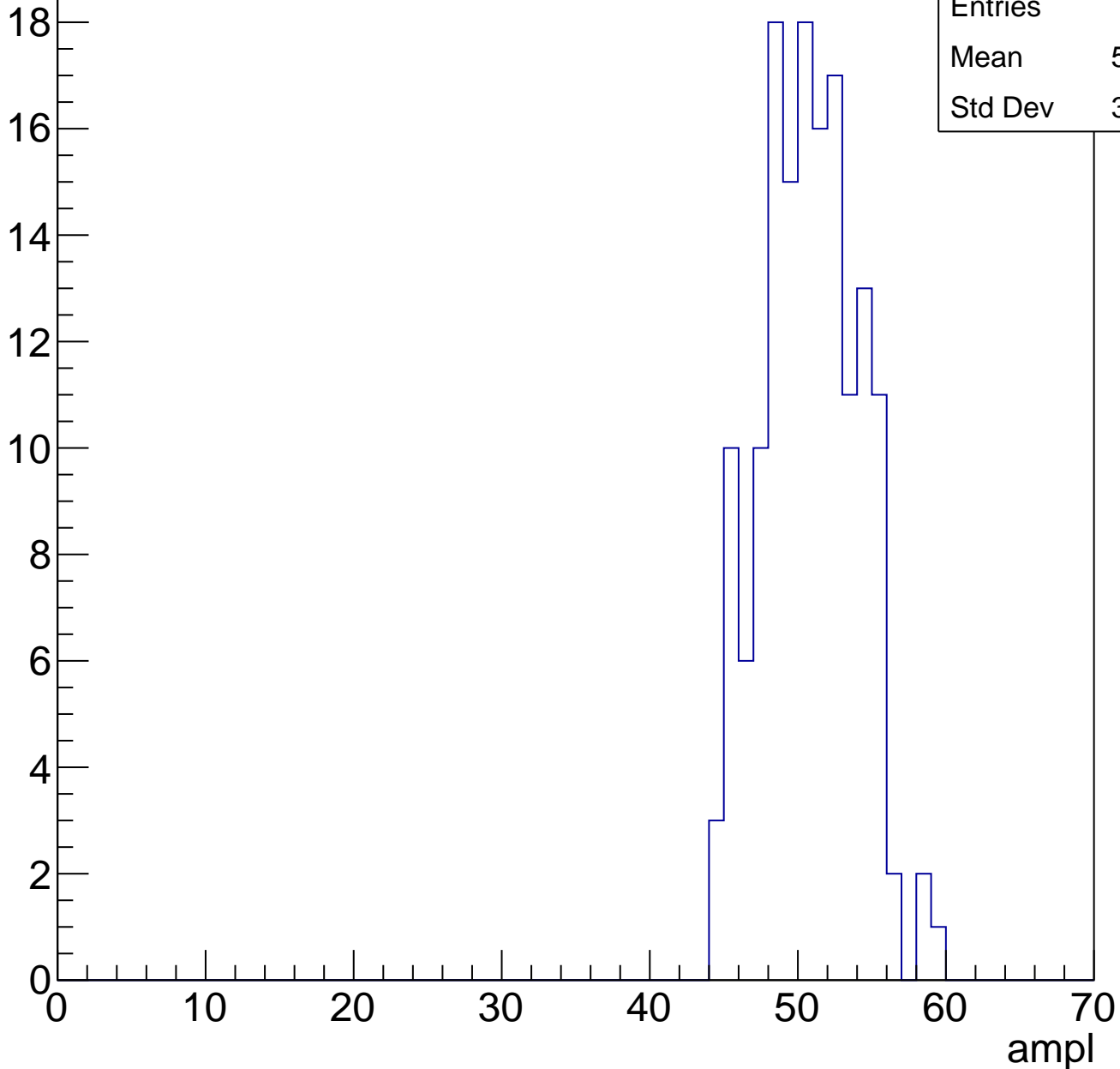
Gaus Width: 2.6895

B1L001S, U22-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	50.35
Std Dev	3.192

Entry



B1L001S, U22-ch24, adc4

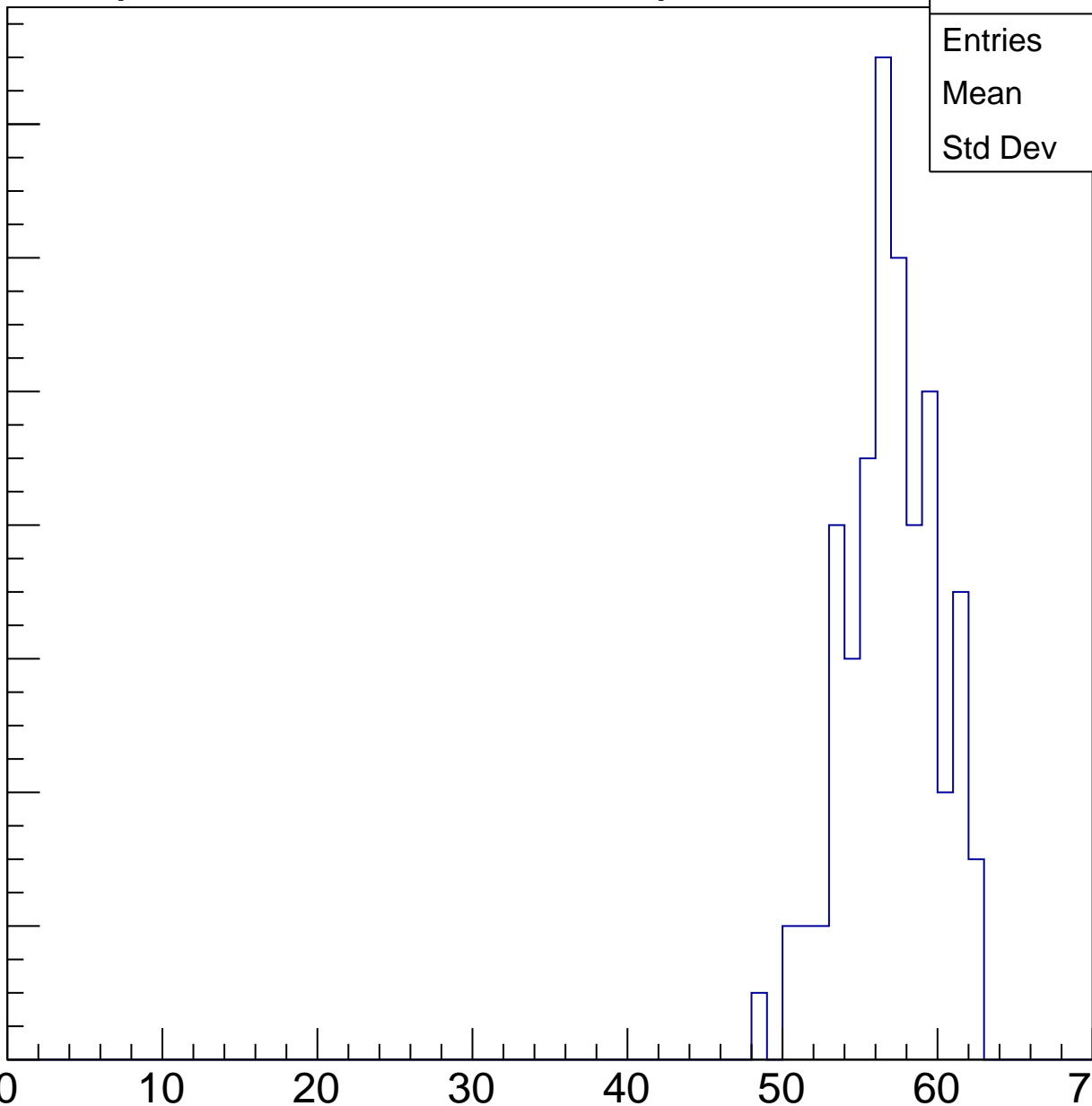
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	56.49
Std Dev	2.965

ampl



B1L001S, U22-ch24, adc5

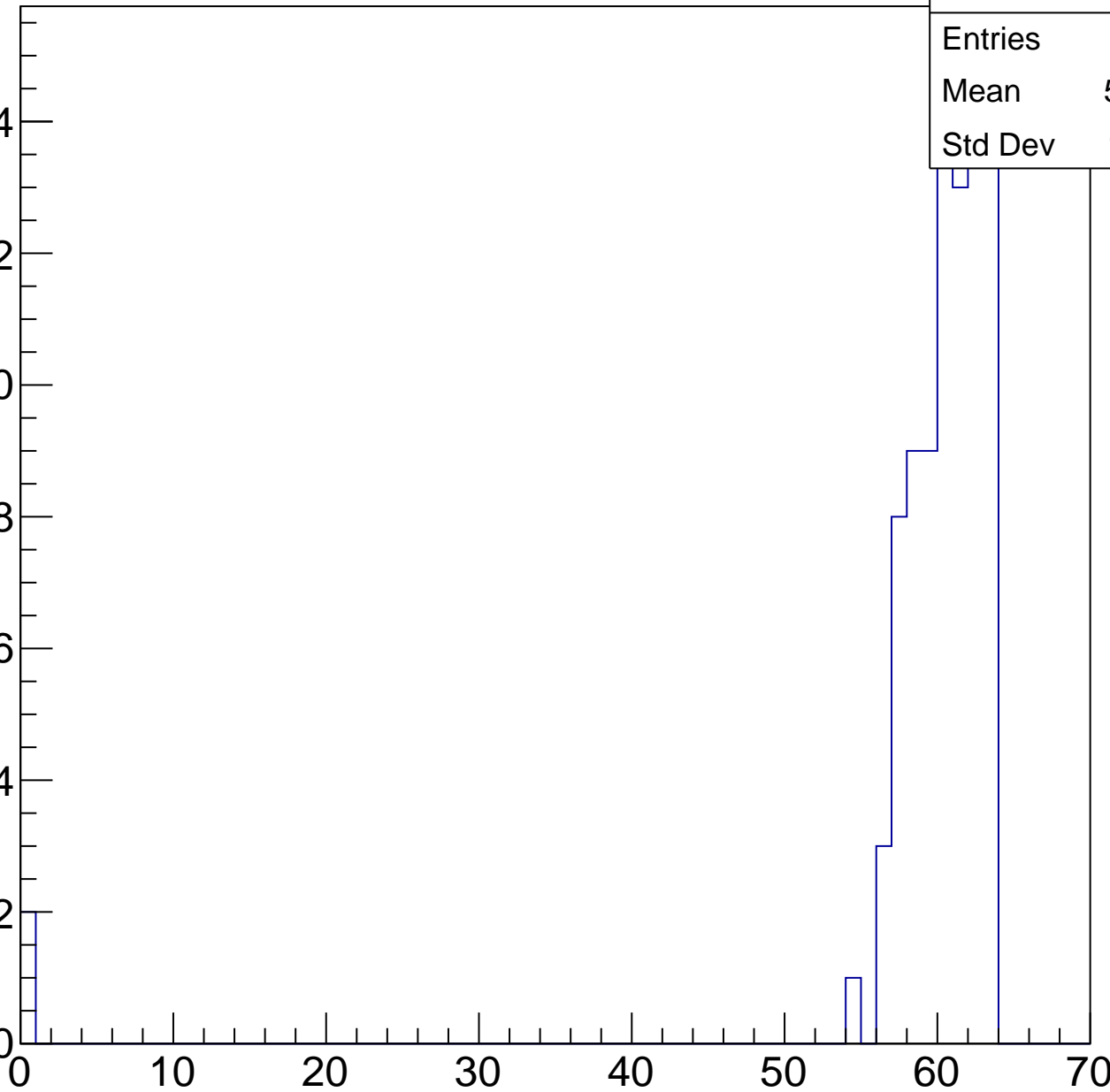
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.84
Std Dev	9.171

ampl

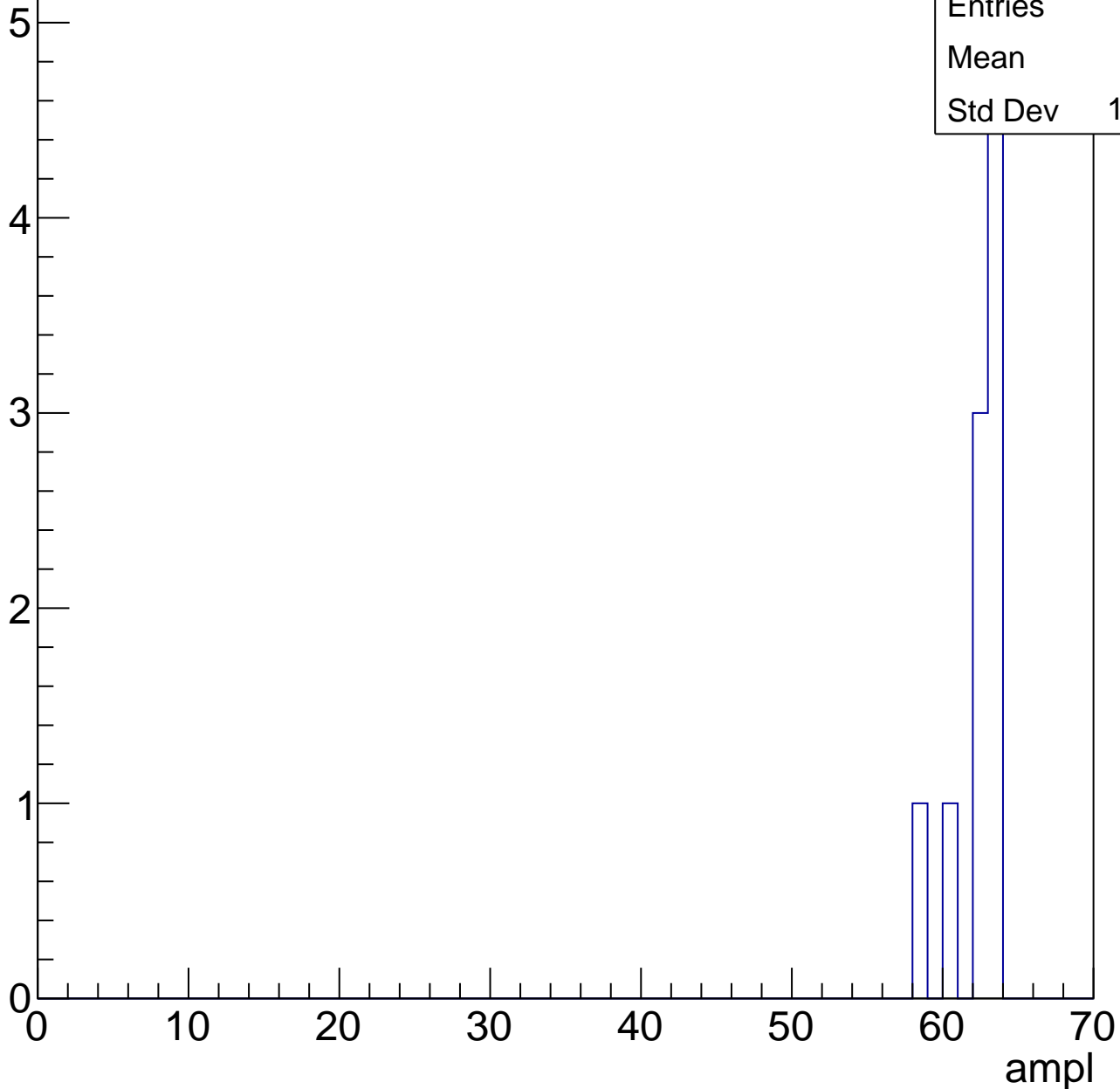


B1L001S, U22-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

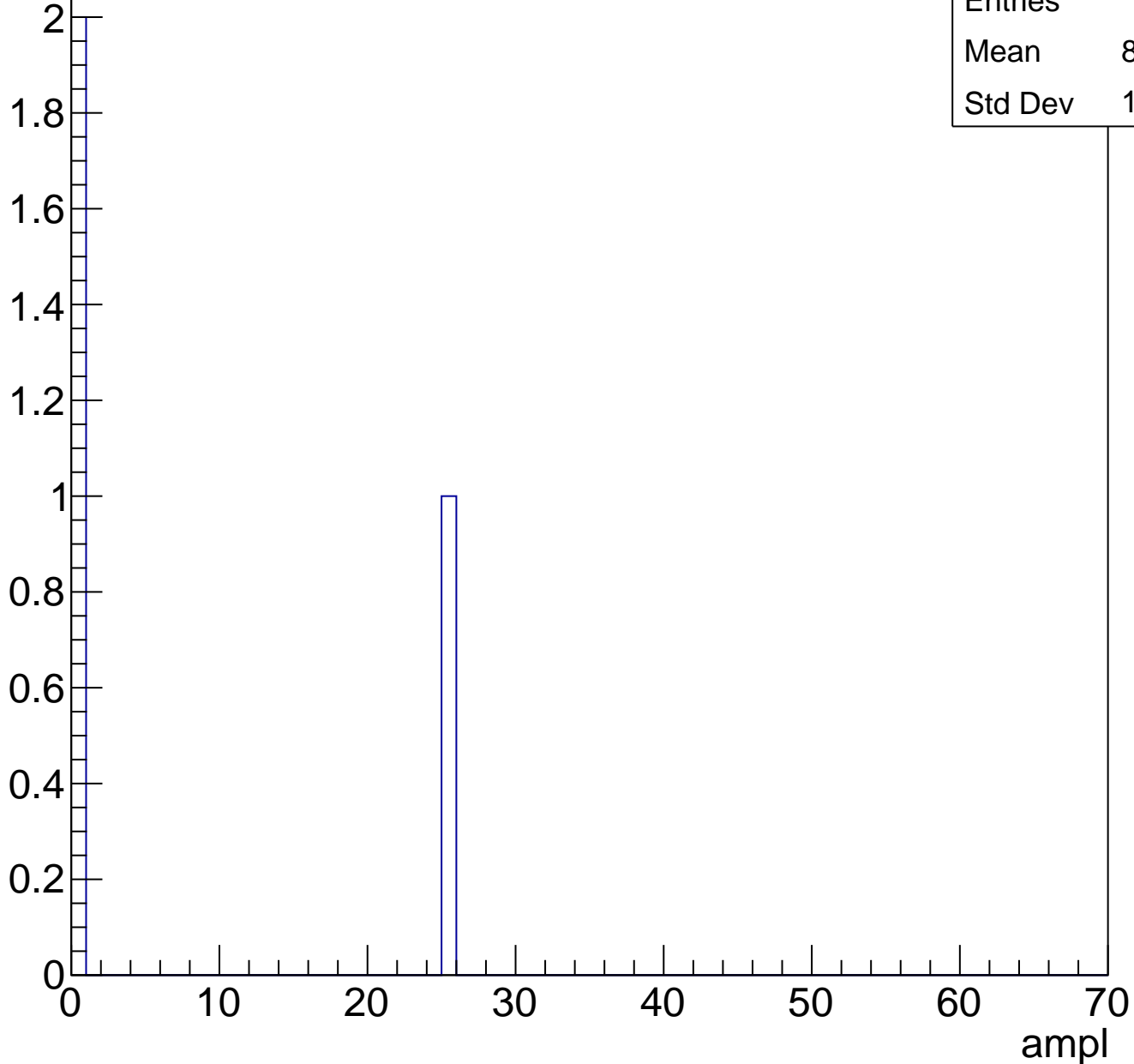
Entries	10
Mean	61.9
Std Dev	1.578



B1L001S, U22-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L001S, U22-ch25, adc0

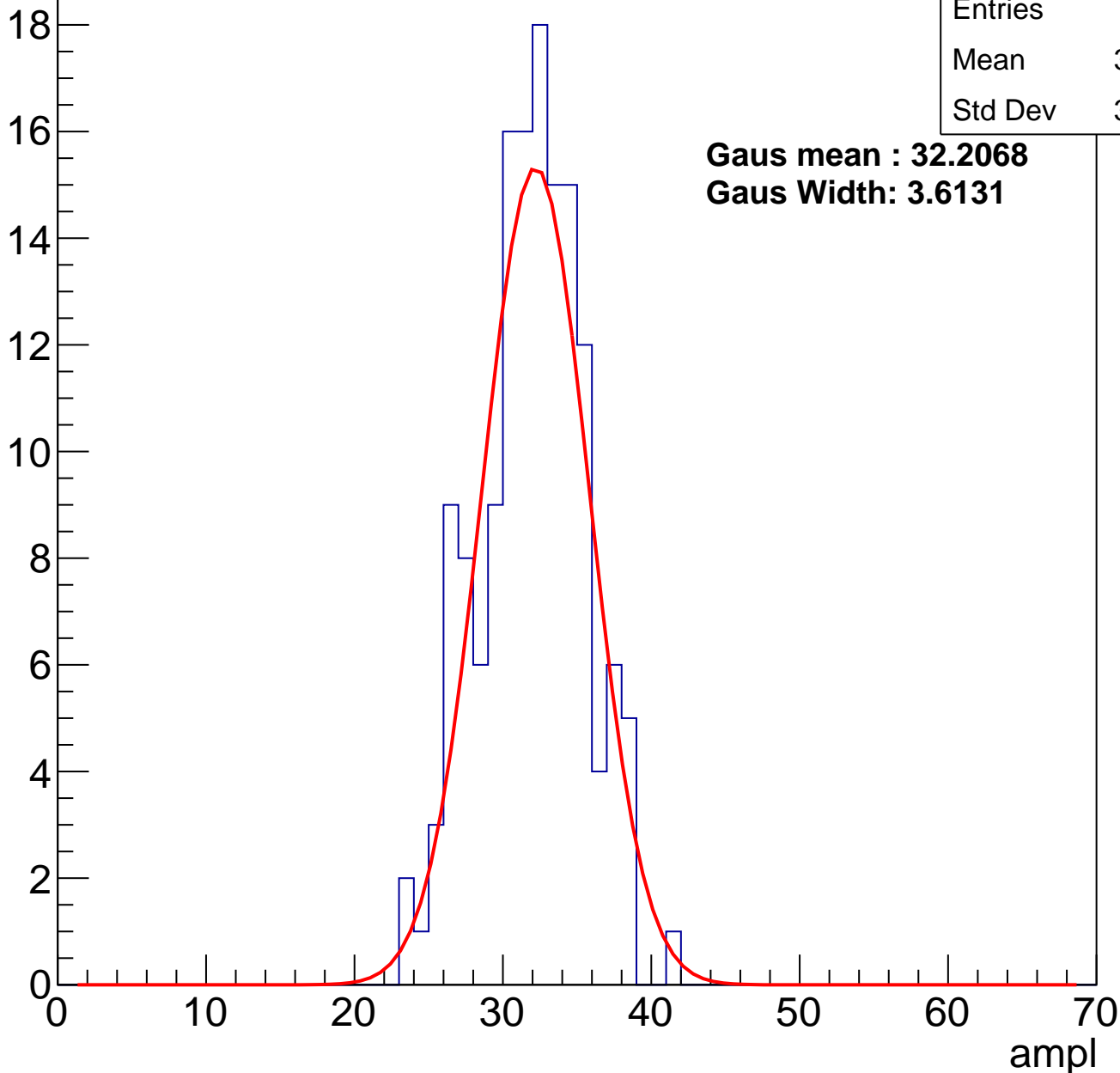
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	31.49
Std Dev	3.486

Gaus mean : 32.2068

Gaus Width: 3.6131

Entry



B1L001S, U22-ch25, adc1

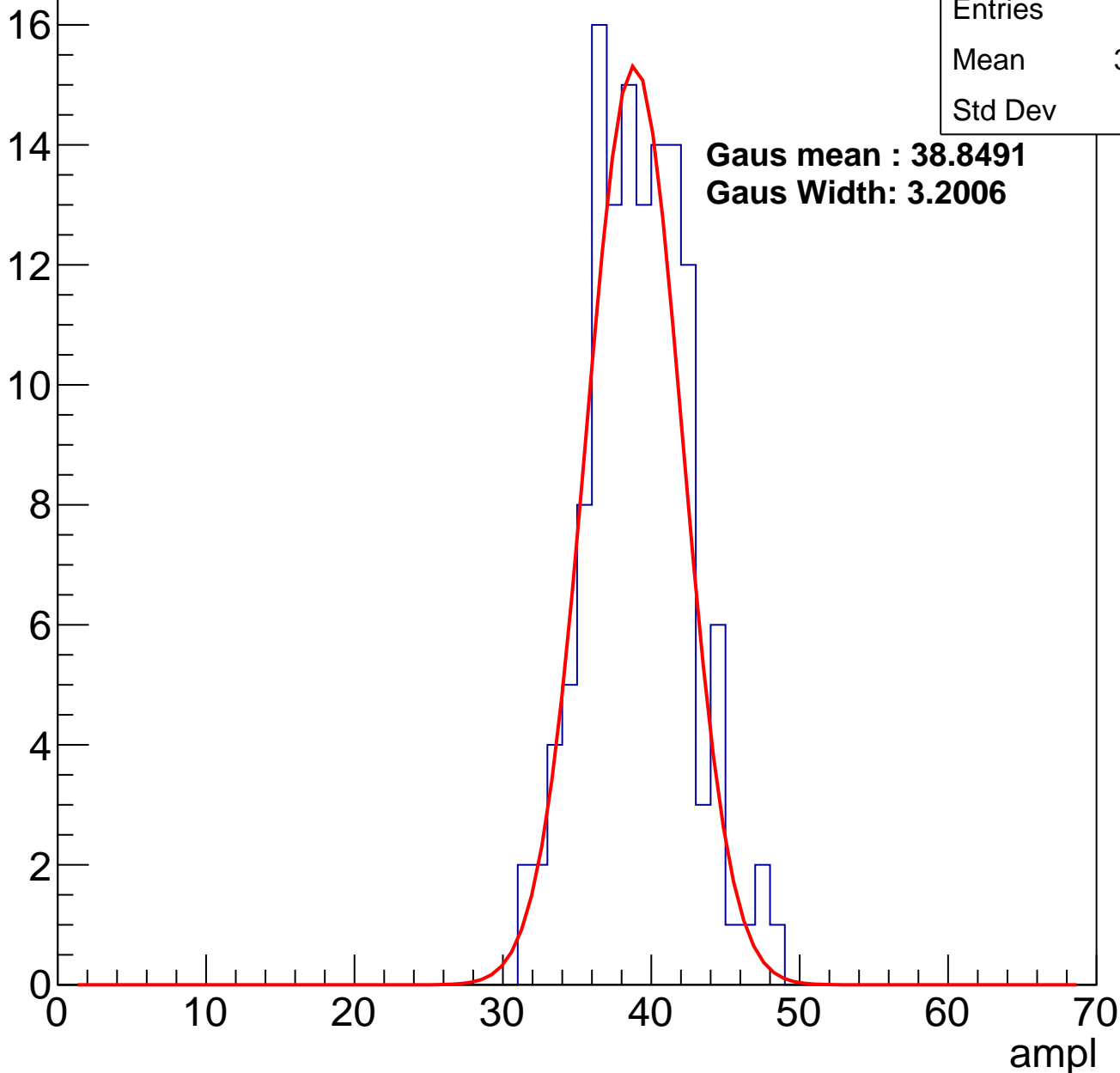
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	38.68
Std Dev	3.36

Gaus mean : 38.8491

Gaus Width: 3.2006

Entry



B1L001S, U22-ch25, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

45.02

Std Dev

3.15

Gaus mean : 45.7323

Gaus Width: 3.2199

0

10

20

30

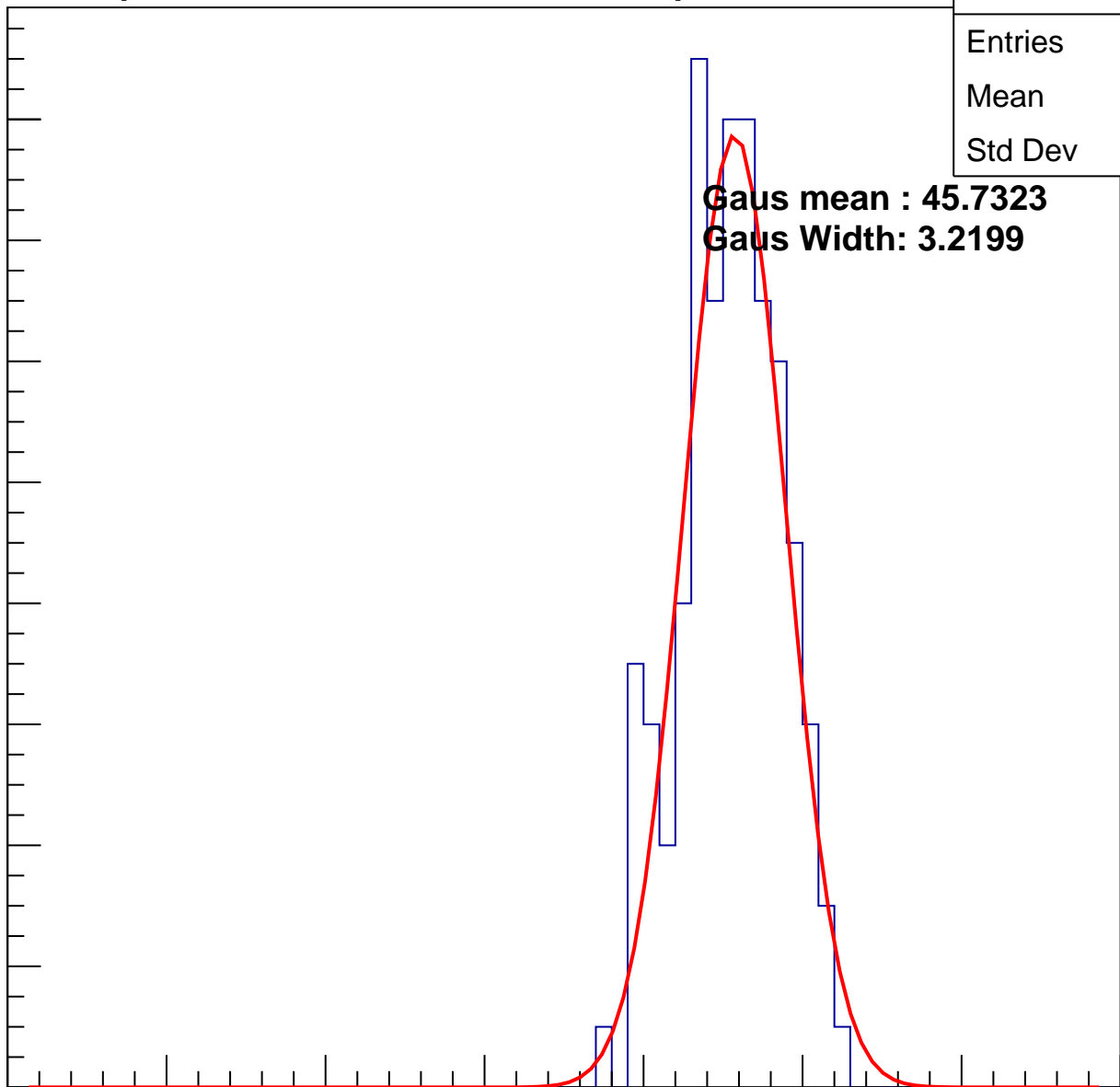
40

50

60

70

ampl



B1L001S, U22-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

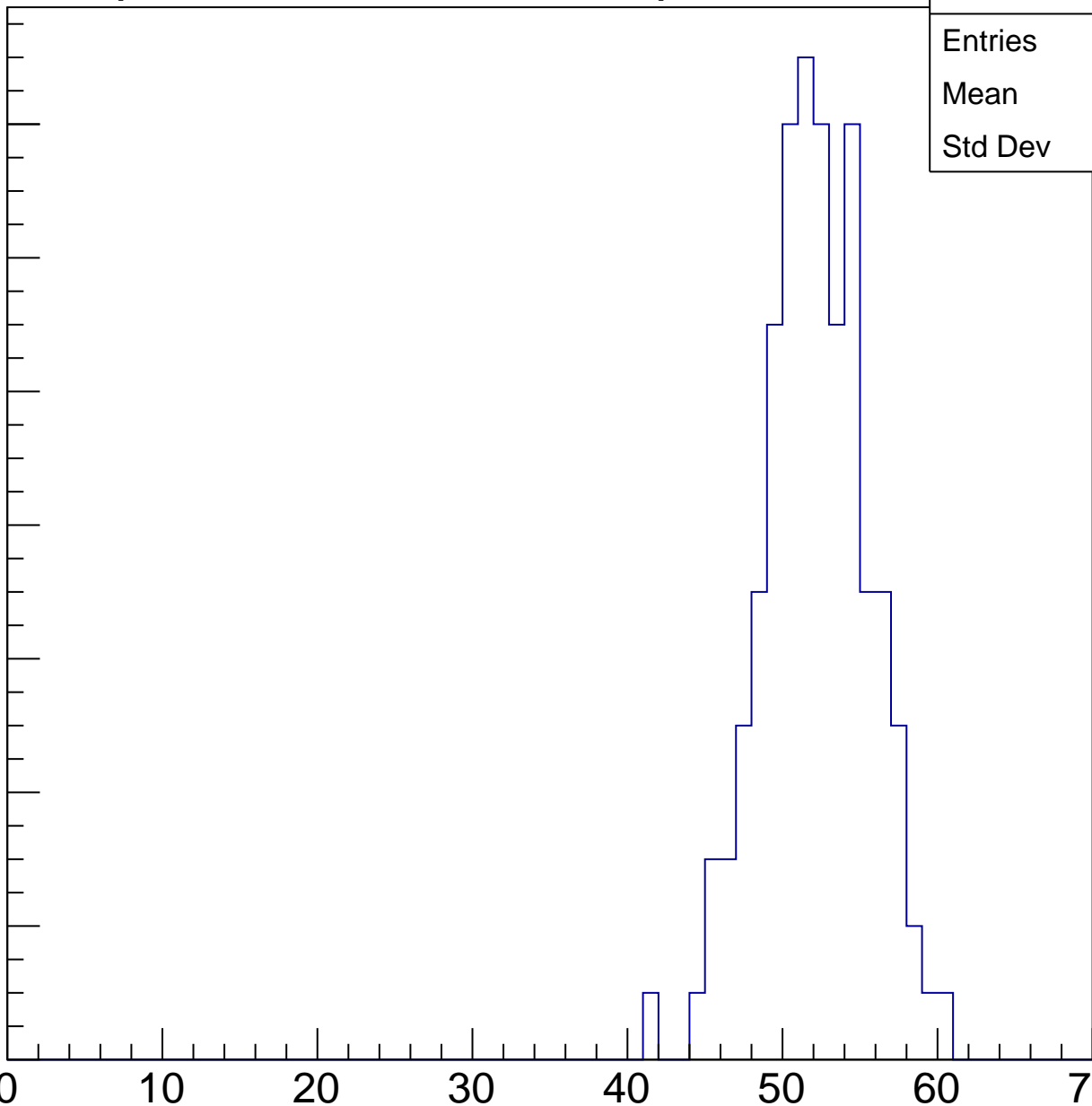
Entries	122
Mean	51.61
Std Dev	3.364

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch25, adc4

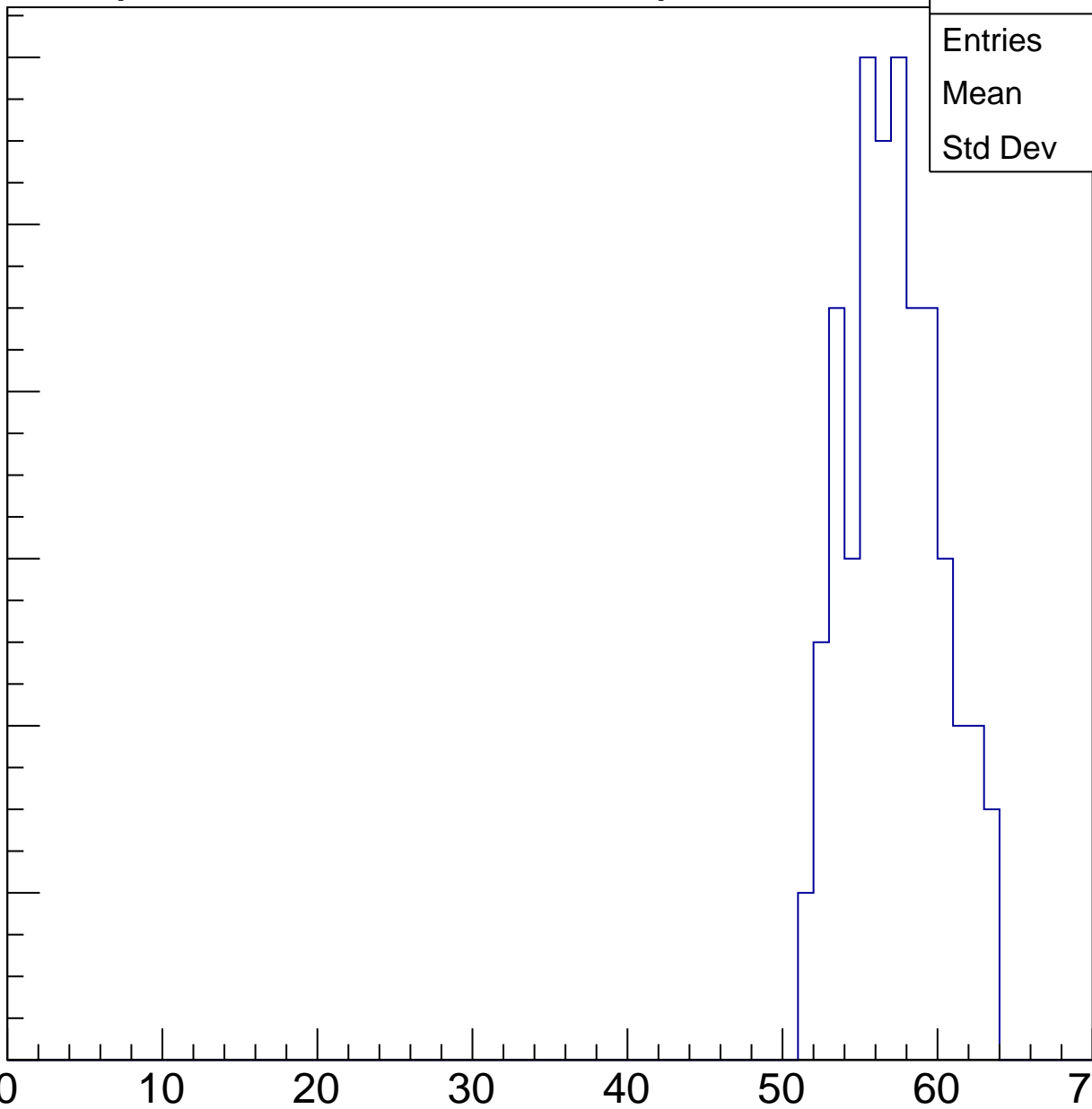
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	92
Mean	56.71
Std Dev	2.98

ampl



B1L001S, U22-ch25, adc5

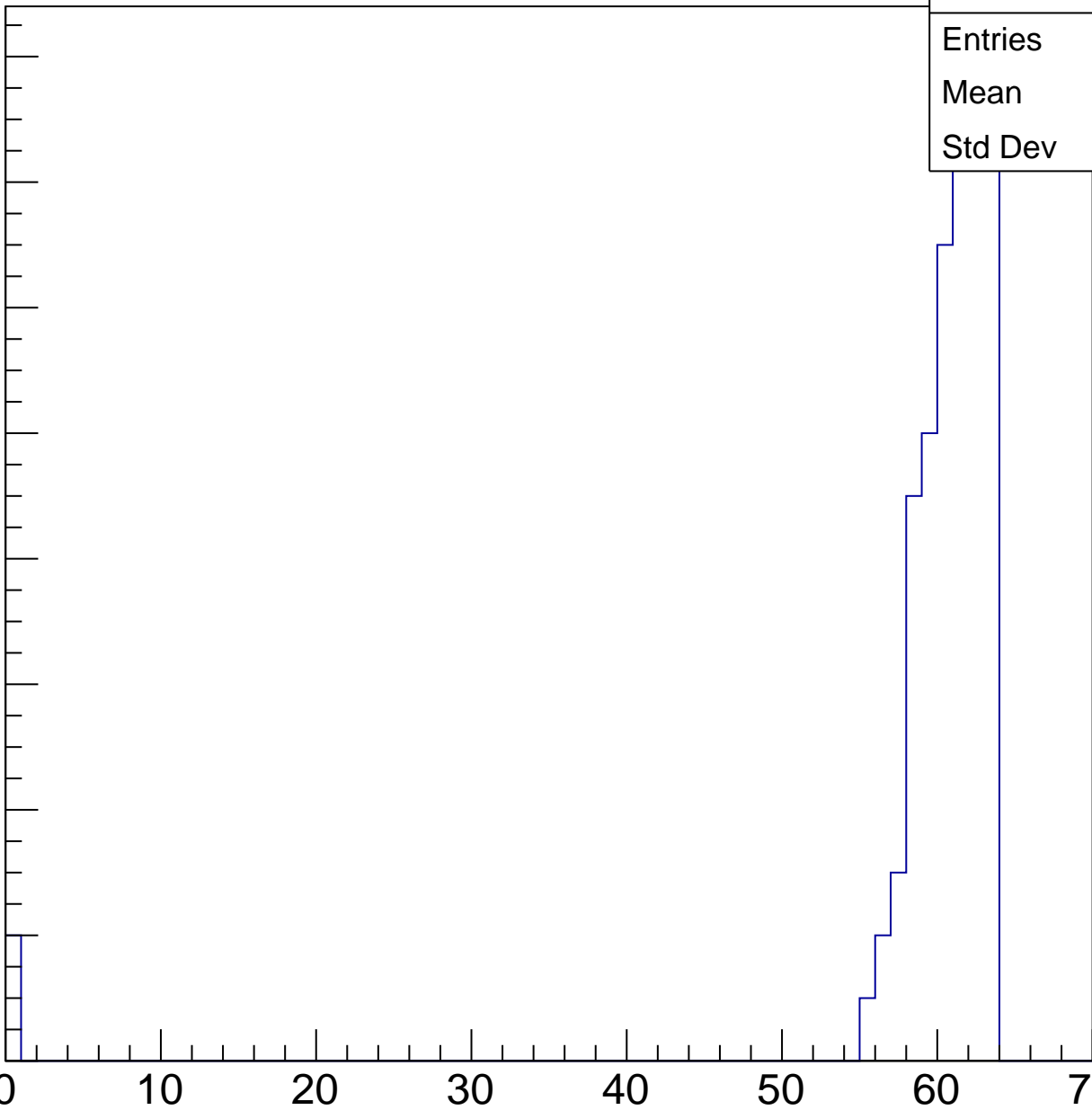
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.1
Std Dev	9.326

ampl



B1L001S, U22-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch26, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.17
Std Dev	3.373

Gaus mean : 28.7143

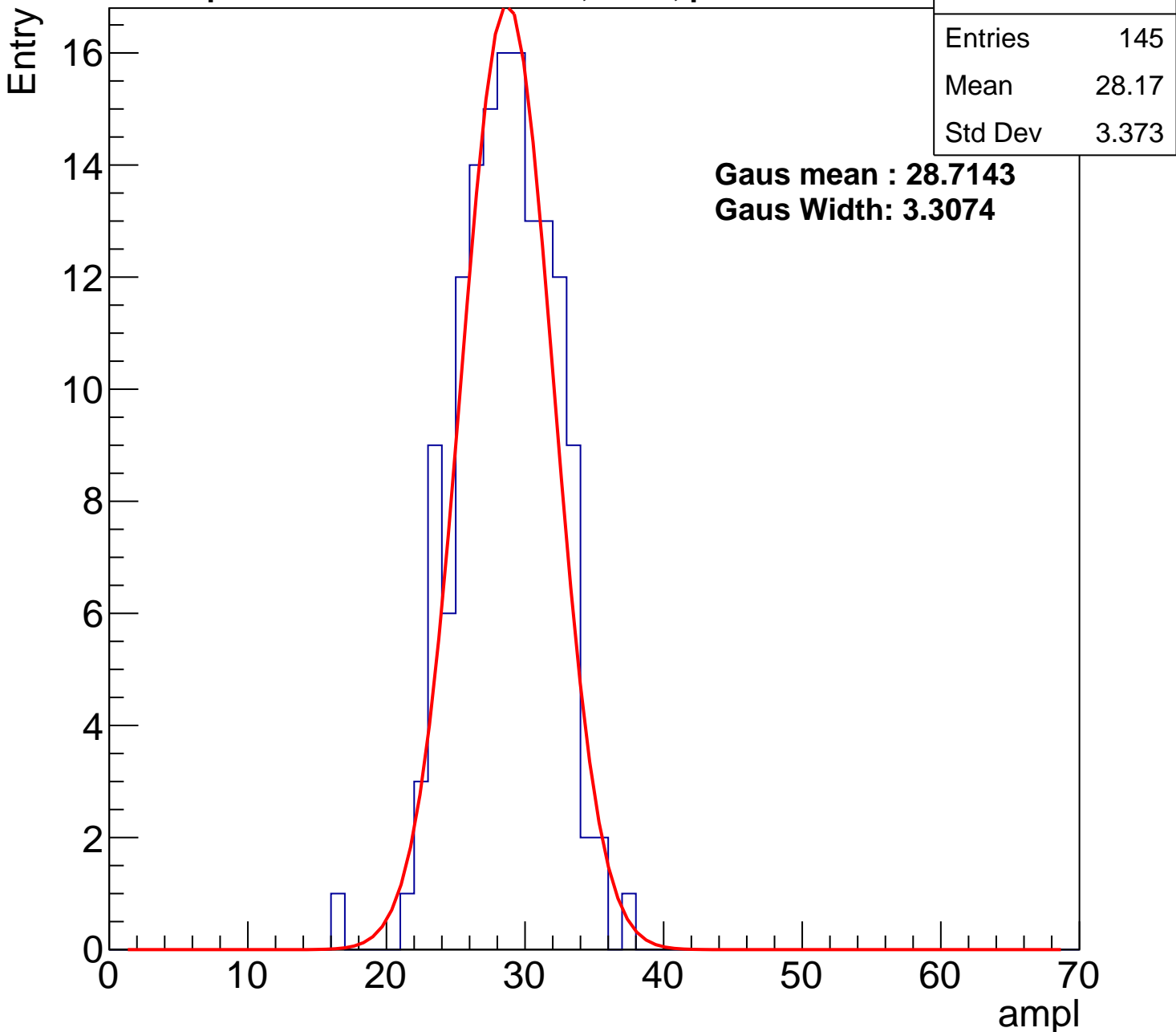
Gaus Width: 3.3074

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



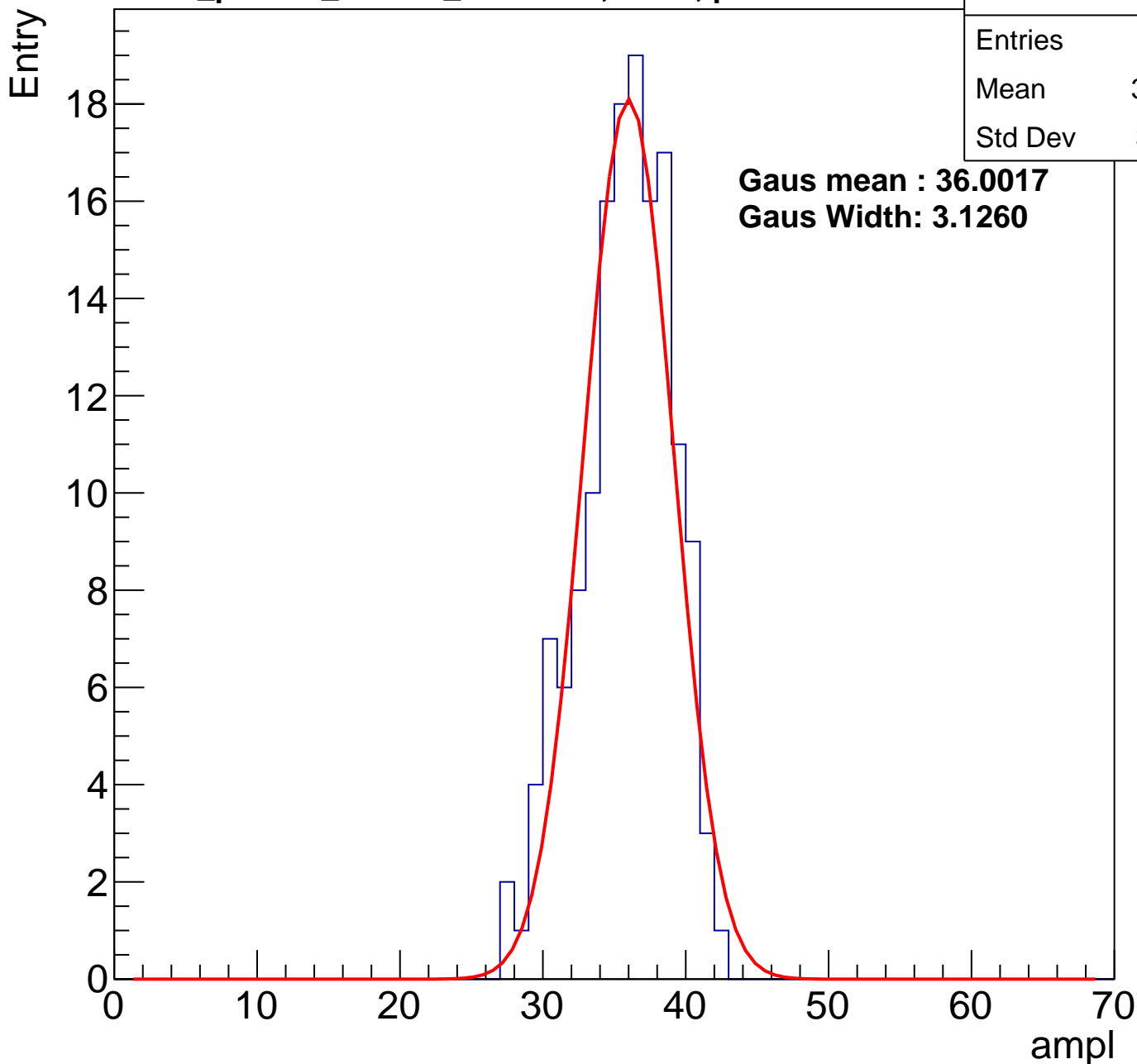
B1L001S, U22-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.34
Std Dev	3.181

Gaus mean : 36.0017

Gaus Width: 3.1260



B1L001S, U22-ch26, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

42.38

Std Dev

3.158

Gaus mean : 43.1619

Gaus Width: 2.7842

0

10

20

30

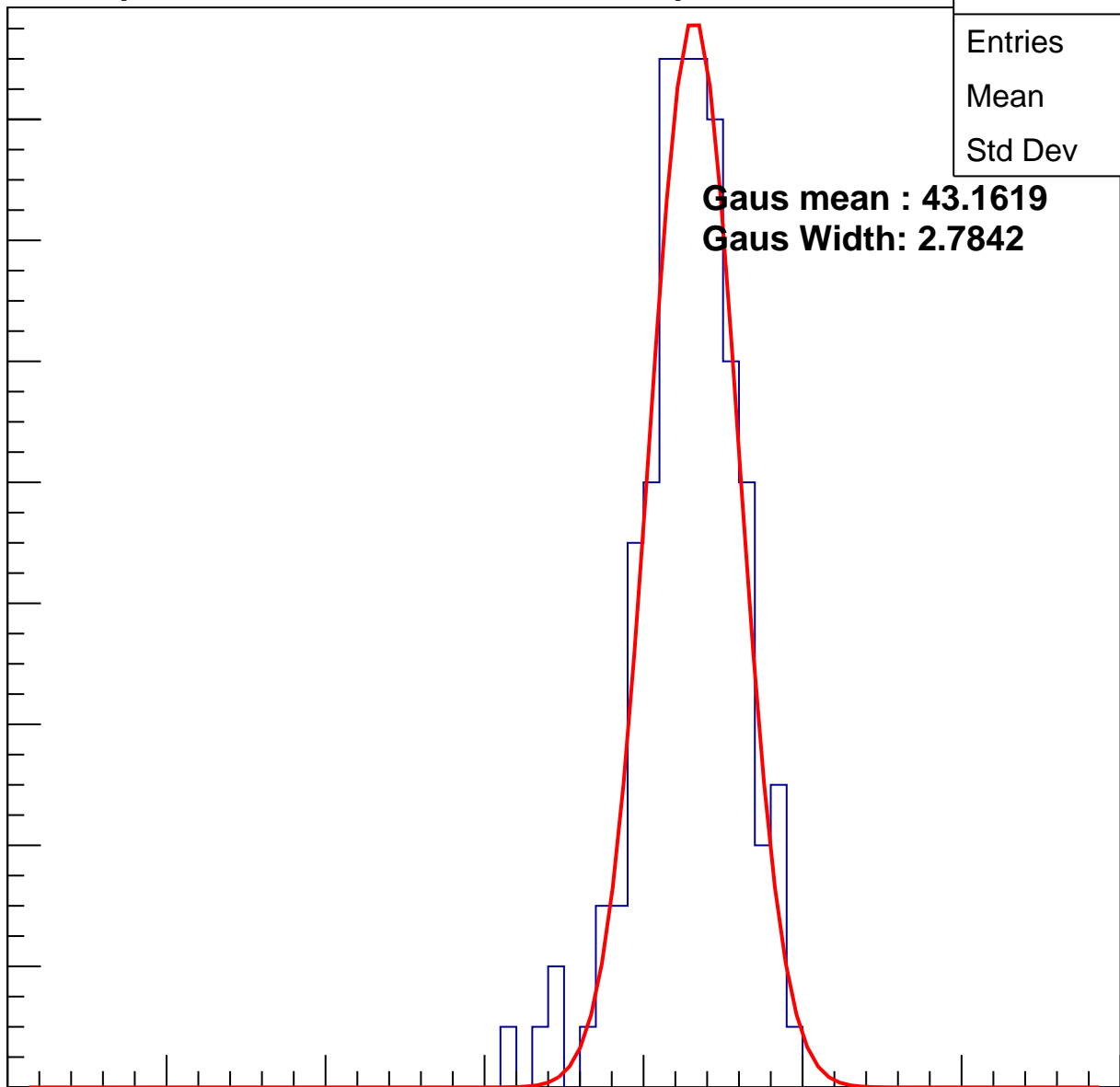
40

50

60

70

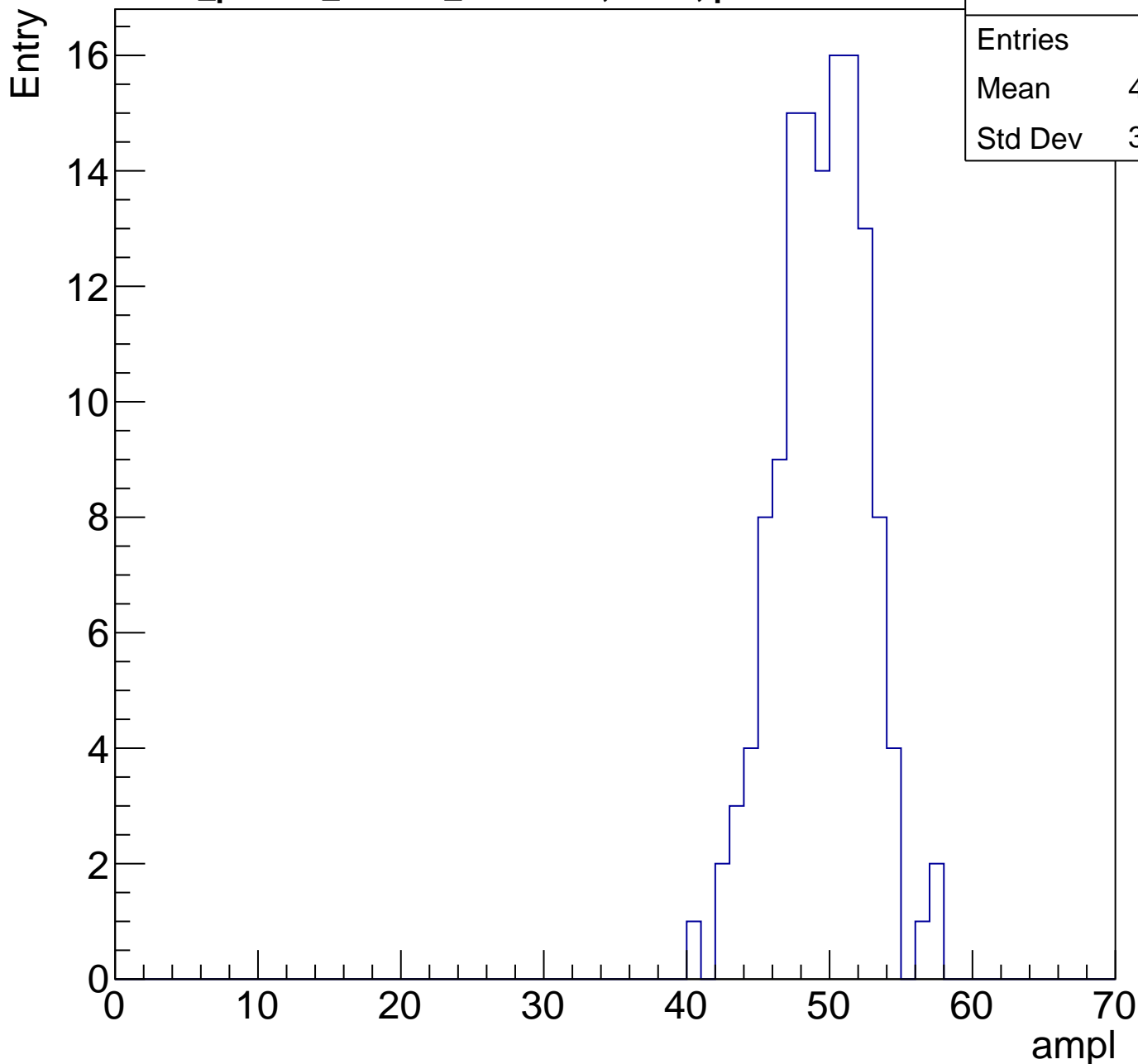
ampl



B1L001S, U22-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	48.98
Std Dev	3.117



B1L001S, U22-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

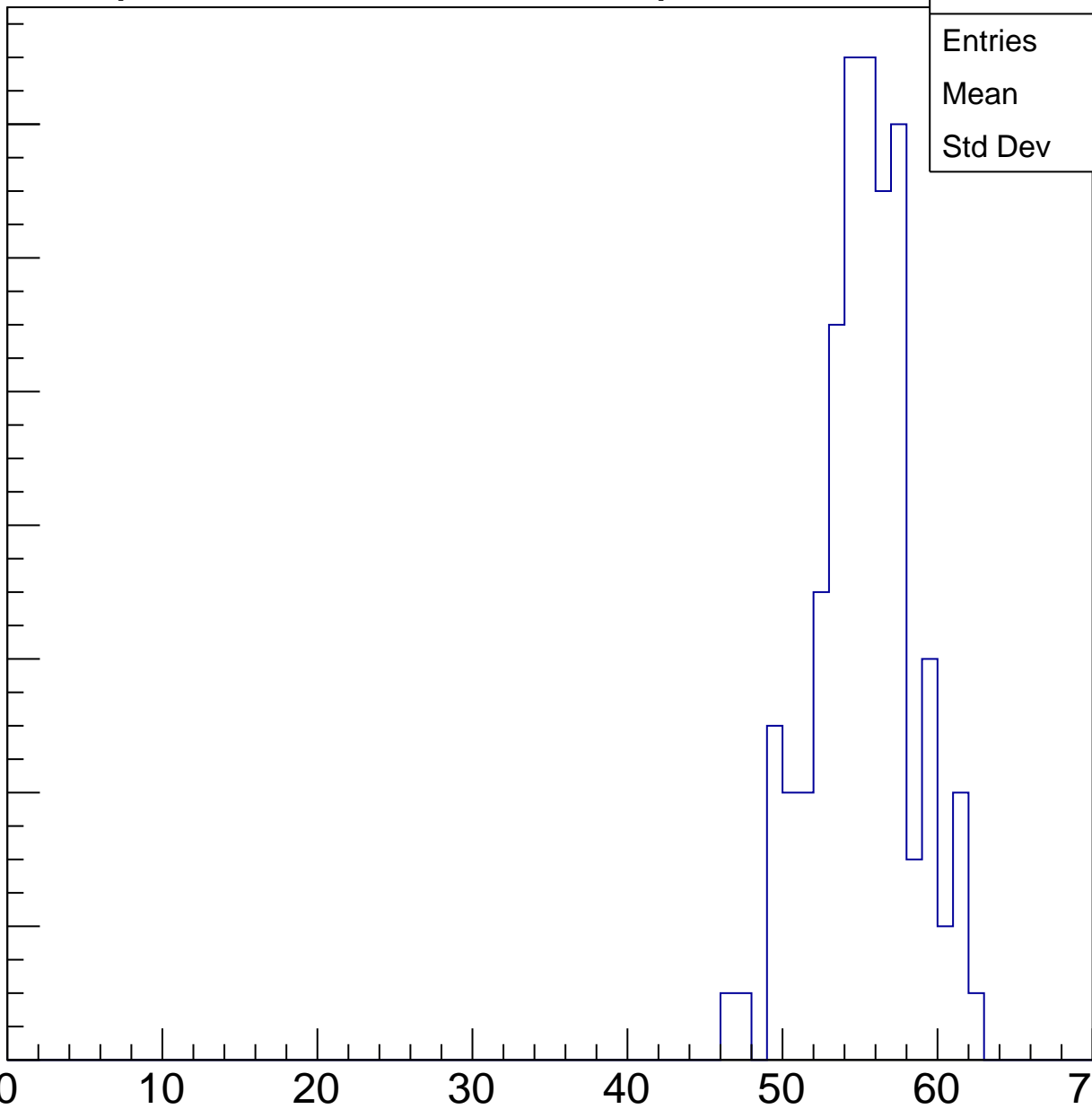
Entries	106
Mean	54.75
Std Dev	3.132

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch26, adc5

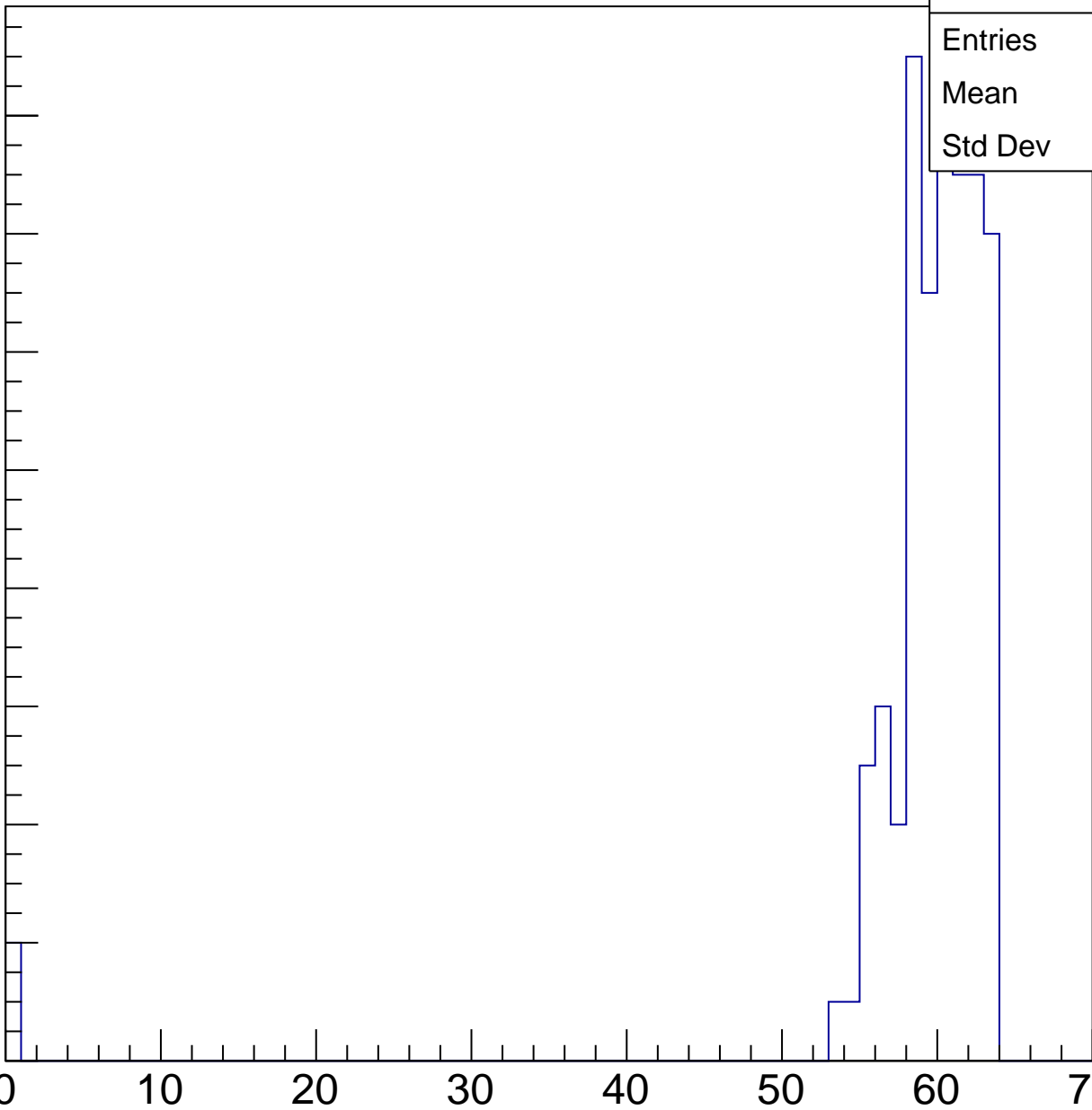
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.6
Std Dev	8.317

ampl

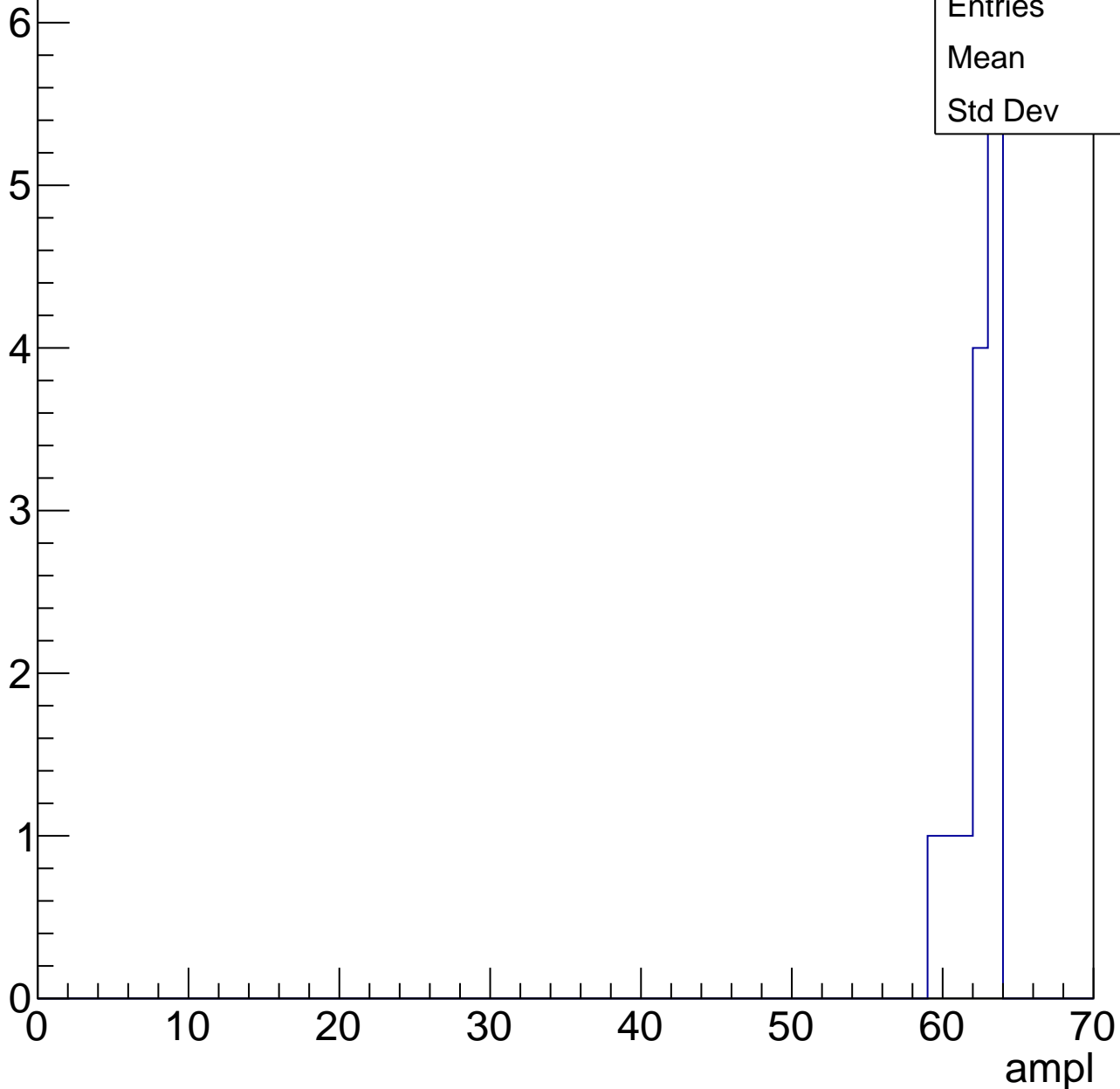


B1L001S, U22-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62
Std Dev	1.24



B1L001S, U22-ch26, adc7

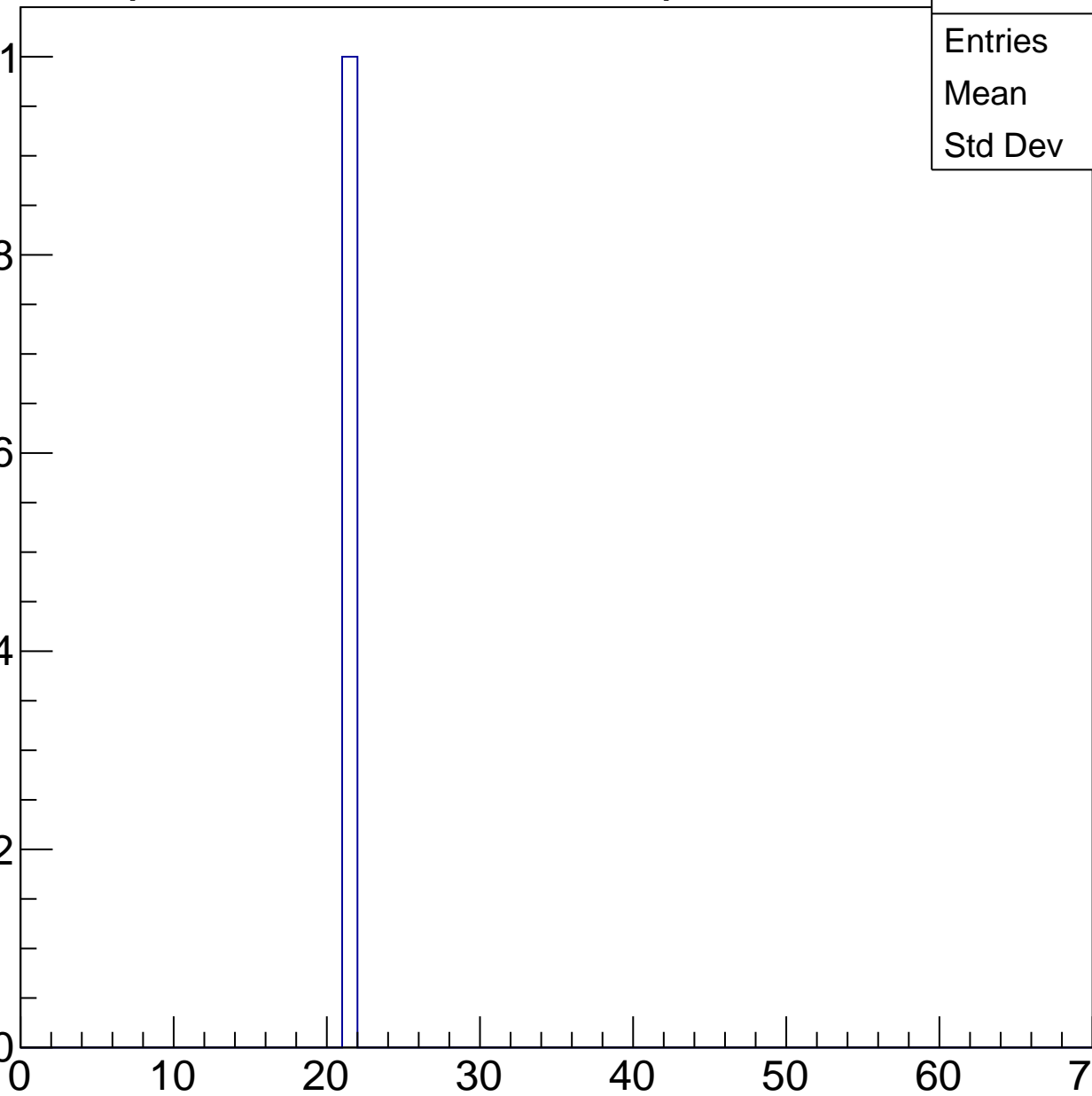
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl



B1L001S, U22-ch27, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	29.73
Std Dev	3.175

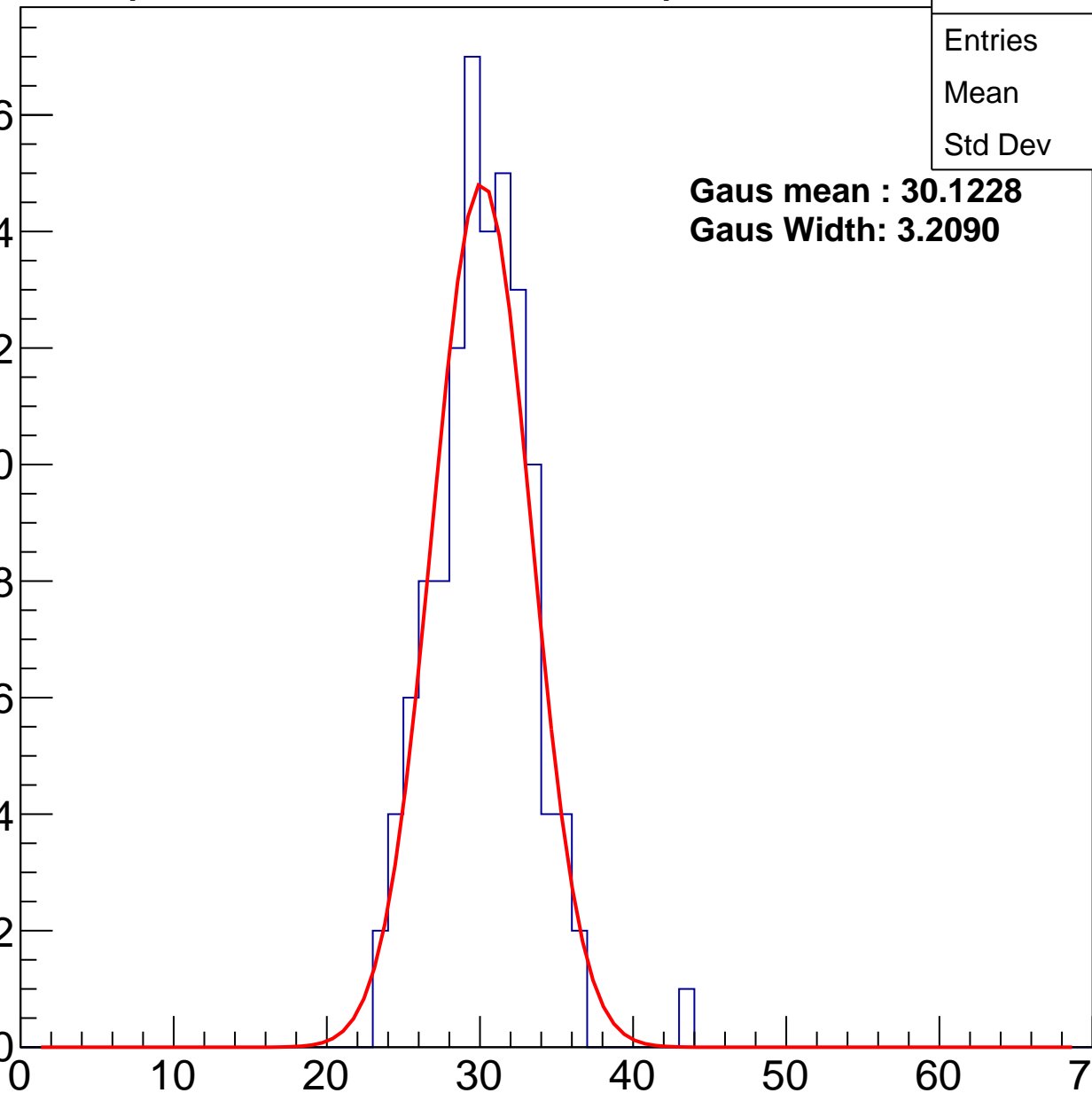
Gaus mean : 30.1228

Gaus Width: 3.2090

Entry

16
14
12
10
8
6
4
2
0

ampl



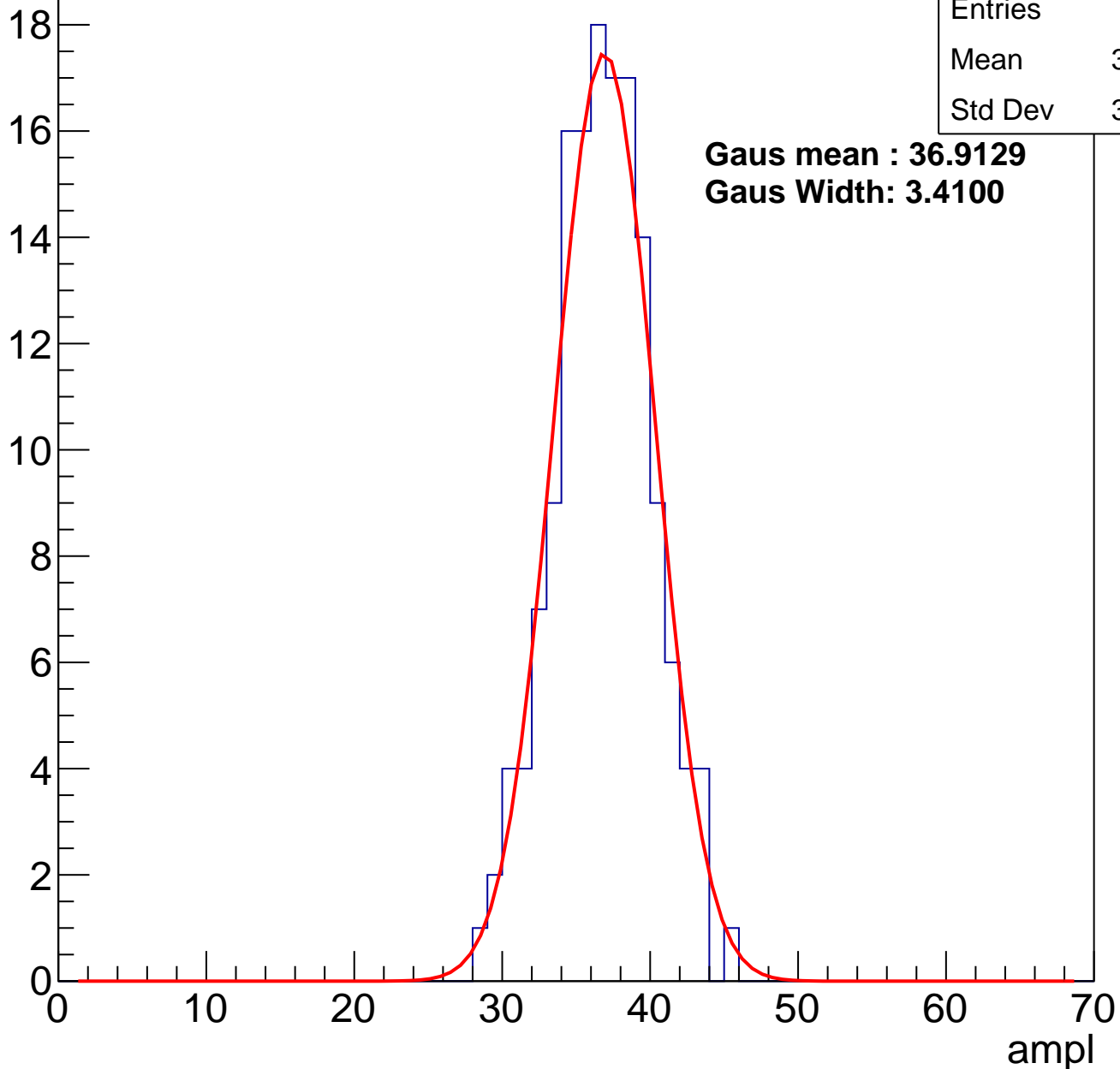
B1L001S, U22-ch27, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.34
Std Dev	3.264

Gaus mean : 36.9129
Gaus Width: 3.4100

Entry



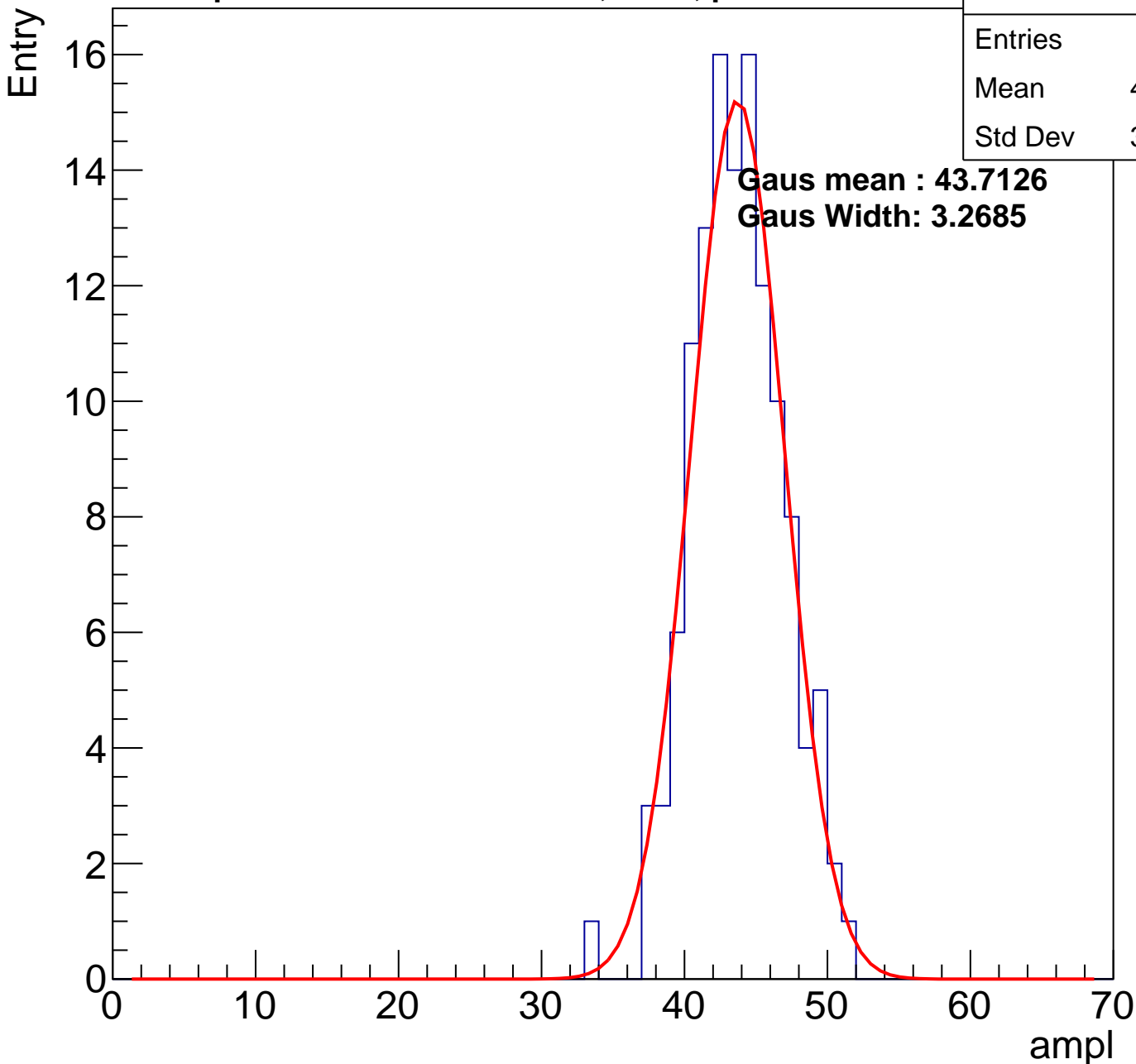
B1L001S, U22-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	43.26
Std Dev	3.177

Gaus mean : 43.7126

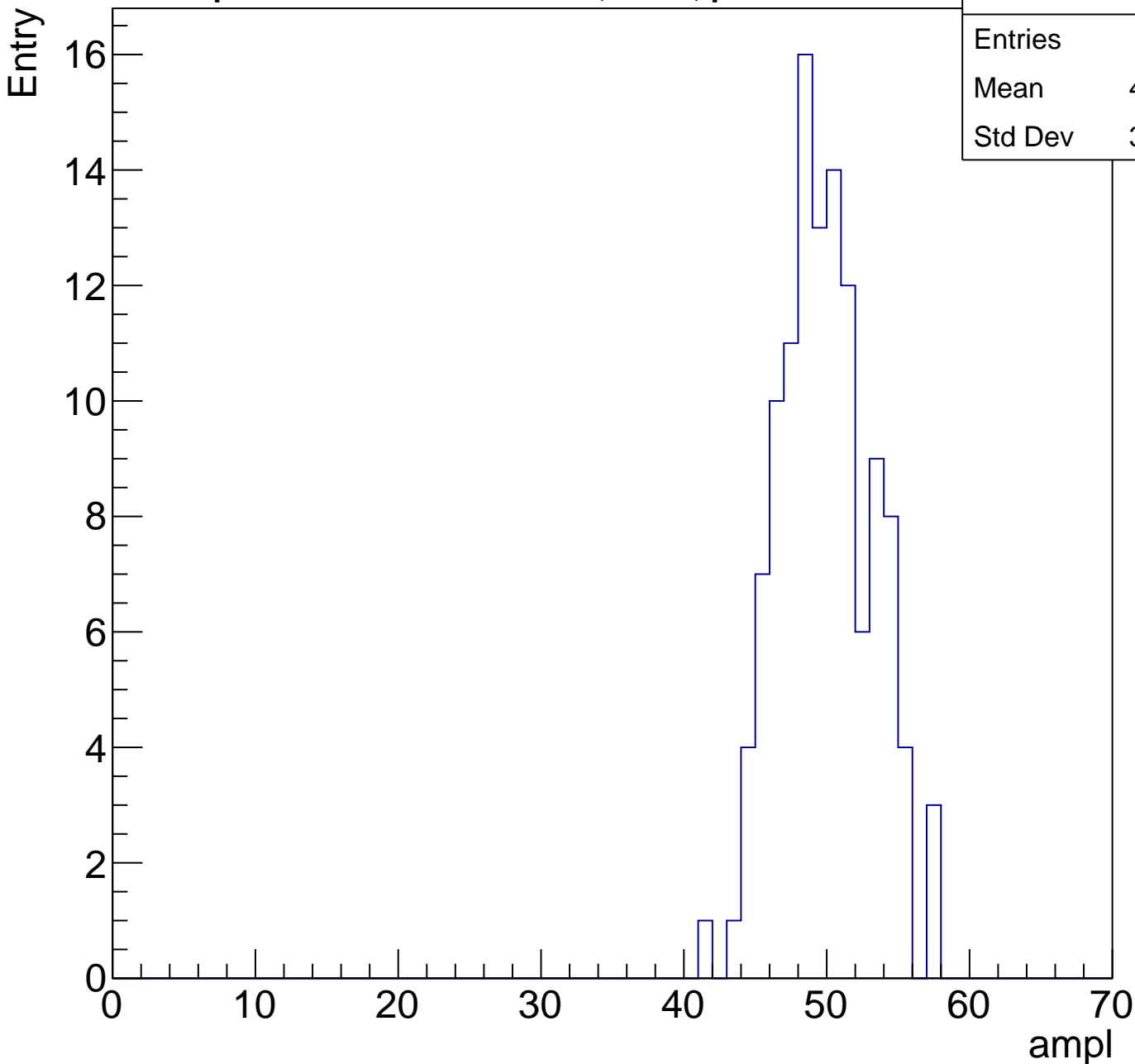
Gaus Width: 3.2685



B1L001S, U22-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

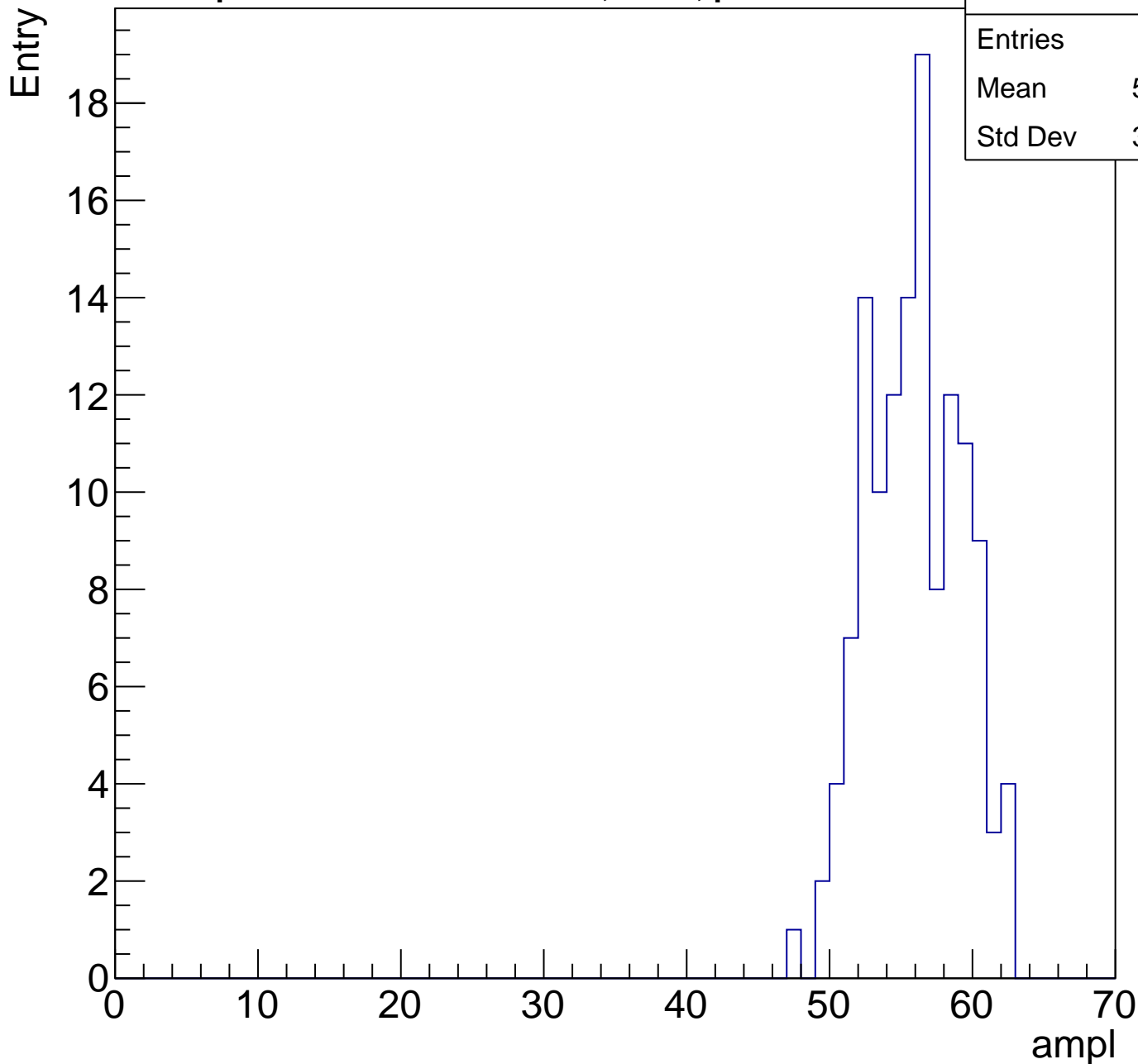
Entries	119
Mean	49.42
Std Dev	3.224



B1L001S, U22-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	55.49
Std Dev	3.233



B1L001S, U22-ch27, adc5

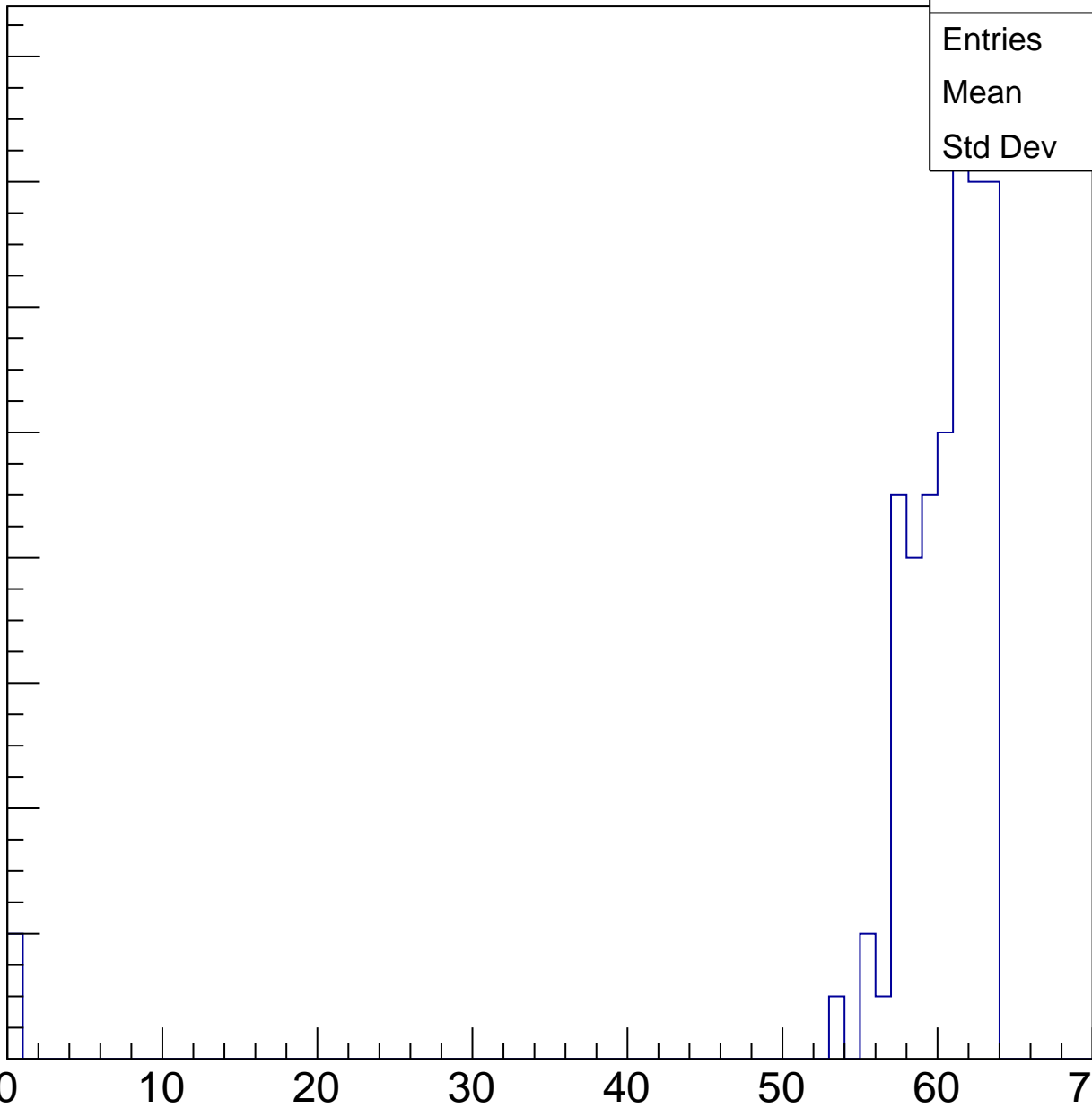
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.76
Std Dev	9.34

ampl

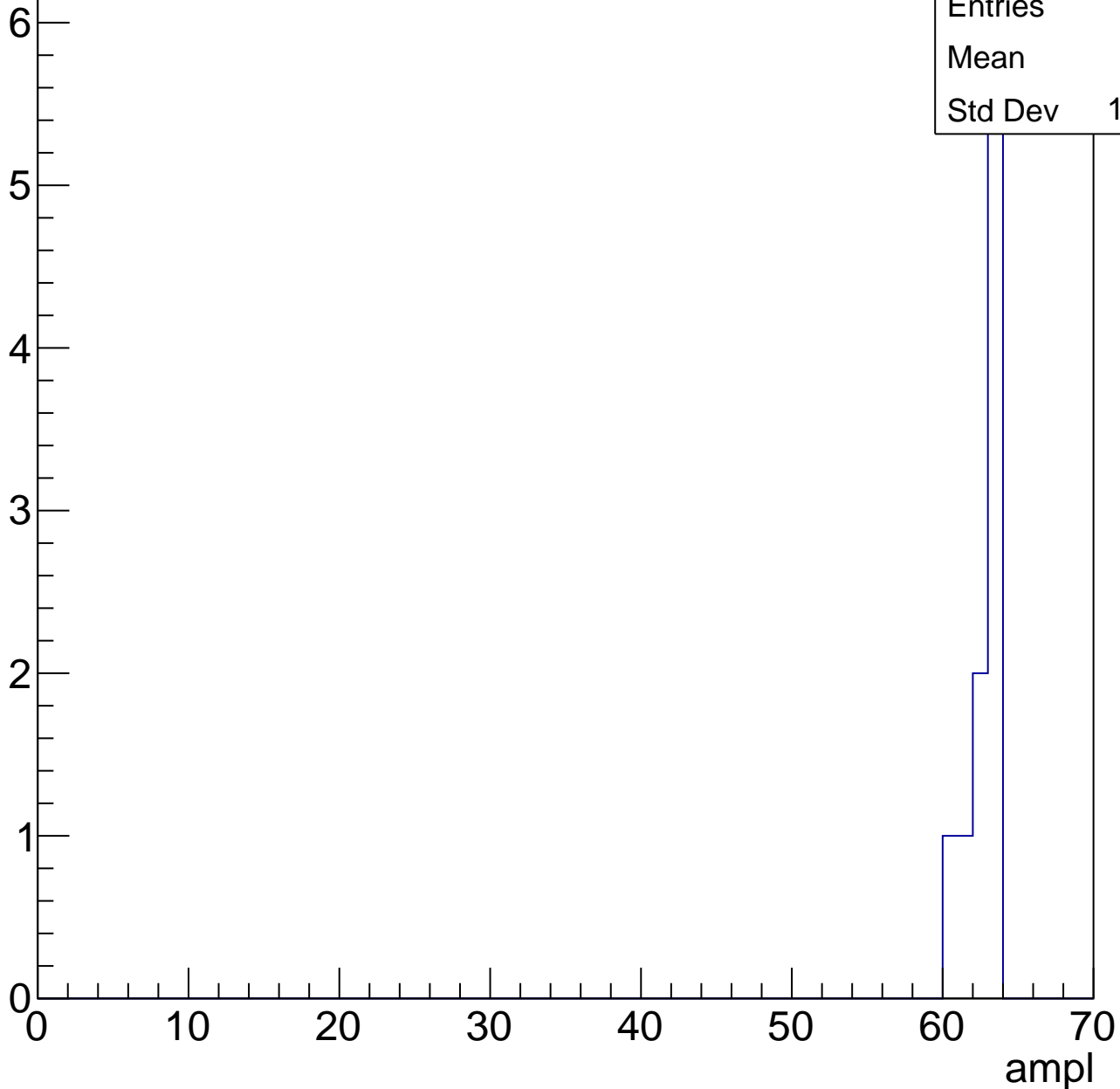


B1L001S, U22-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

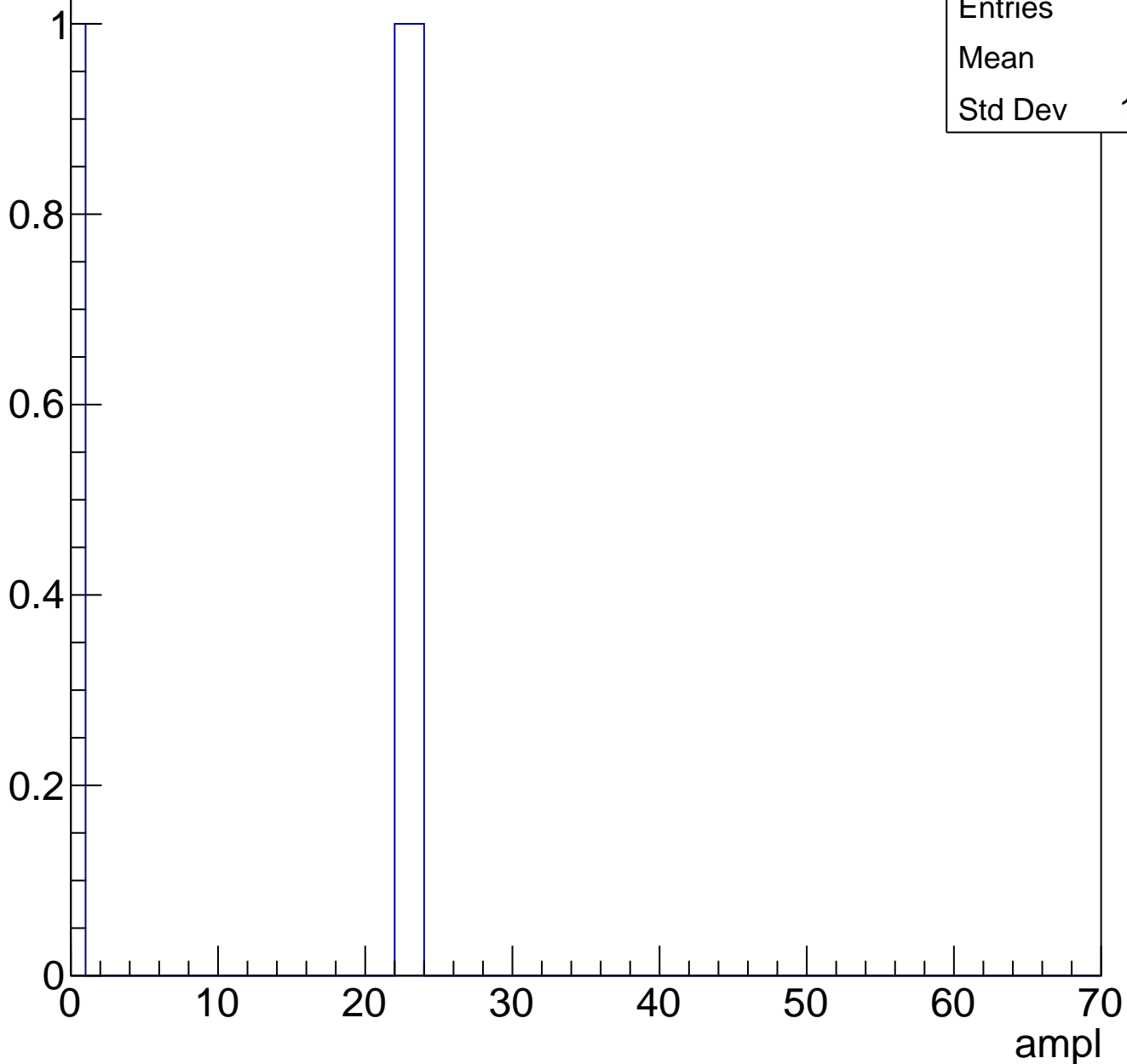
Entries	10
Mean	62.3
Std Dev	1.005



B1L001S, U22-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch28, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

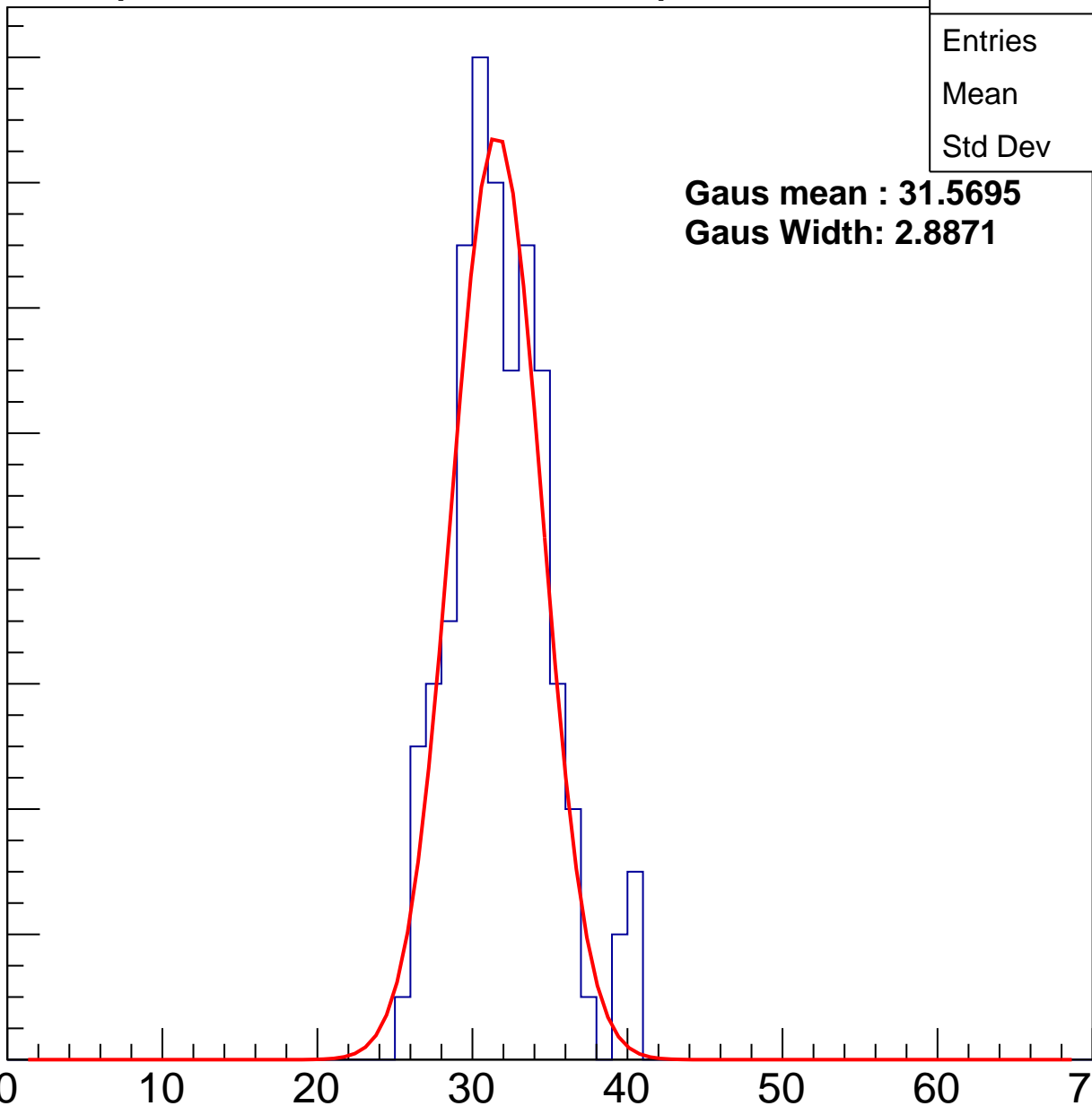
16
14
12
10
8
6
4
2
0

Entries	113
Mean	31.4
Std Dev	3.169

Gaus mean : 31.5695

Gaus Width: 2.8871

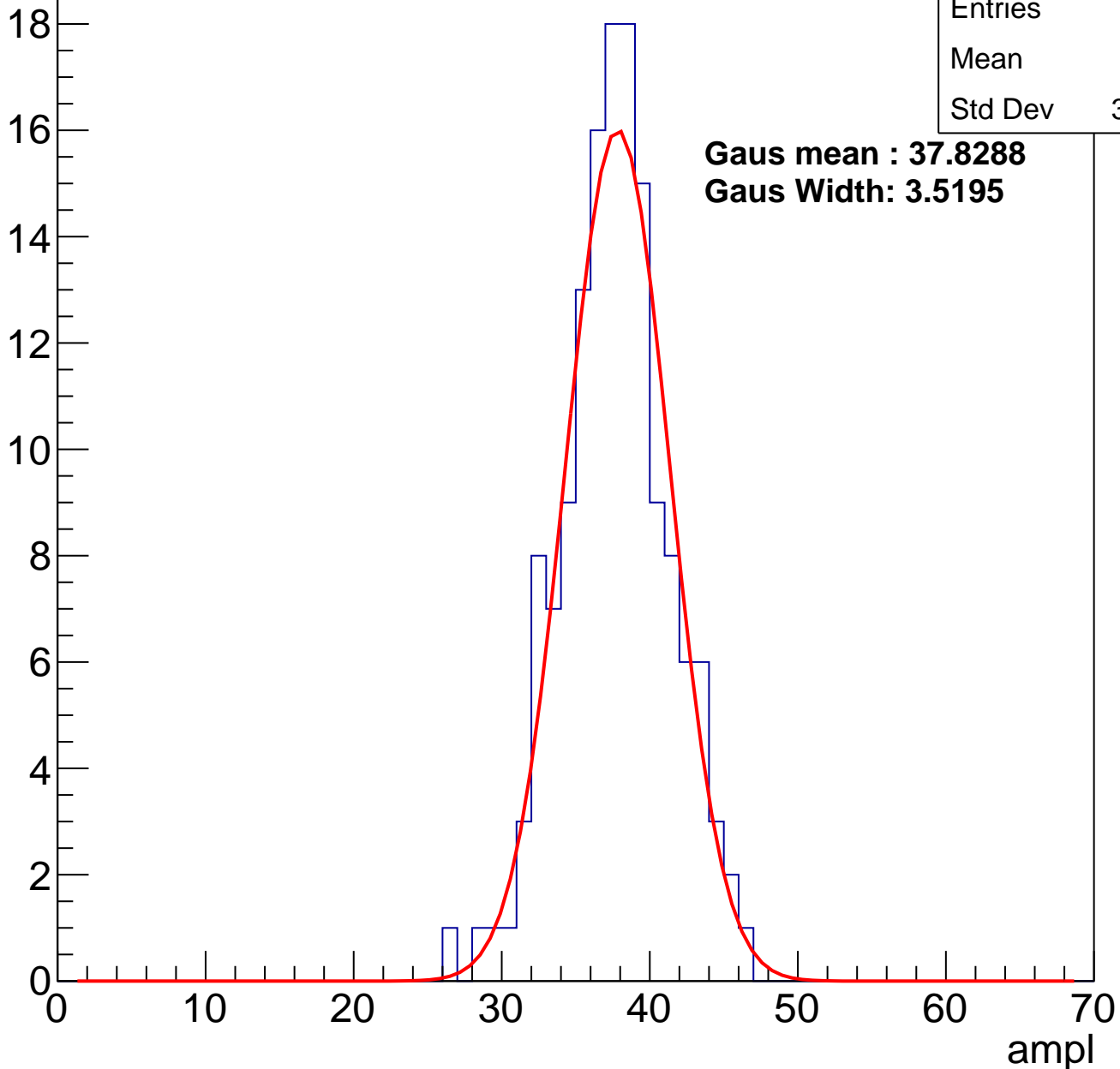
0 10 20 30 40 50 60 70
ampl



B1L001S, U22-ch28, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	43.91
Std Dev	3.013

Gaus mean : 44.3500

Gaus Width: 2.8794

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

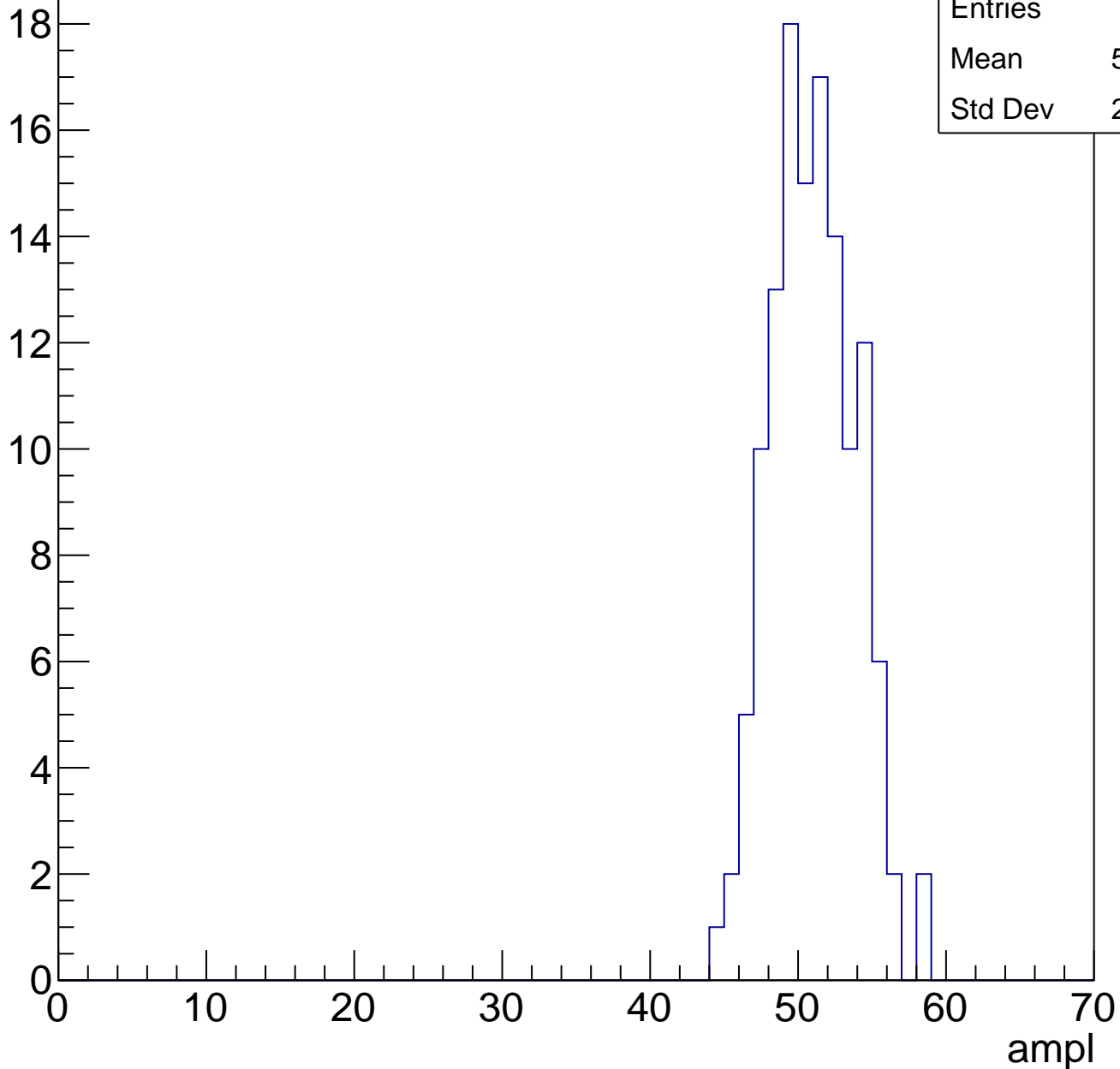
70

B1L001S, U22-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	50.56
Std Dev	2.794

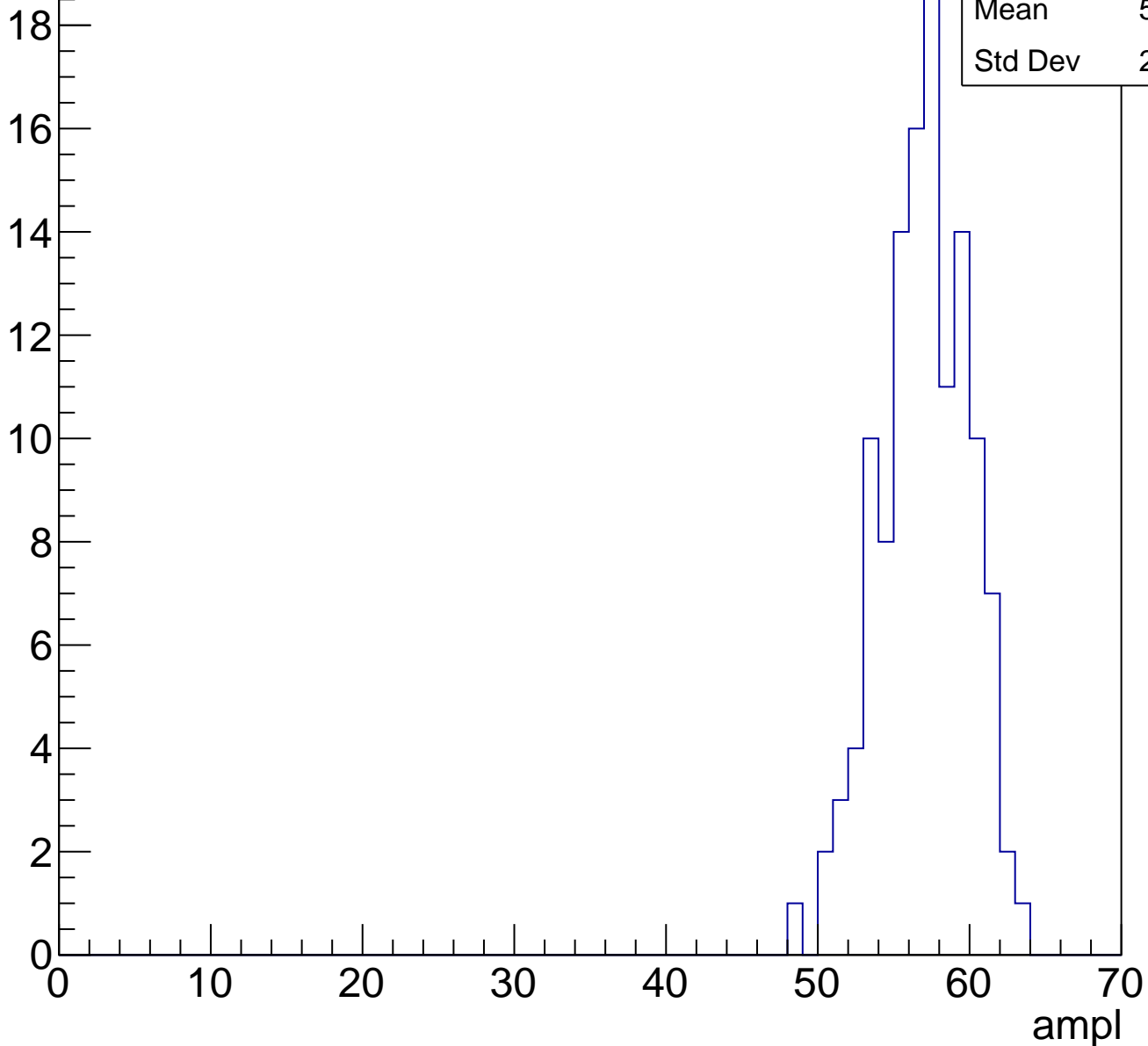
Entry



B1L001S, U22-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	122
Mean	56.54
Std Dev	2.892

B1L001S, U22-ch28, adc5

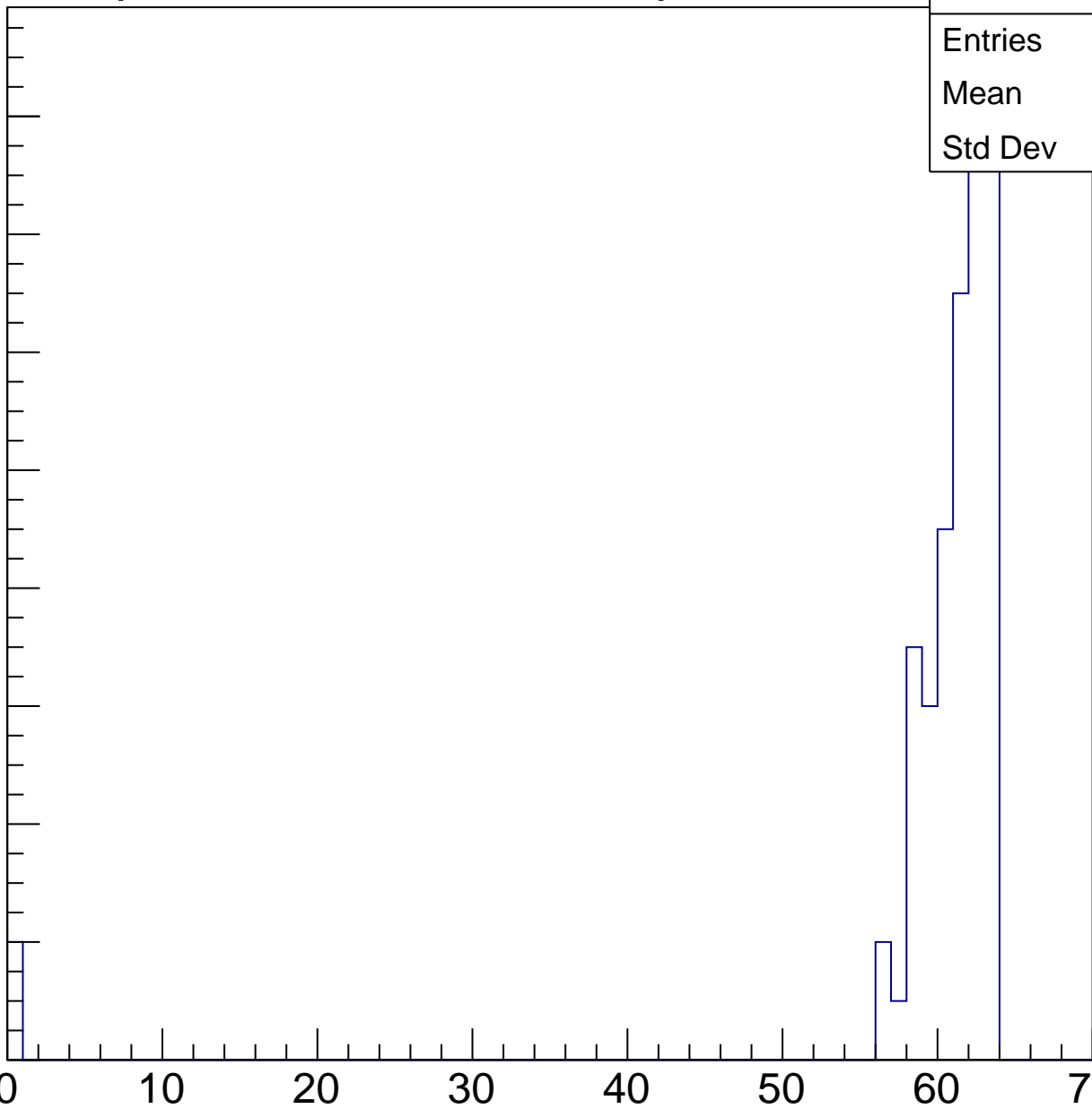
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.23
Std Dev	10.11

ampl



B1L001S, U22-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

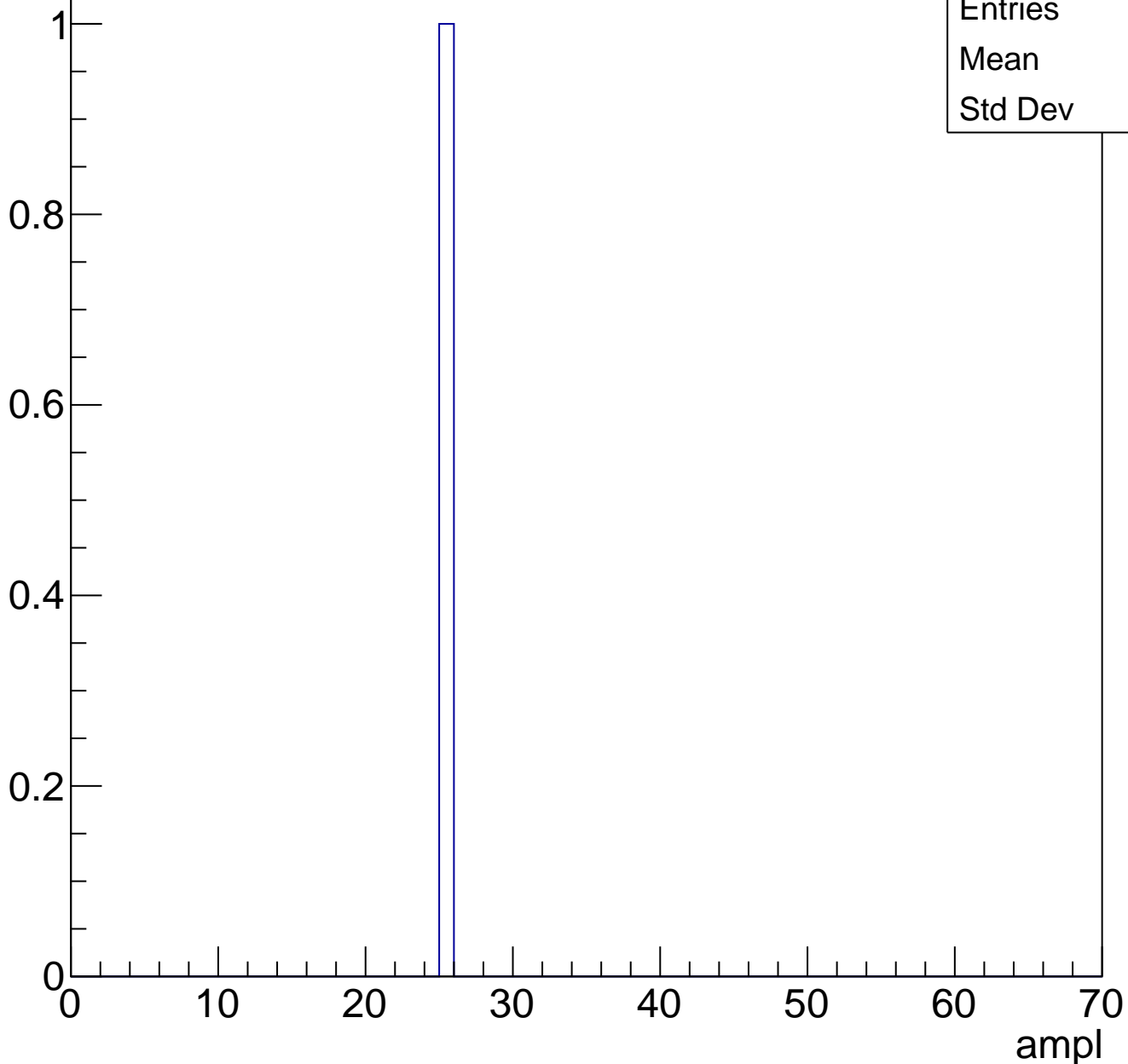
Entry



B1L001S, U22-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	25
Std Dev	0

B1L001S, U22-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.73
Std Dev	3.319

Gaus mean : 31.6276

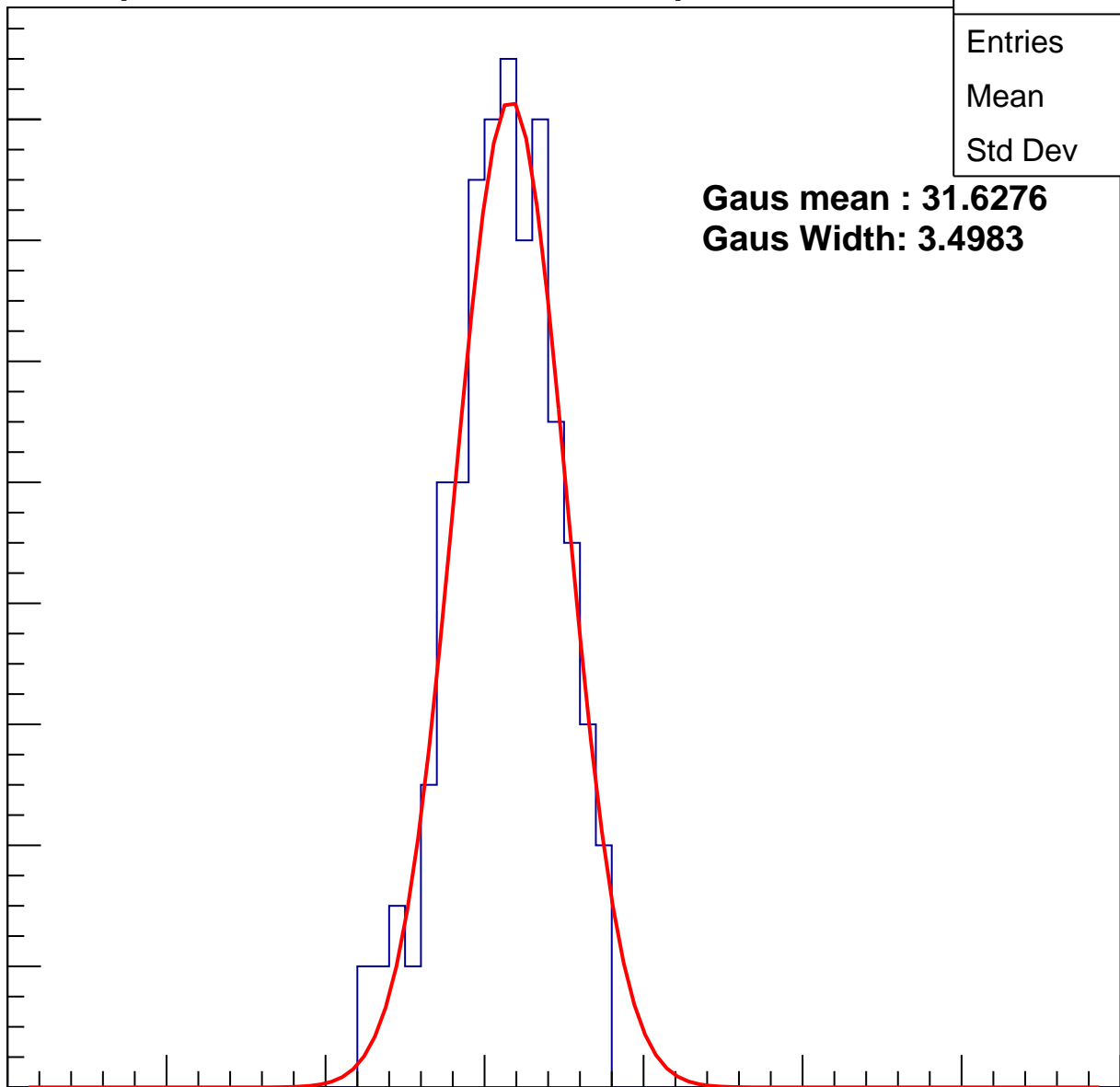
Gaus Width: 3.4983

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch29, adc1

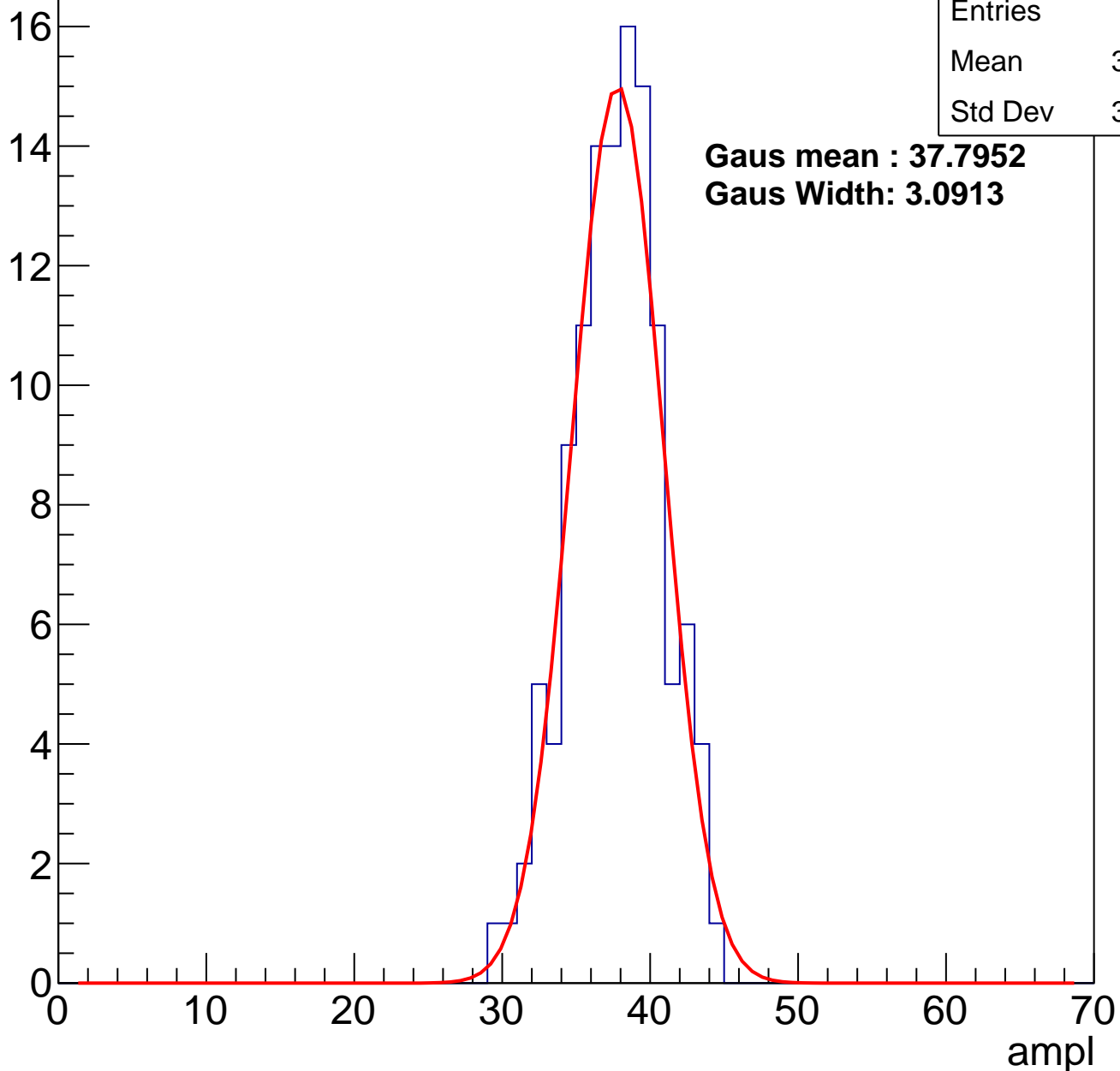
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.24
Std Dev	3.046

Gaus mean : 37.7952

Gaus Width: 3.0913

Entry



B1L001S, U22-ch29, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.75
Std Dev	3.077

Gaus mean : 44.1953

Gaus Width: 3.2841

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

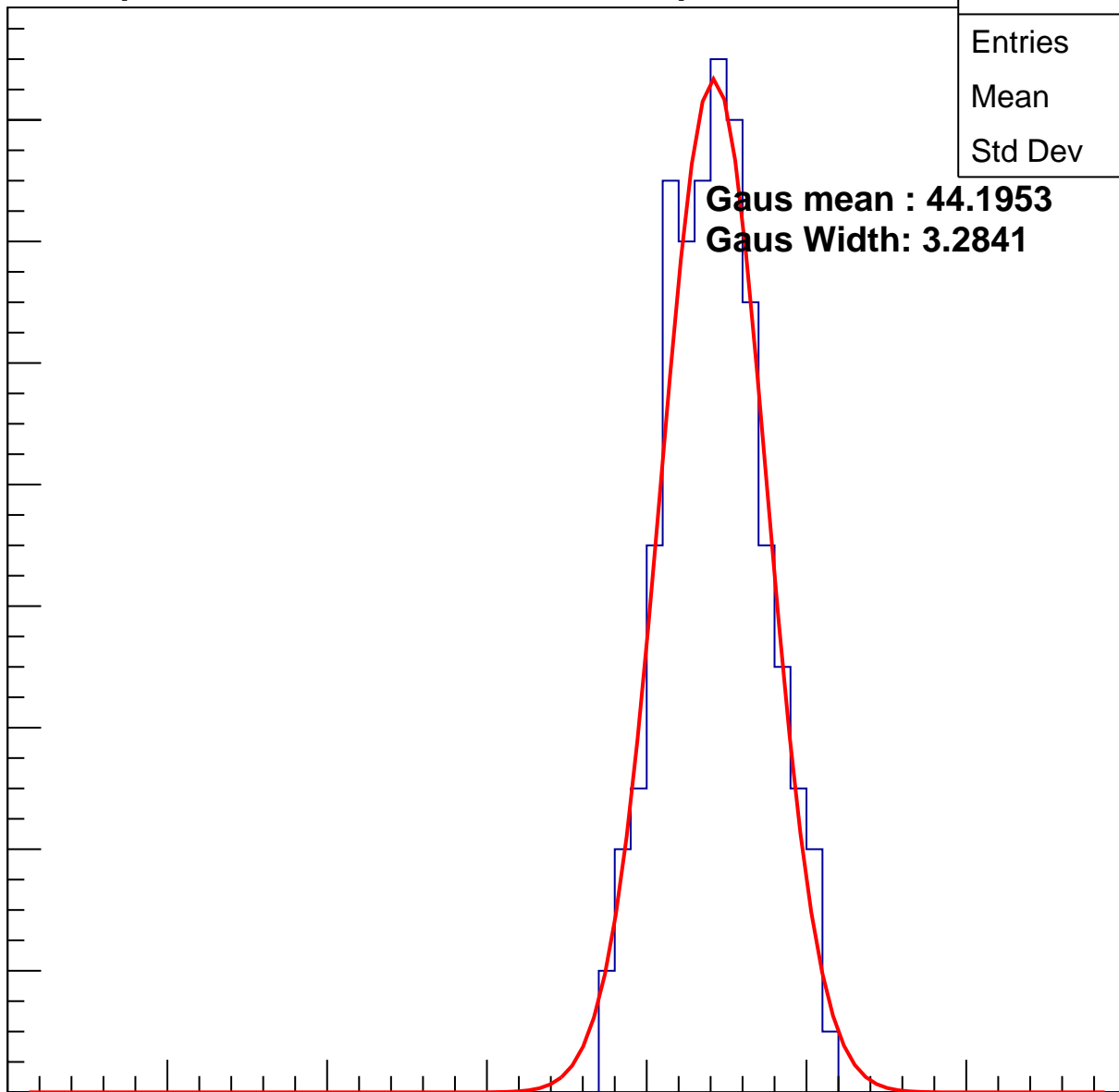
40

50

60

70

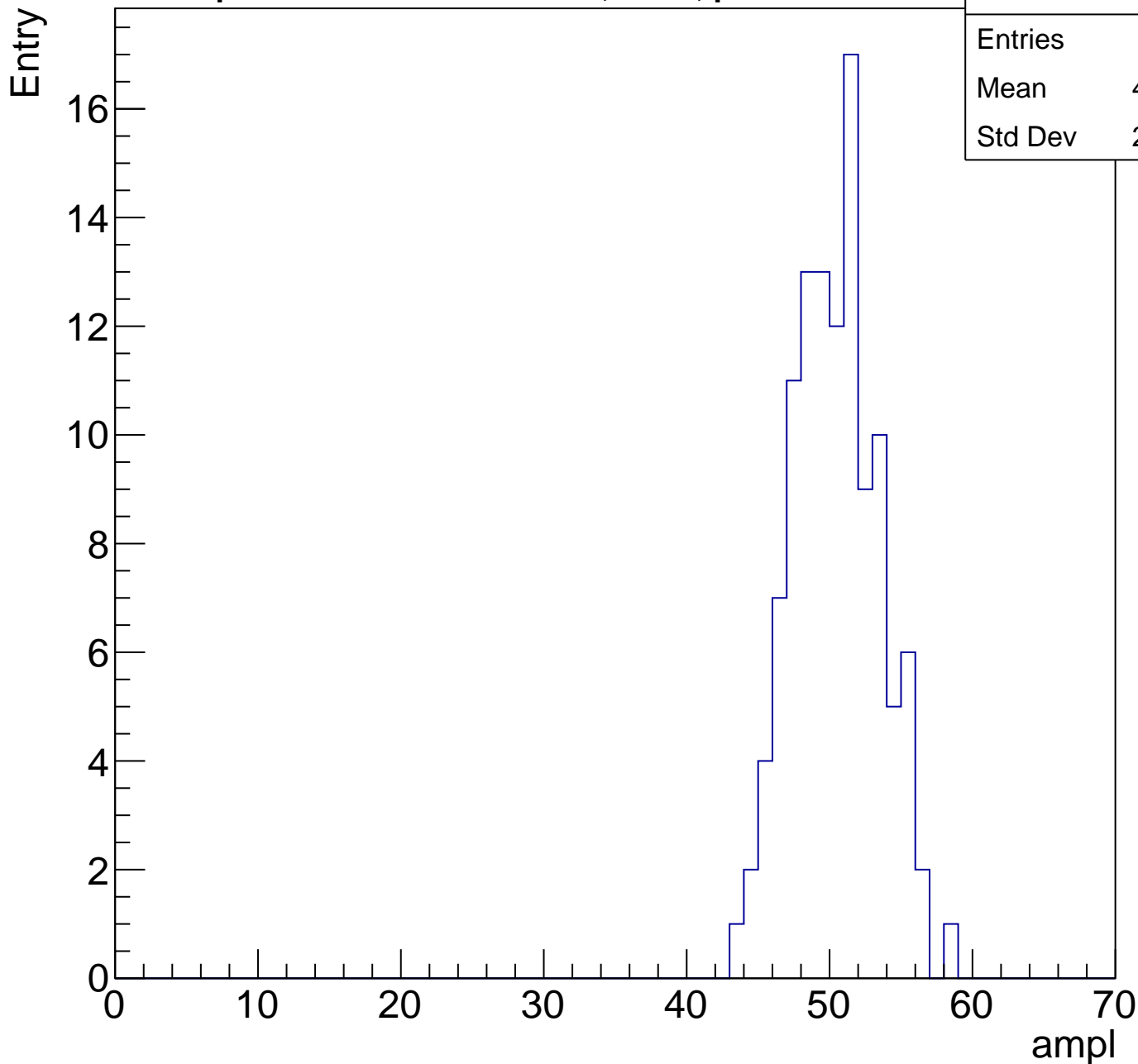
ampl



B1L001S, U22-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	49.96
Std Dev	2.984

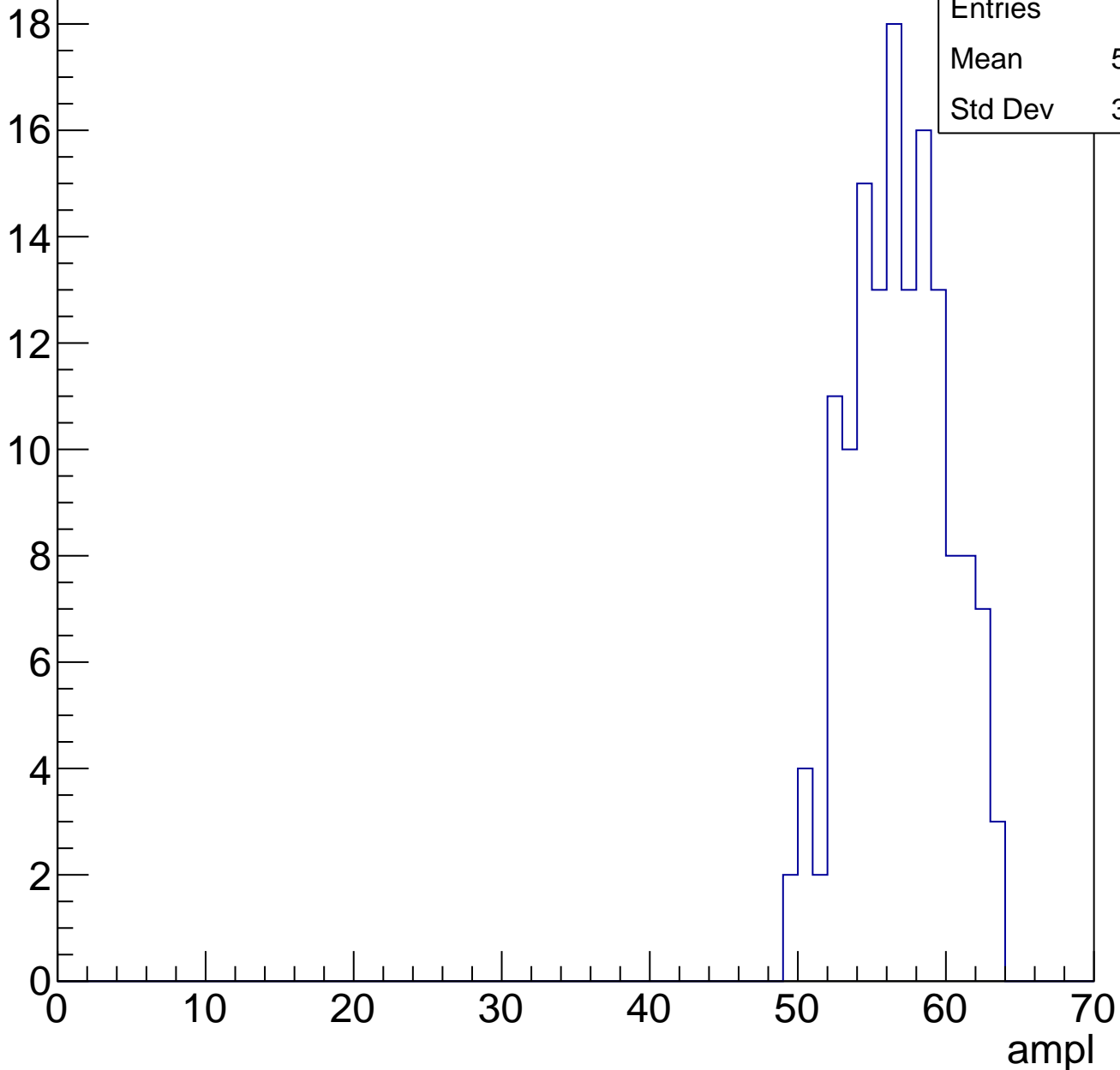


B1L001S, U22-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	56.38
Std Dev	3.273

Entry



B1L001S, U22-ch29, adc5

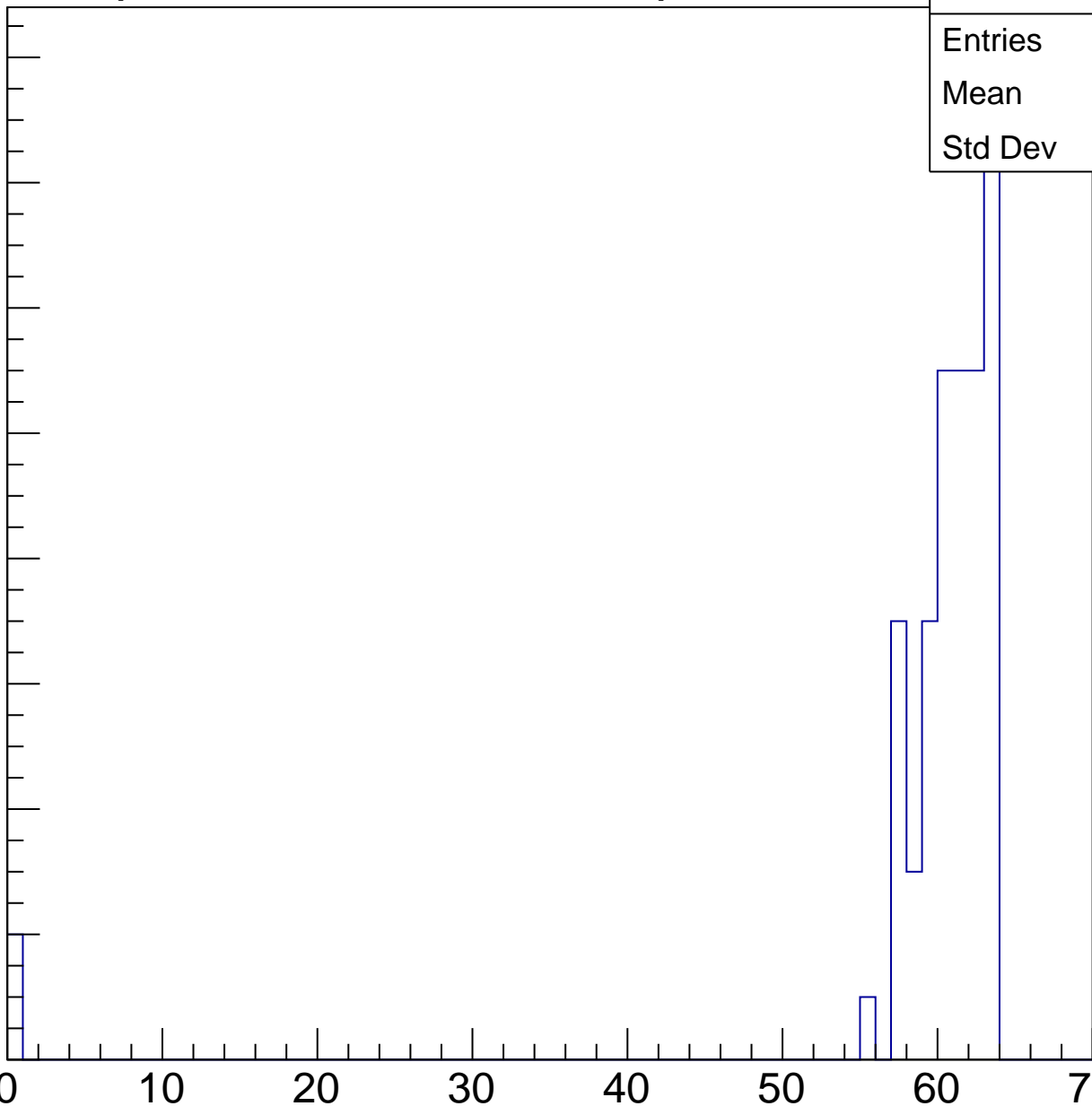
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.87
Std Dev	10.37

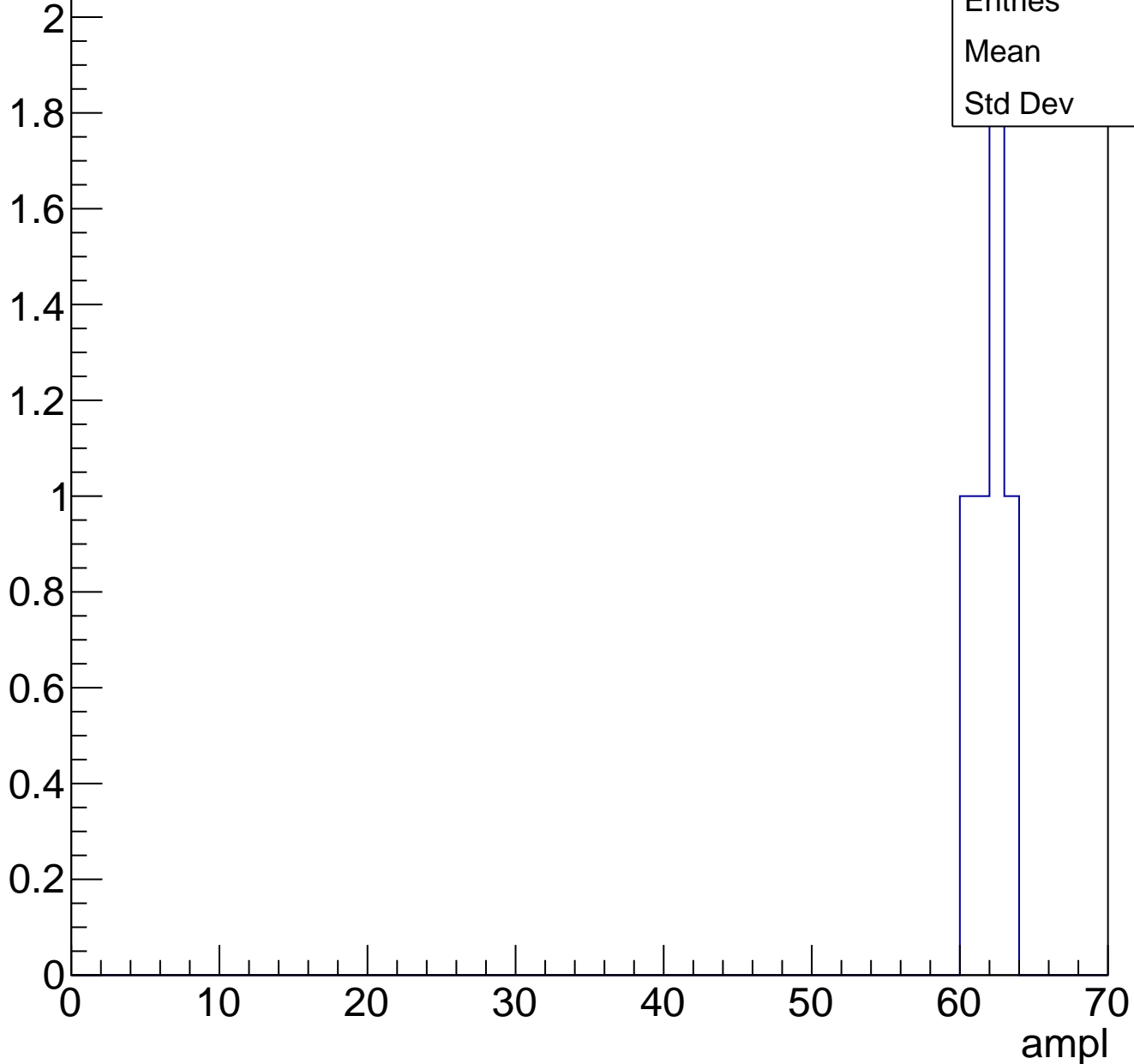
ampl



B1L001S, U22-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B1L001S, U22-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L001S, U22-ch30, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	28.6
Std Dev	3.936

Gaus mean : 29.2466

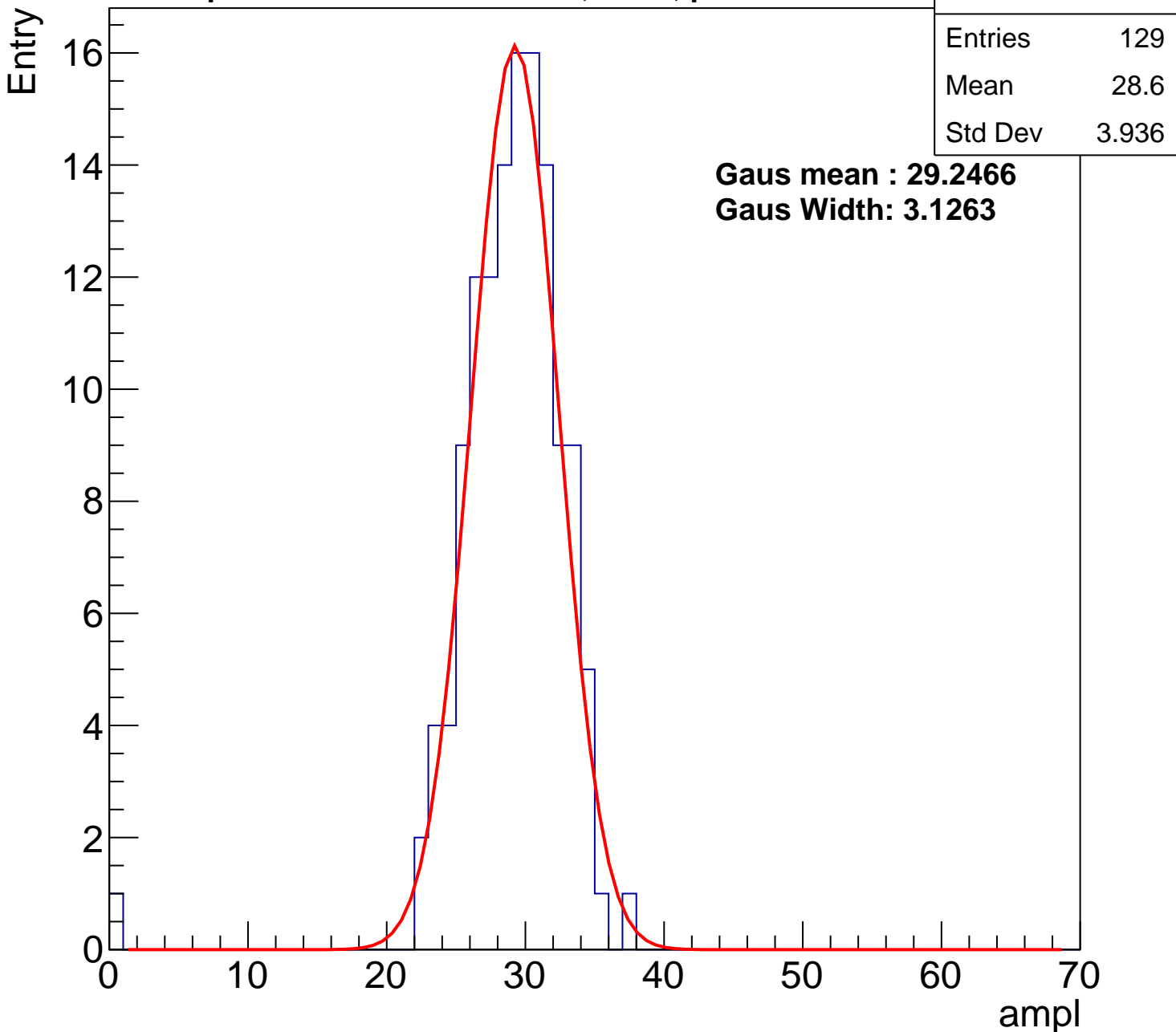
Gaus Width: 3.1263

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch30, adc1

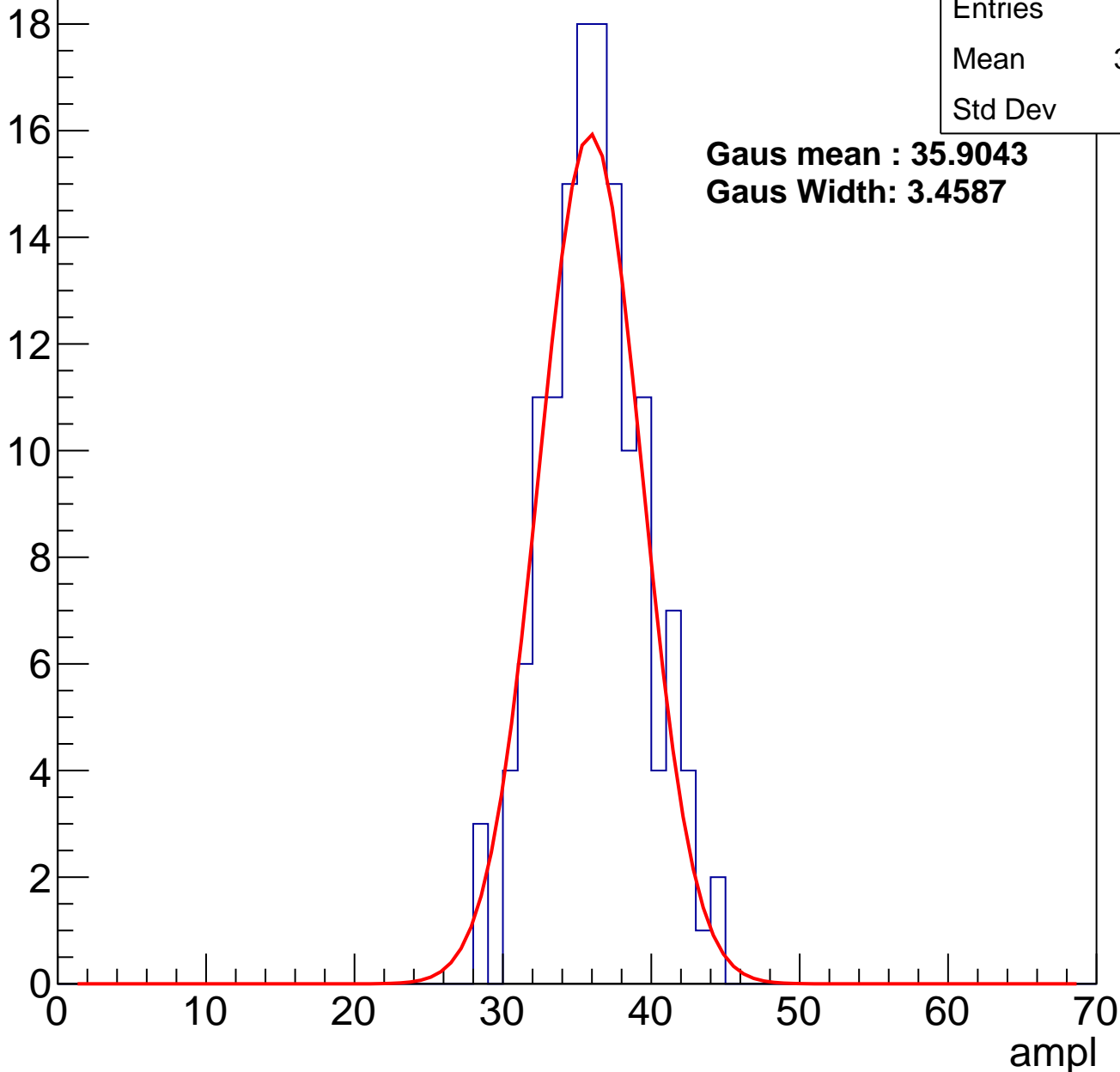
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.74
Std Dev	3.32

Gaus mean : 35.9043

Gaus Width: 3.4587

Entry



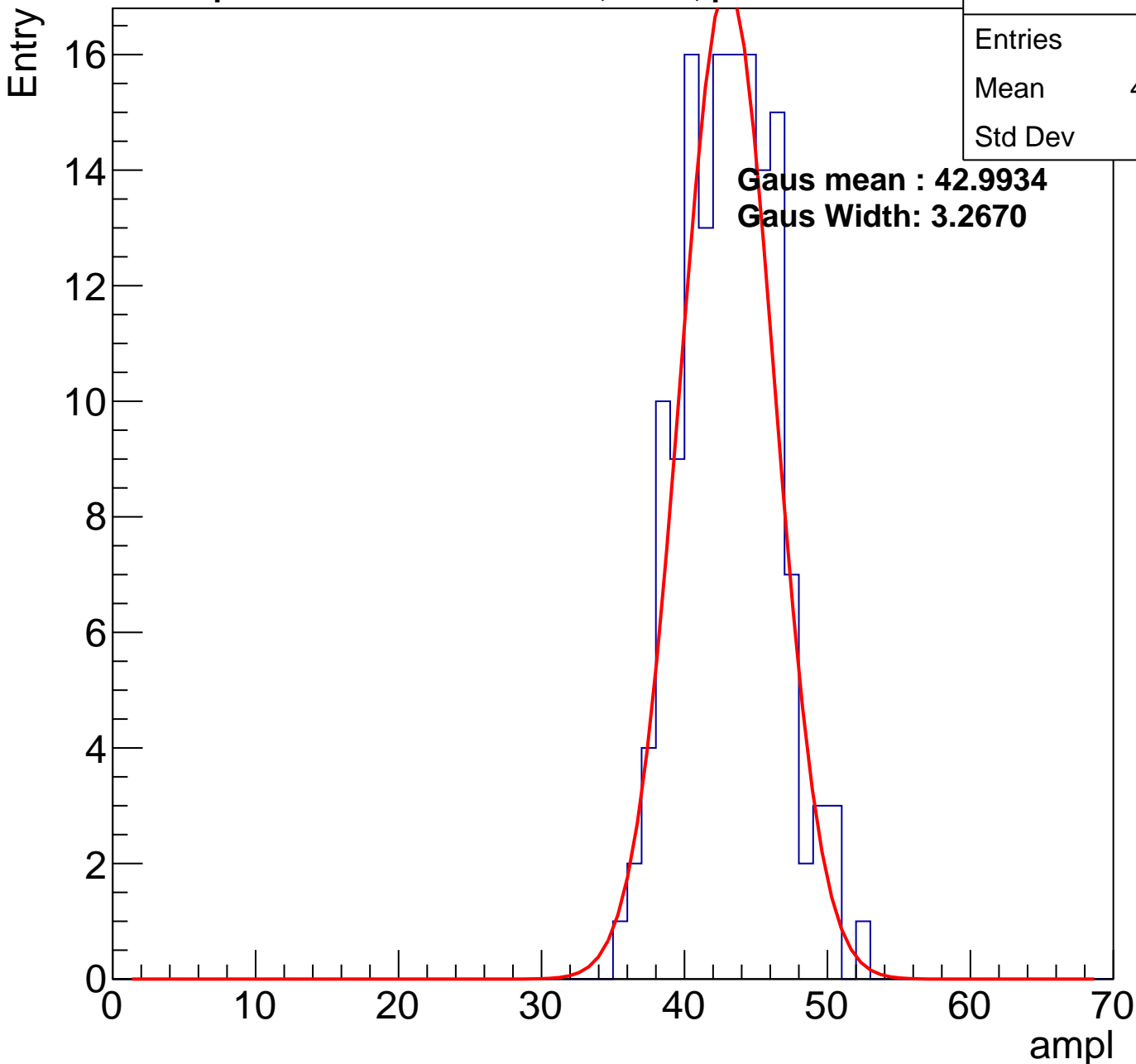
B1L001S, U22-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	42.68
Std Dev	3.29

Gaus mean : 42.9934

Gaus Width: 3.2670



B1L001S, U22-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

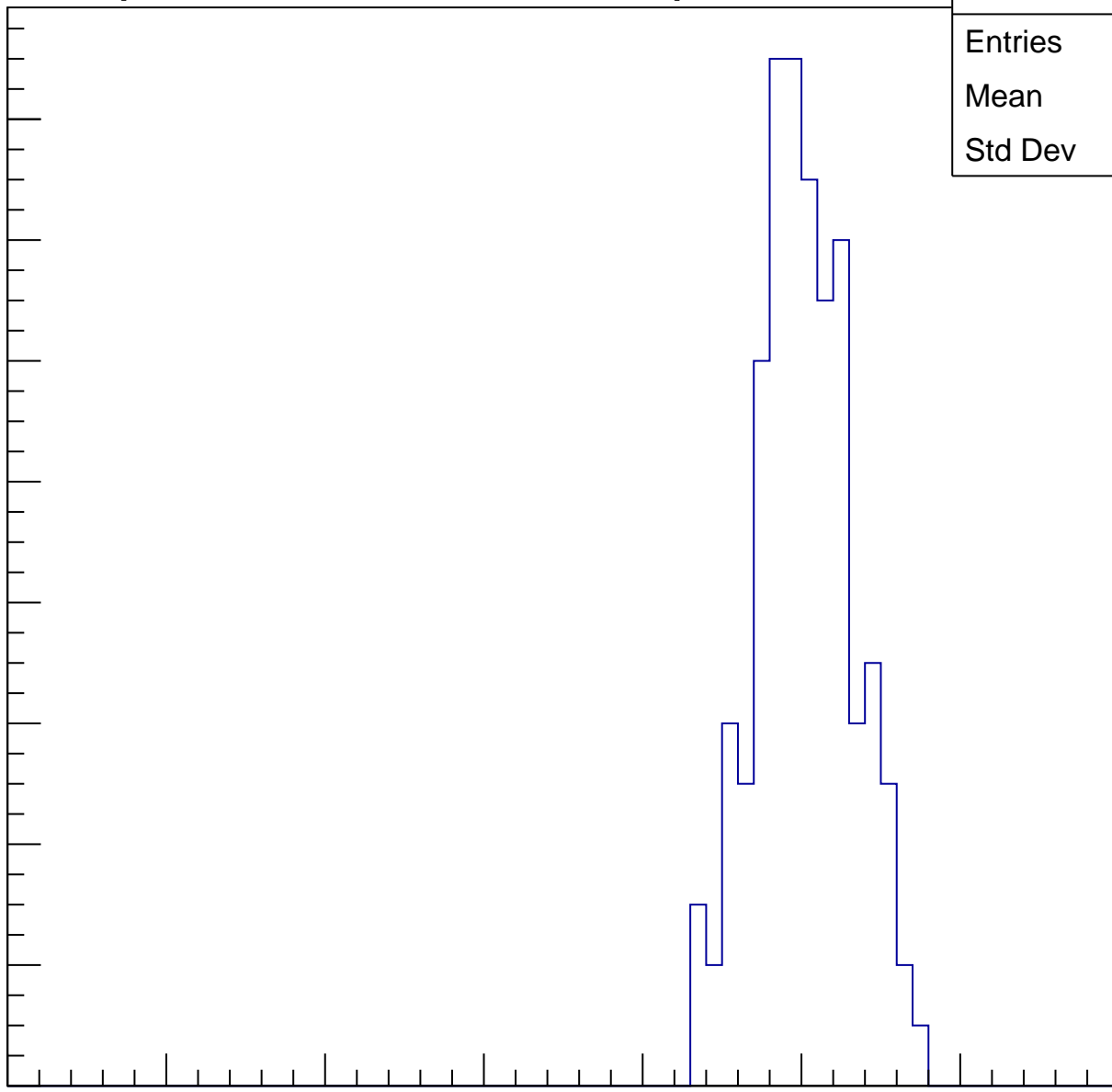
Entries	125
Mean	49.69
Std Dev	2.981

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

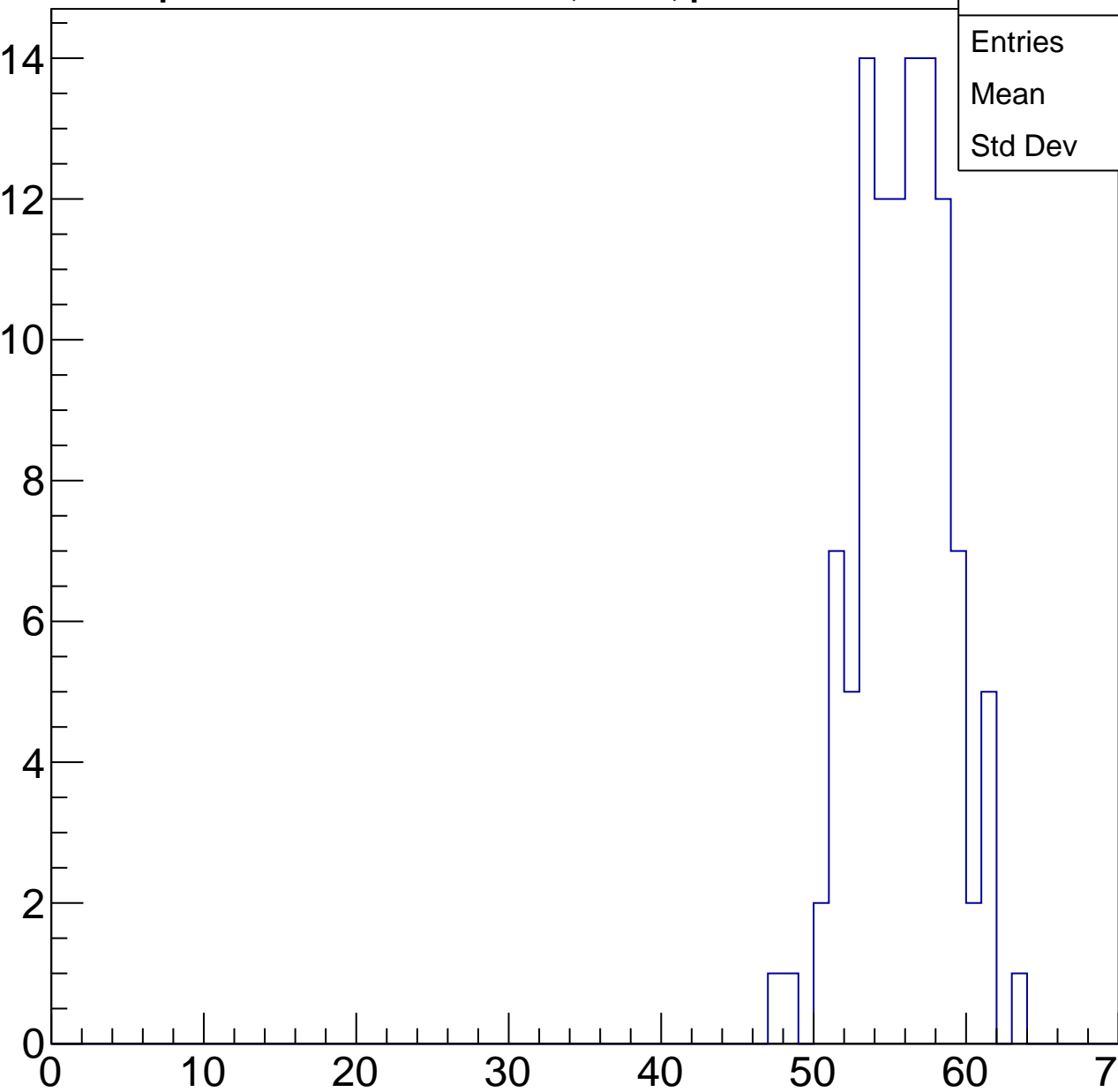
Entries	109
Mean	55.42
Std Dev	2.956

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch30, adc5

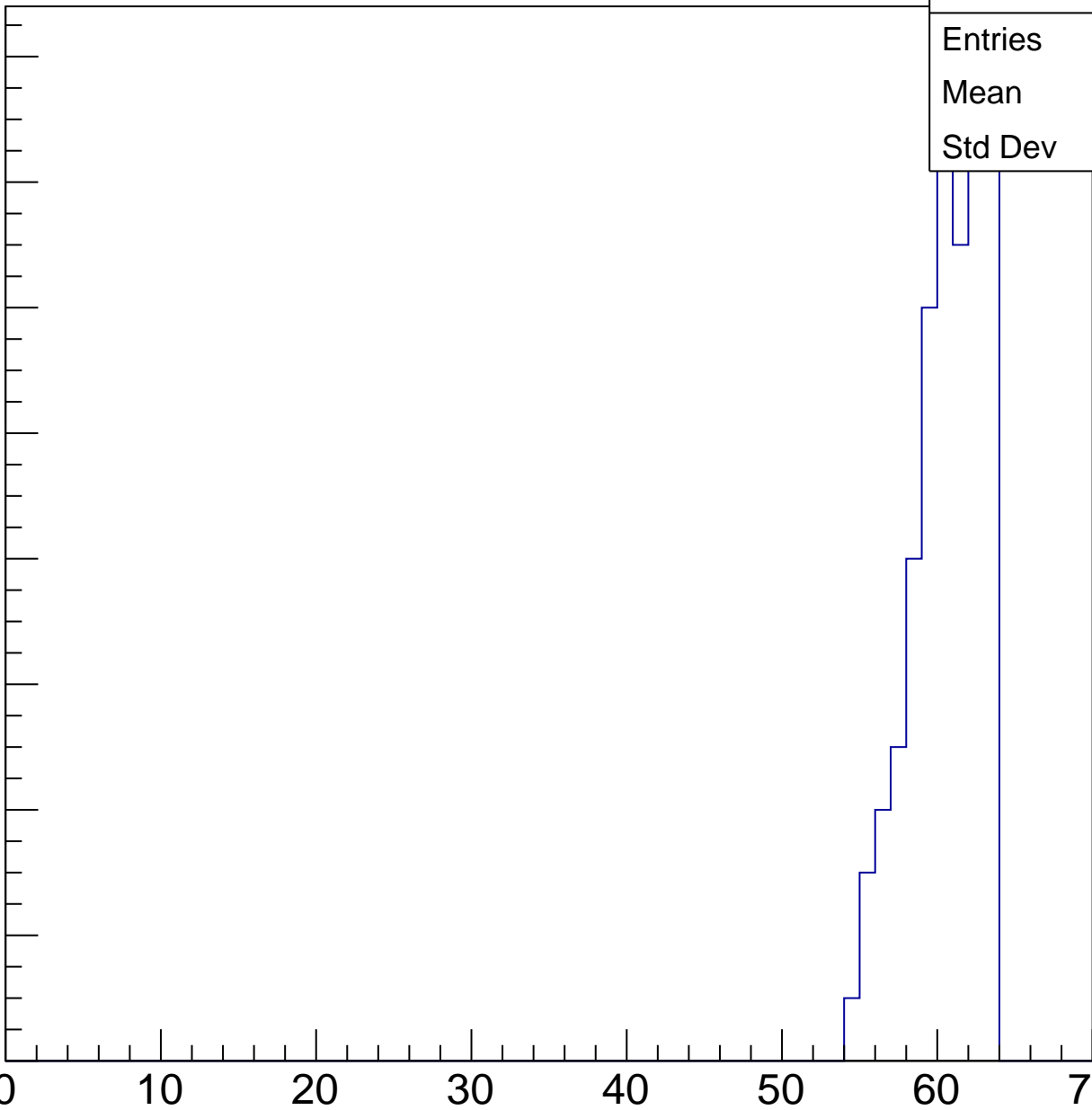
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	60.11
Std Dev	2.26

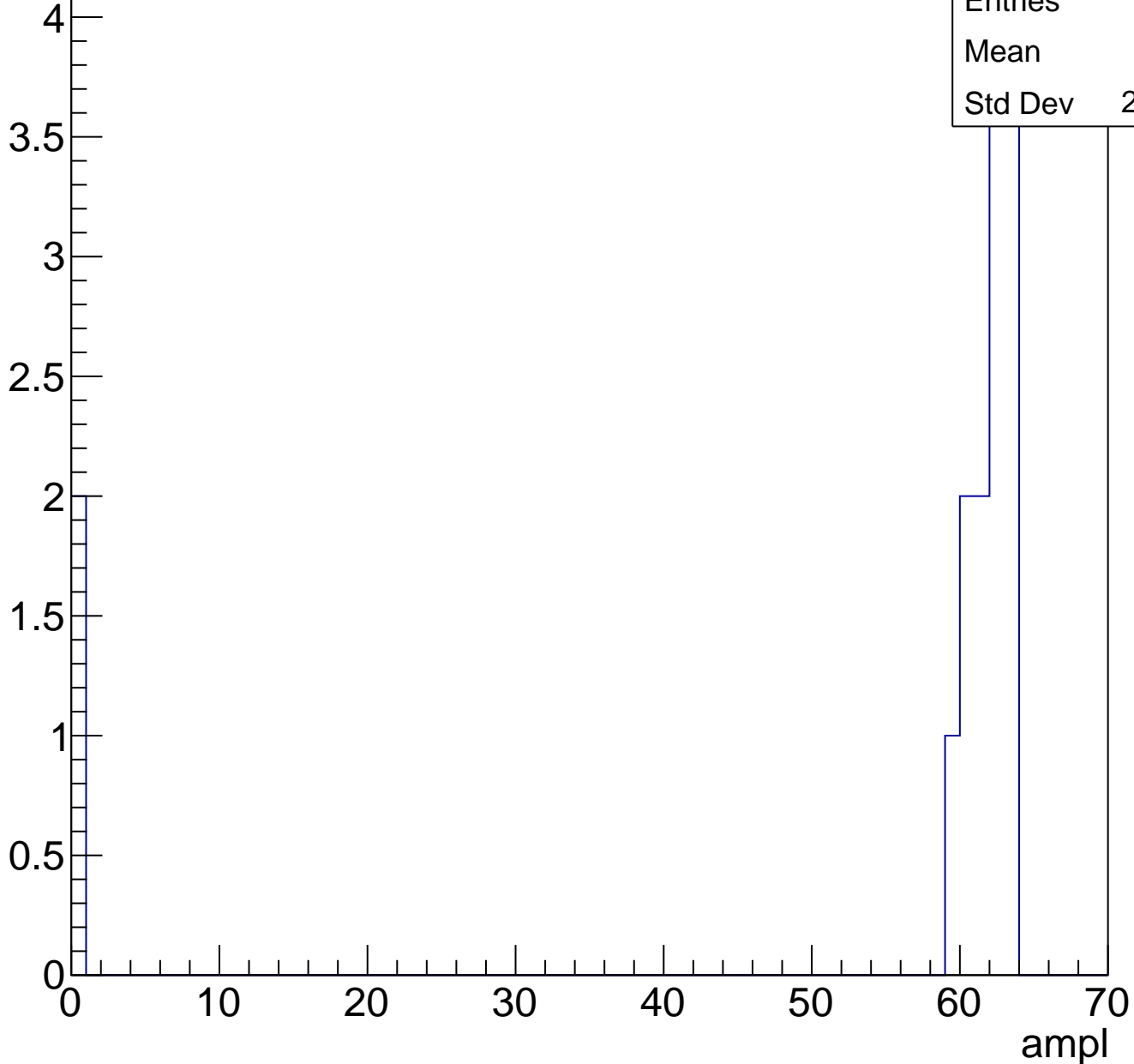
ampl



B1L001S, U22-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

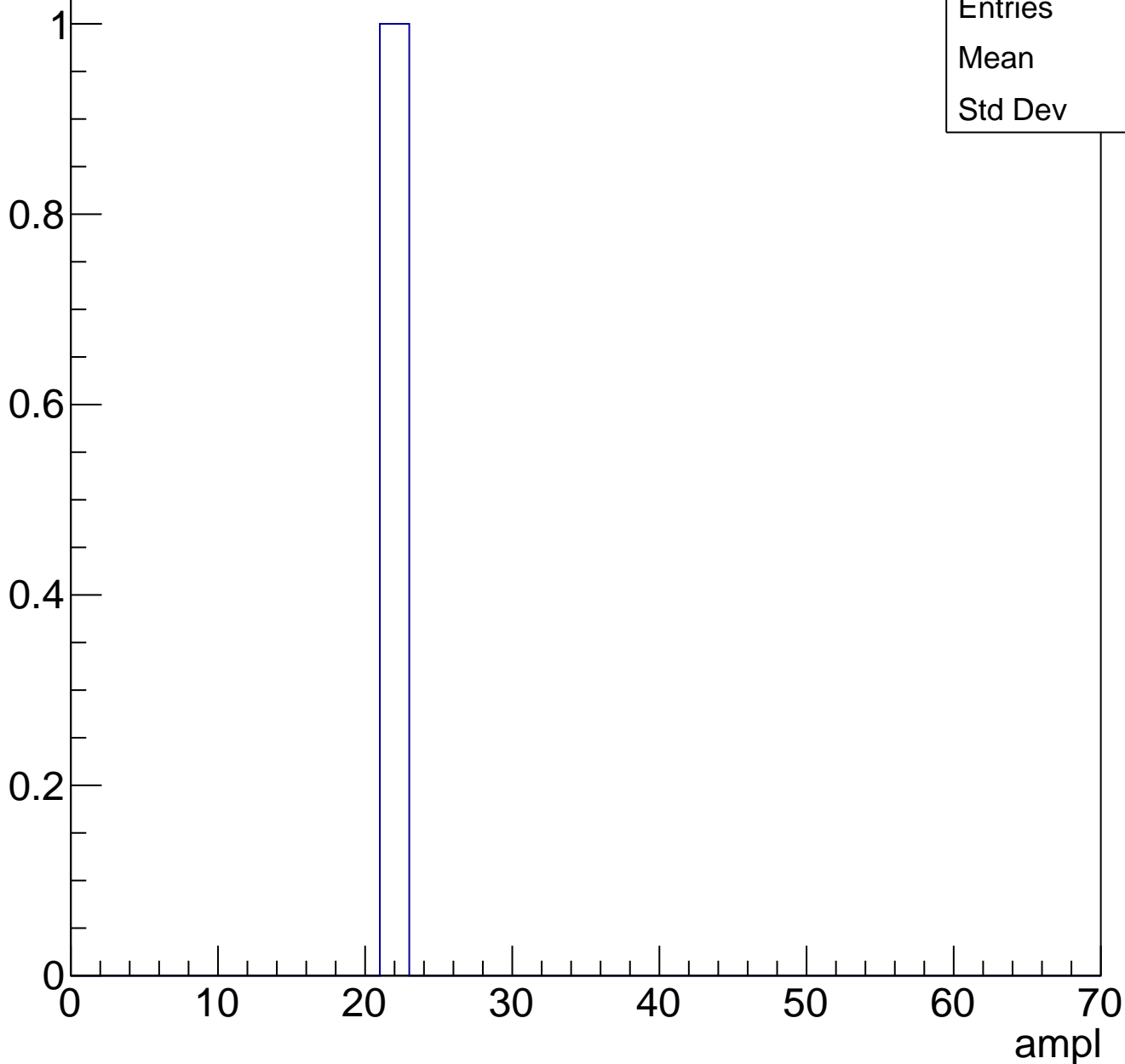
Entry



B1L001S, U22-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



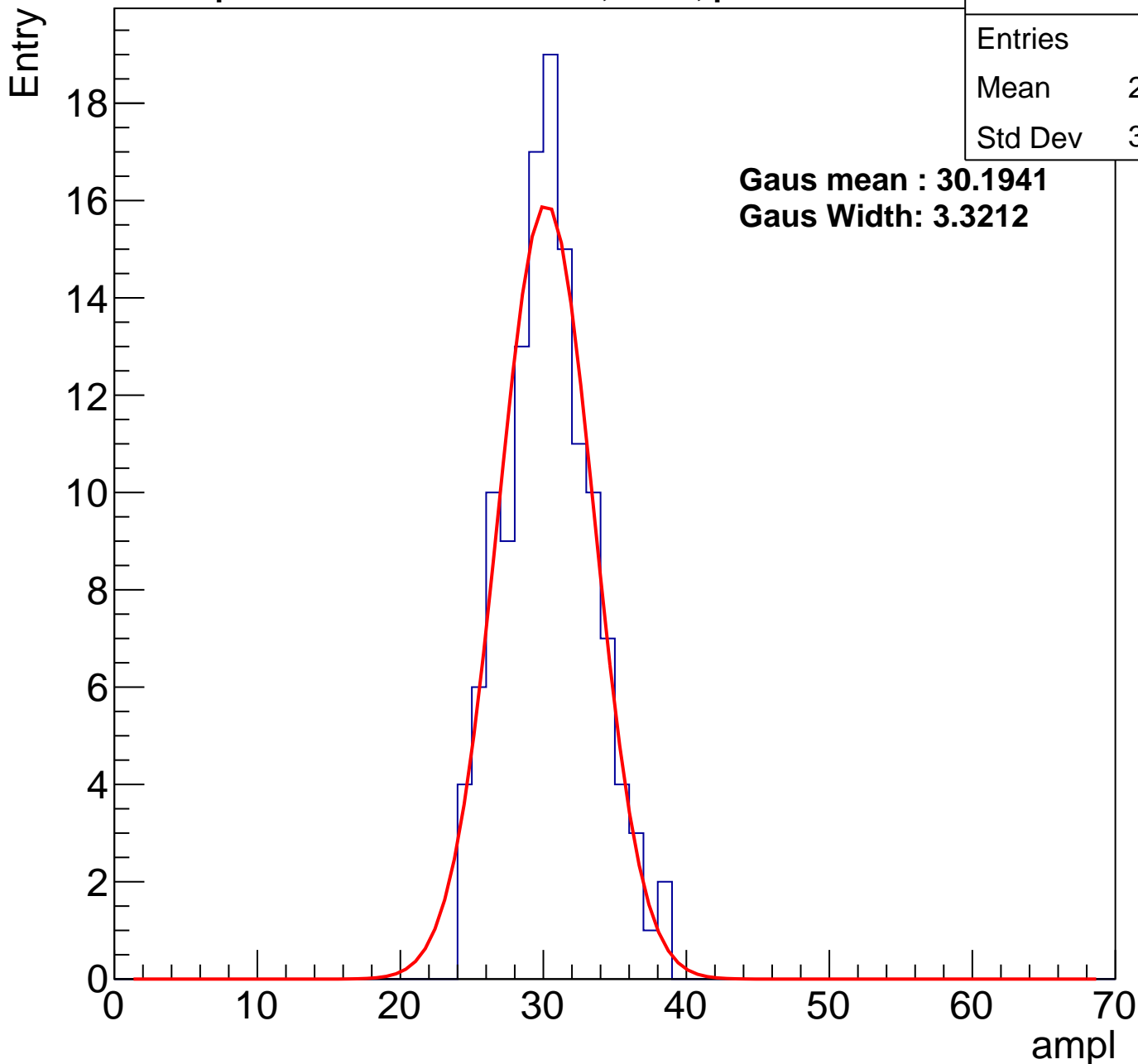
B1L001S, U22-ch31, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	29.94
Std Dev	3.088

Gaus mean : 30.1941

Gaus Width: 3.3212

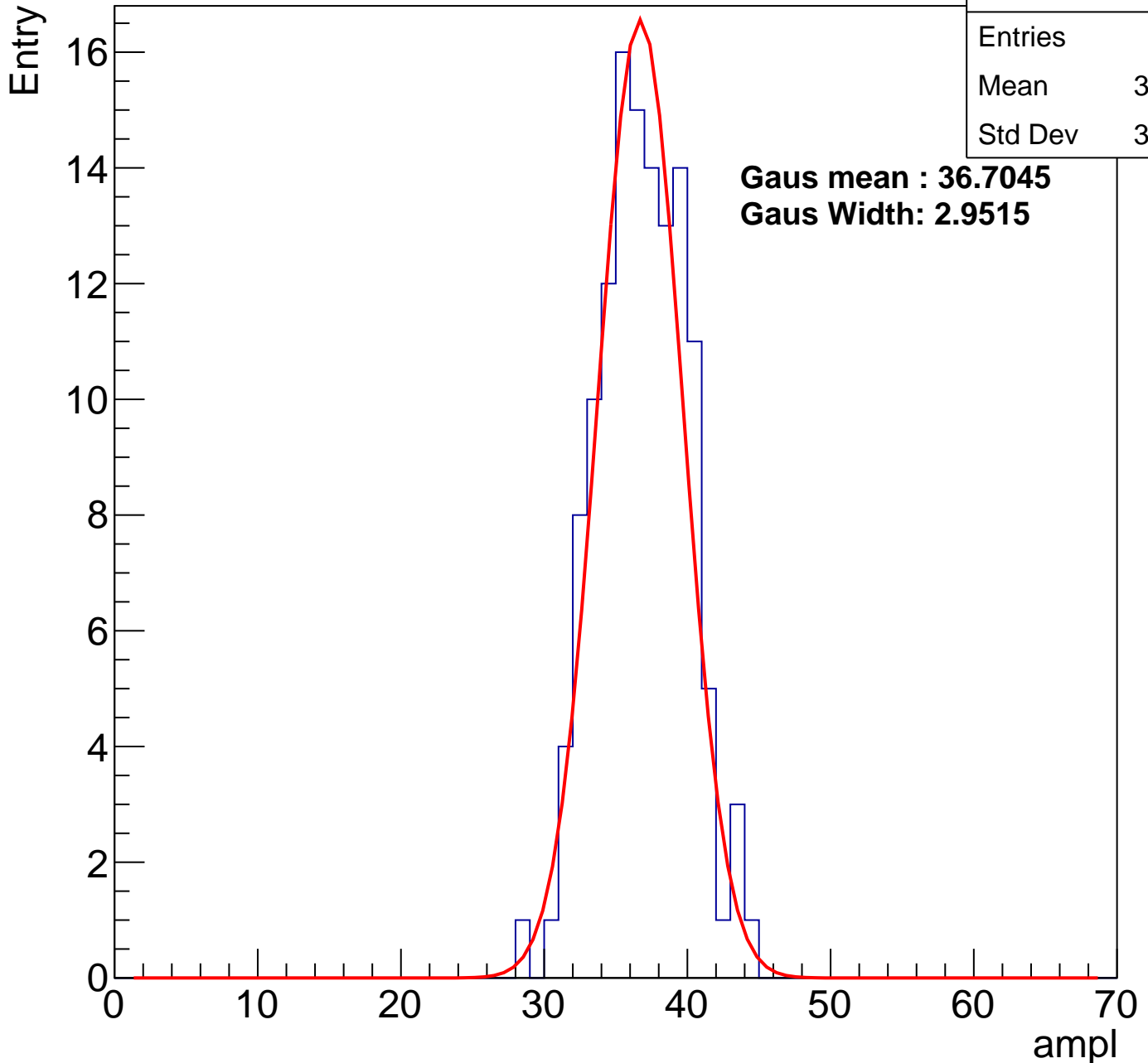


B1L001S, U22-ch31, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.39
Std Dev	3.053

Gaus mean : 36.7045
Gaus Width: 2.9515



B1L001S, U22-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	43.04
Std Dev	3.293

Entry

Gaus mean : 43.5813

Gaus Width: 3.4380

16
14
12
10
8
6
4
2
0

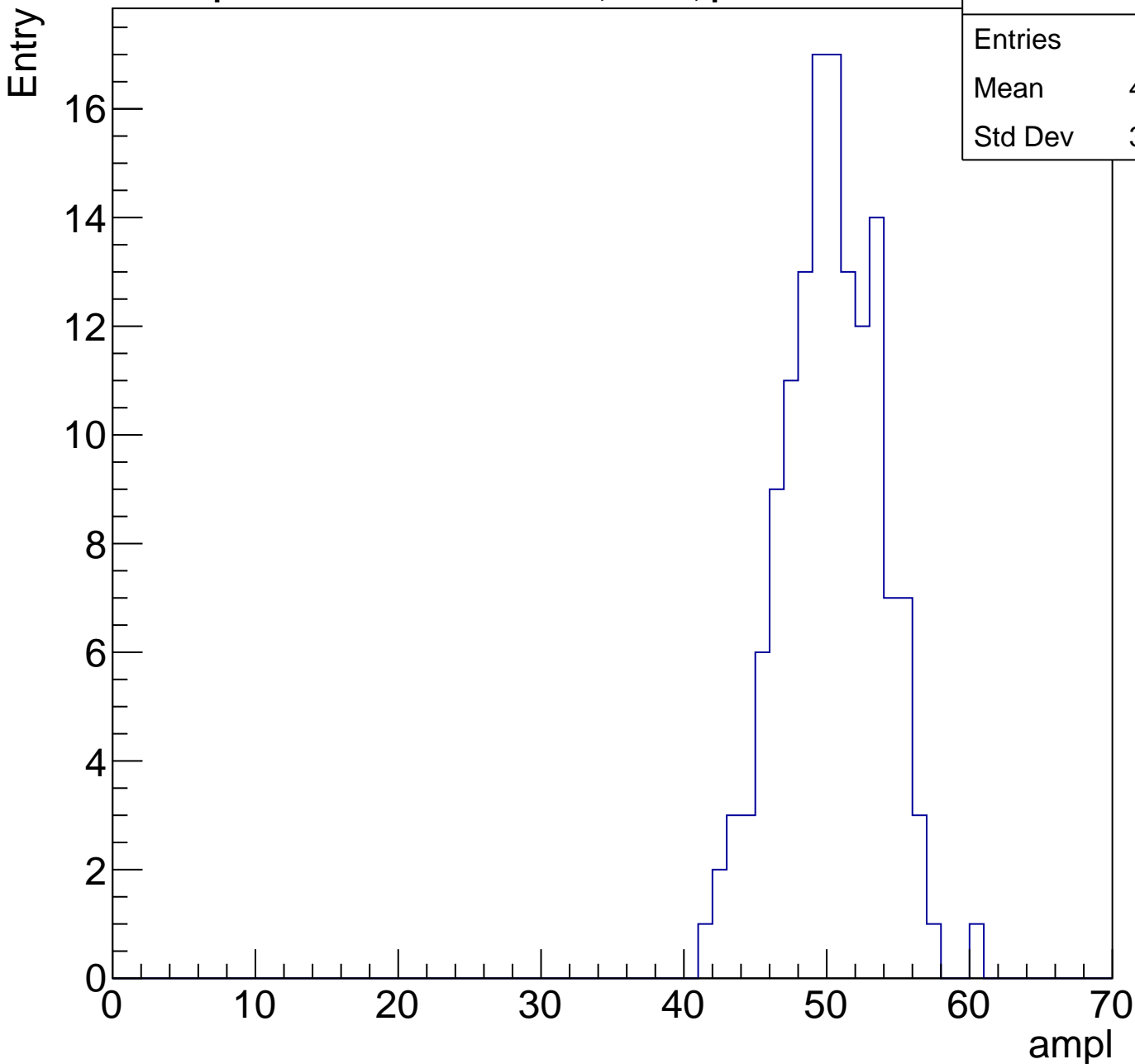
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	49.79
Std Dev	3.434



B1L001S, U22-ch31, adc4

calib_packv5_042523_0143.root, FC#2, port C2

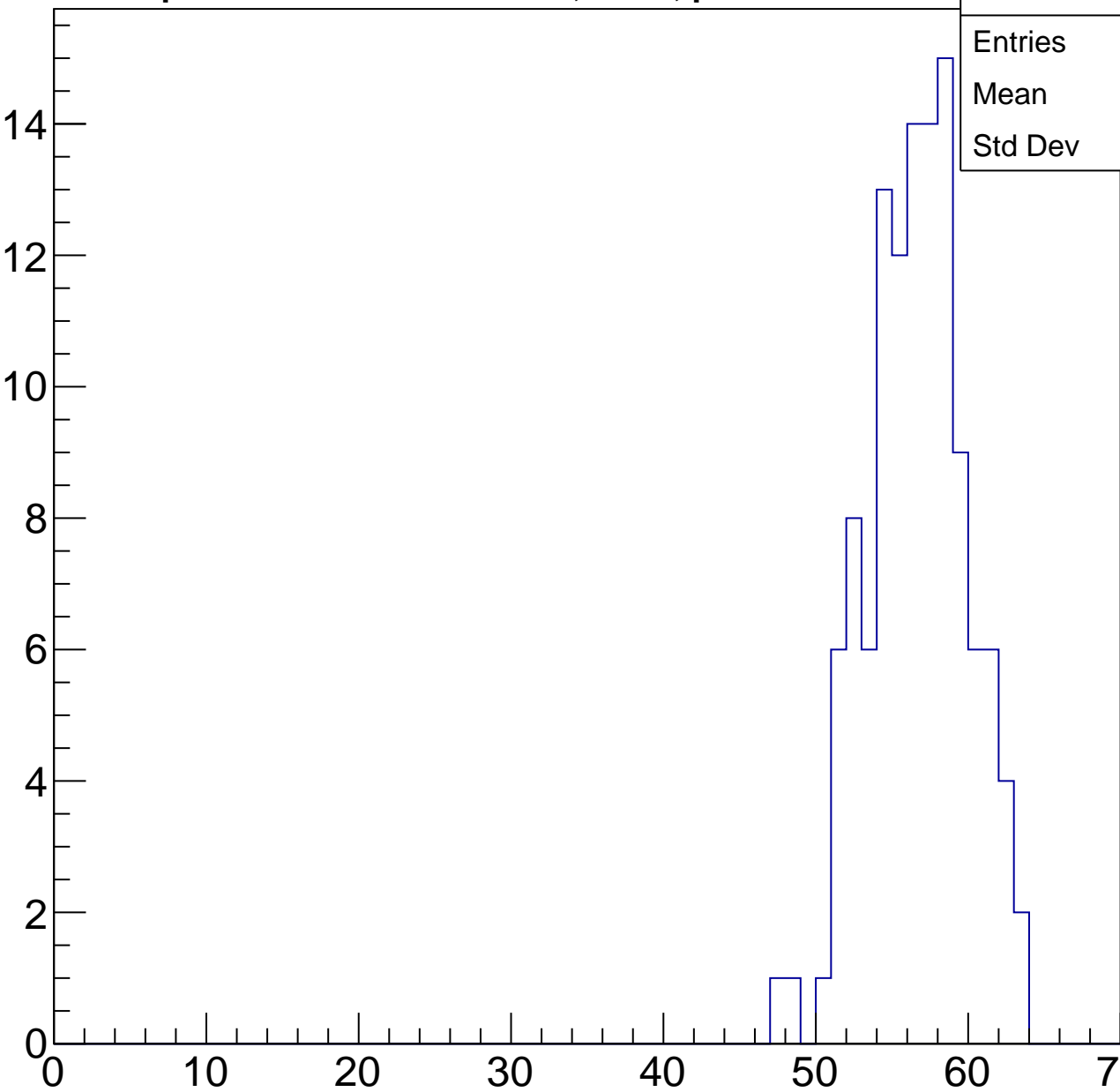
Entries	118
Mean	56.19
Std Dev	3.21

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch31, adc5

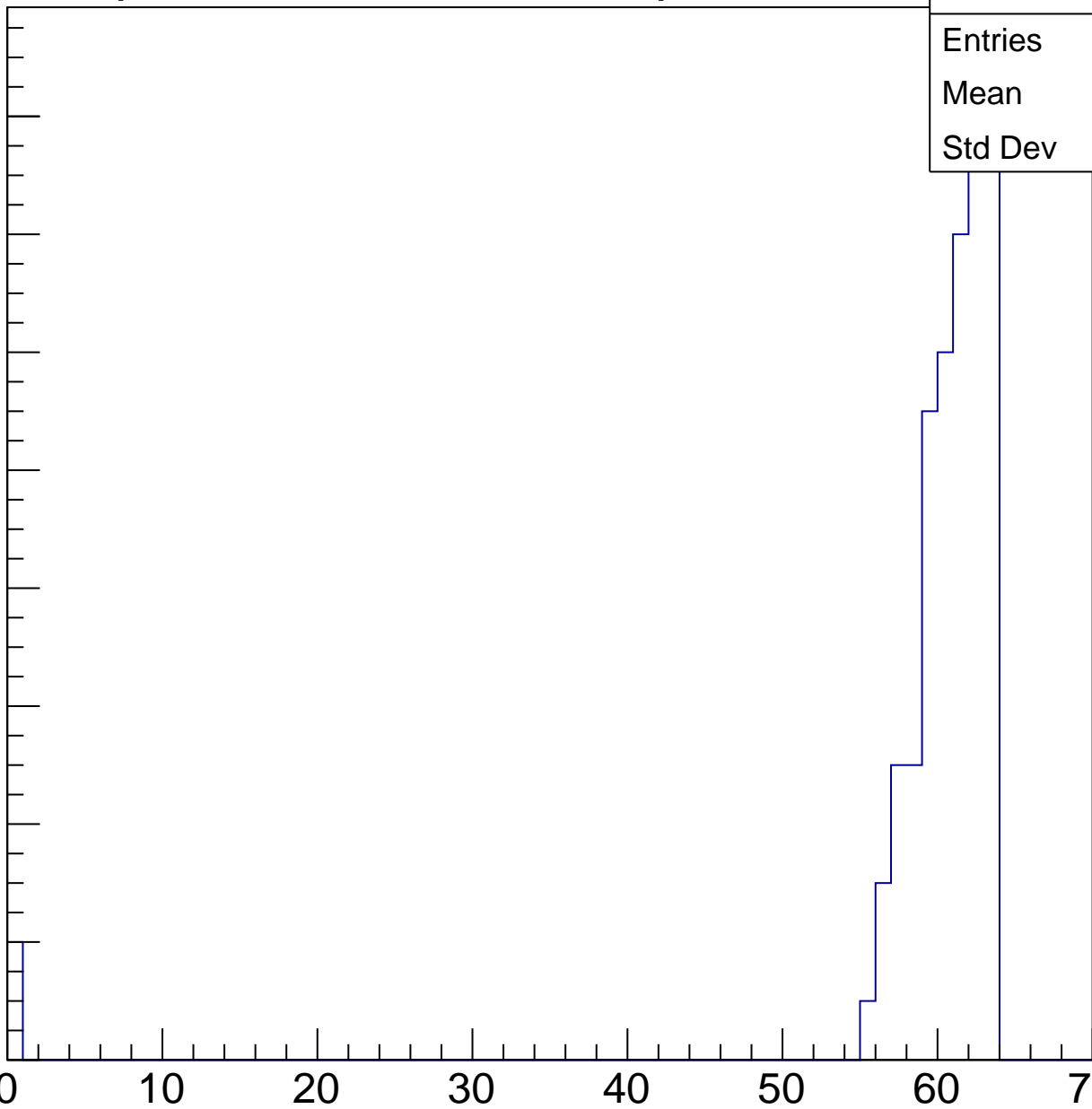
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.12
Std Dev	9.347

ampl



B1L001S, U22-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

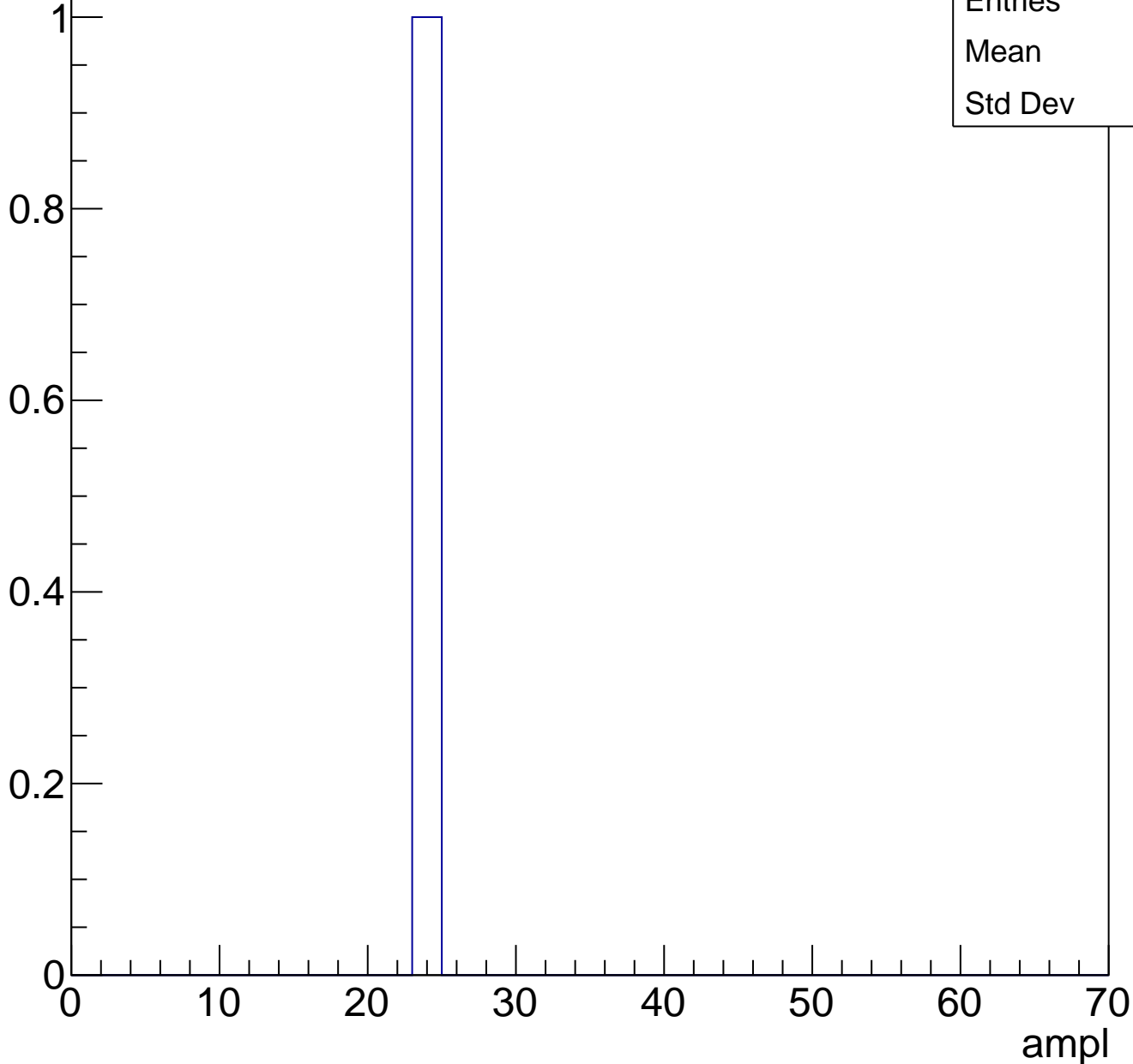
Entry



B1L001S, U22-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch32, adc0

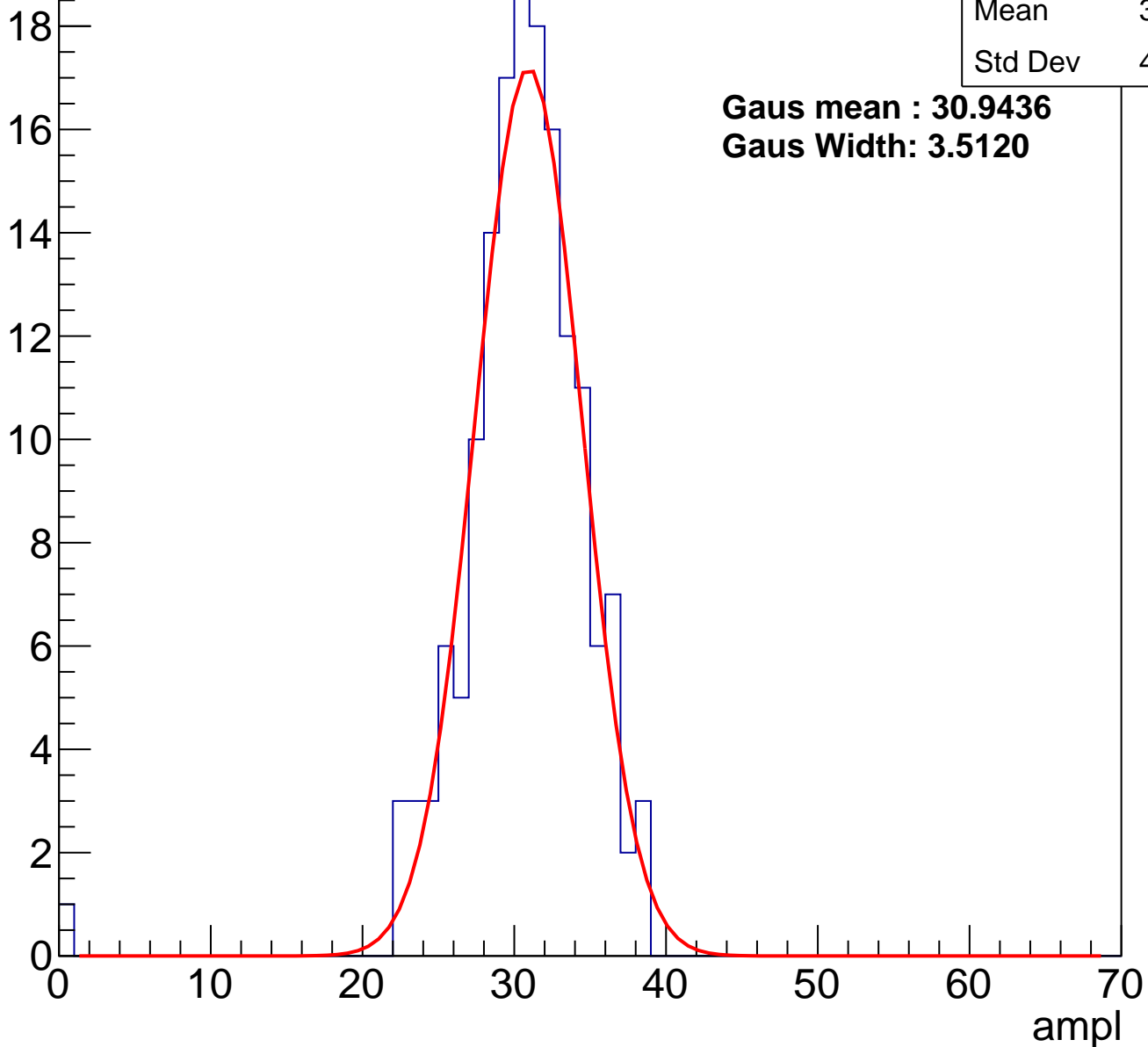
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	30.14
Std Dev	4.233

Gaus mean : 30.9436

Gaus Width: 3.5120

Entry



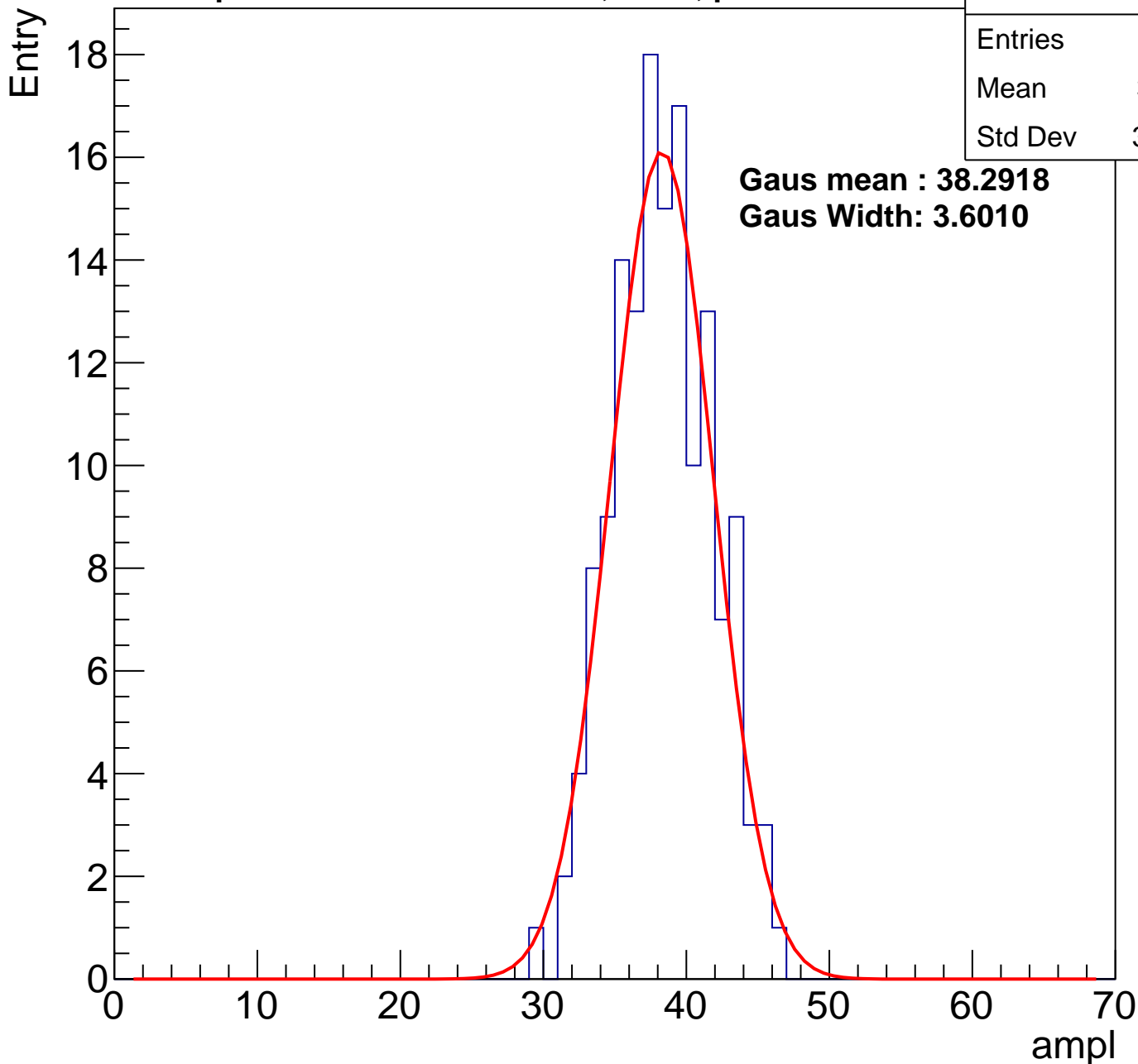
B1L001S, U22-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.91
Std Dev	3.393

Gaus mean : 38.2918

Gaus Width: 3.6010



B1L001S, U22-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

115

Mean

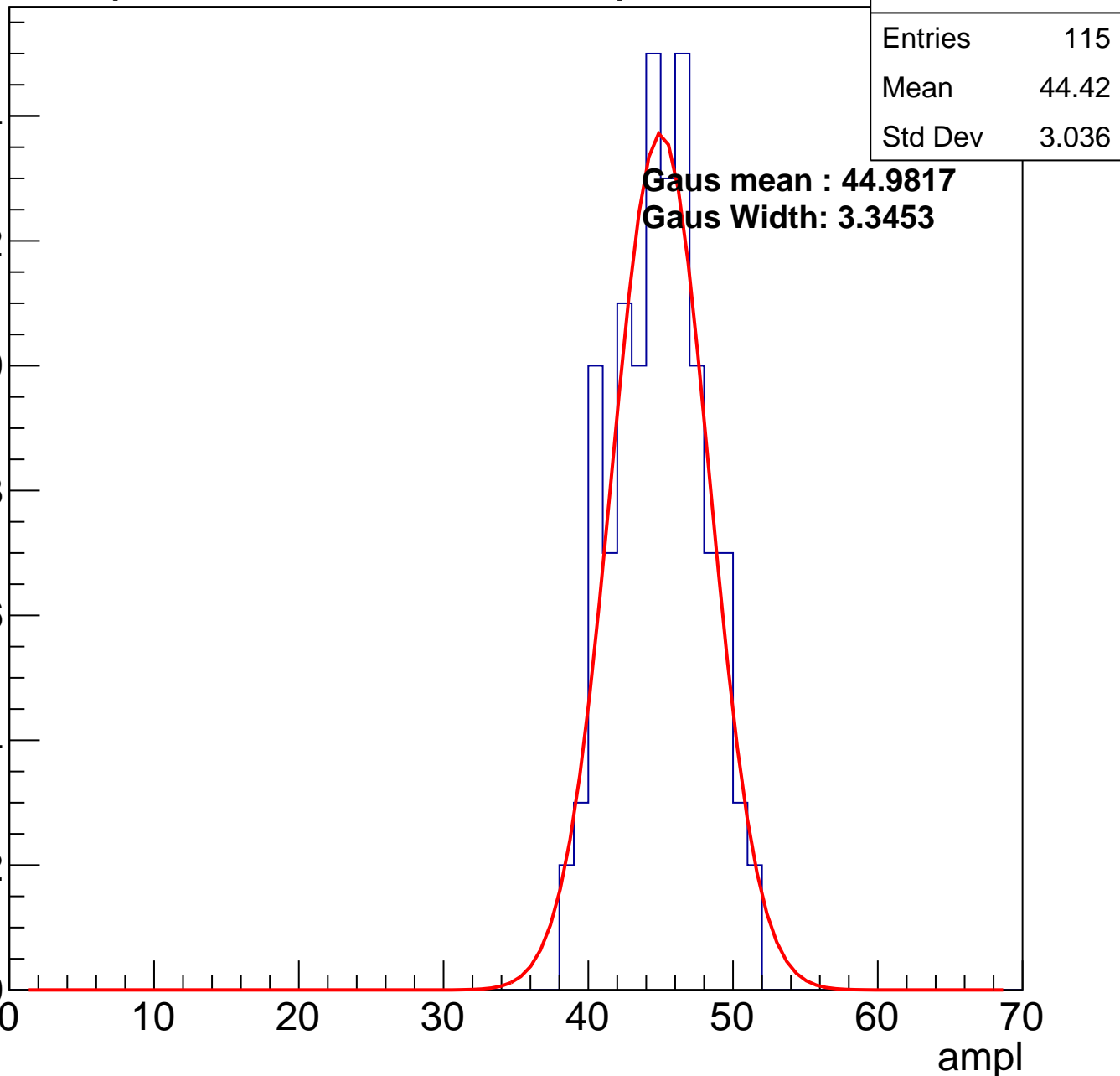
44.42

Std Dev

3.036

Gaus mean : 44.9817

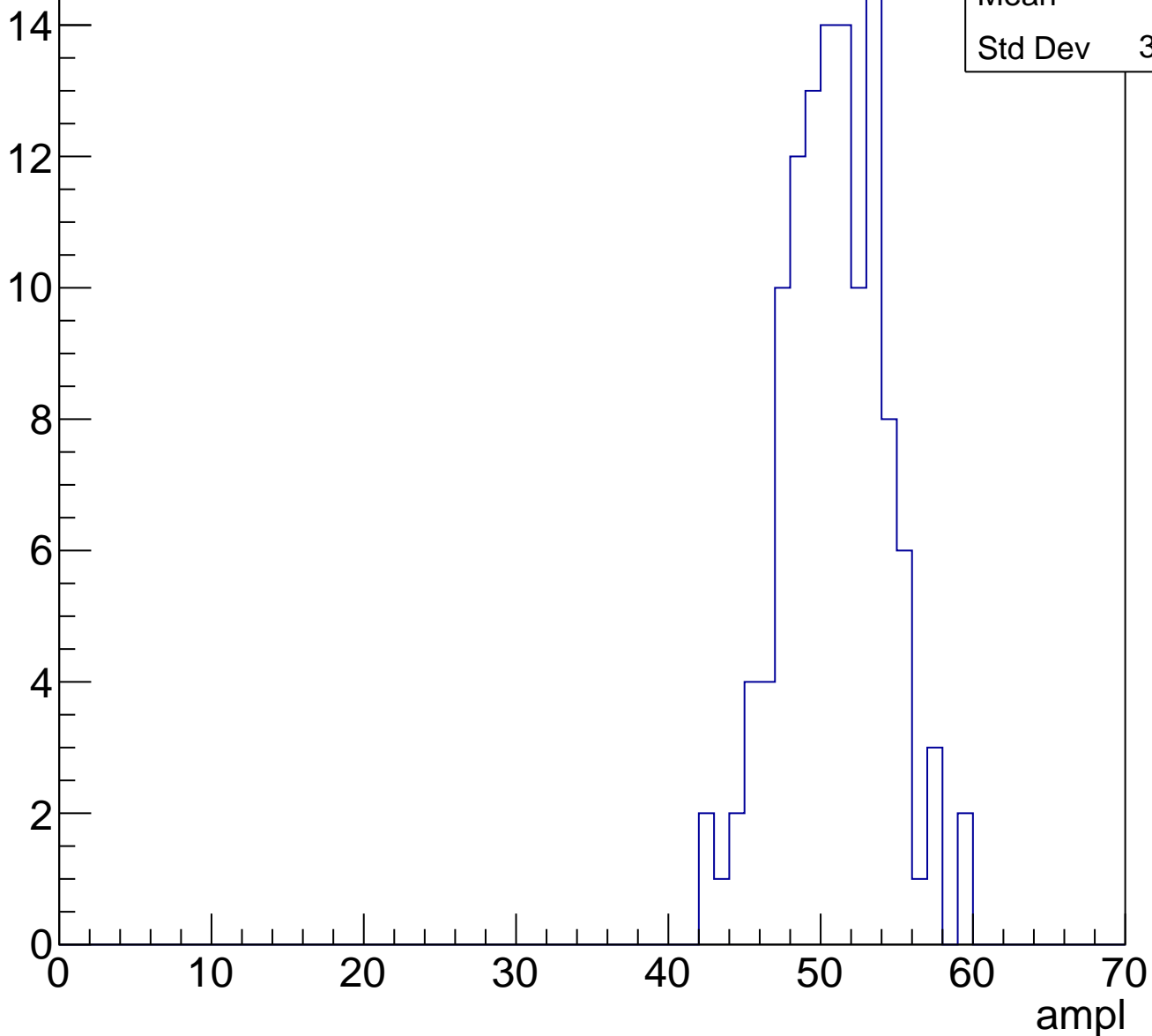
Gaus Width: 3.3453



B1L001S, U22-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

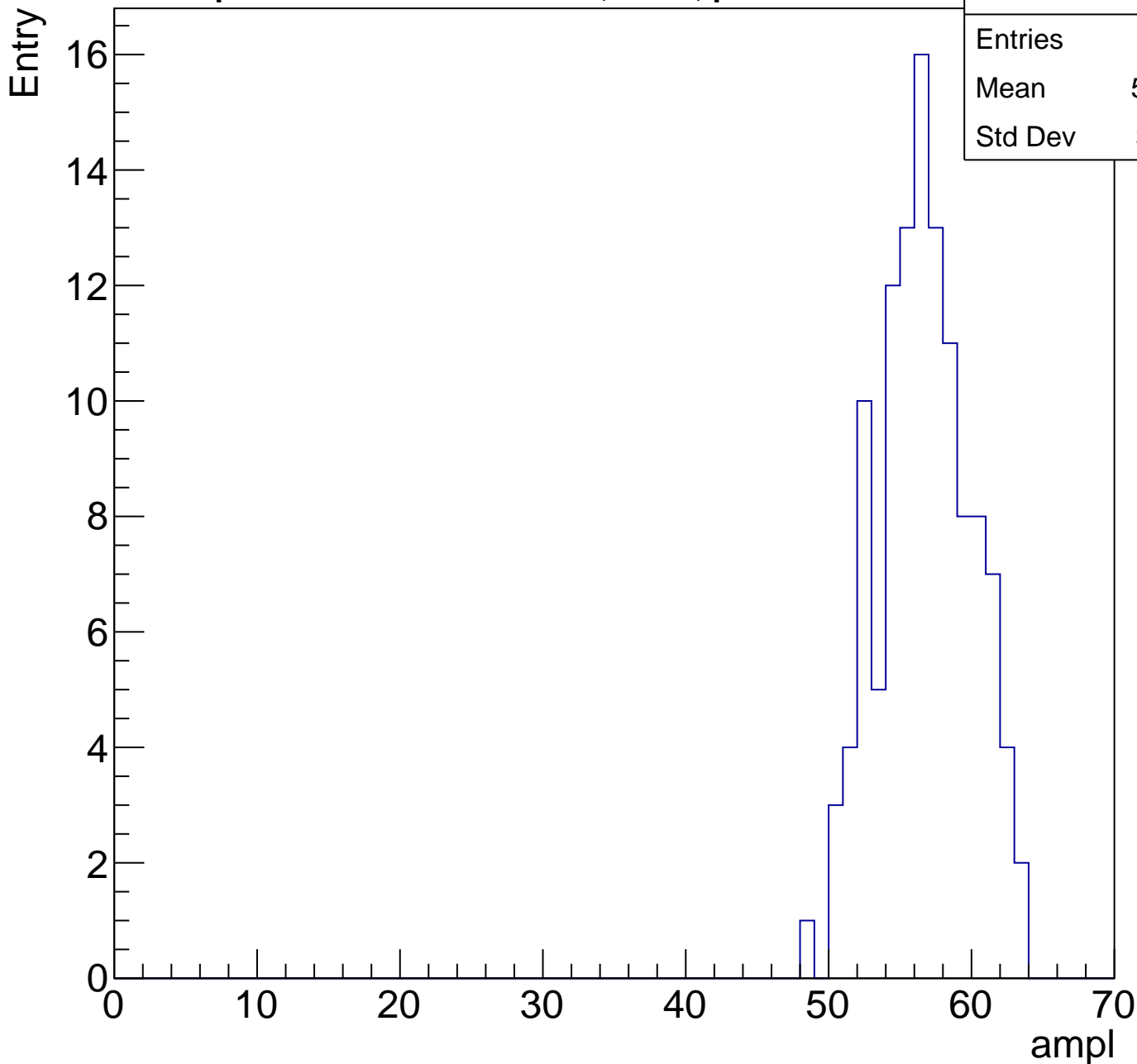


Entries	121
Mean	50.4
Std Dev	3.349

B1L001S, U22-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	56.22
Std Dev	3.211



B1L001S, U22-ch32, adc5

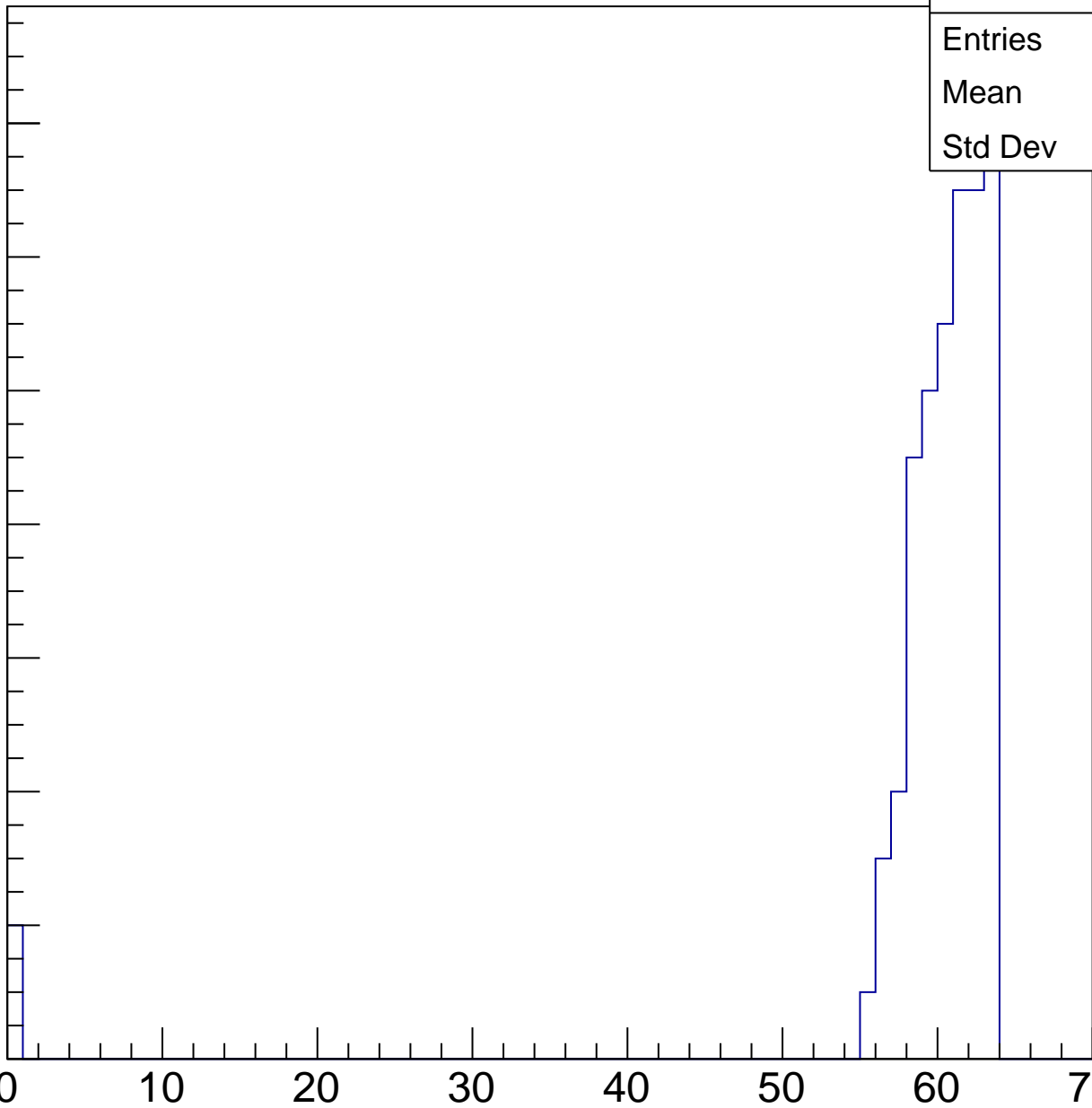
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.85
Std Dev	9.589

ampl



B1L001S, U22-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

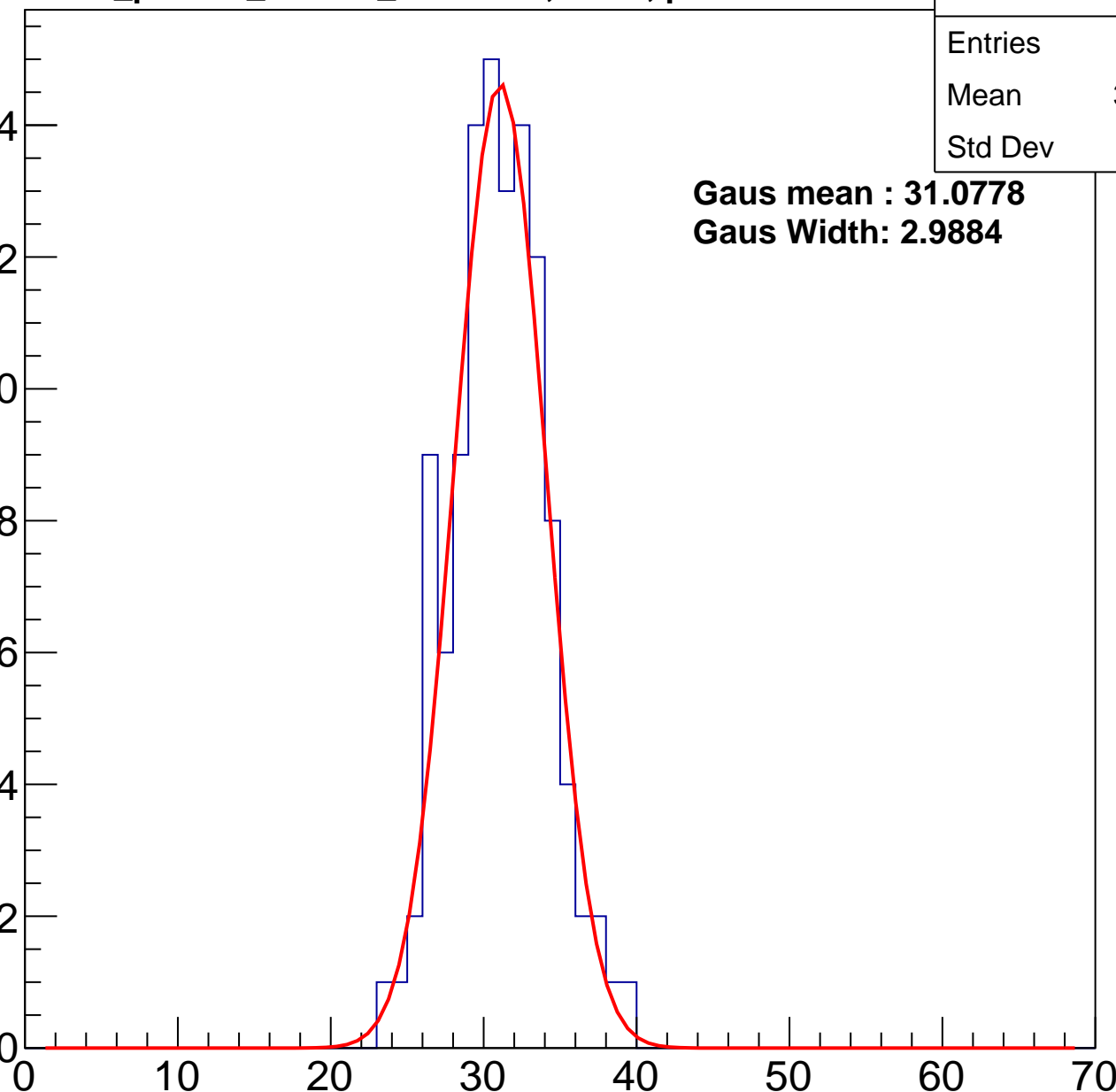
Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	30.55
Std Dev	3.061

Gaus mean : 31.0778

Gaus Width: 2.9884



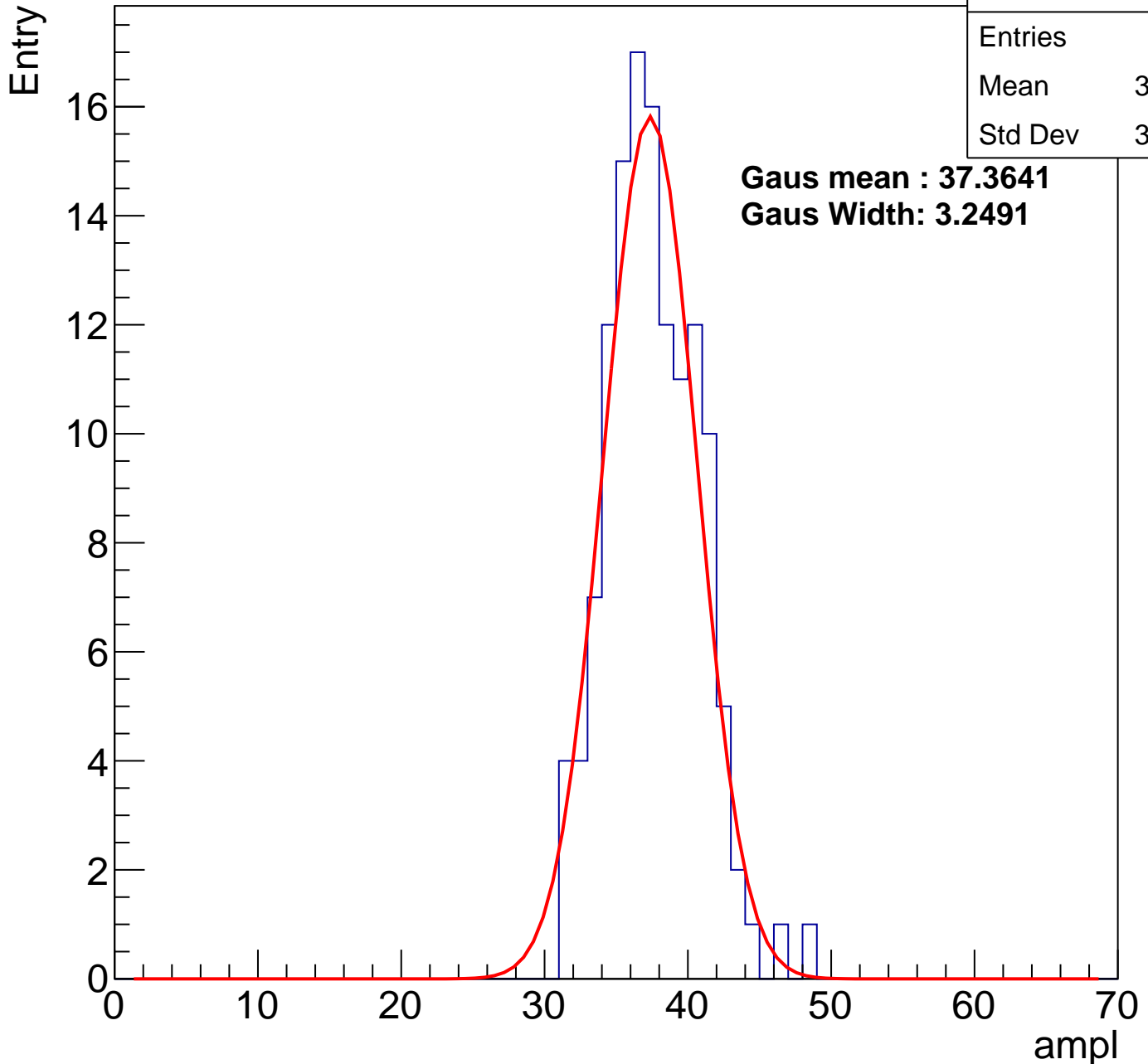
ampl

B1L001S, U22-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	37.15
Std Dev	3.175

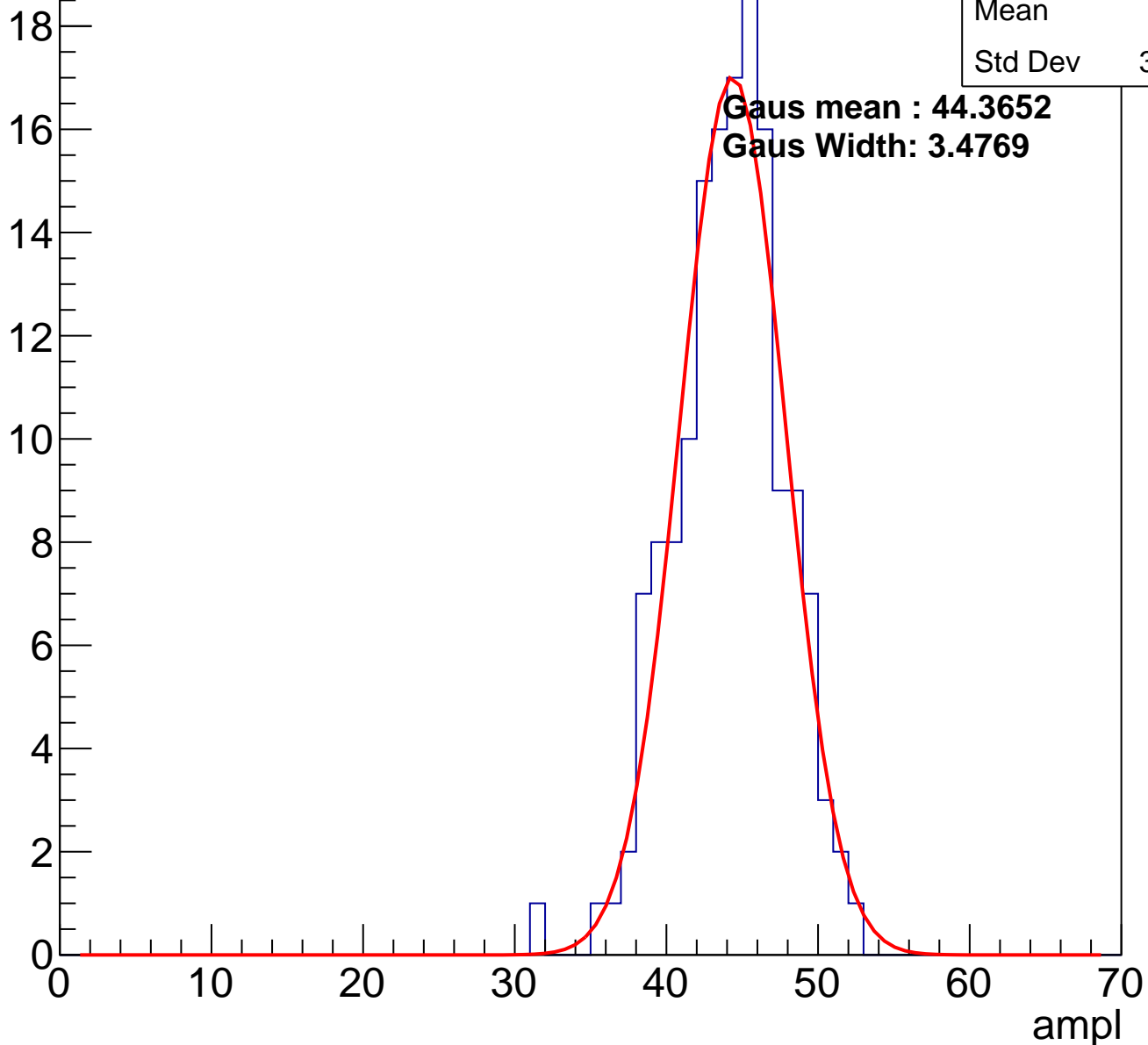
Gaus mean : 37.3641
Gaus Width: 3.2491



B1L001S, U22-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	51.17
Std Dev	3.34

Entry

16

14

12

10

8

6

4

2

0

0

10

20

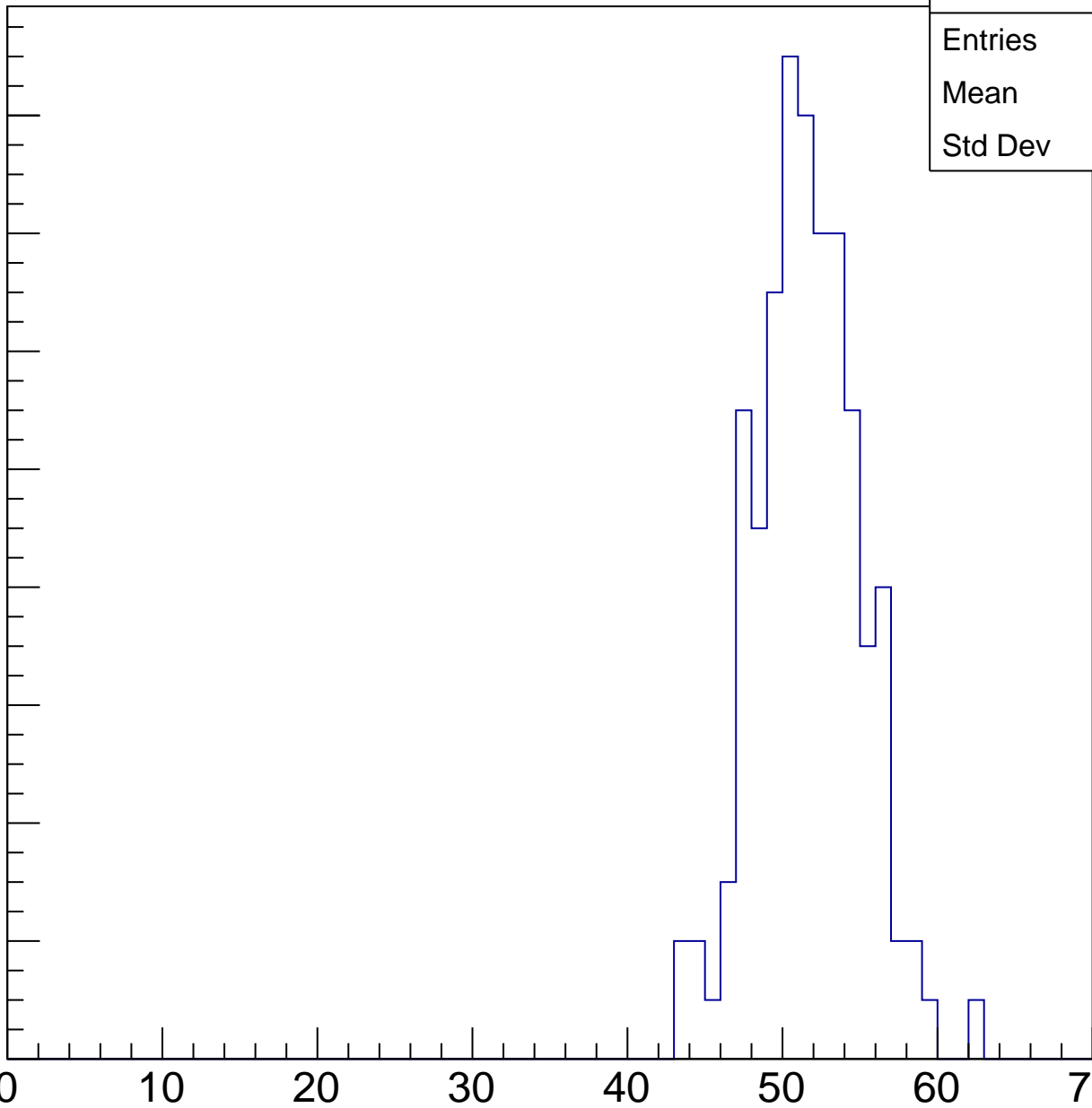
30

40

50

60

ampl

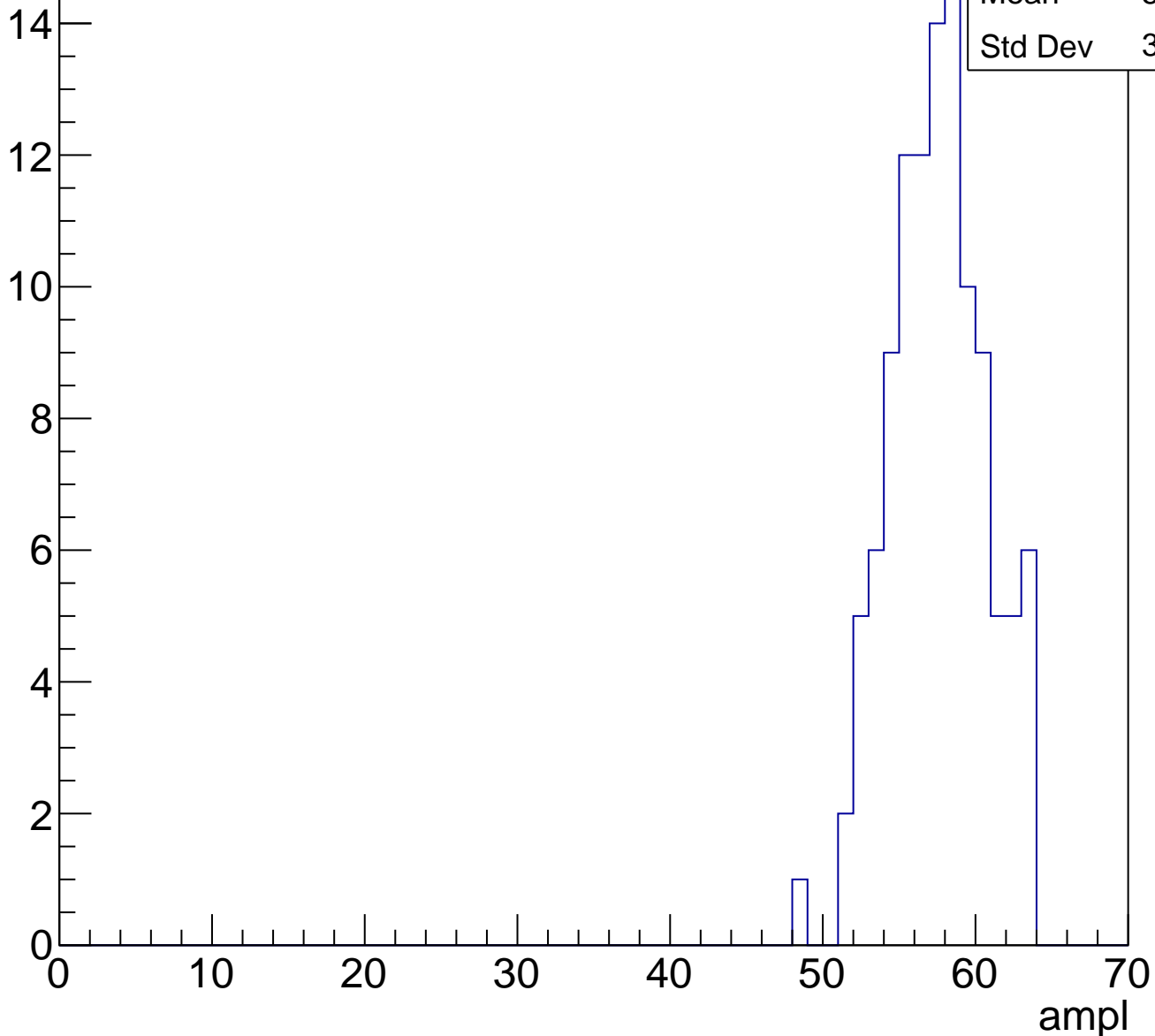


B1L001S, U22-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	57.09
Std Dev	3.106

Entry



B1L001S, U22-ch33, adc5

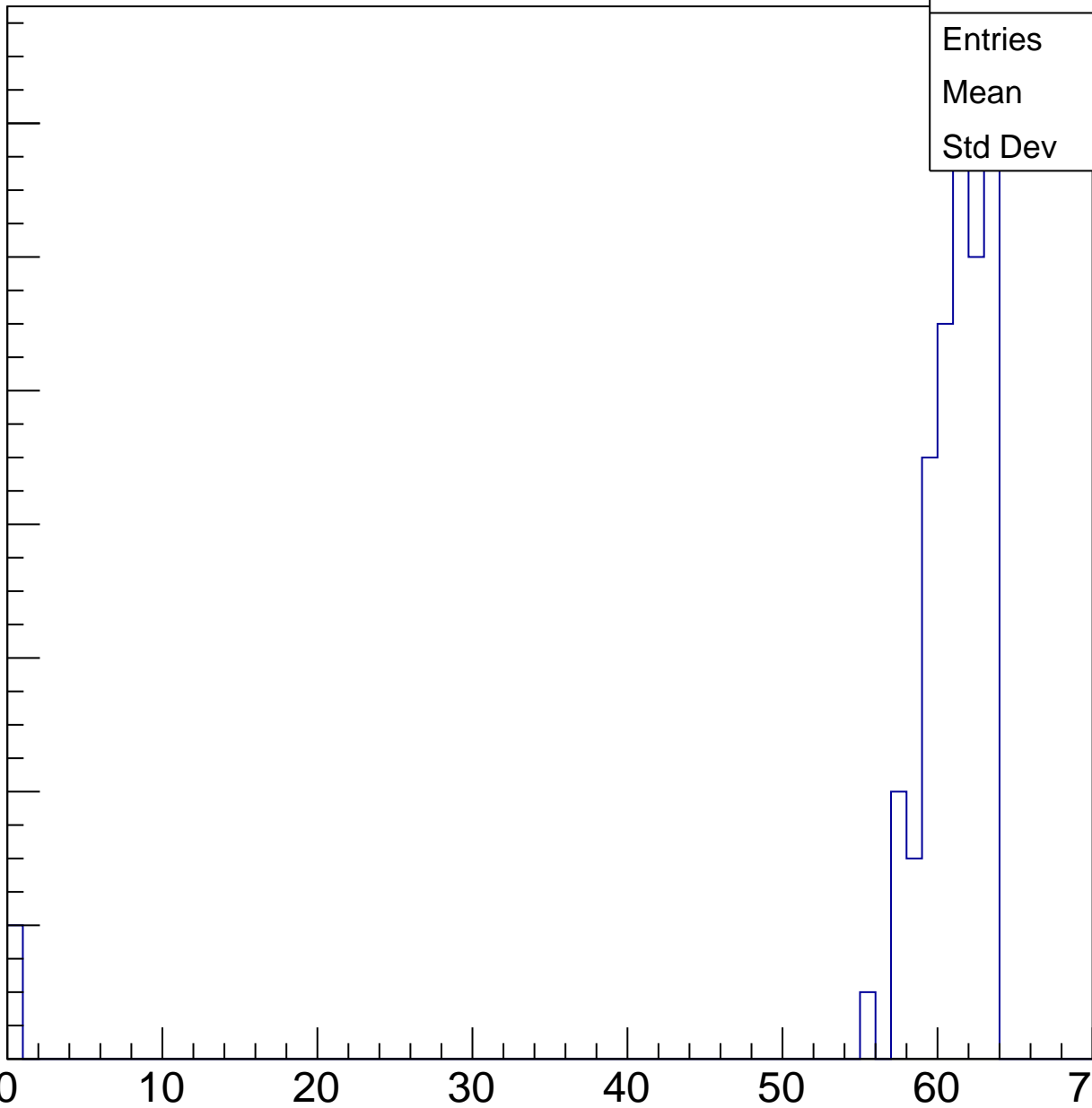
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59
Std Dev	10.21

ampl



B1L001S, U22-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L001S, U22-ch34, adc0

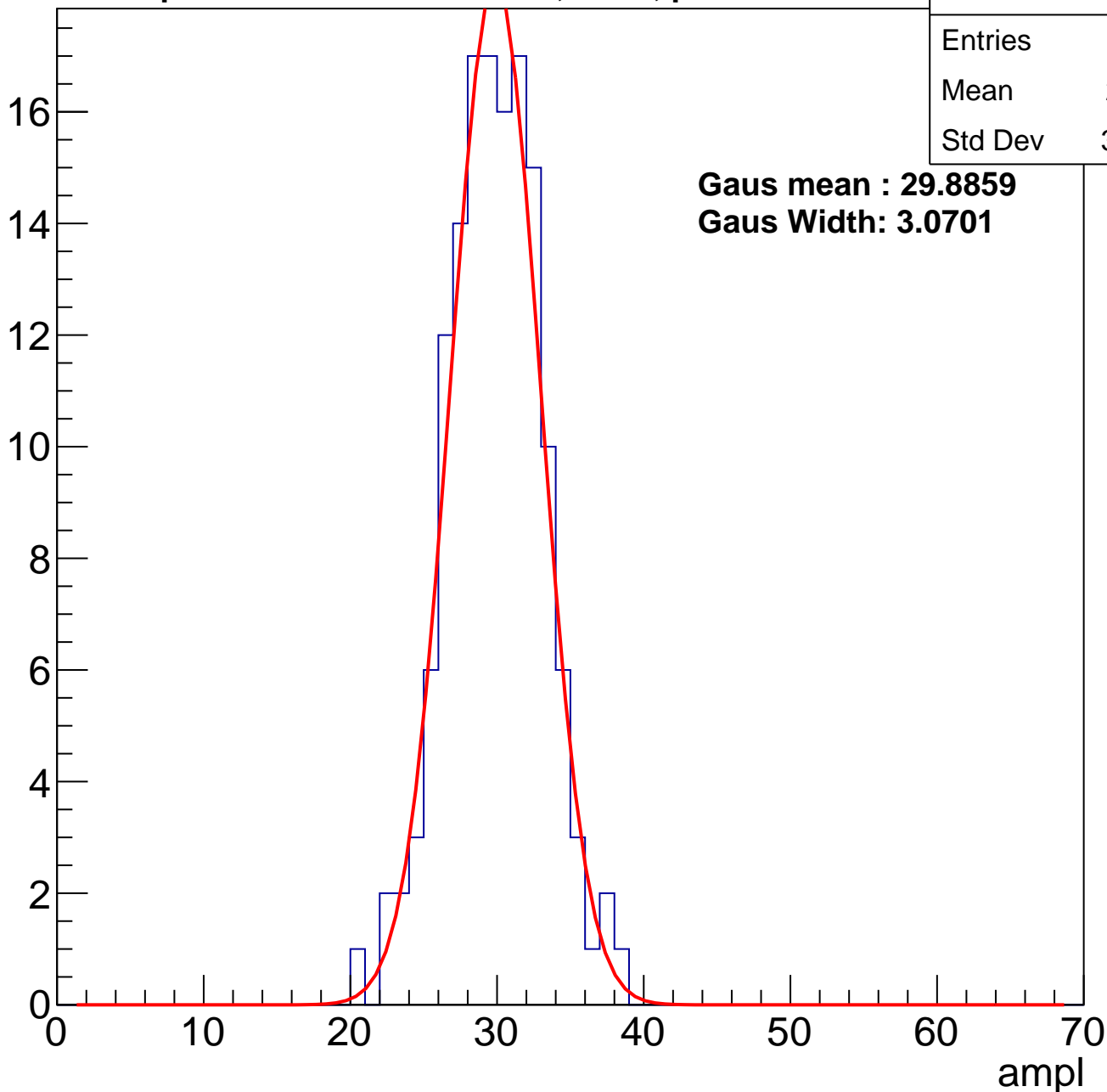
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.41
Std Dev	3.187

Gaus mean : 29.8859

Gaus Width: 3.0701

Entry



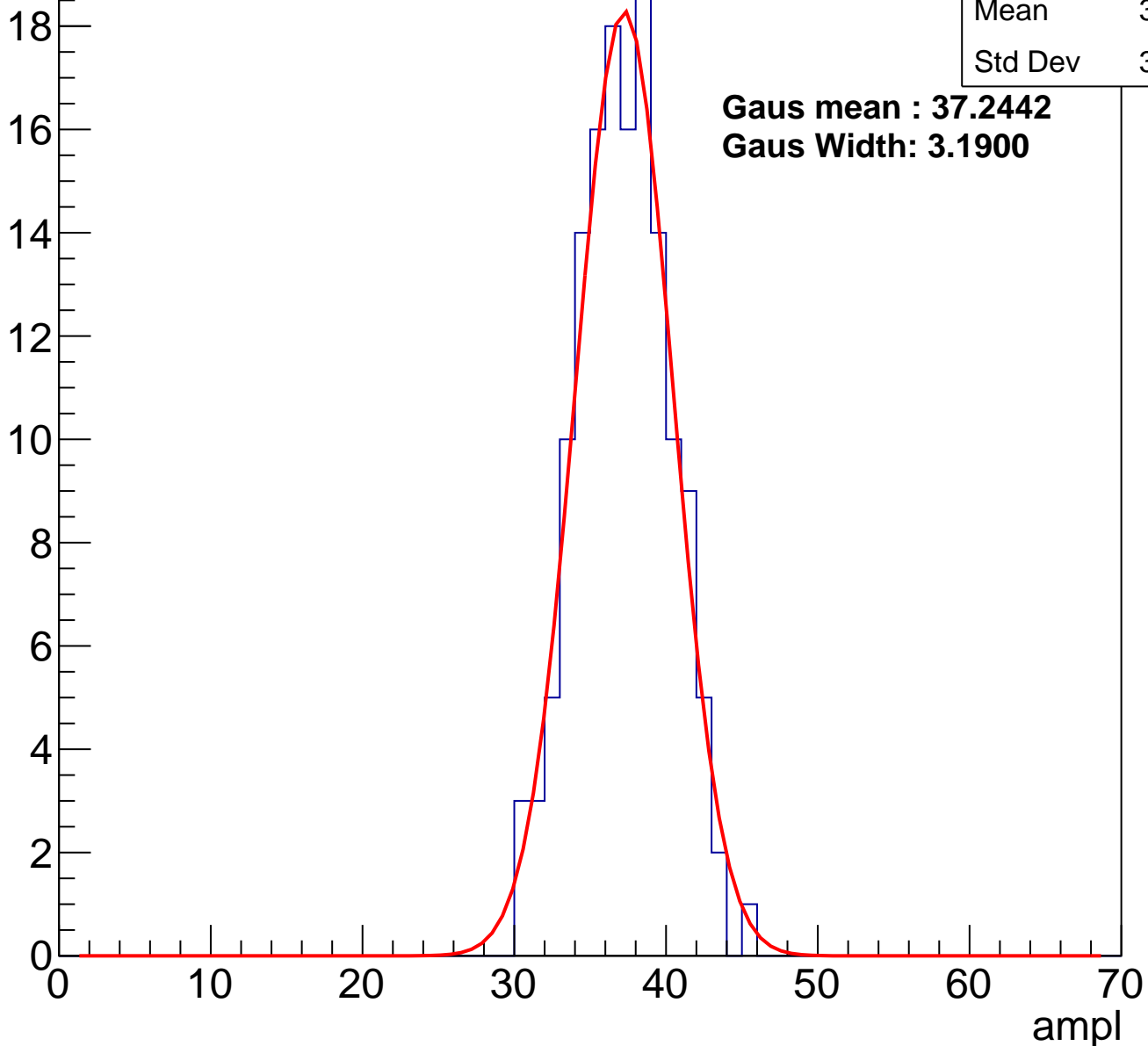
B1L001S, U22-ch34, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.74
Std Dev	3.003

Gaus mean : 37.2442
Gaus Width: 3.1900

Entry



B1L001S, U22-ch34, adc2

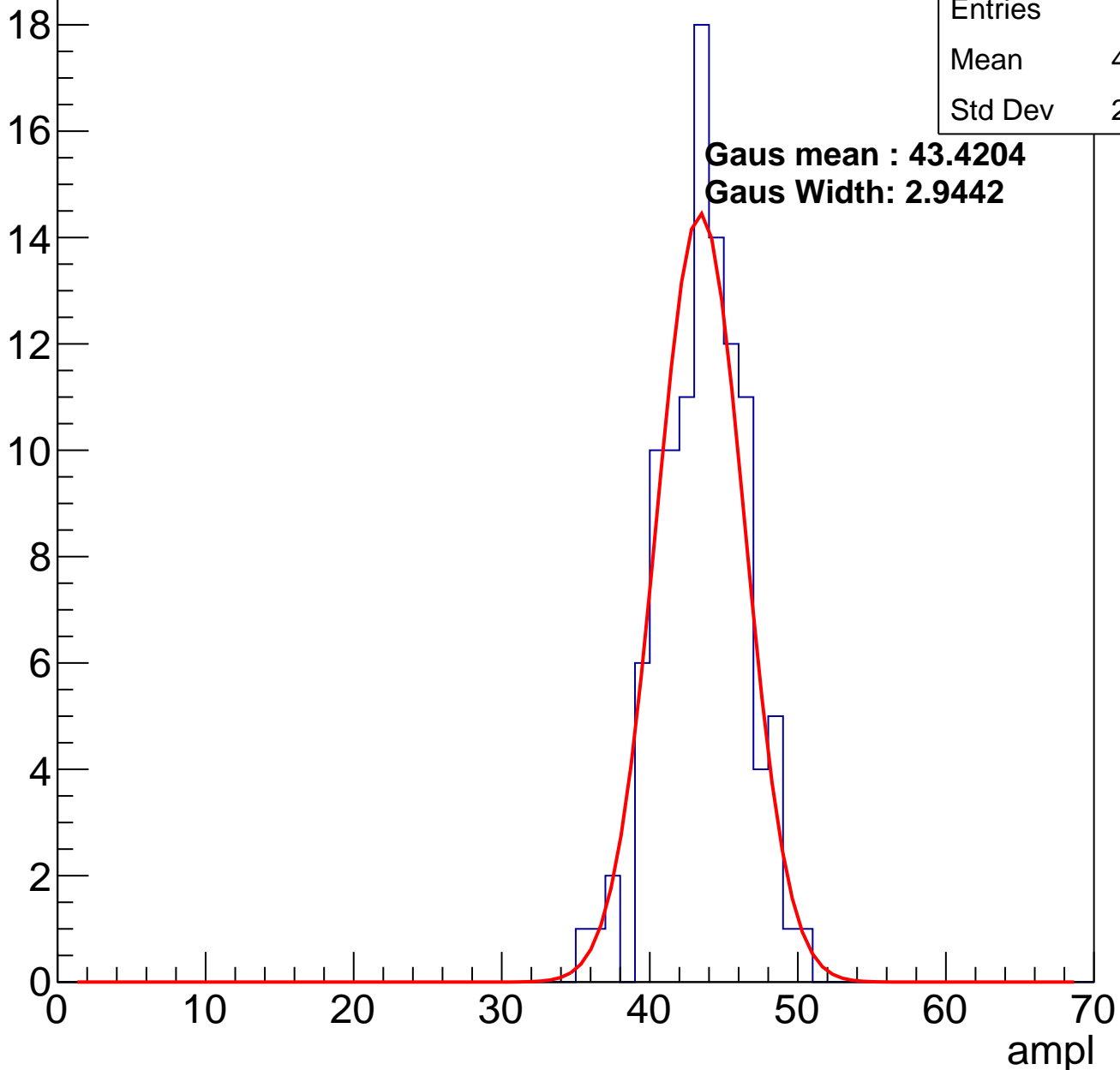
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	43.12
Std Dev	2.827

Gaus mean : 43.4204

Gaus Width: 2.9442

Entry



B1L001S, U22-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	49.07
Std Dev	3.151

Entry

16

14

12

10

8

6

4

2

0

0

10

20

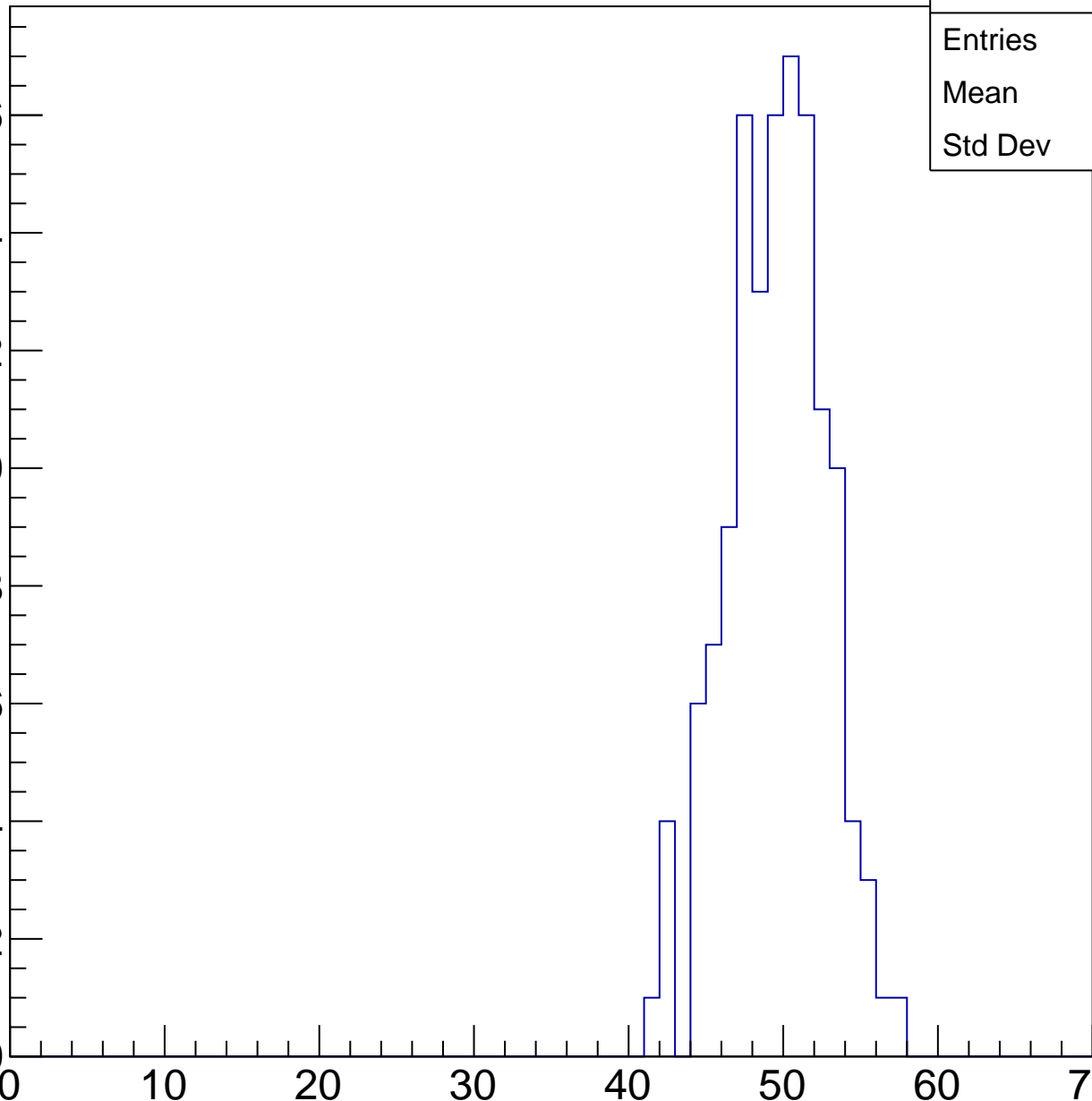
30

40

50

60

ampl



B1L001S, U22-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

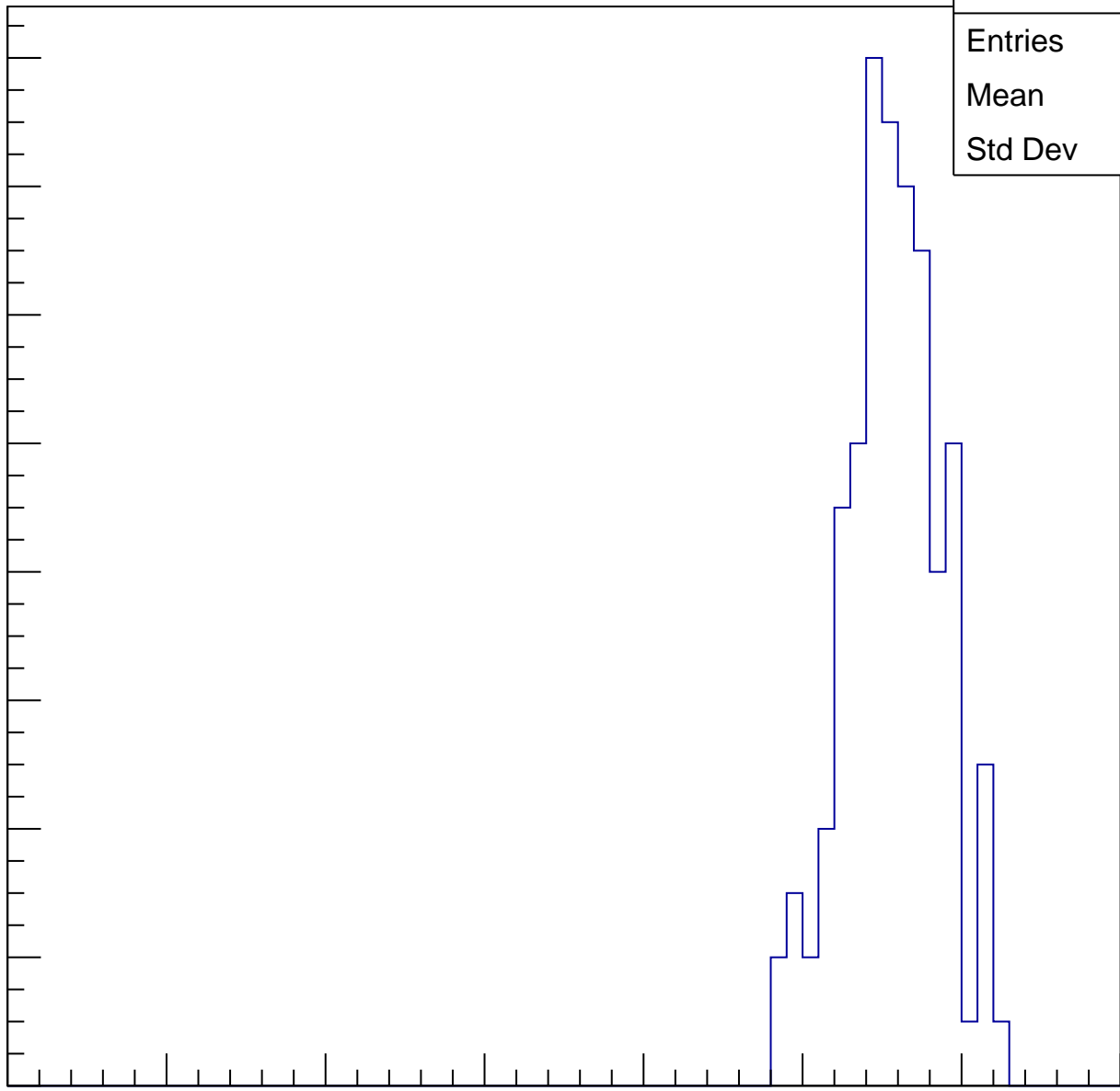
Entries	113
Mean	55.22
Std Dev	2.98

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch34, adc5

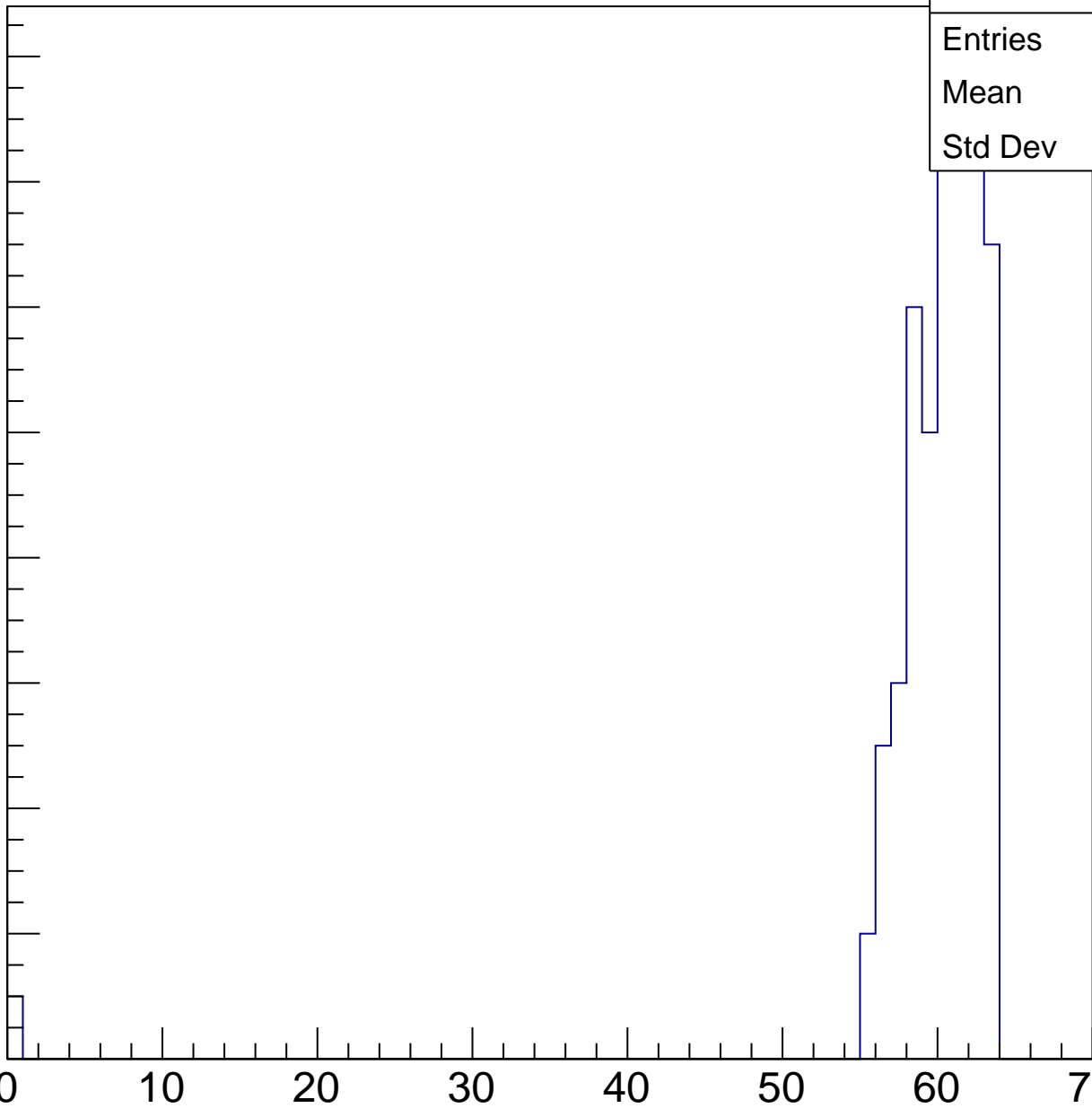
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.41
Std Dev	6.493

ampl

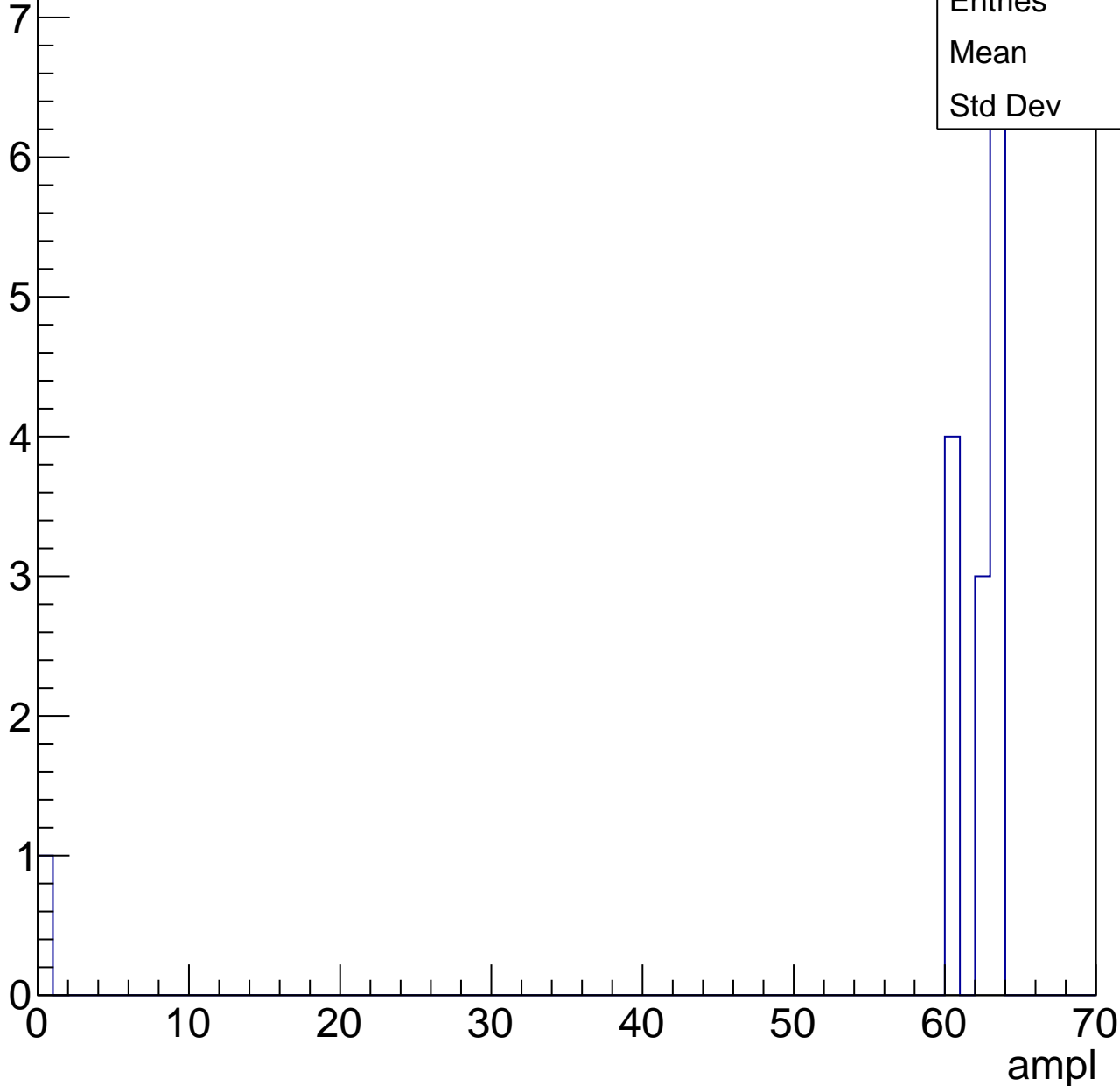


B1L001S, U22-ch34, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	57.8
Std Dev	15.5



B1L001S, U22-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L001S, U22-ch35, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.04
Std Dev	3.363

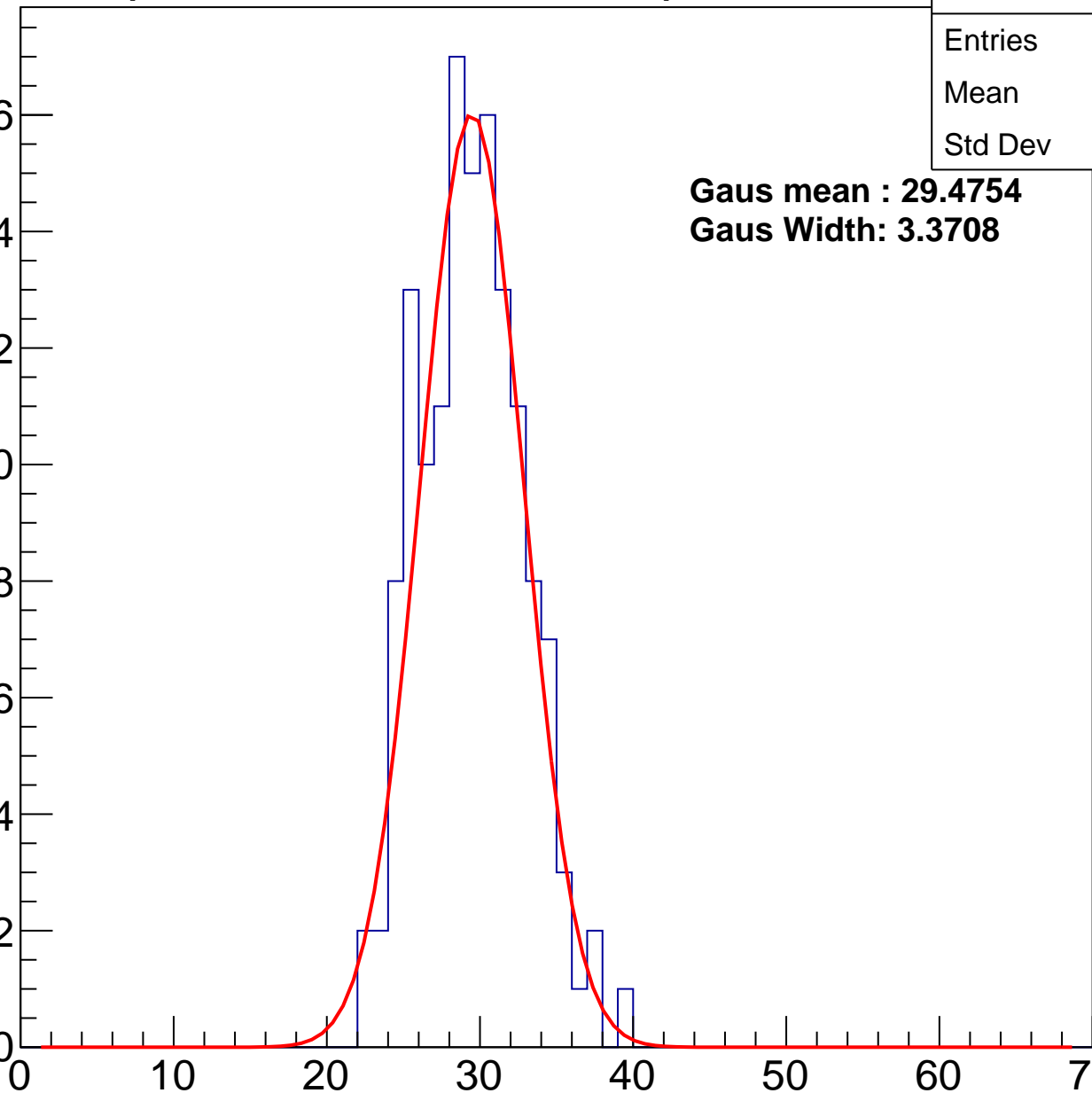
Gaus mean : 29.4754

Gaus Width: 3.3708

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch35, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	36.22
Std Dev	3.52

Gaus mean : 36.4836

Gaus Width: 3.4658

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

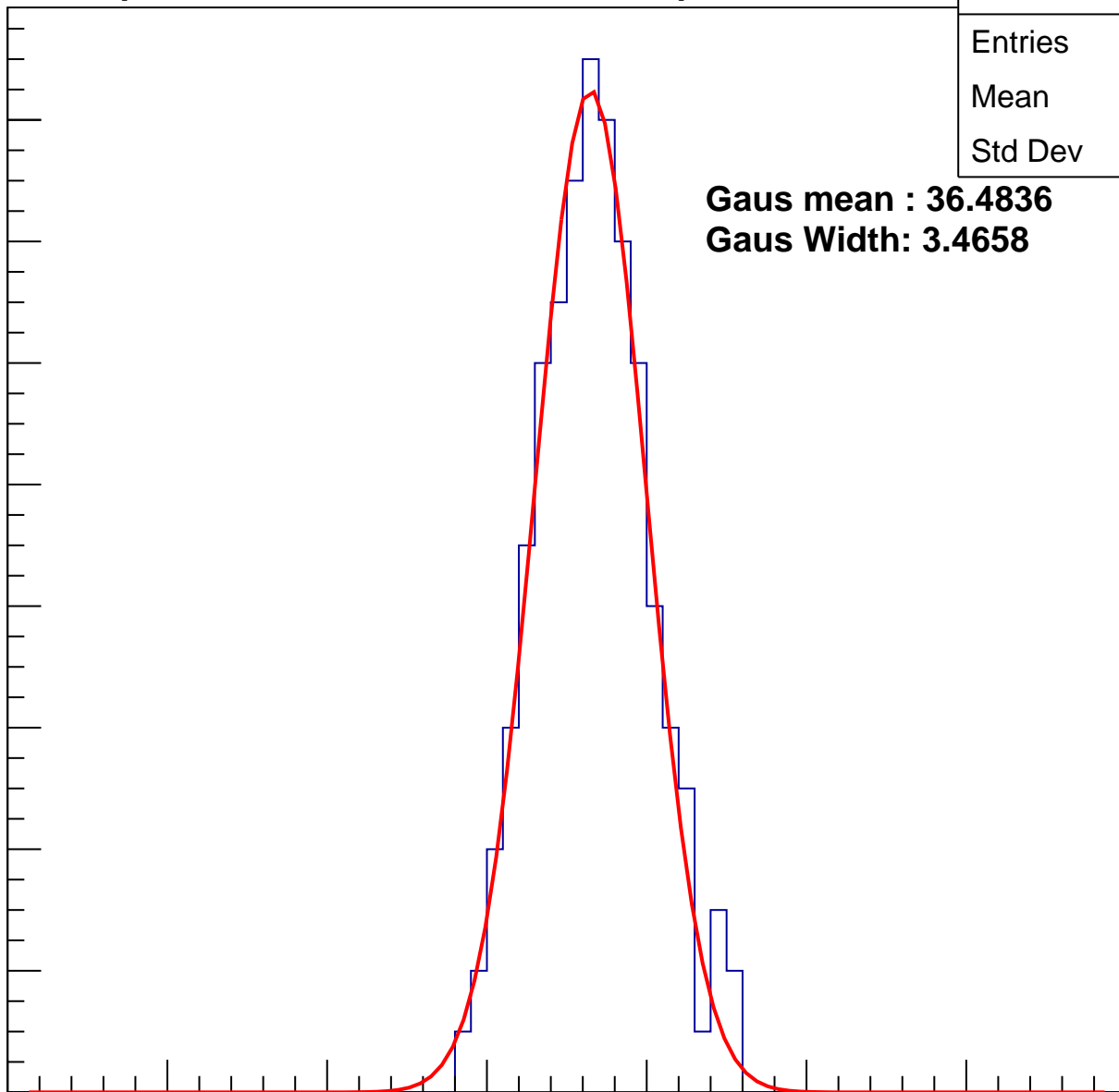
40

50

60

70

ampl



B1L001S, U22-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	43.15
Std Dev	3.45

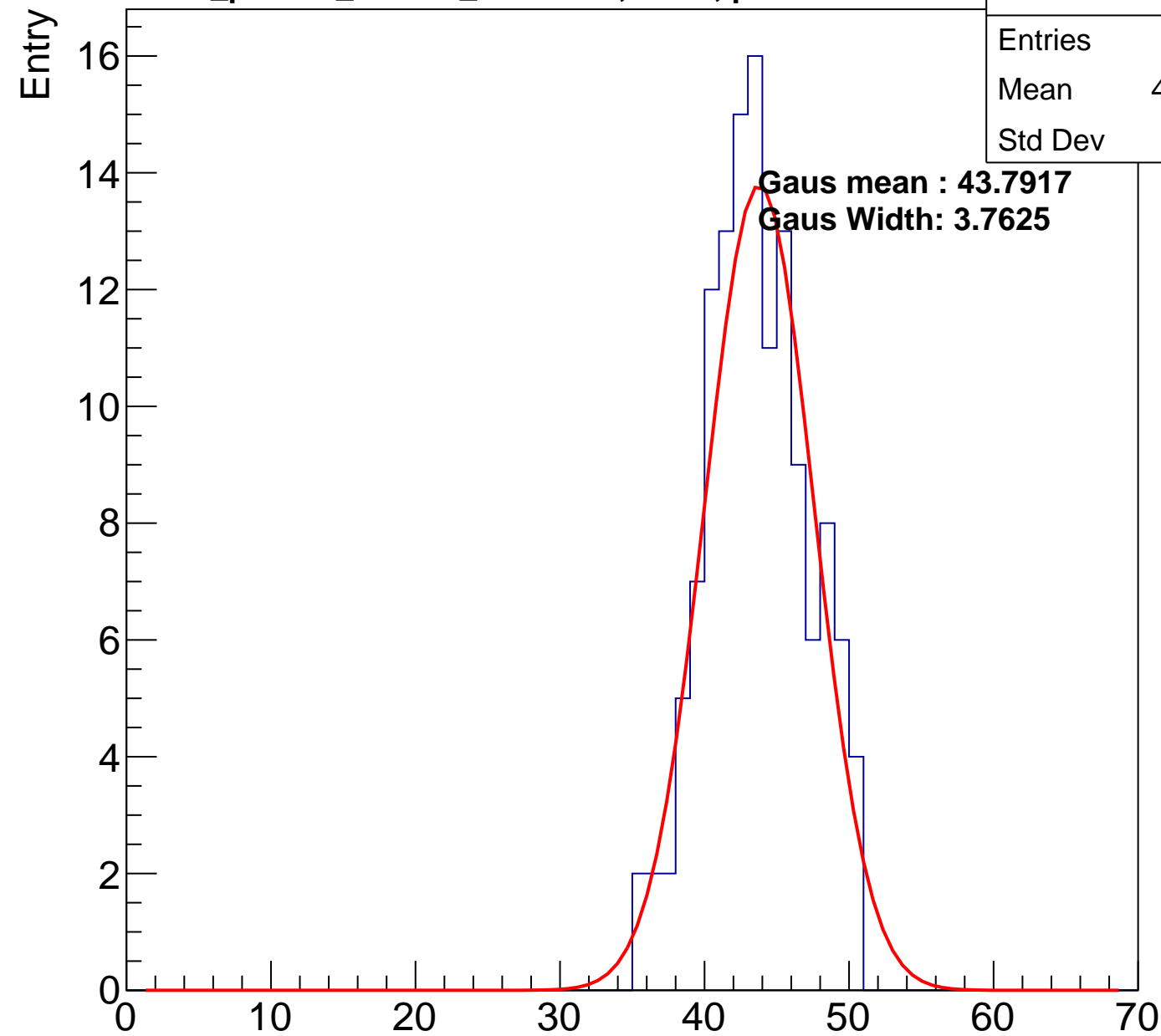
Gaus mean : 43.7917

Gaus Width: 3.7625

Entry

16
14
12
10
8
6
4
2
0

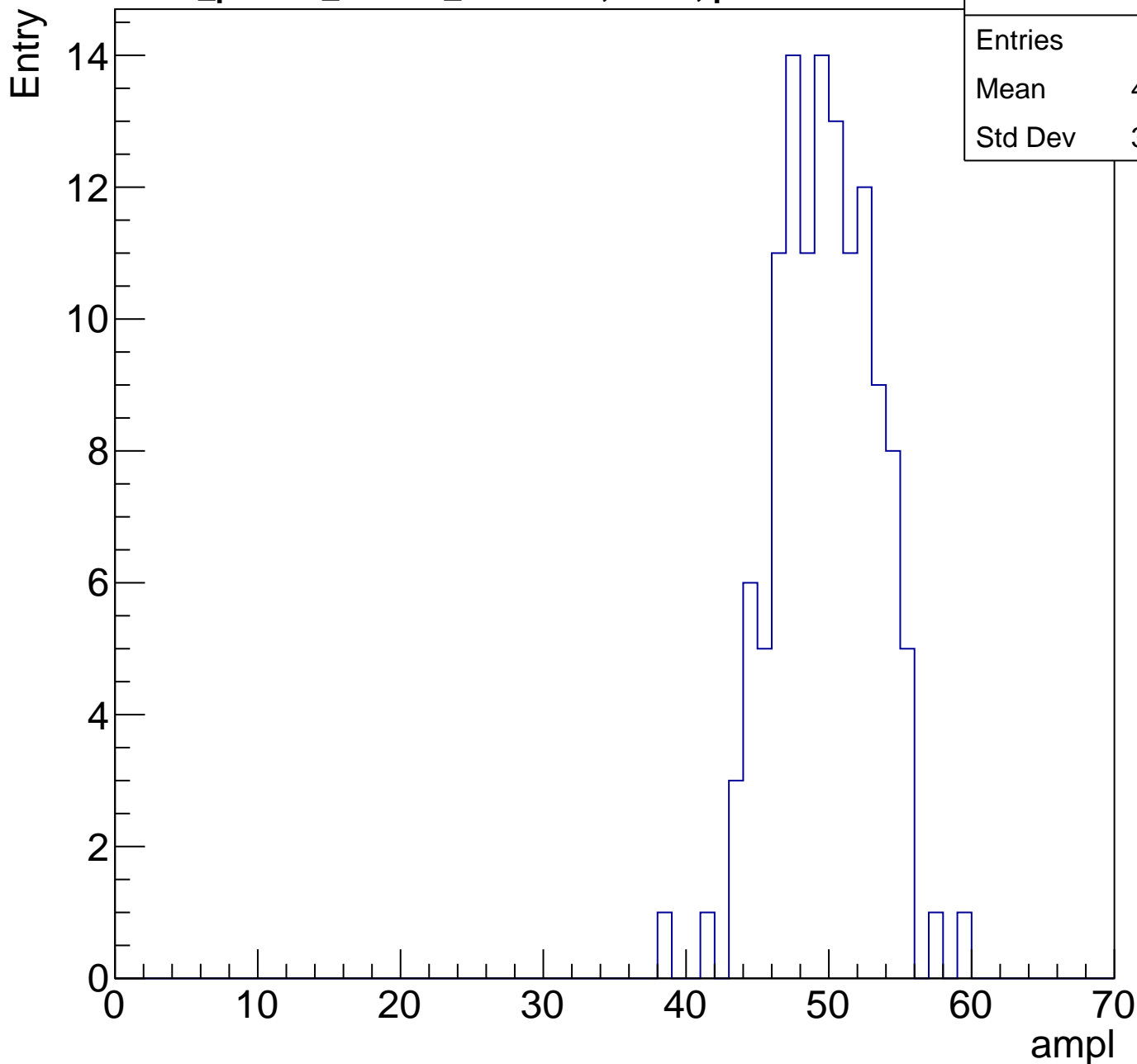
ampl



B1L001S, U22-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	49.29
Std Dev	3.487



B1L001S, U22-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

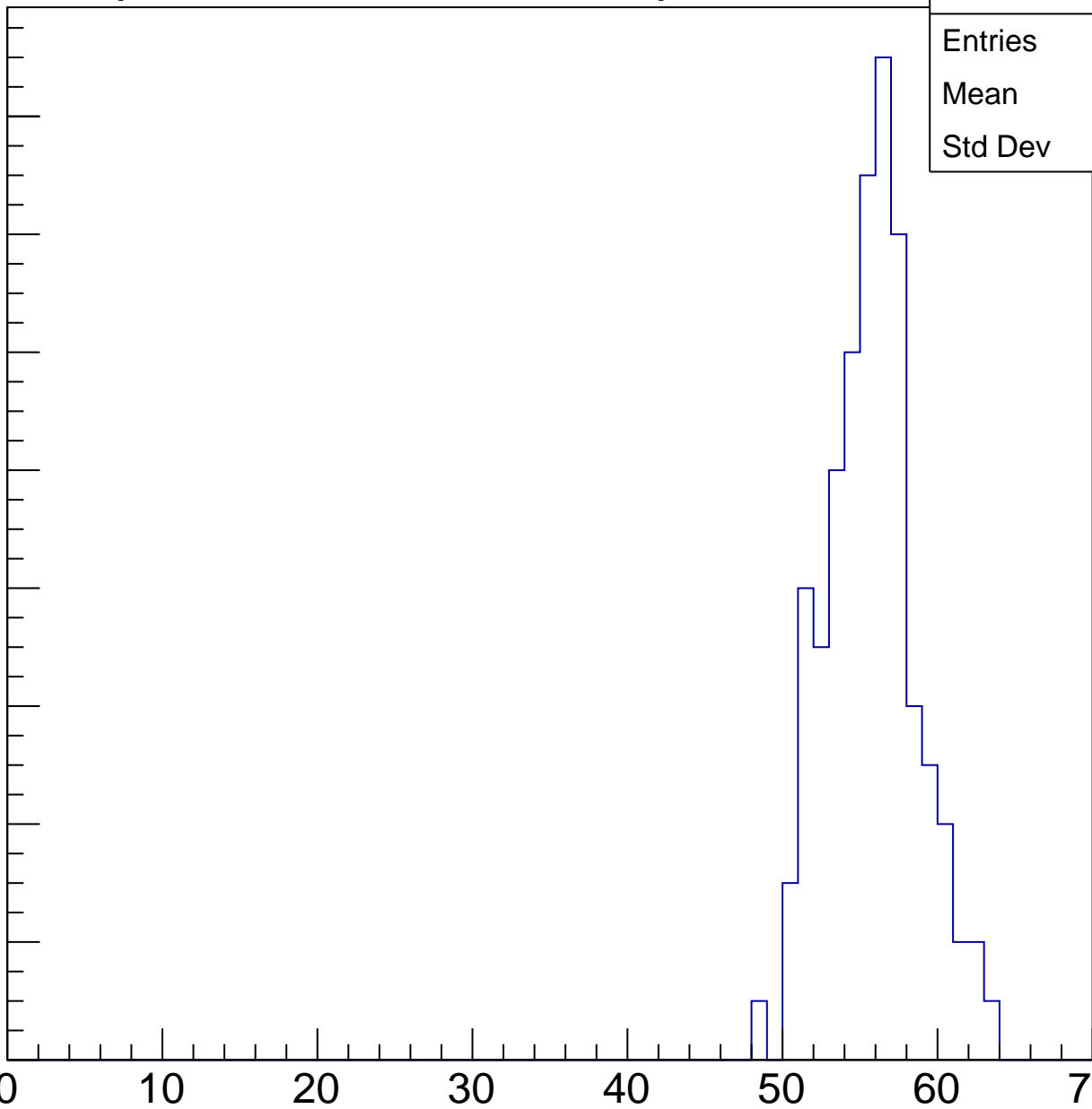
Entries	107
Mean	55.28
Std Dev	2.9

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch35, adc5

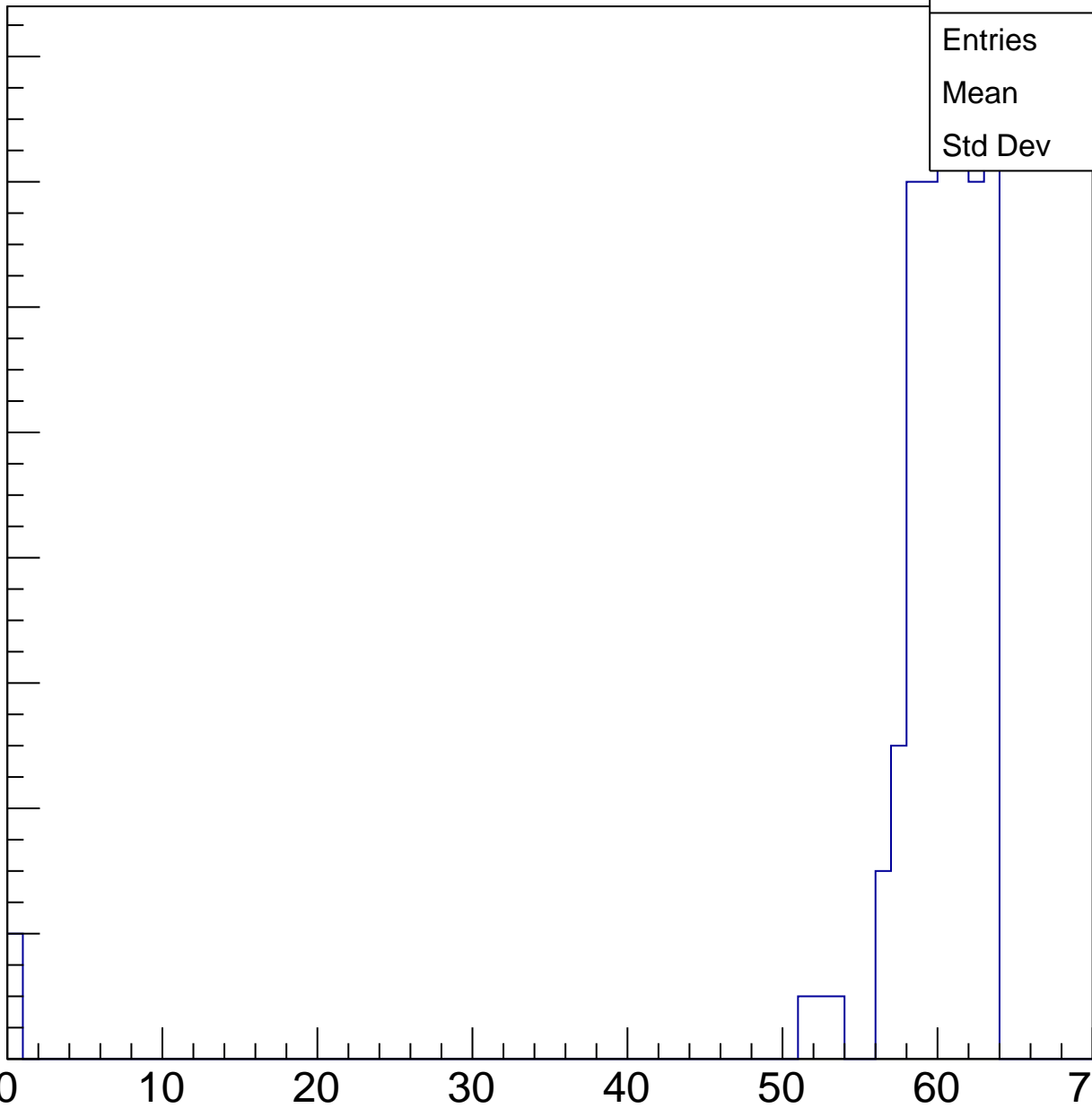
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.81
Std Dev	8.649

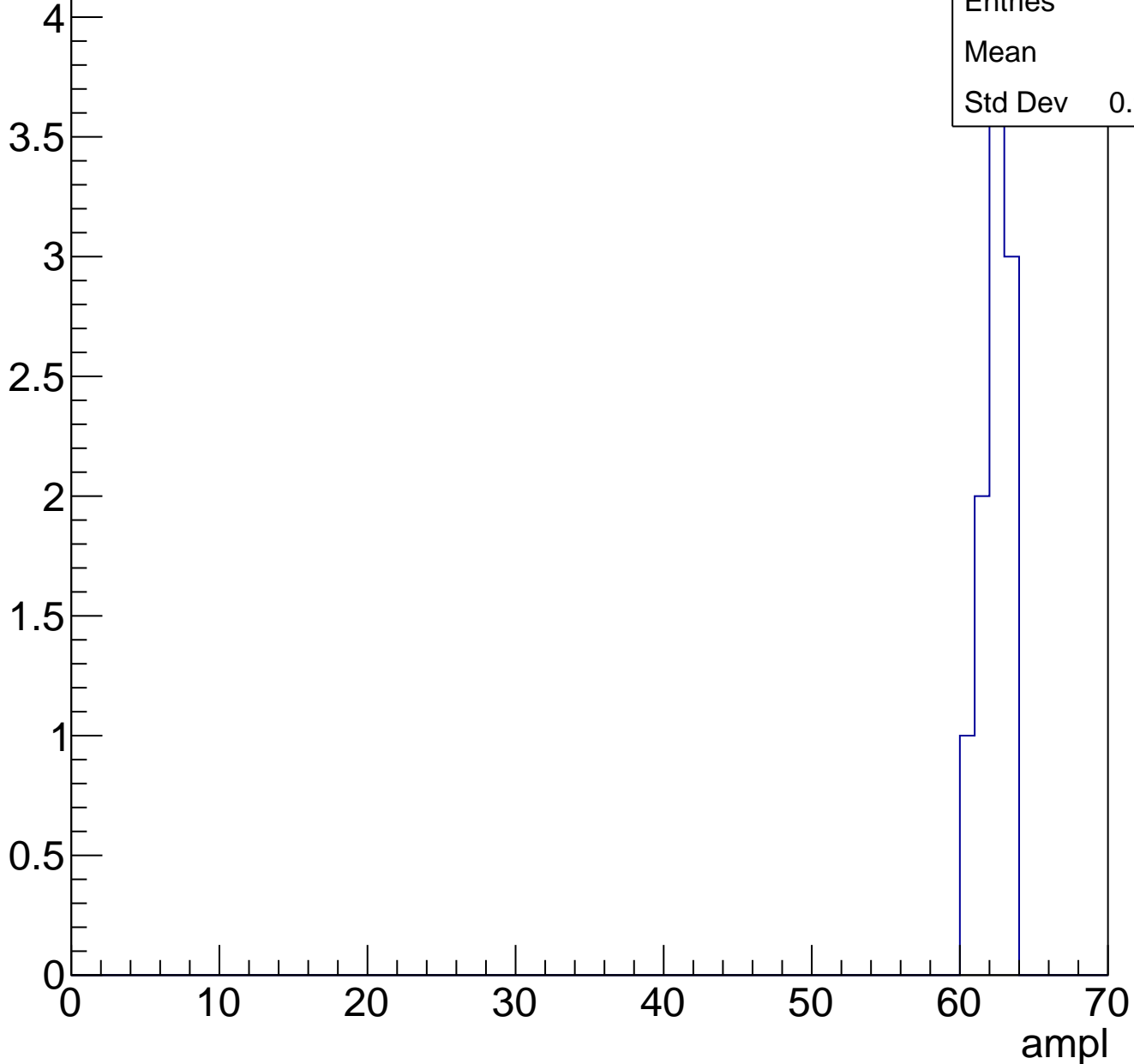
ampl



B1L001S, U22-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

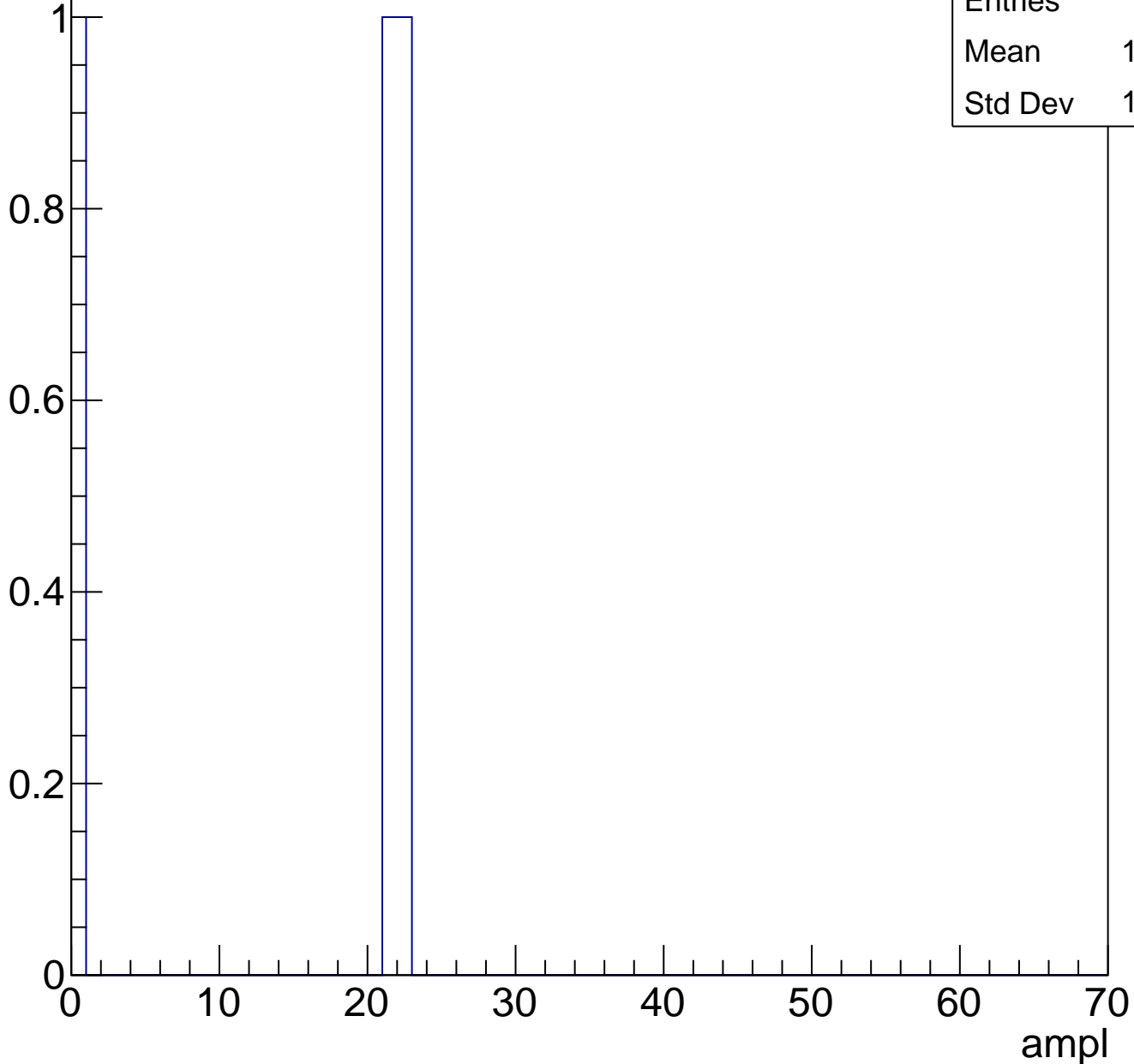
Entry



B1L001S, U22-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	14.33
Std Dev	10.14

B1L001S, U22-ch36, adc0

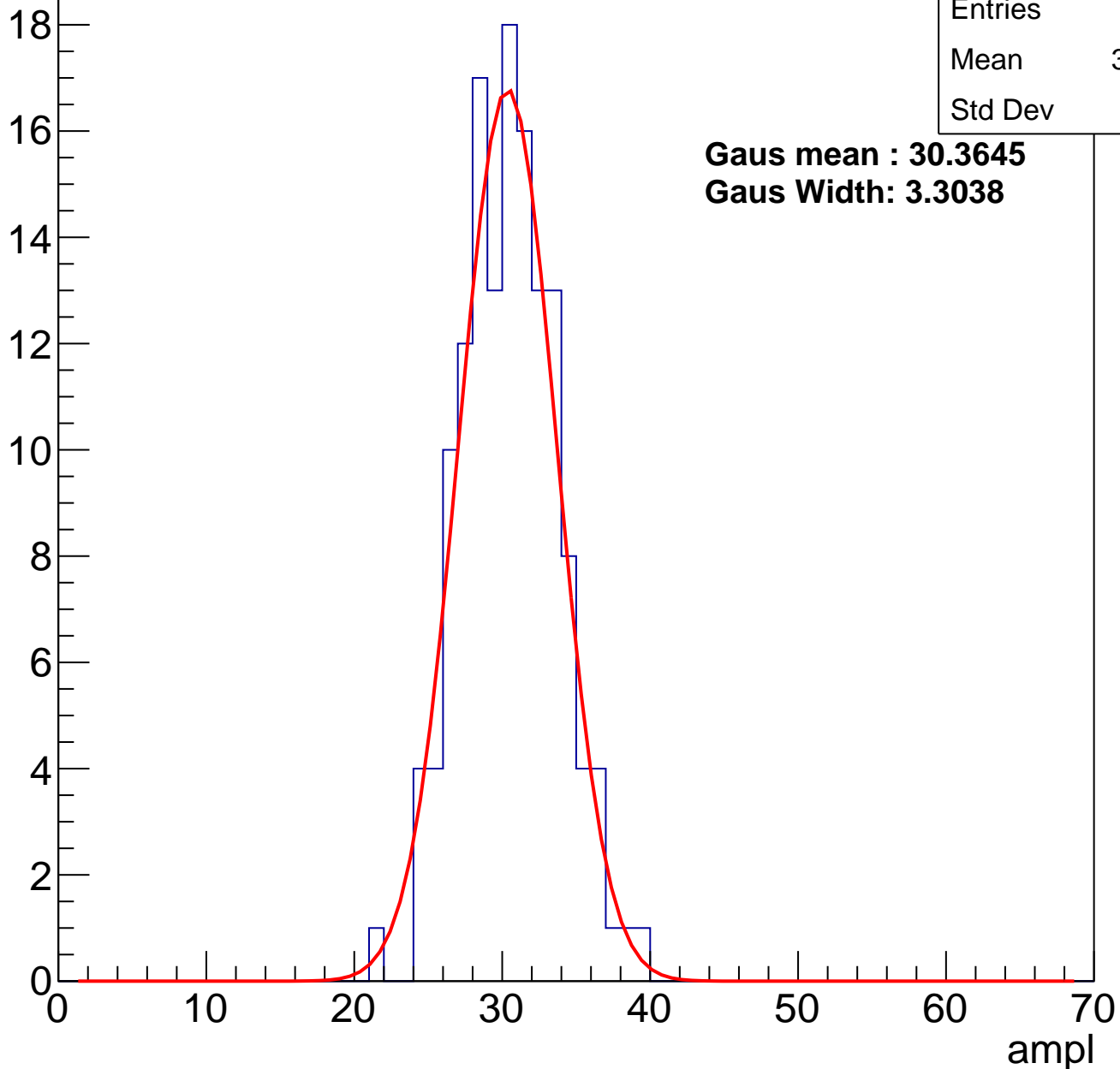
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.04
Std Dev	3.19

Gaus mean : 30.3645

Gaus Width: 3.3038

Entry



B1L001S, U22-ch36, adc1

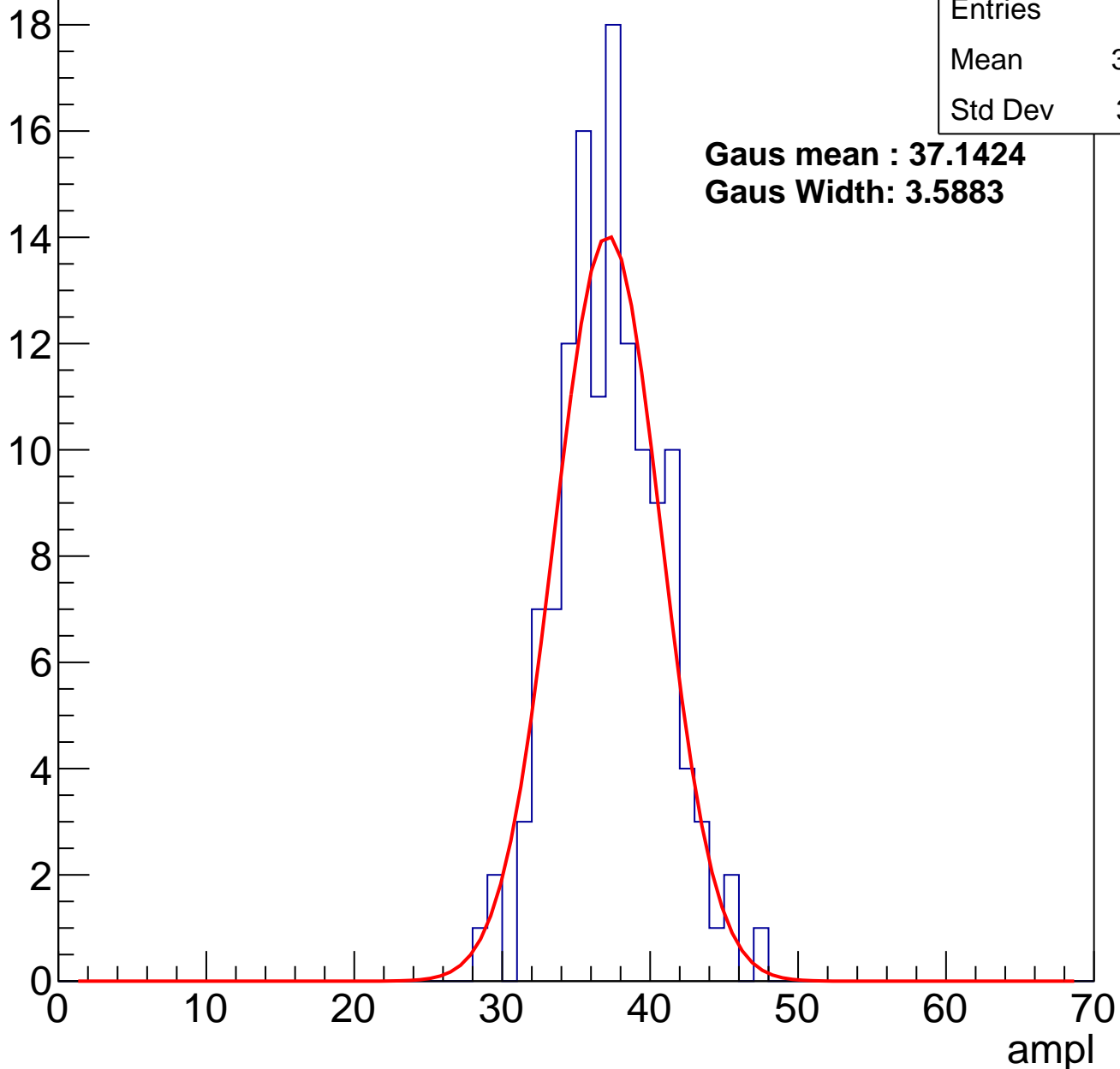
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.88
Std Dev	3.481

Gaus mean : 37.1424

Gaus Width: 3.5883

Entry



B1L001S, U22-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	43.04
Std Dev	3.323

Gaus mean : 43.8459

Gaus Width: 3.2630

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

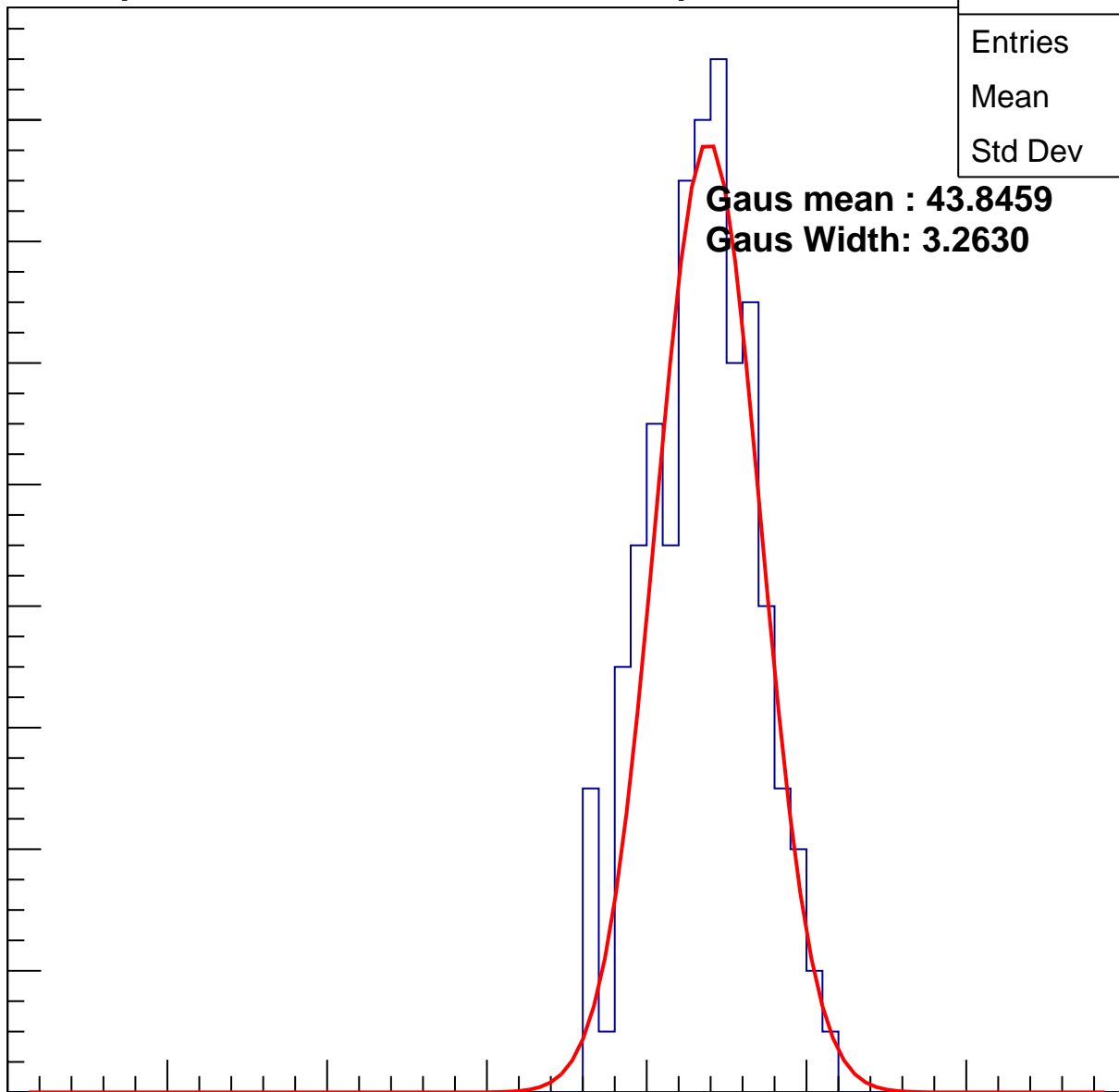
40

50

60

70

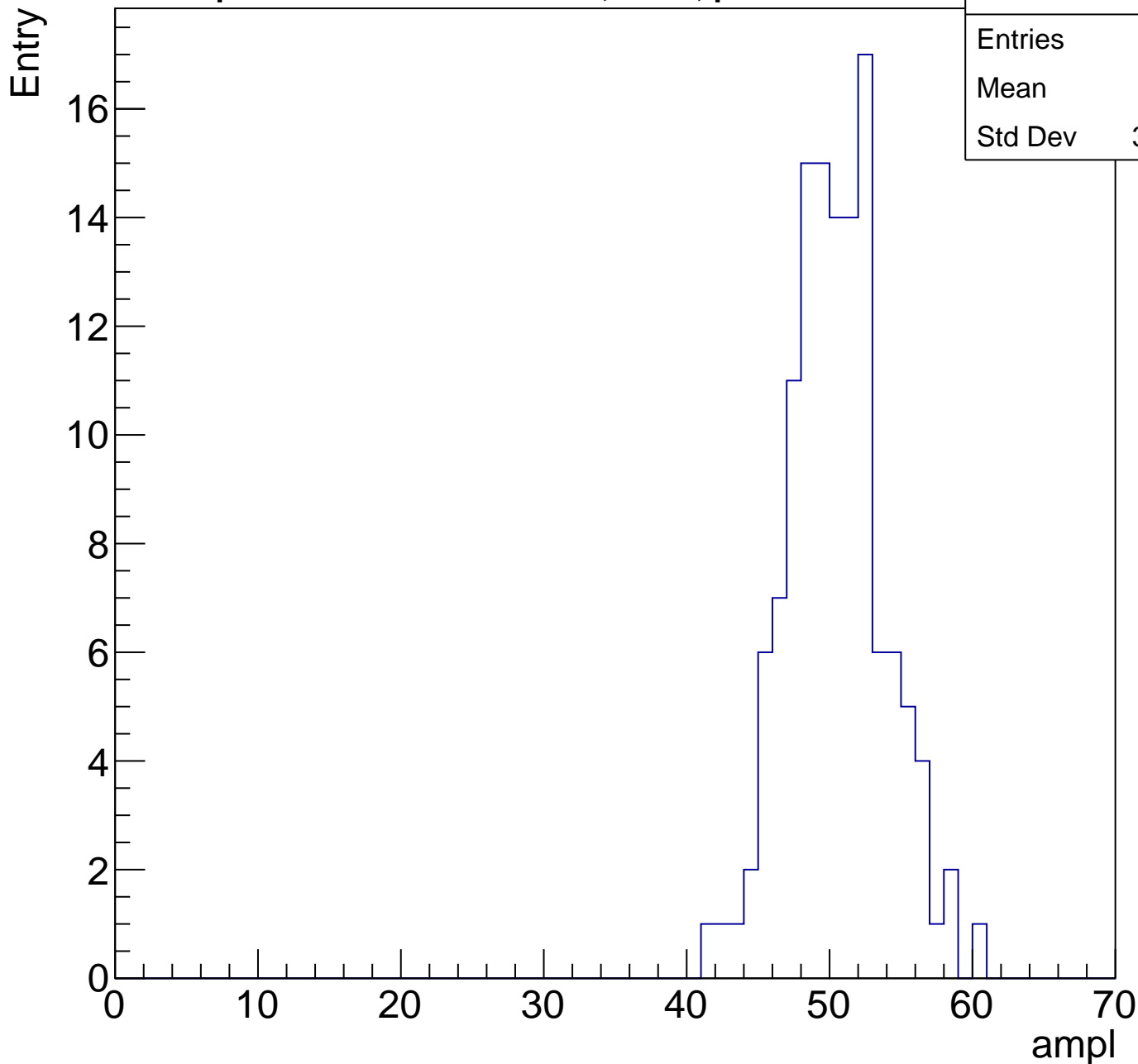
ampl



B1L001S, U22-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

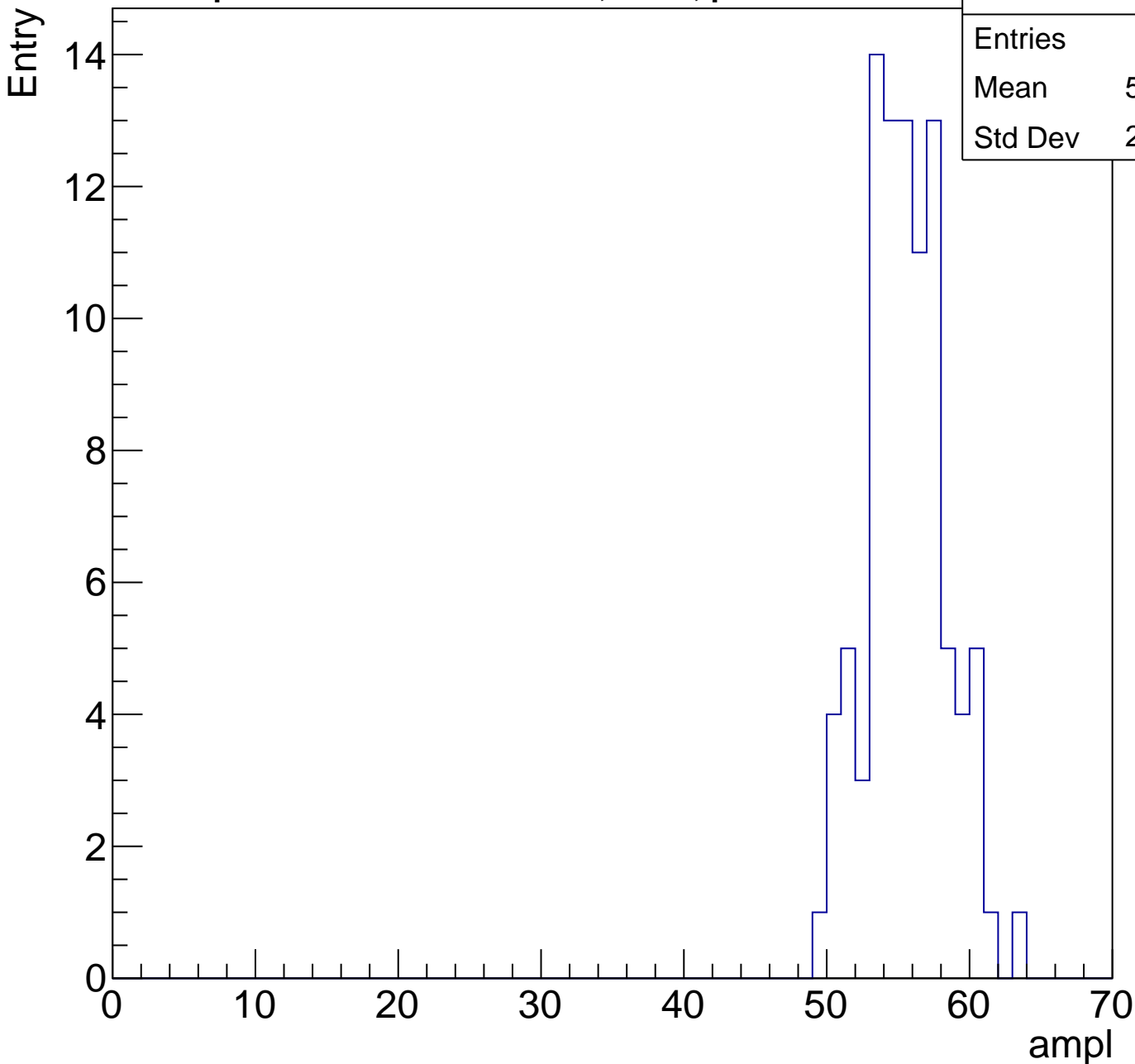
Entries	129
Mean	50
Std Dev	3.387



B1L001S, U22-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	55.12
Std Dev	2.759



B1L001S, U22-ch36, adc5

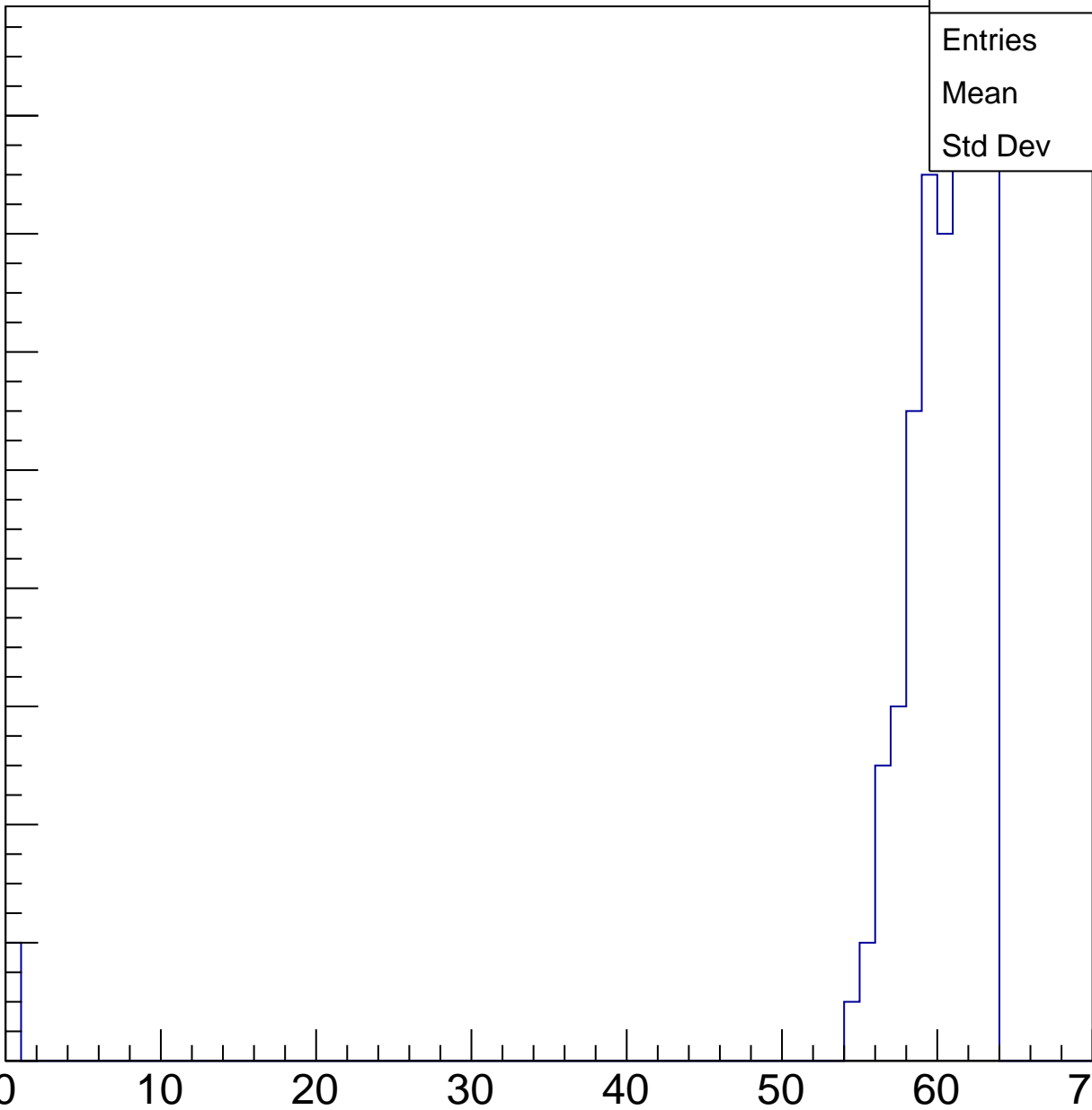
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.98
Std Dev	8.431

ampl



B1L001S, U22-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

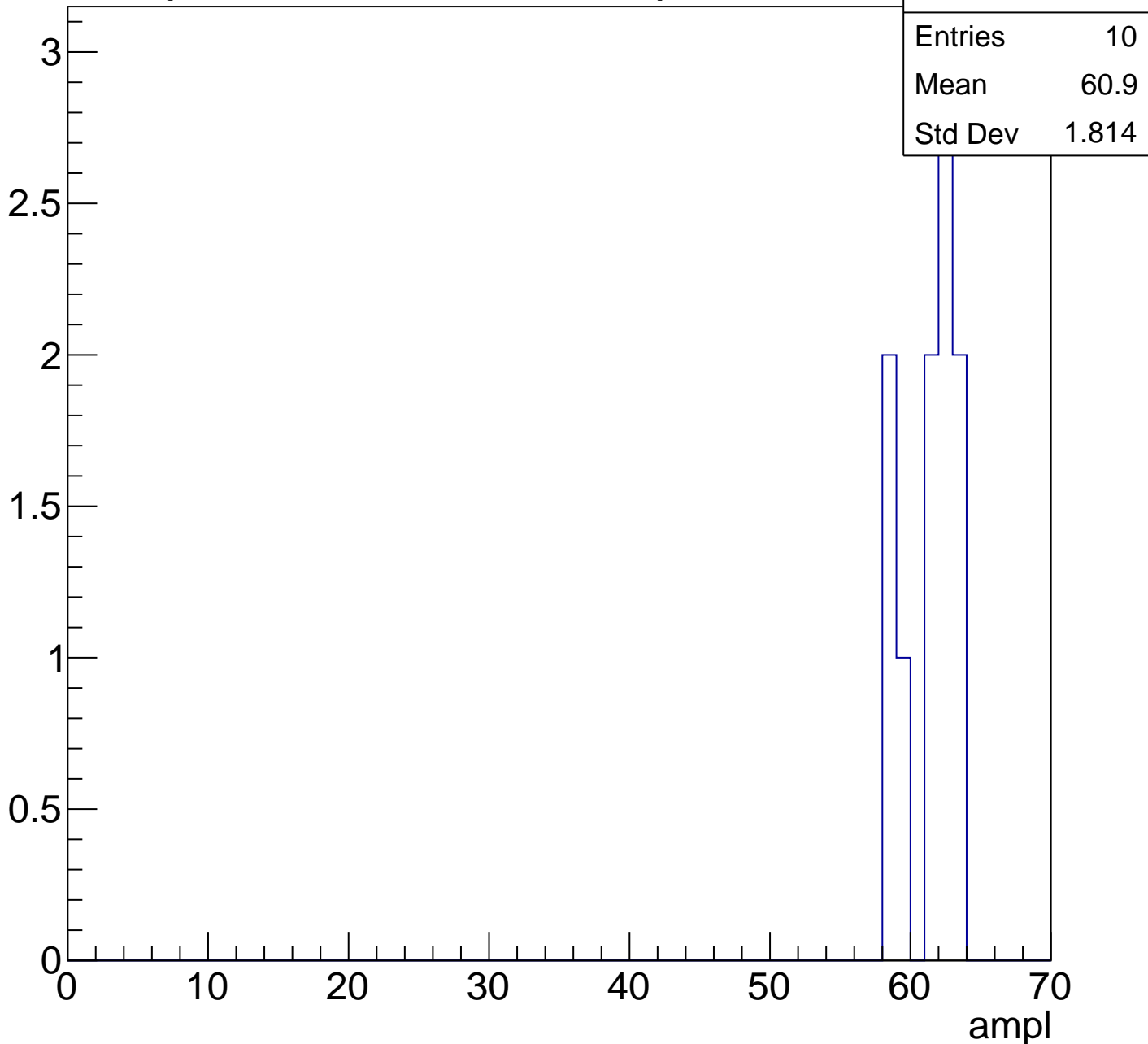
0.5

0

Entries	10
Mean	60.9
Std Dev	1.814

0 10 20 30 40 50 60 70

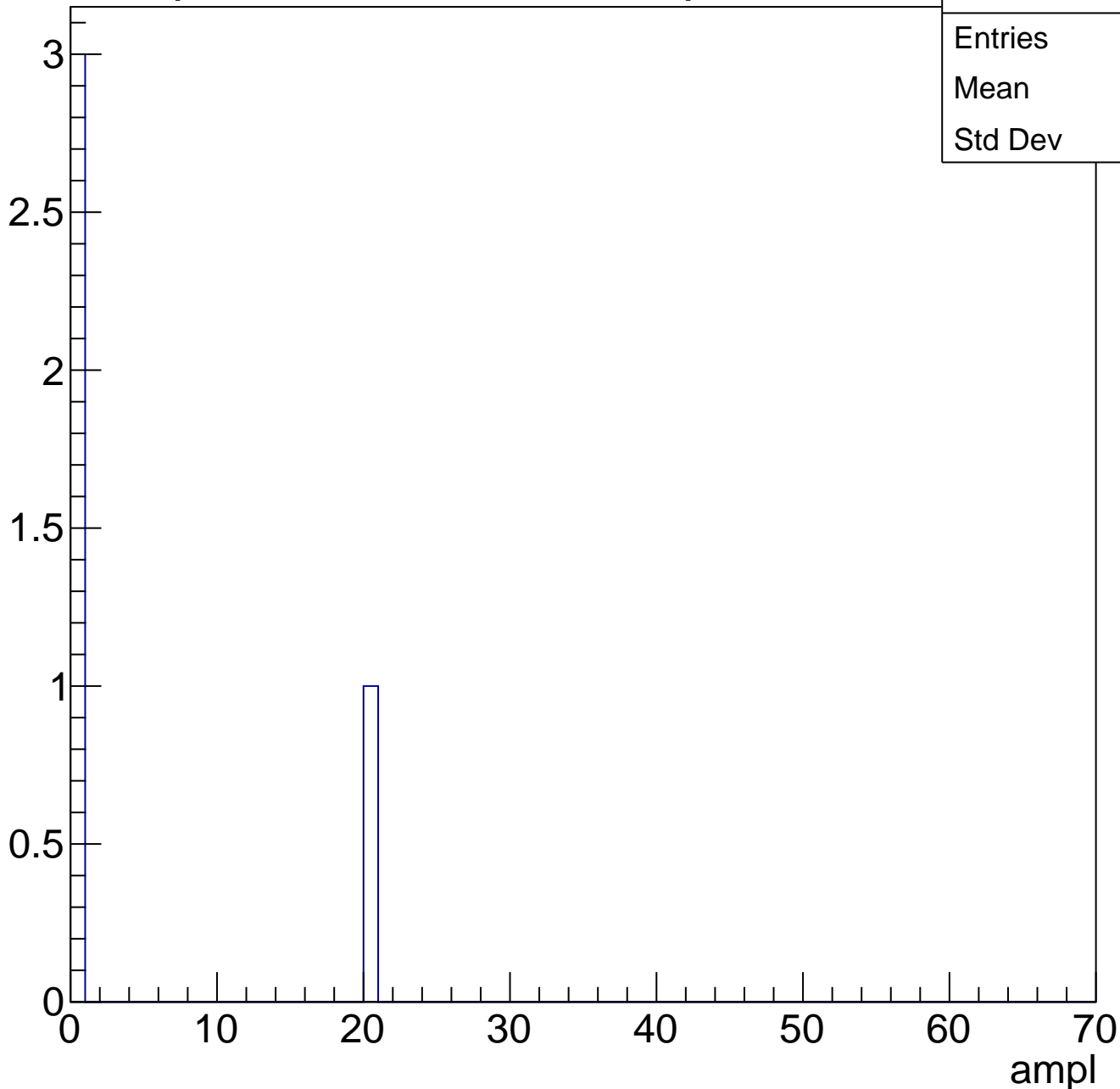
ampl



B1L001S, U22-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	5
Std Dev	8.66

B1L001S, U22-ch37, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

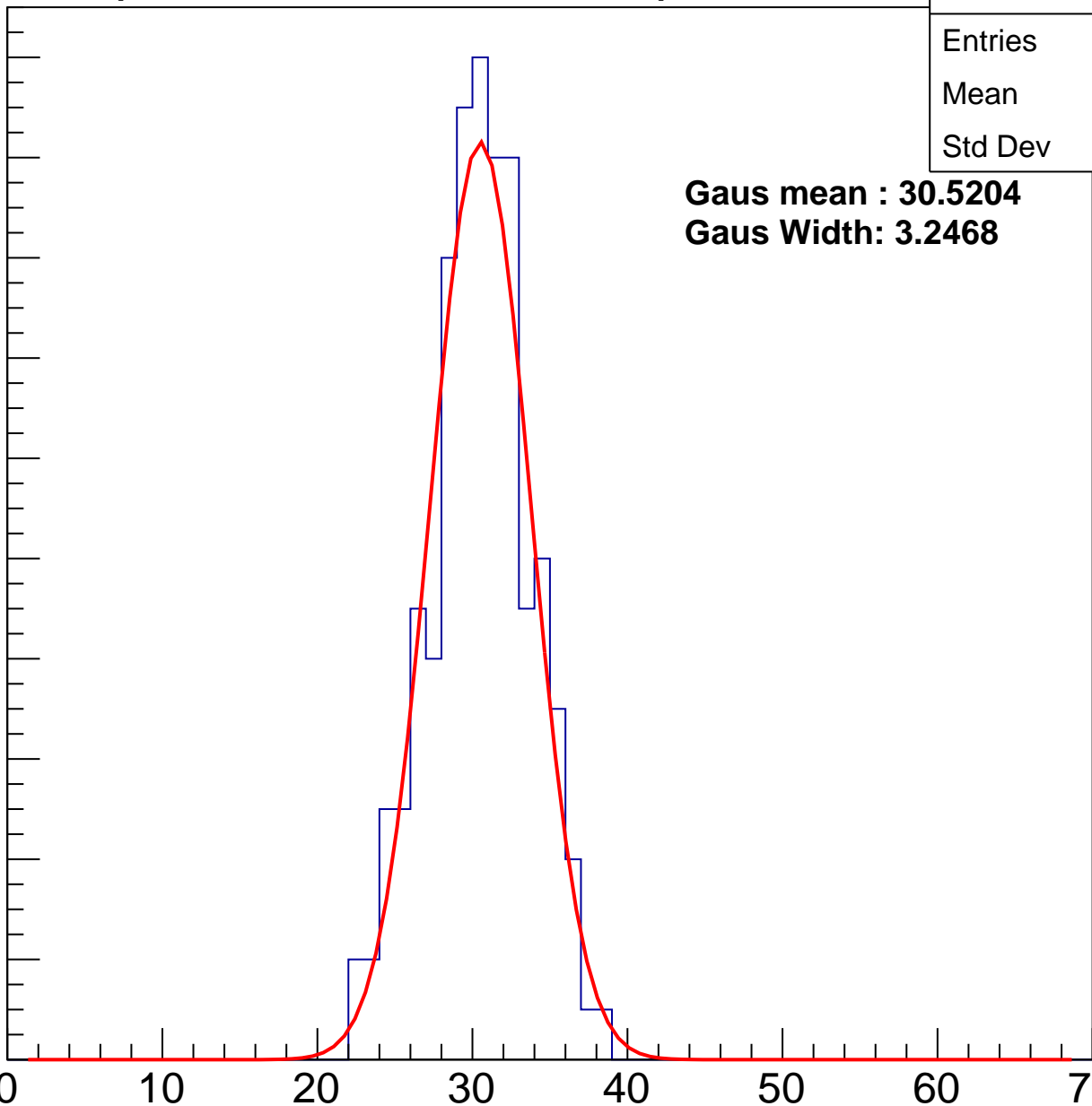
20
18
16
14
12
10
8
6
4
2
0

Entries	154
Mean	29.99
Std Dev	3.216

Gaus mean : 30.5204

Gaus Width: 3.2468

0 10 20 30 40 50 60 70
ampl



B1L001S, U22-ch37, adc1

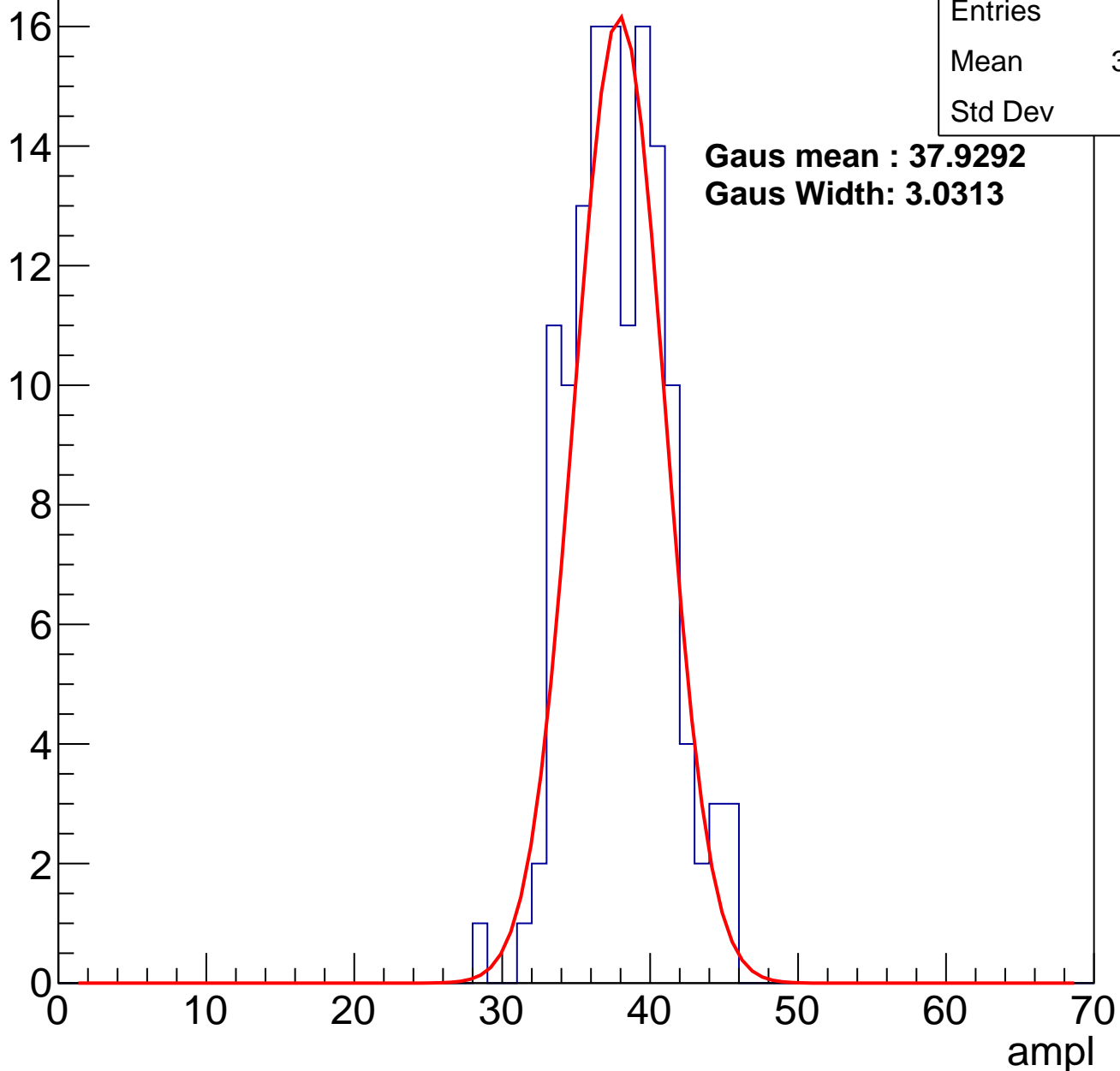
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	37.46
Std Dev	3.18

Gaus mean : 37.9292

Gaus Width: 3.0313

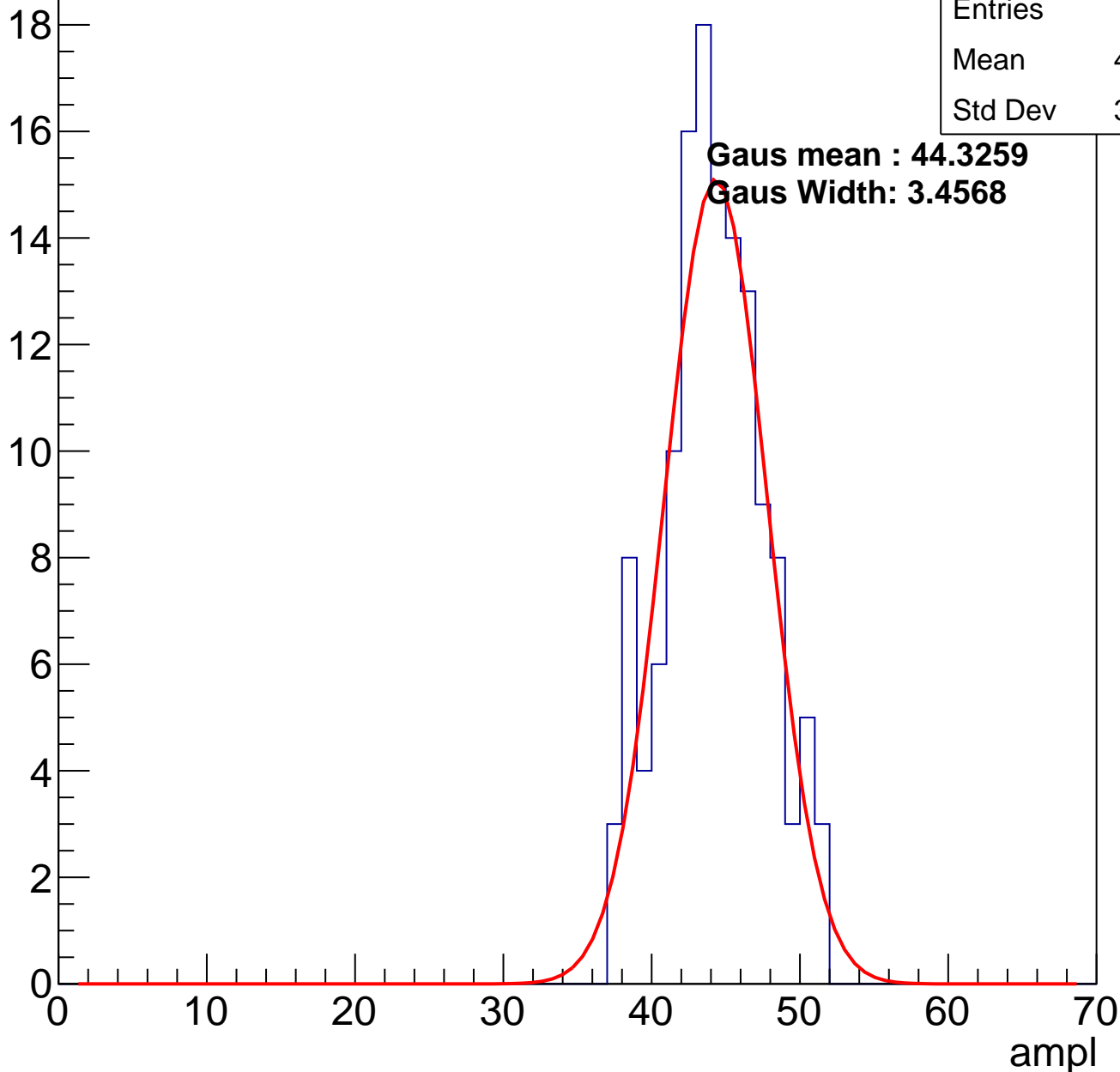
Entry



B1L001S, U22-ch37, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

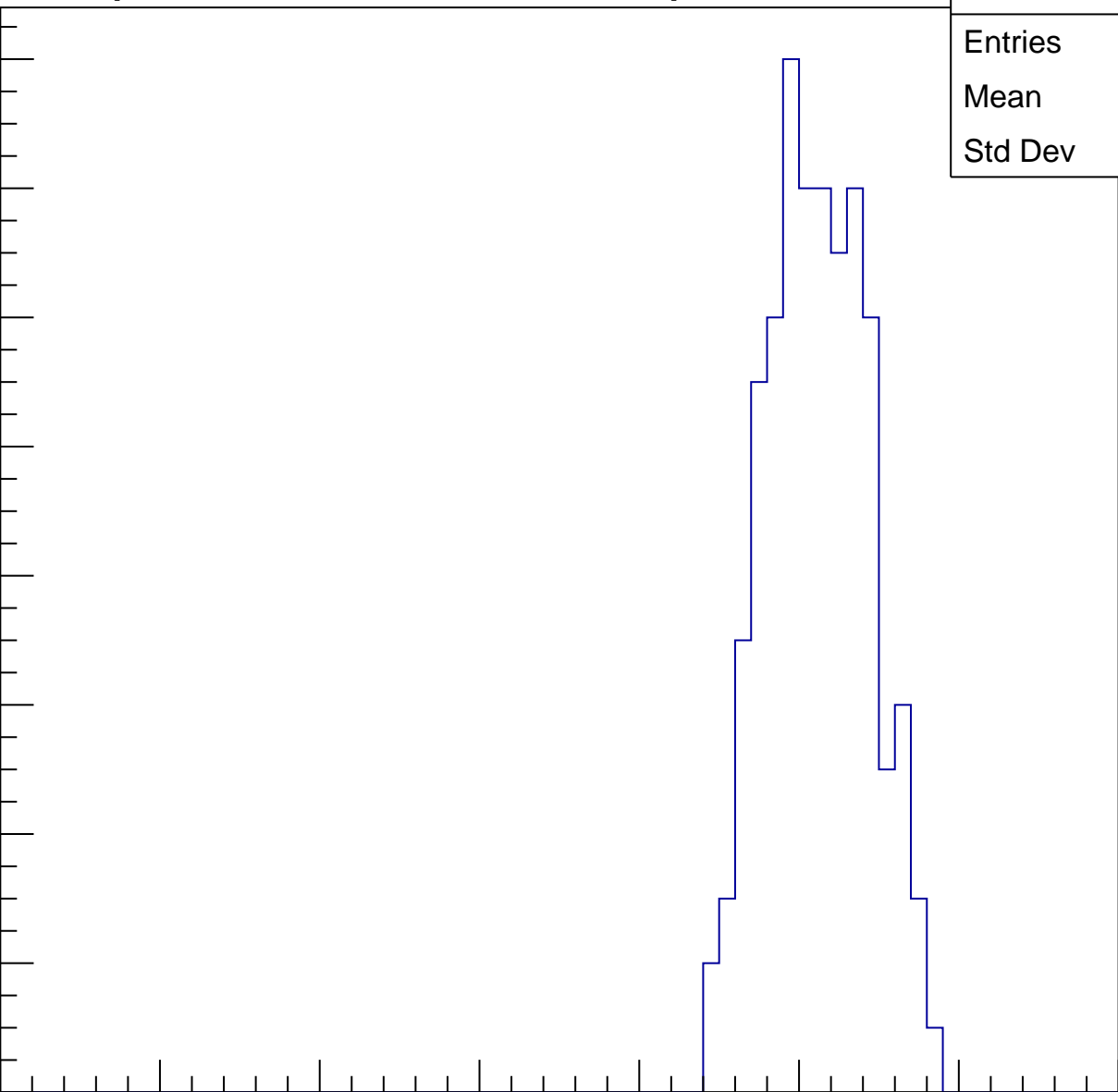
Entries	133
Mean	50.69
Std Dev	3.12

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch37, adc4

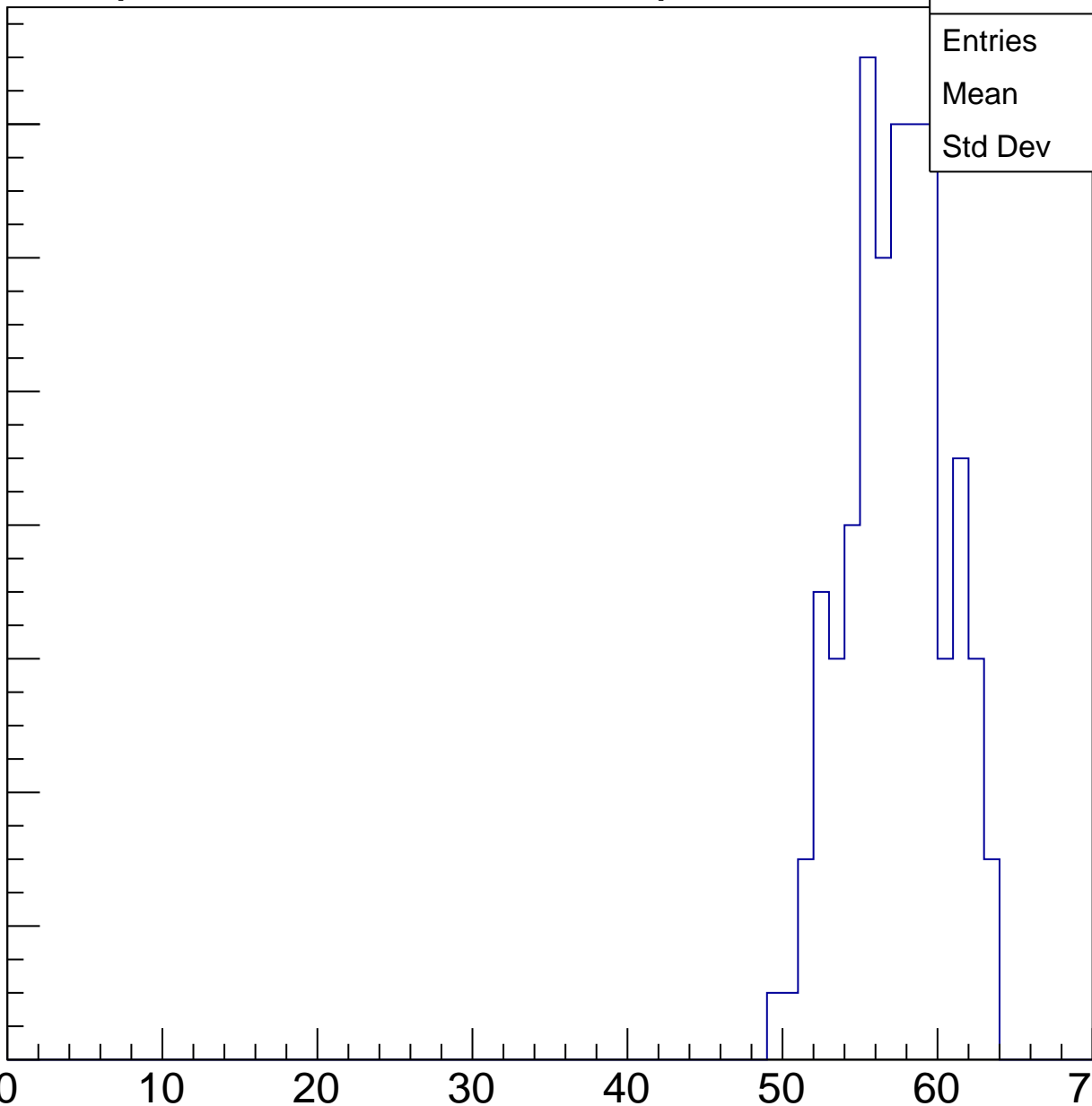
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	56.88
Std Dev	3.123

ampl



B1L001S, U22-ch37, adc5

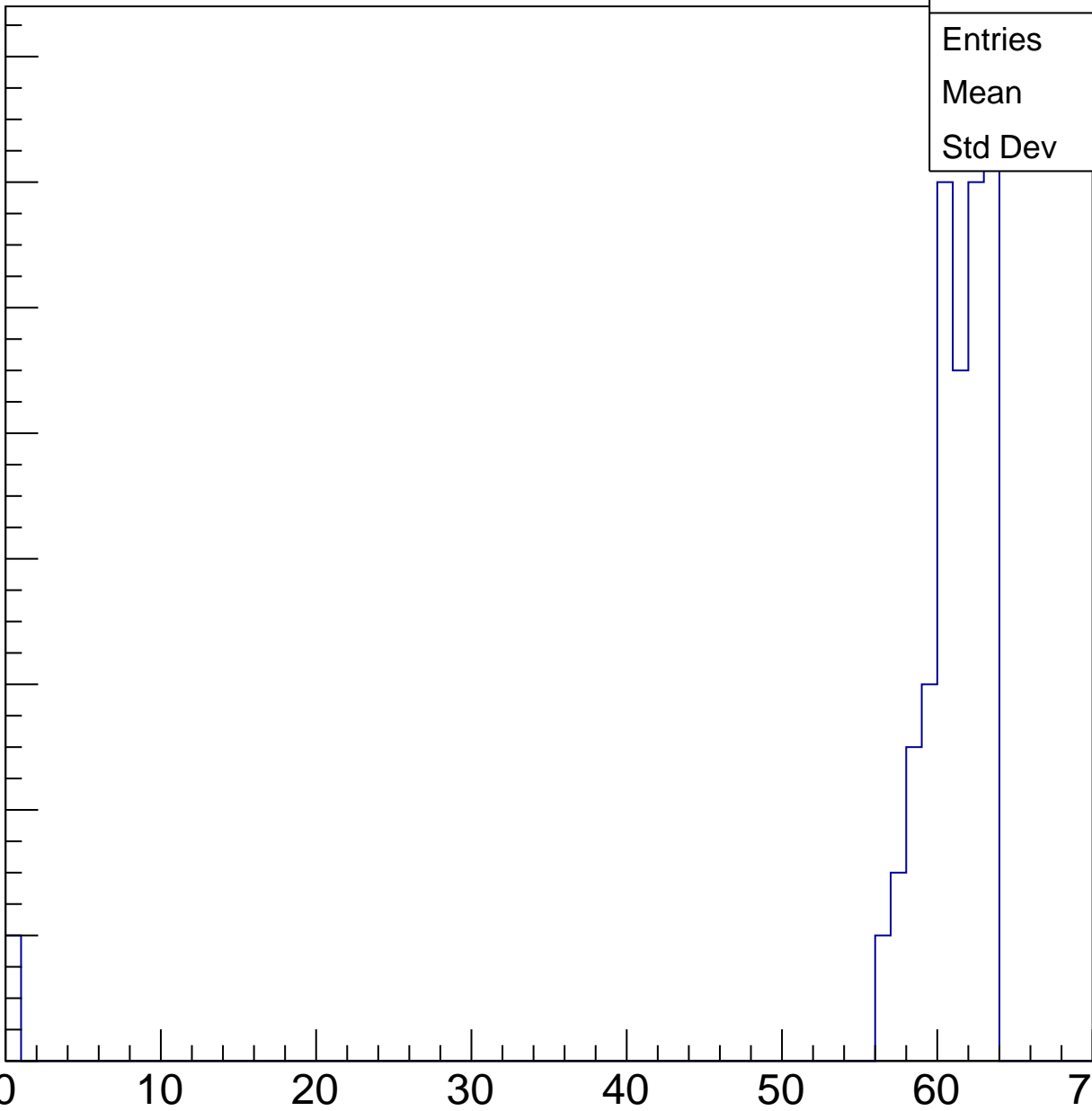
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.1
Std Dev	10.09

ampl



B1L001S, U22-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch38, adc0

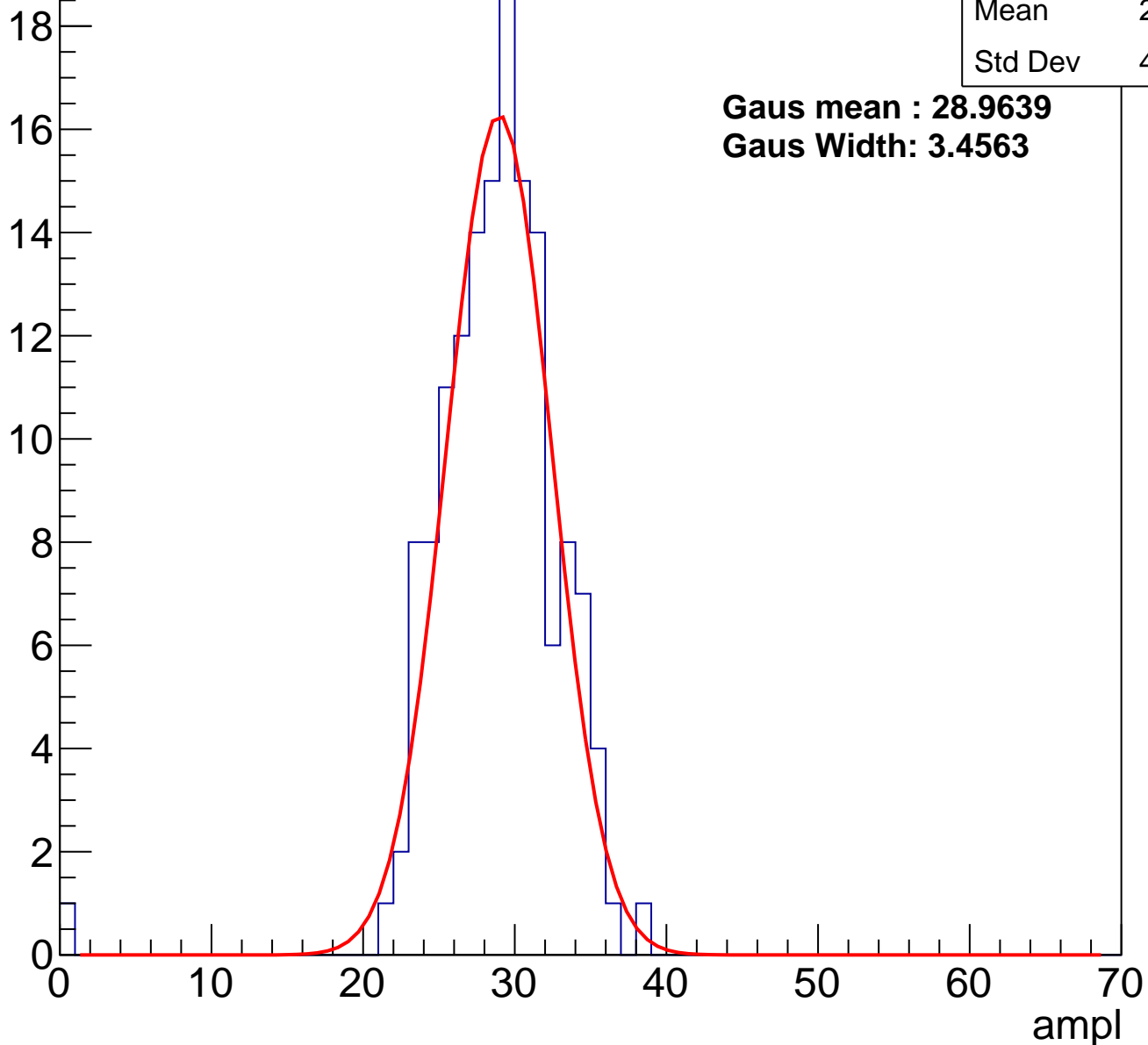
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	28.36
Std Dev	4.114

Gaus mean : 28.9639

Gaus Width: 3.4563

Entry



B1L001S, U22-ch38, adc1

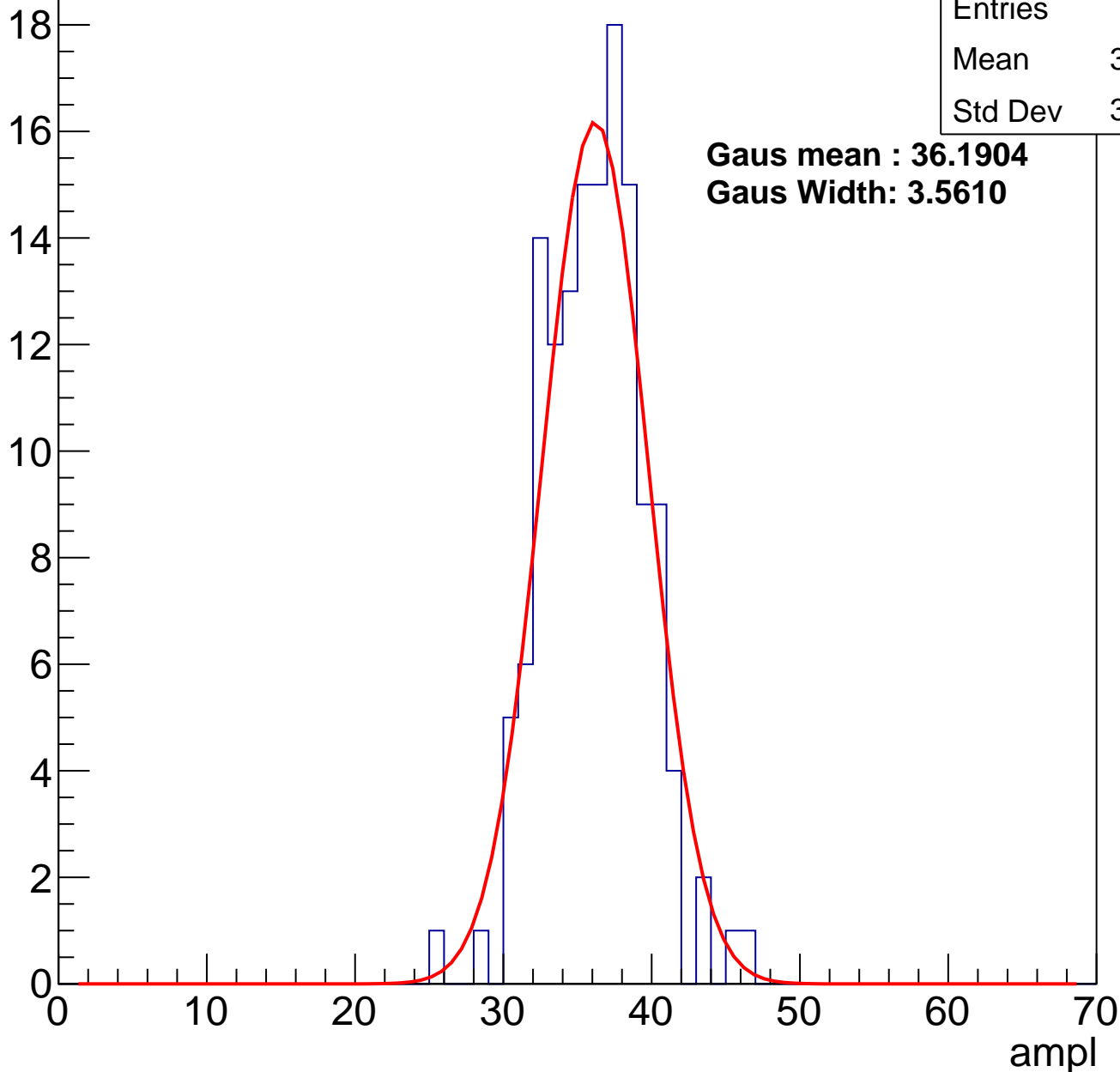
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	35.66
Std Dev	3.347

Gaus mean : 36.1904

Gaus Width: 3.5610

Entry



B1L001S, U22-ch38, adc2

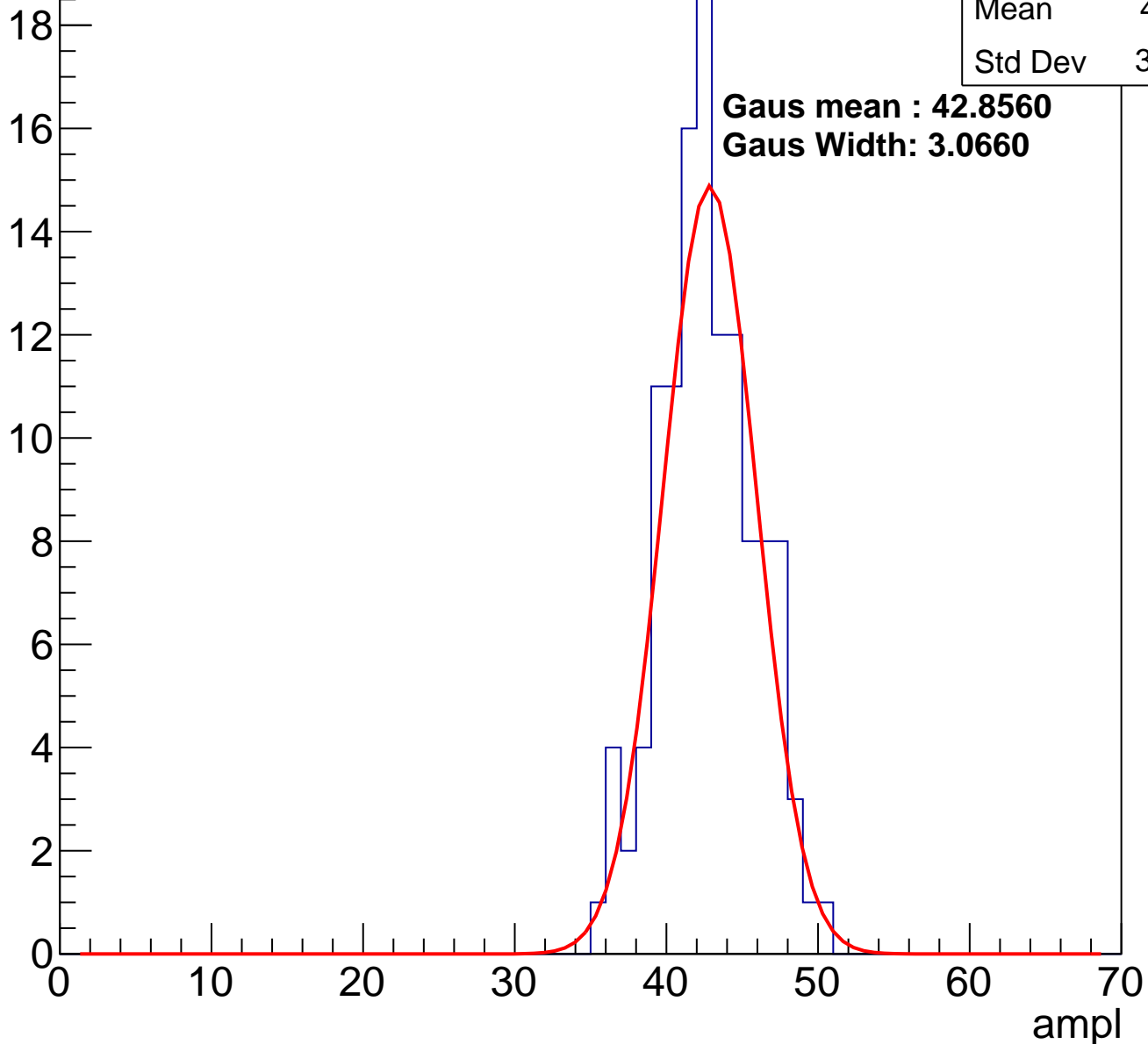
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.31
Std Dev	3.064

Gaus mean : 42.8560

Gaus Width: 3.0660

Entry



B1L001S, U22-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

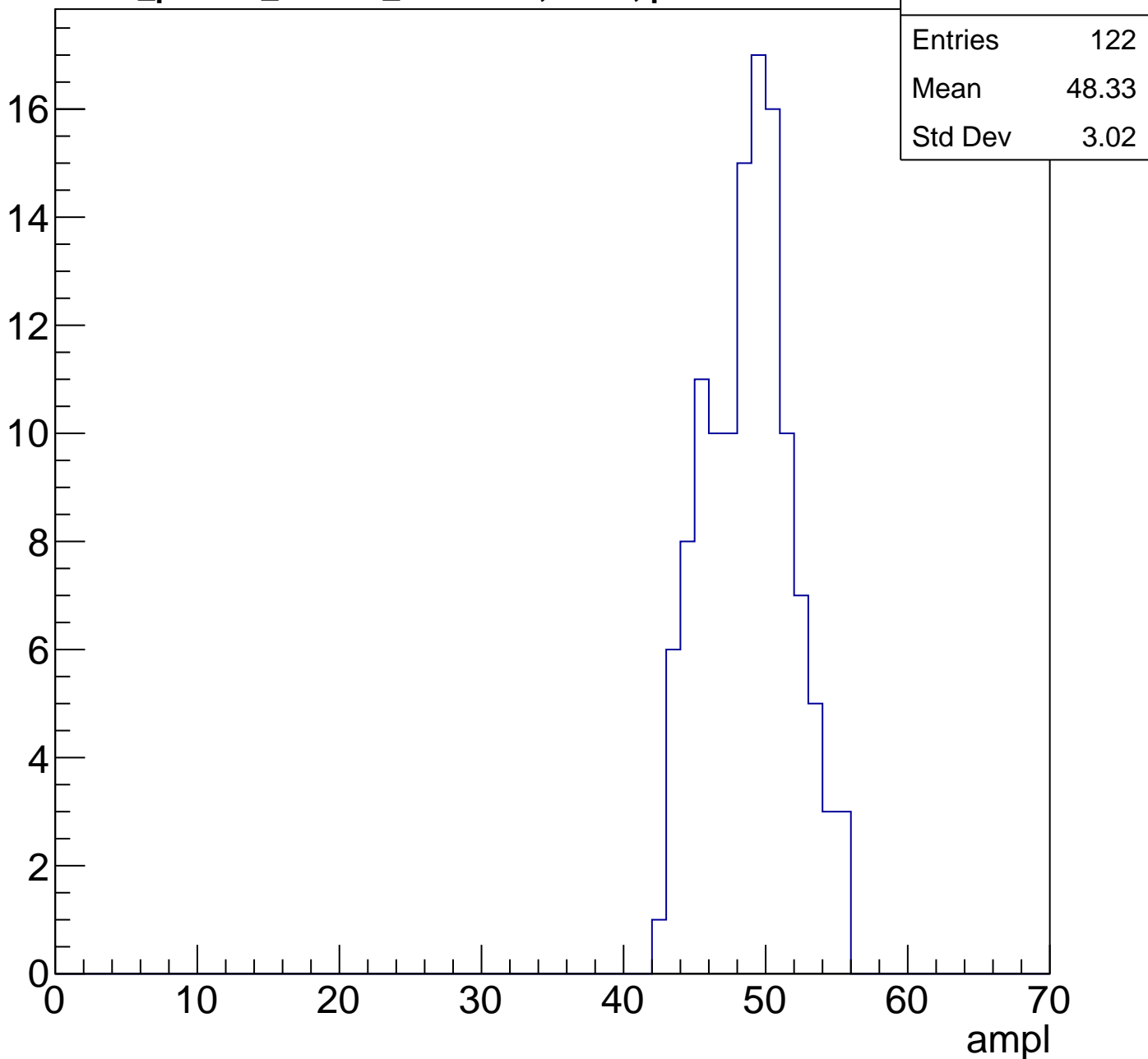
122

Mean

48.33

Std Dev

3.02

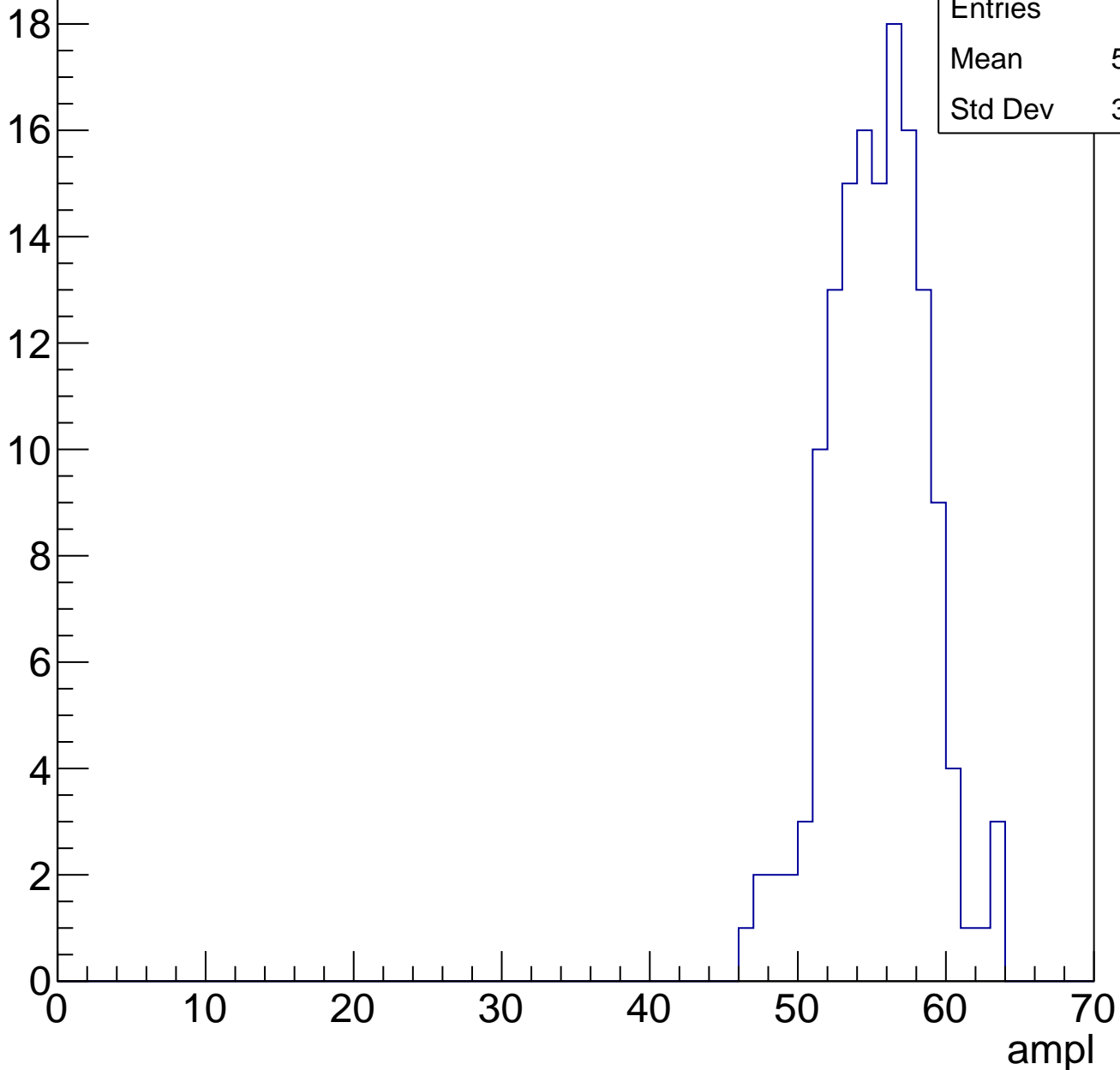


B1L001S, U22-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	54.94
Std Dev	3.256

Entry



B1L001S, U22-ch38, adc5

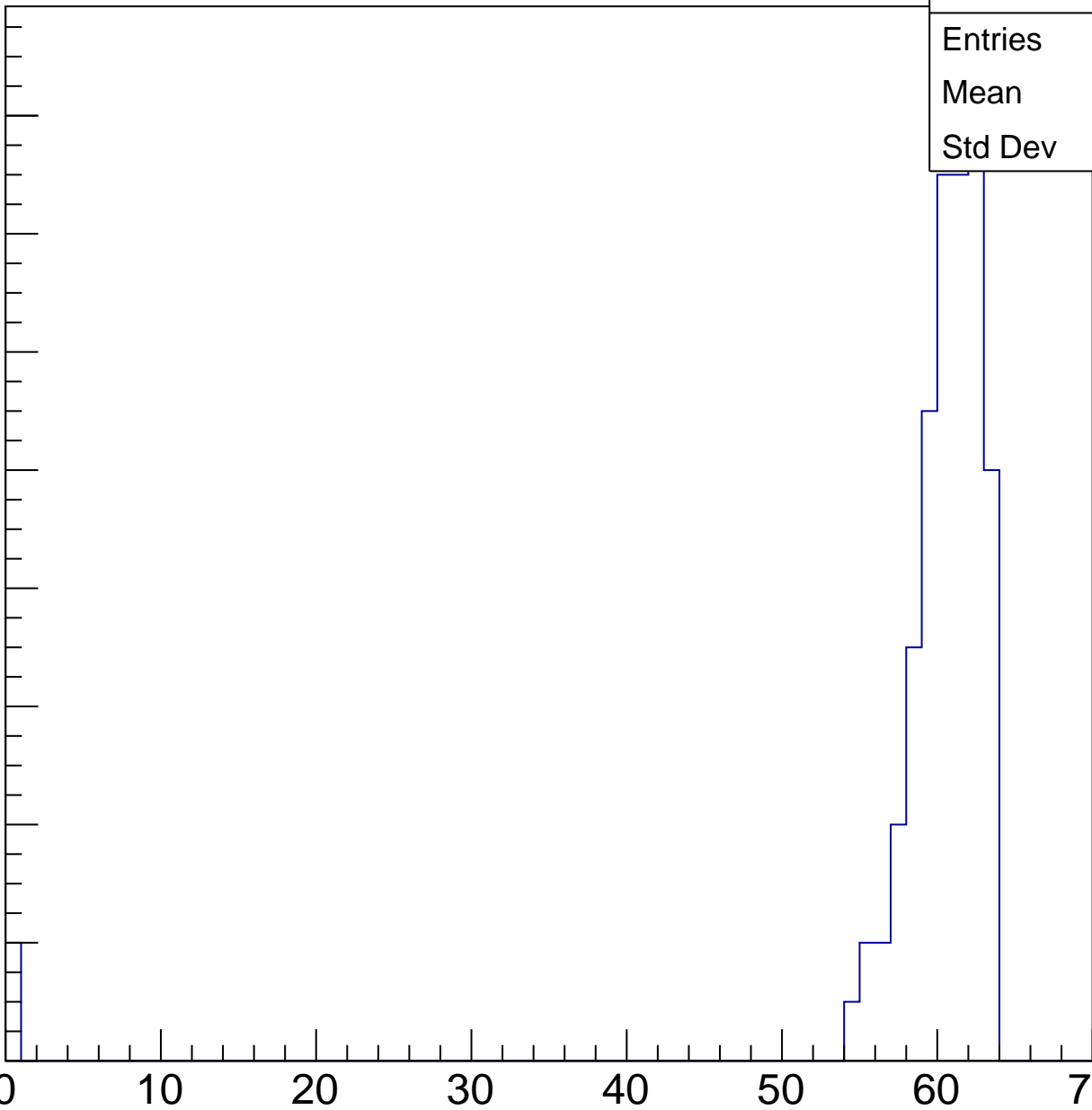
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.81
Std Dev	9.304

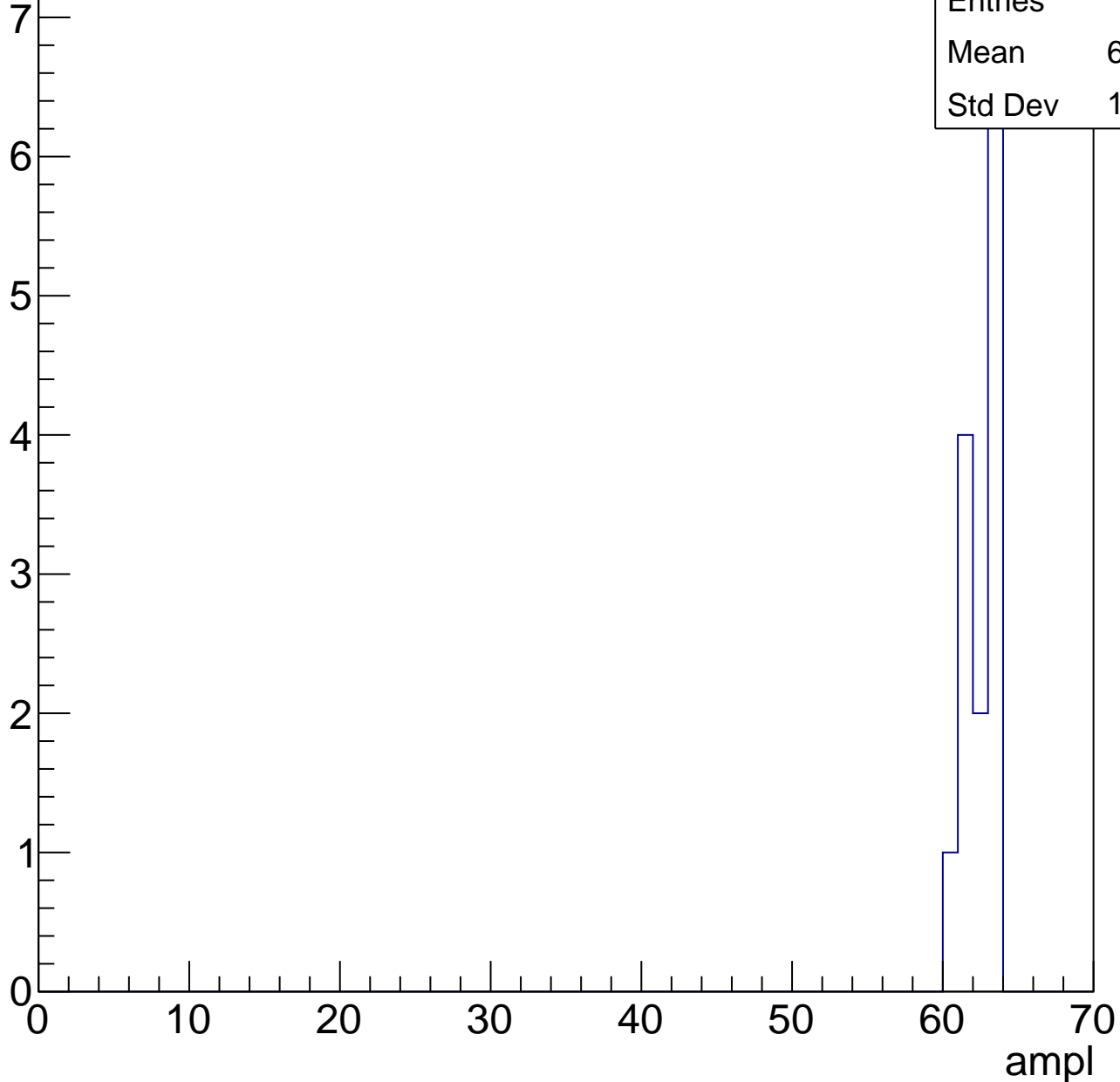
ampl



B1L001S, U22-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch38, adc7

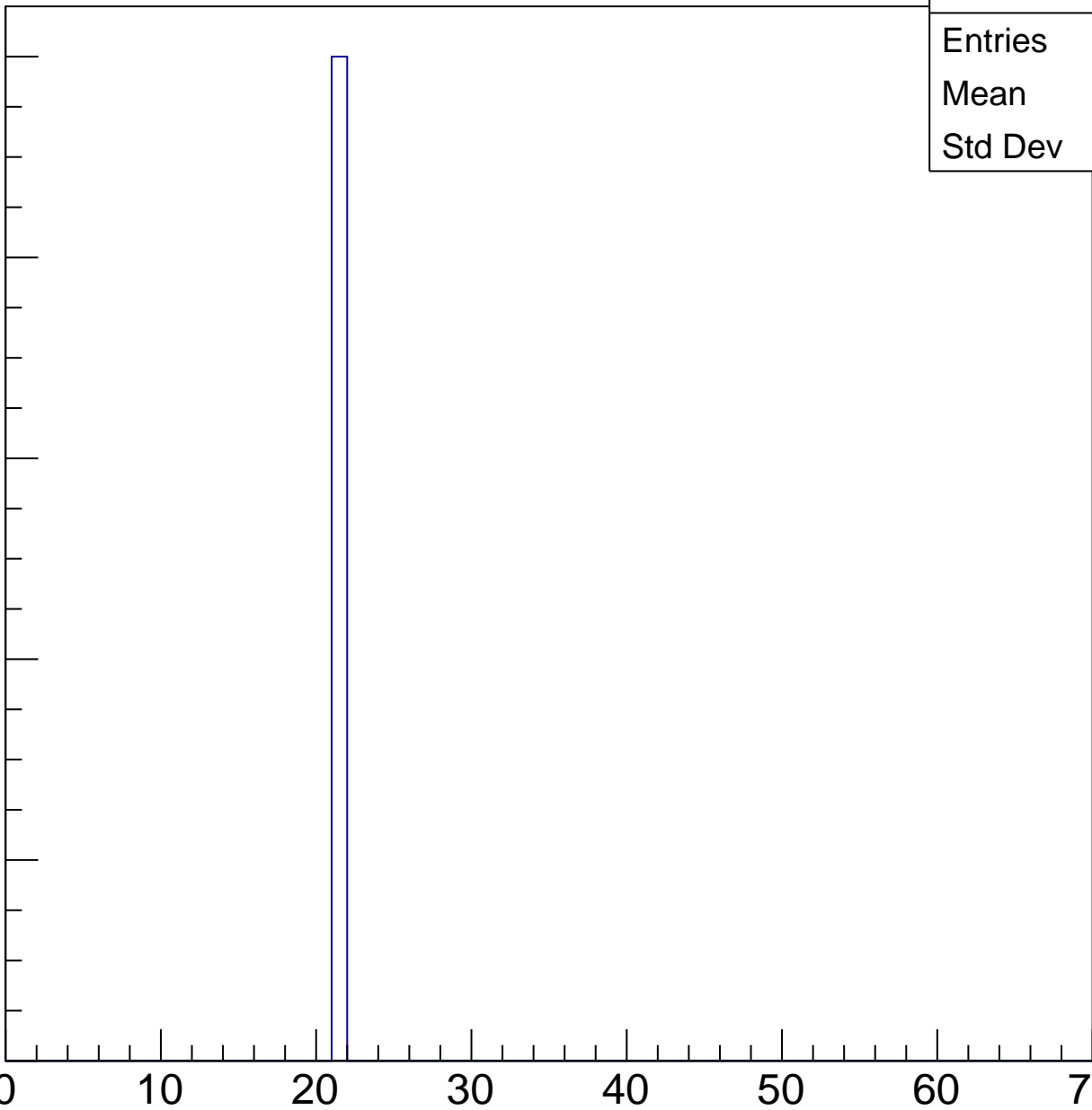
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl



B1L001S, U22-ch39, adc0

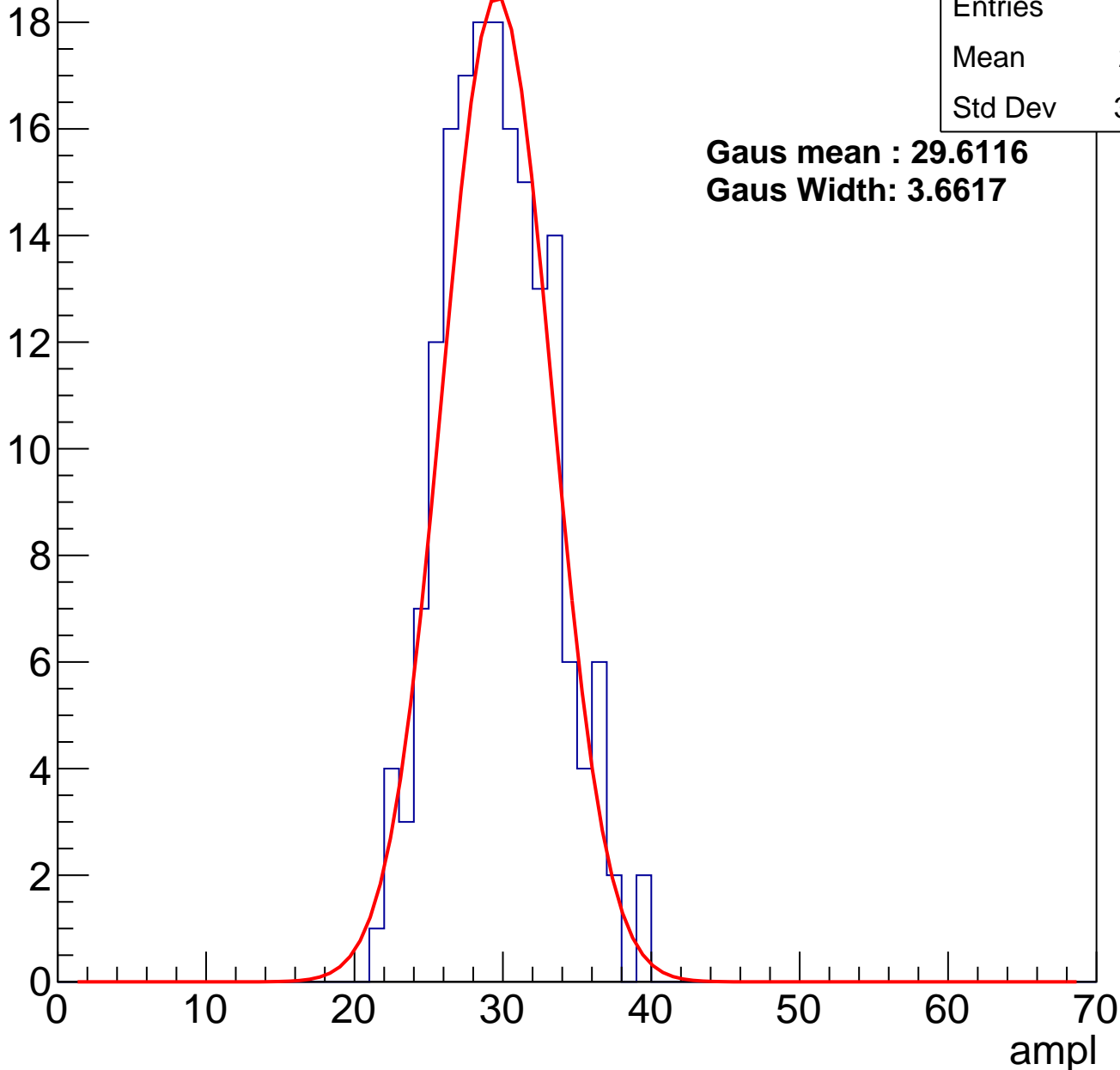
calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	29.21
Std Dev	3.628

Gaus mean : 29.6116

Gaus Width: 3.6617

Entry



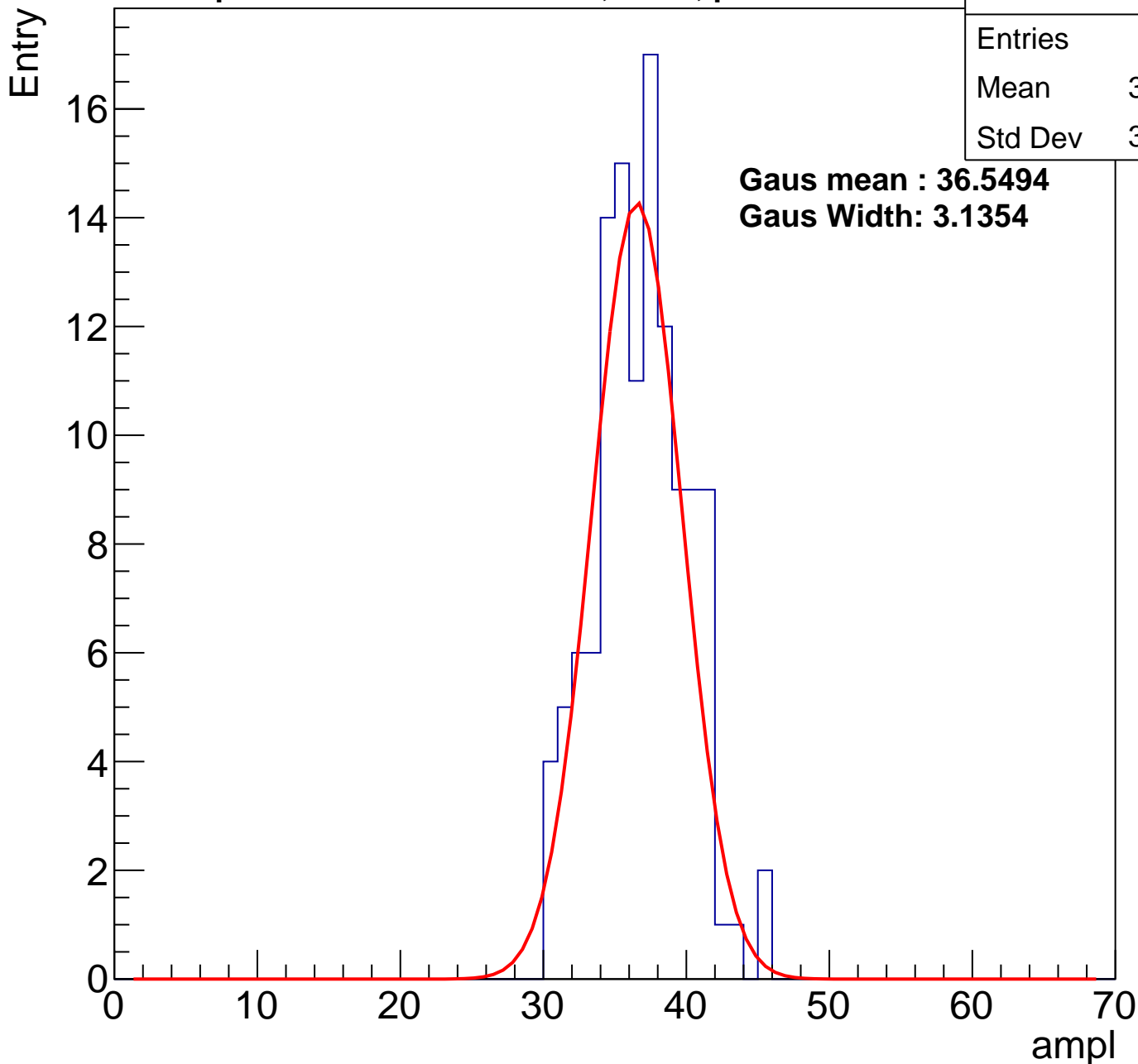
B1L001S, U22-ch39, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	36.38
Std Dev	3.194

Gaus mean : 36.5494

Gaus Width: 3.1354



B1L001S, U22-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

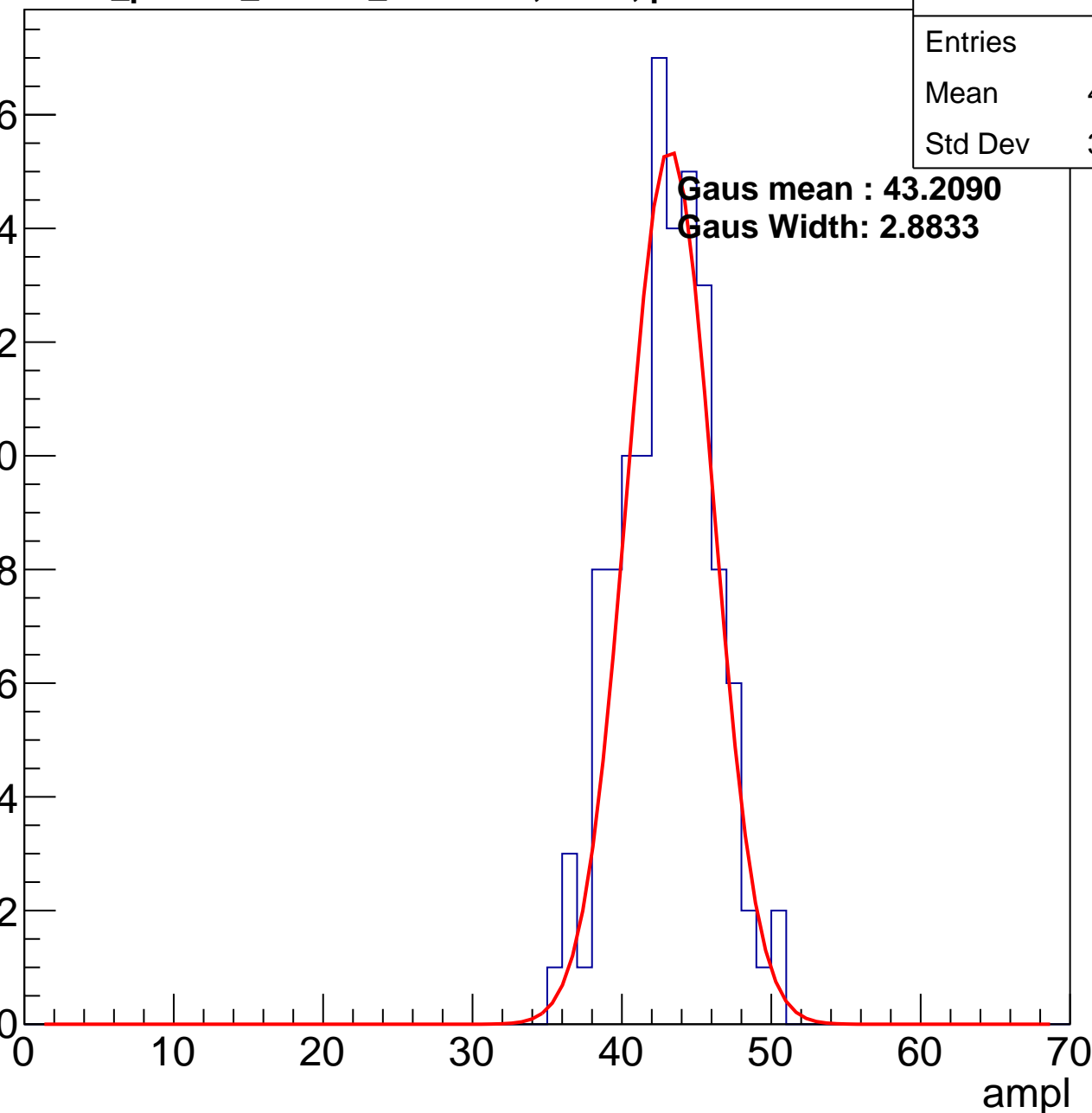
Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	42.54
Std Dev	3.062

Gaus mean : 43.2090

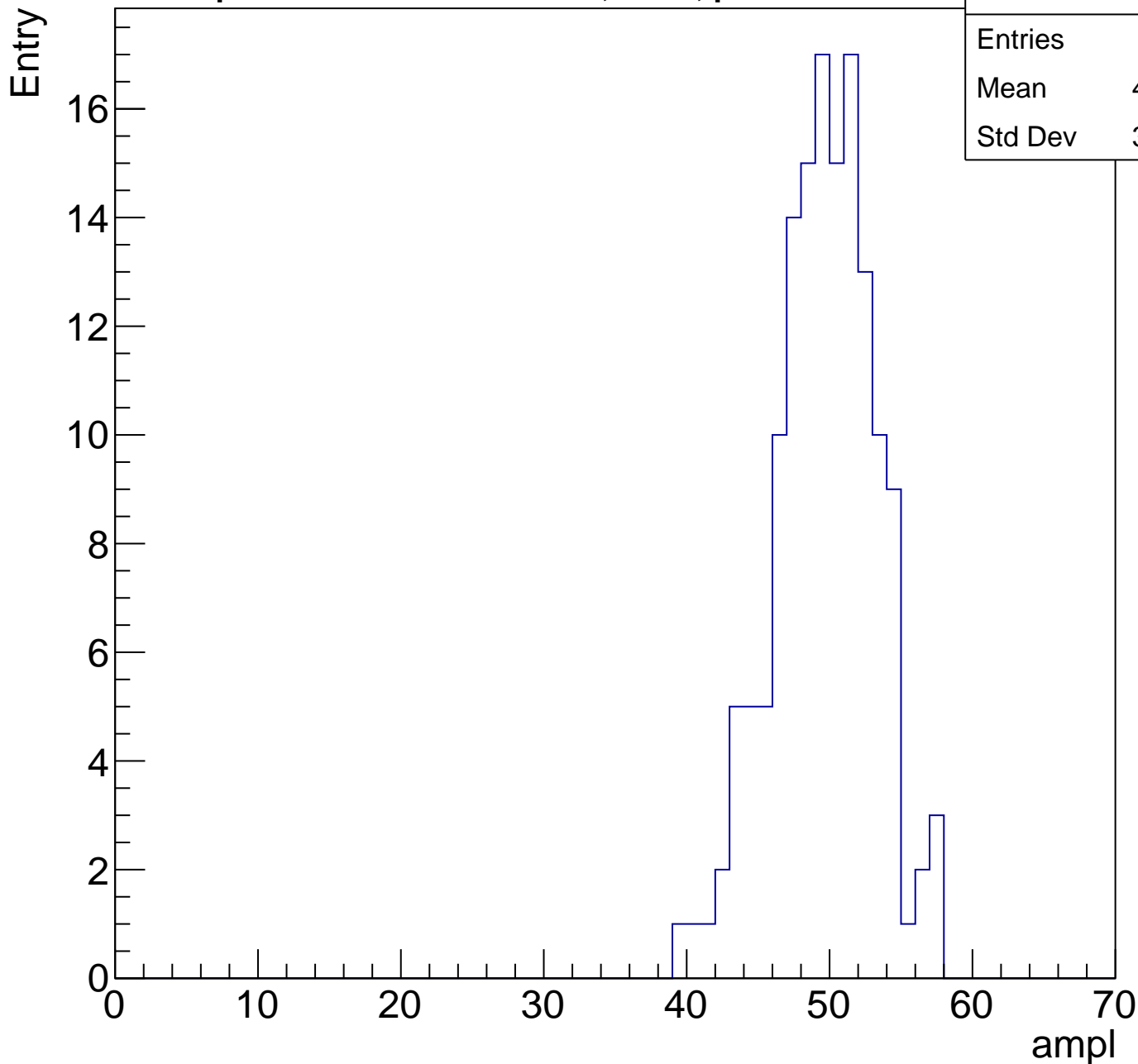
Gaus Width: 2.8833



B1L001S, U22-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	49.19
Std Dev	3.506



B1L001S, U22-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

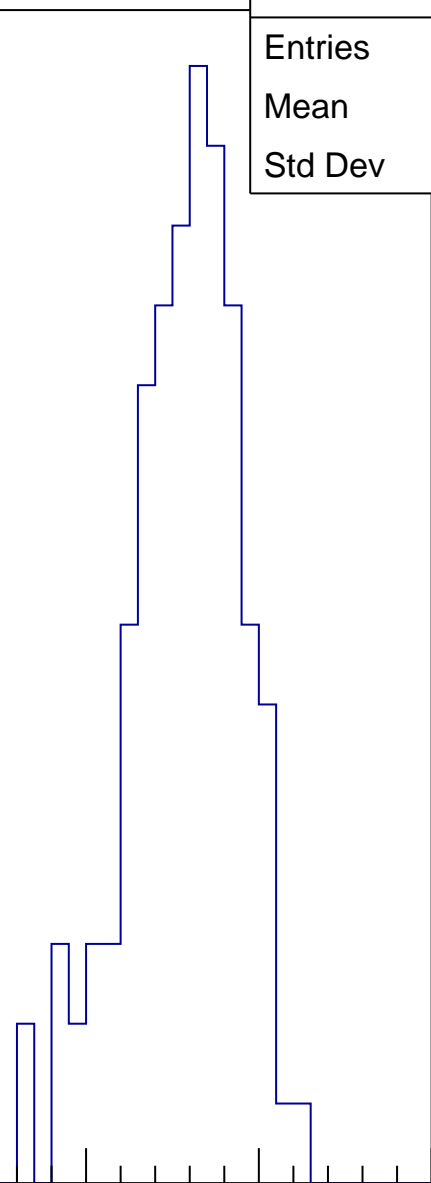
Entries	106
Mean	55.13
Std Dev	3.262

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch39, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries	93
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Mean	59.86
------	-------

Std Dev	2.326
---------	-------

ampl

0

10

20

30

40

50

60

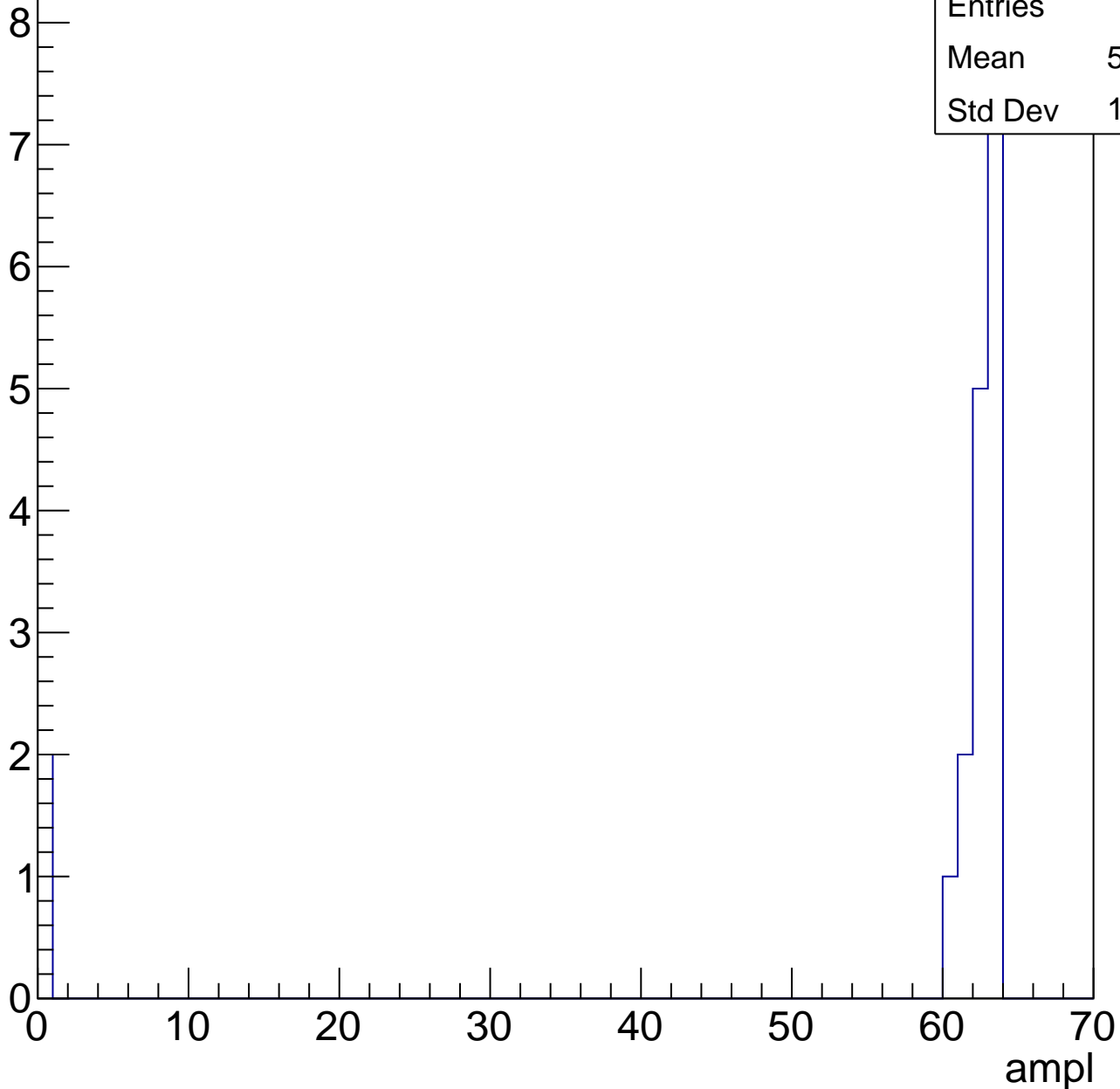
70

B1L001S, U22-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

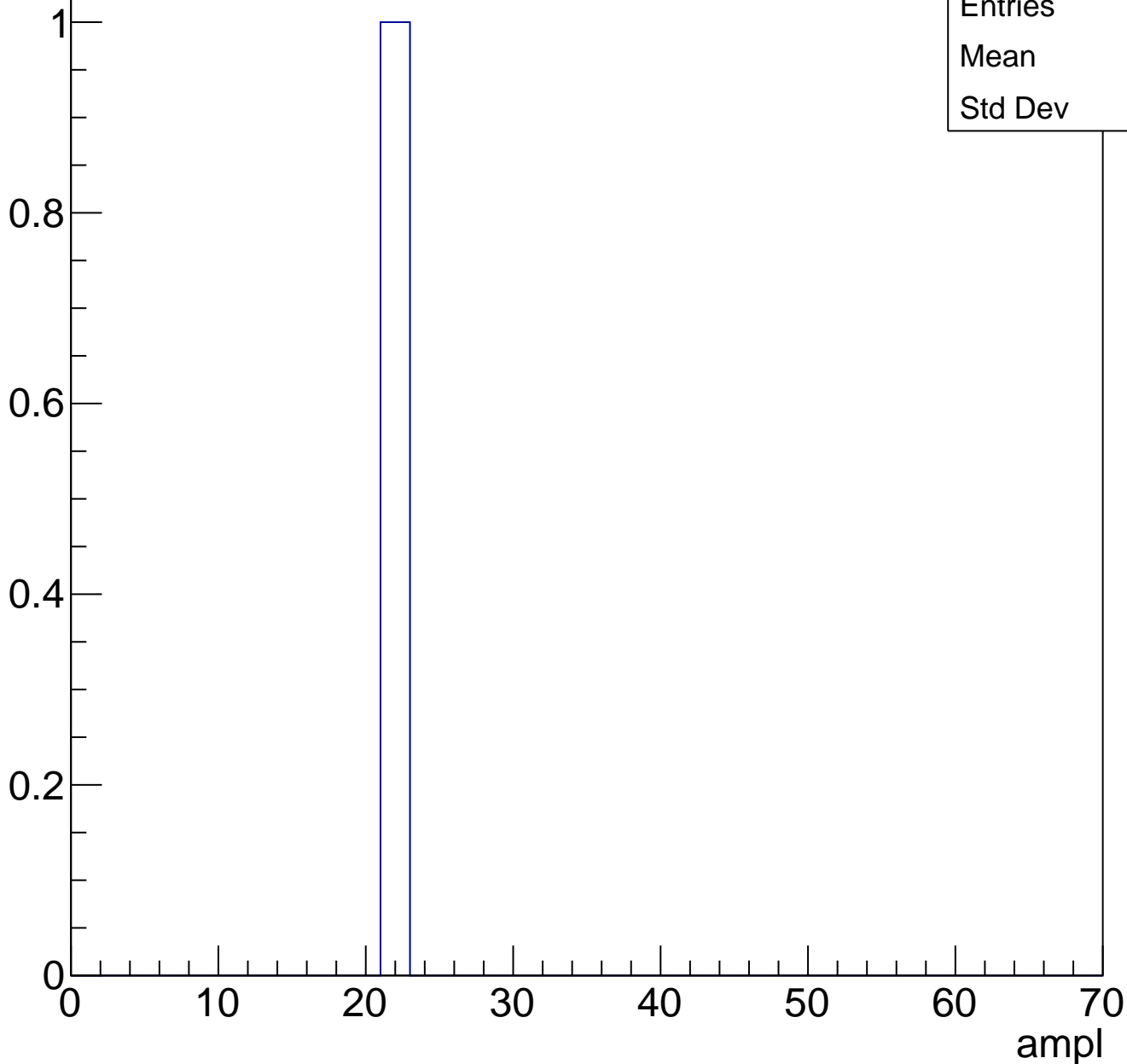
Entries	18
Mean	55.33
Std Dev	19.58



B1L001S, U22-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch40, adc0

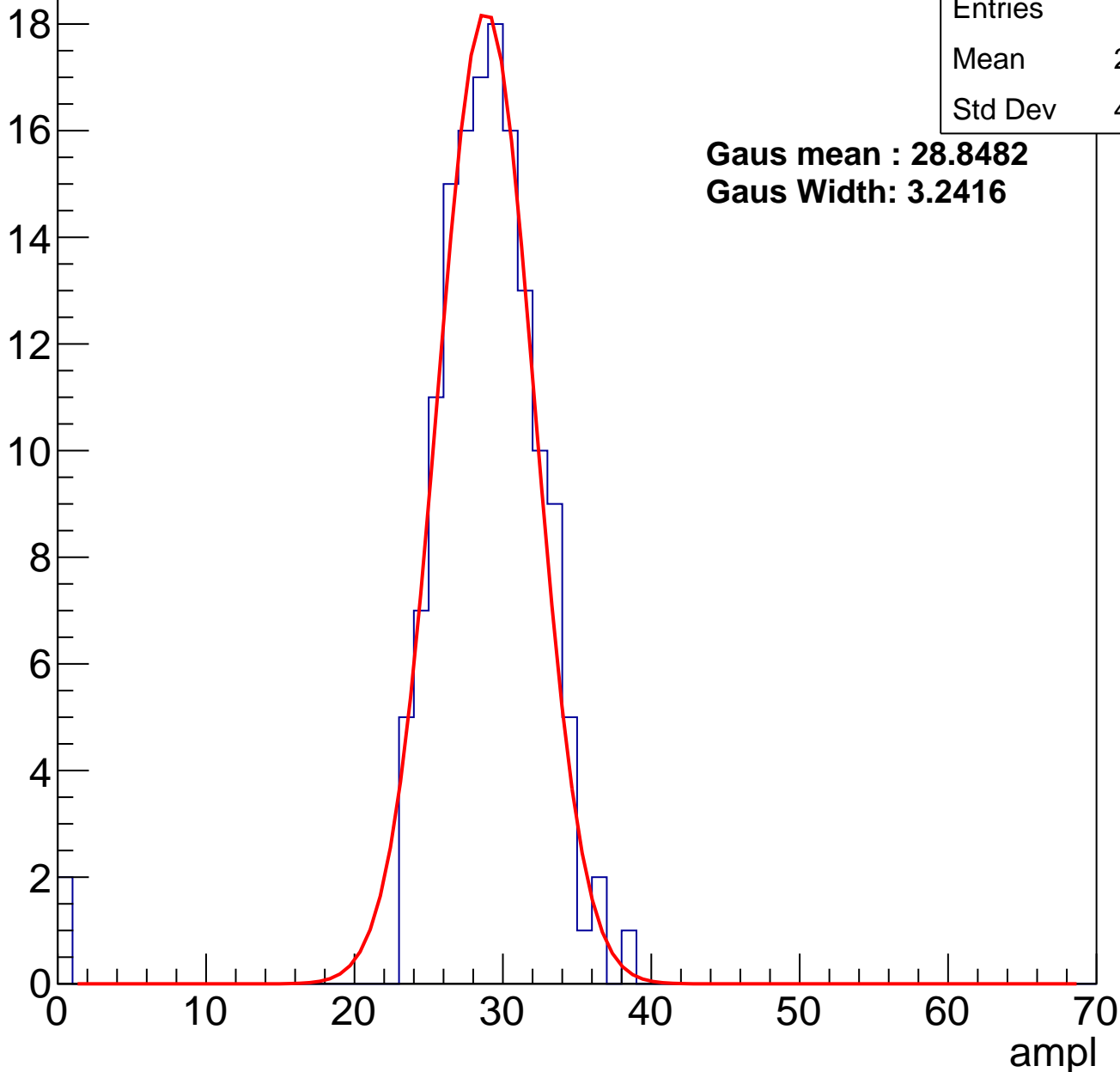
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	28.33
Std Dev	4.506

Gaus mean : 28.8482

Gaus Width: 3.2416

Entry



B1L001S, U22-ch40, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	35.57
Std Dev	3.281

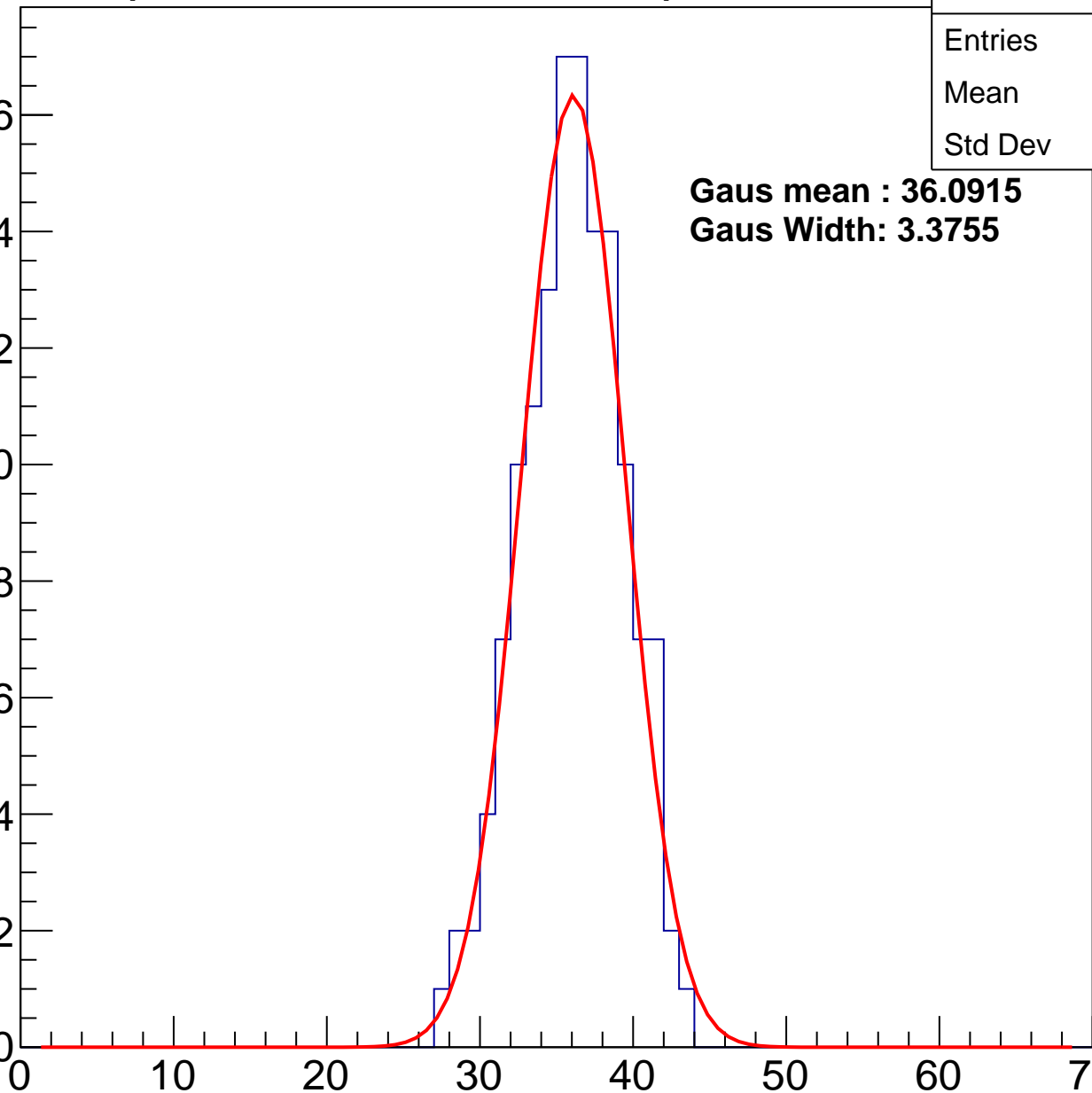
Gaus mean : 36.0915

Gaus Width: 3.3755

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	42.04
Std Dev	3.21

Gaus mean : 42.7023

Gaus Width: 3.0996

0

10

20

30

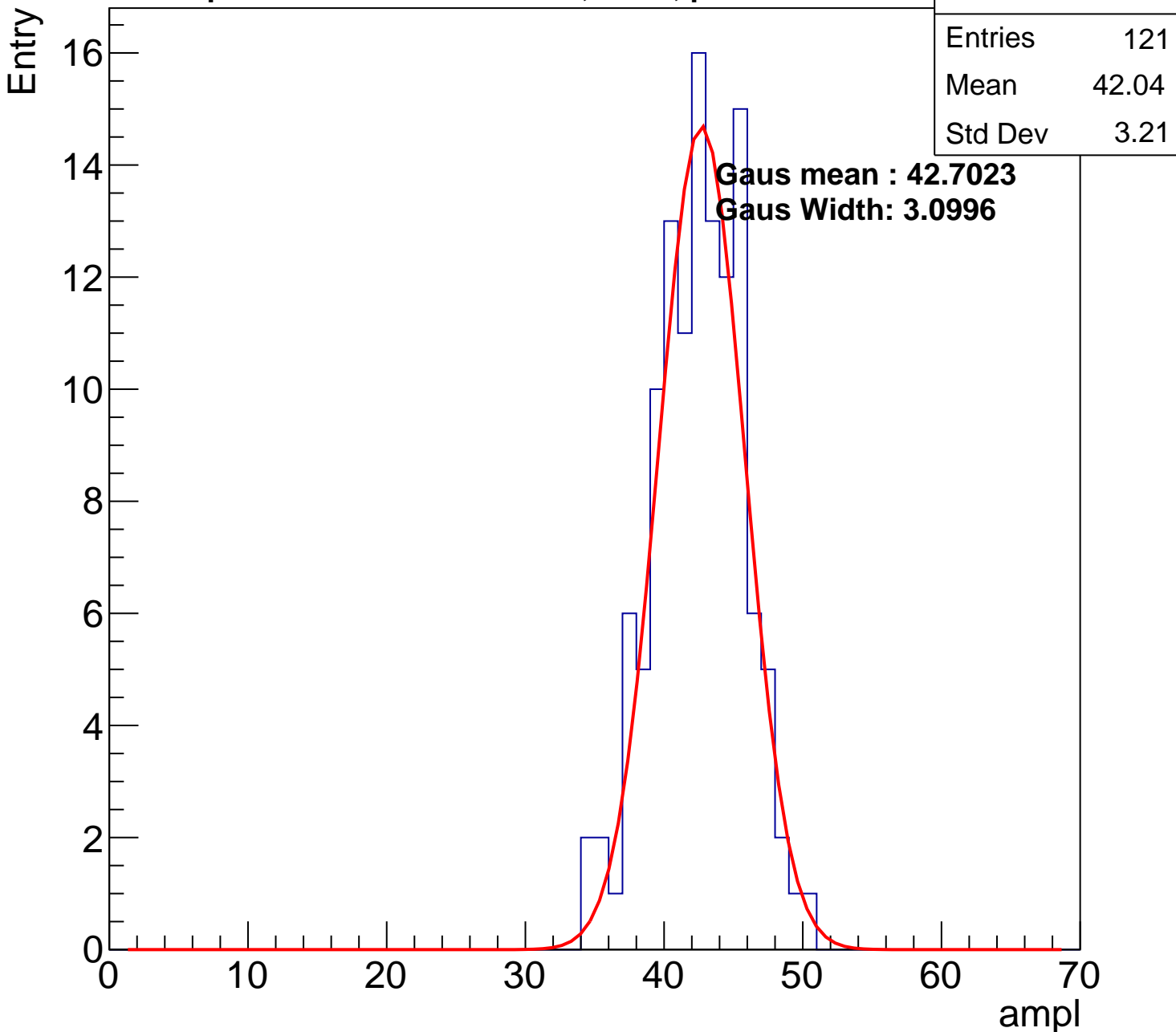
40

50

60

70

ampl



B1L001S, U22-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

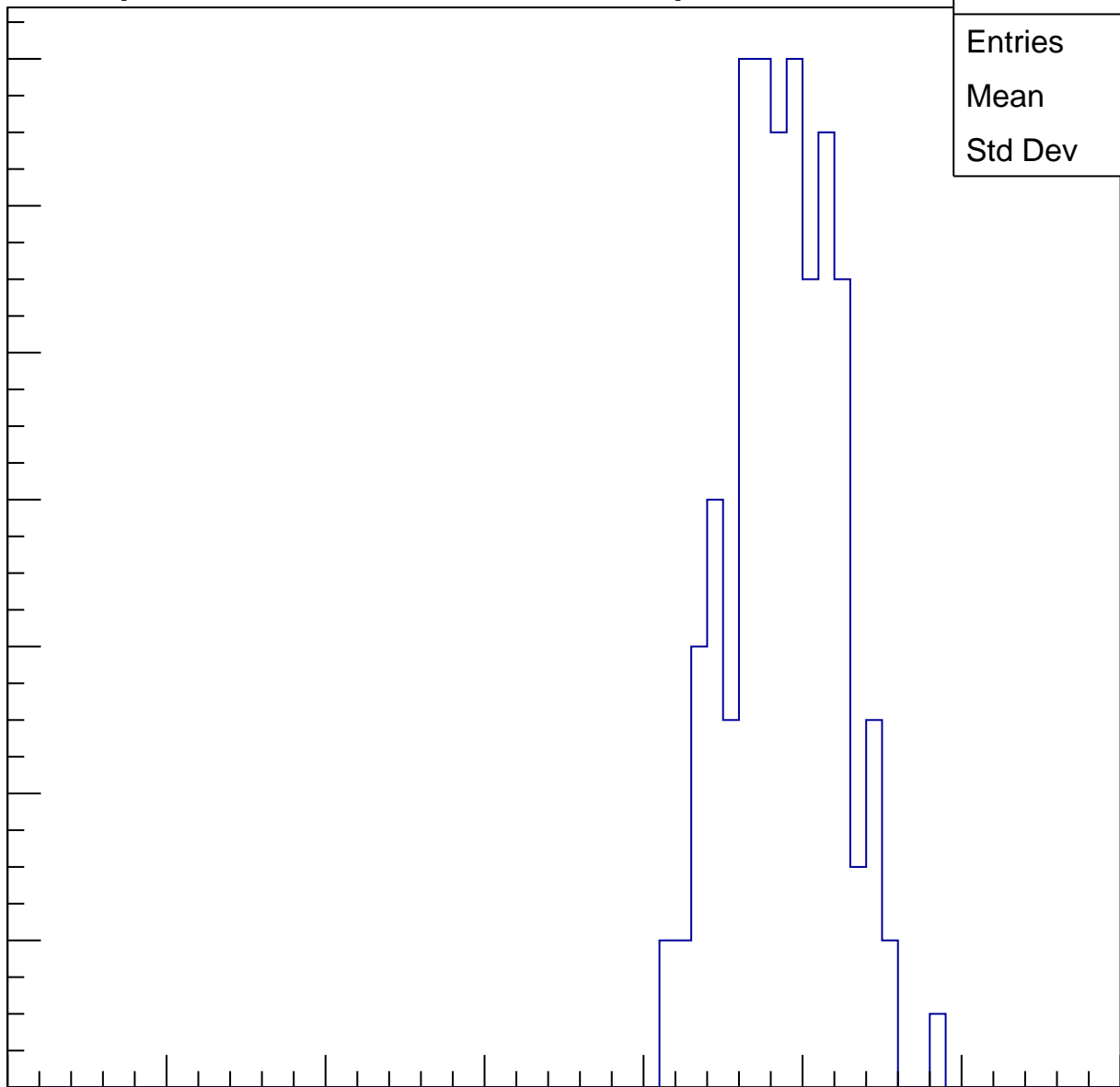
Entries	124
Mean	48.35
Std Dev	3.302

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

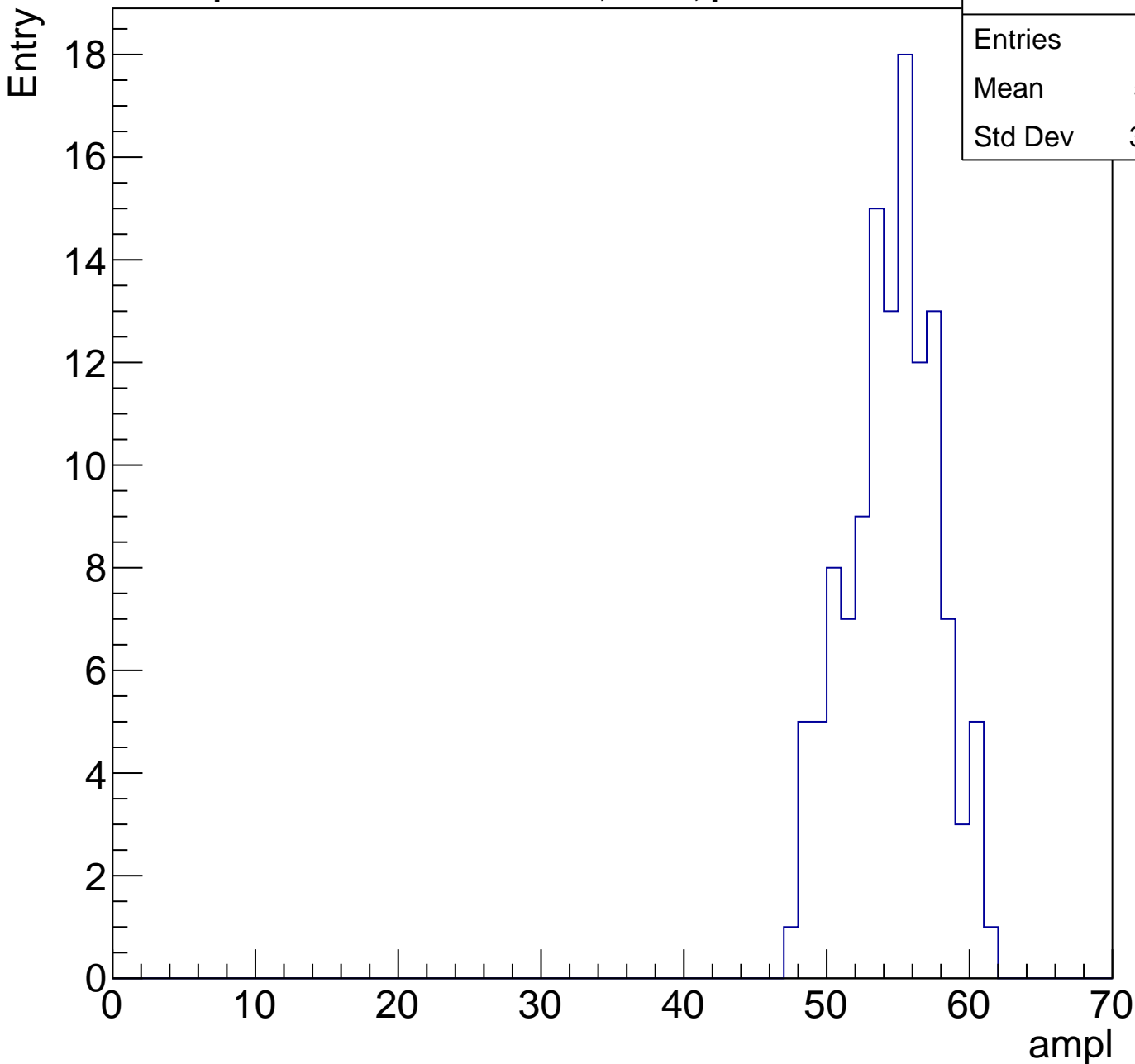
ampl



B1L001S, U22-ch40, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	54.11
Std Dev	3.128



B1L001S, U22-ch40, adc5

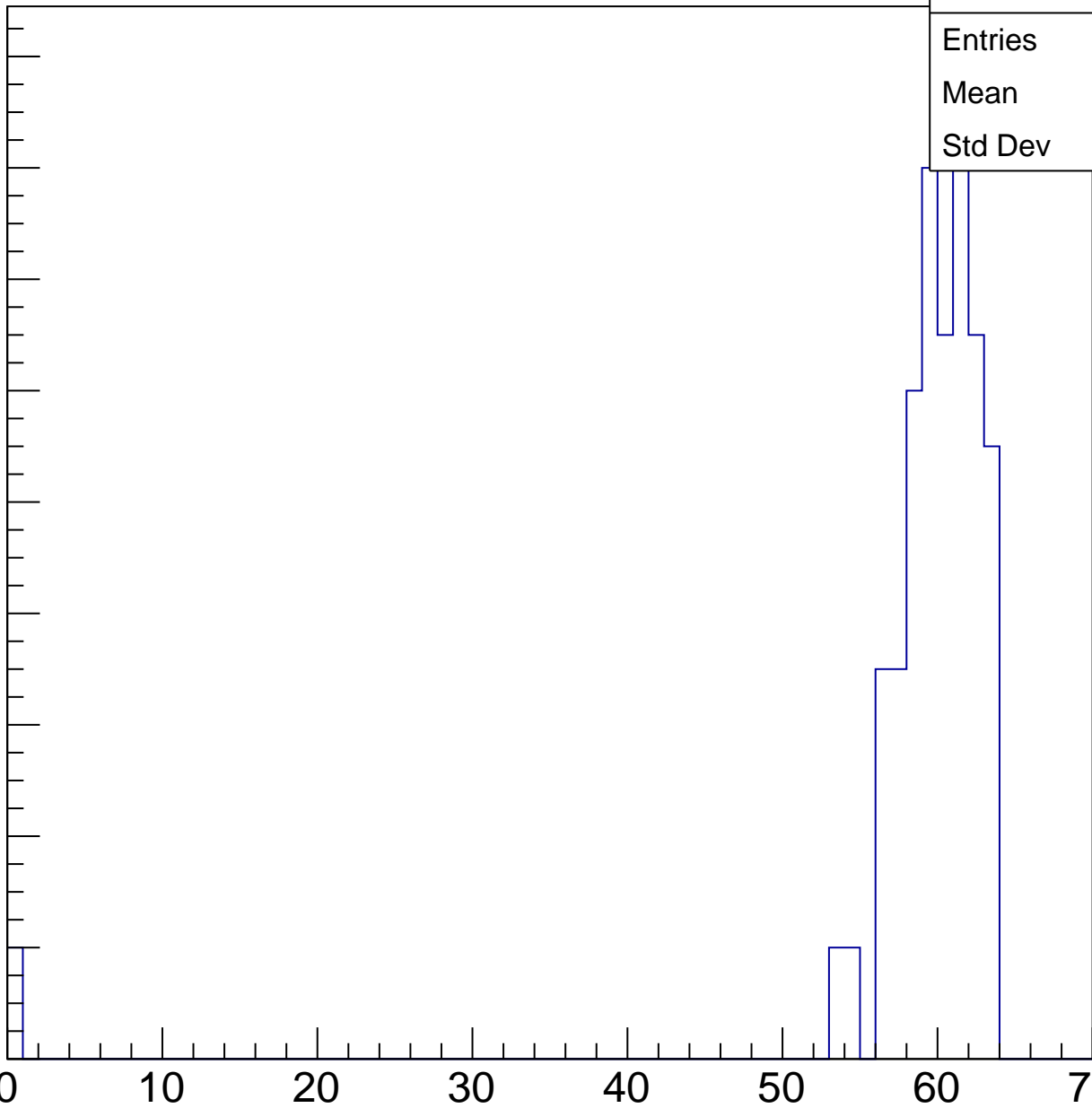
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.47
Std Dev	8.553

ampl

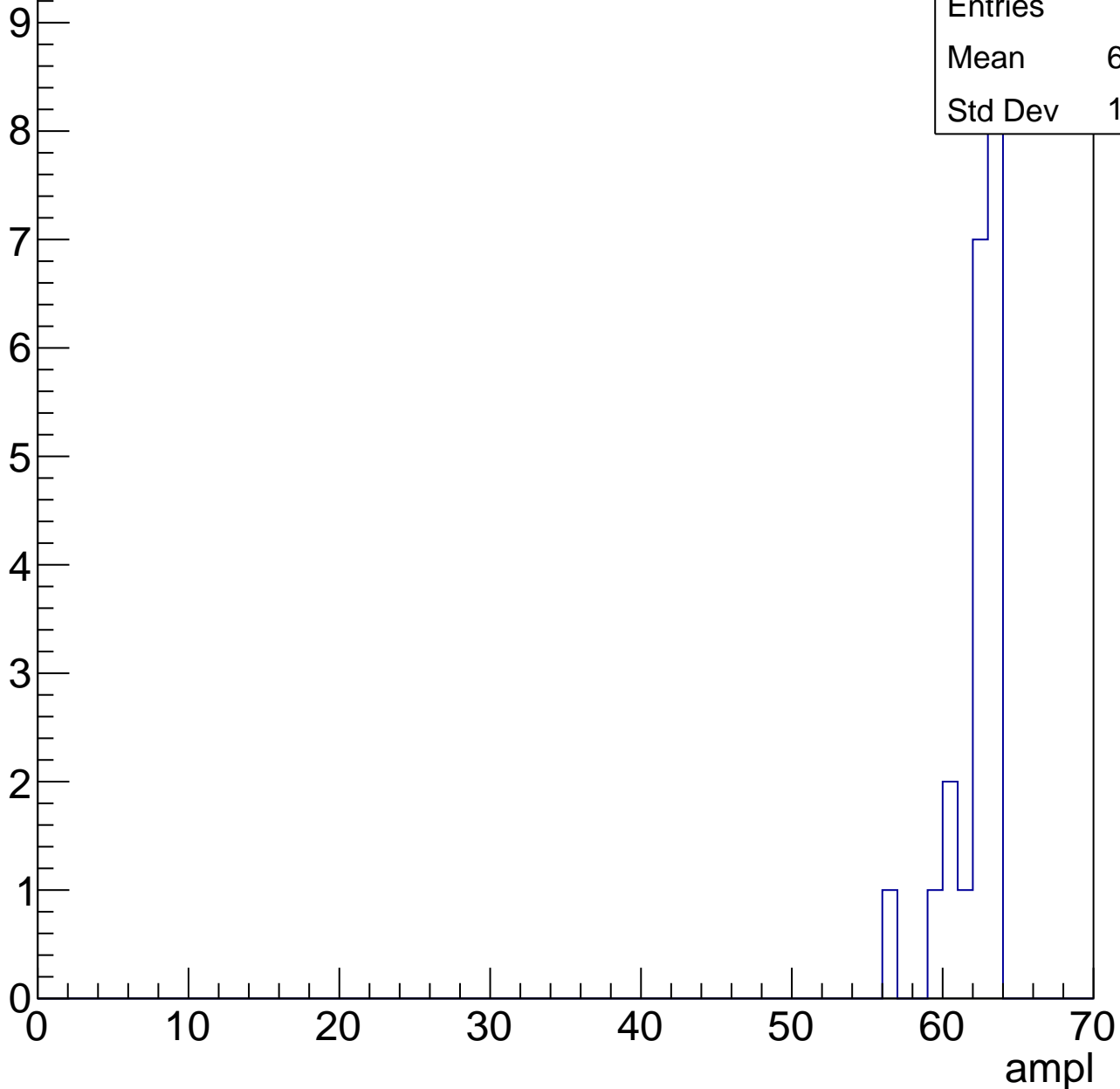


B1L001S, U22-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

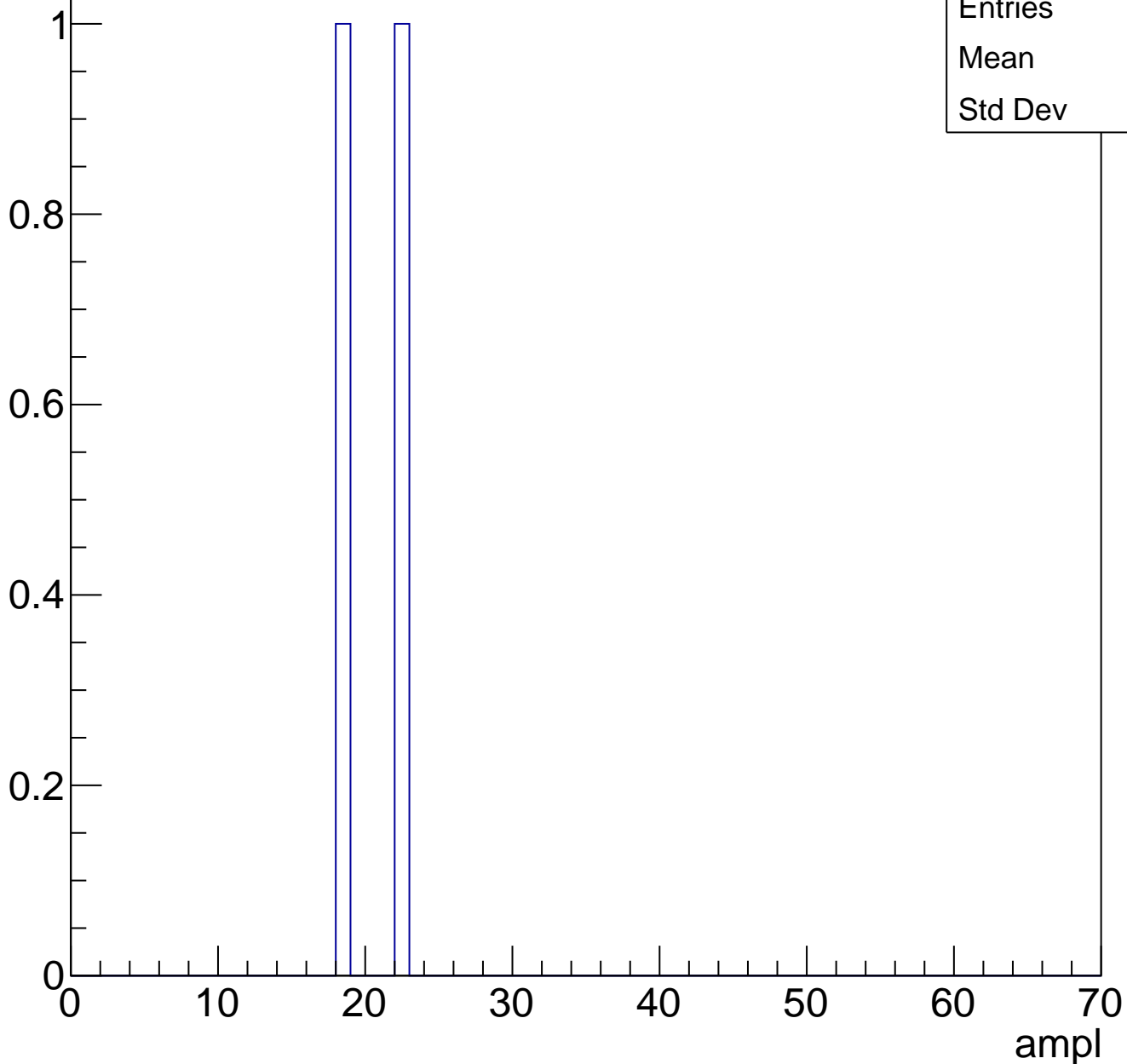
Entries	21
Mean	61.76
Std Dev	1.716



B1L001S, U22-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch41, adc0

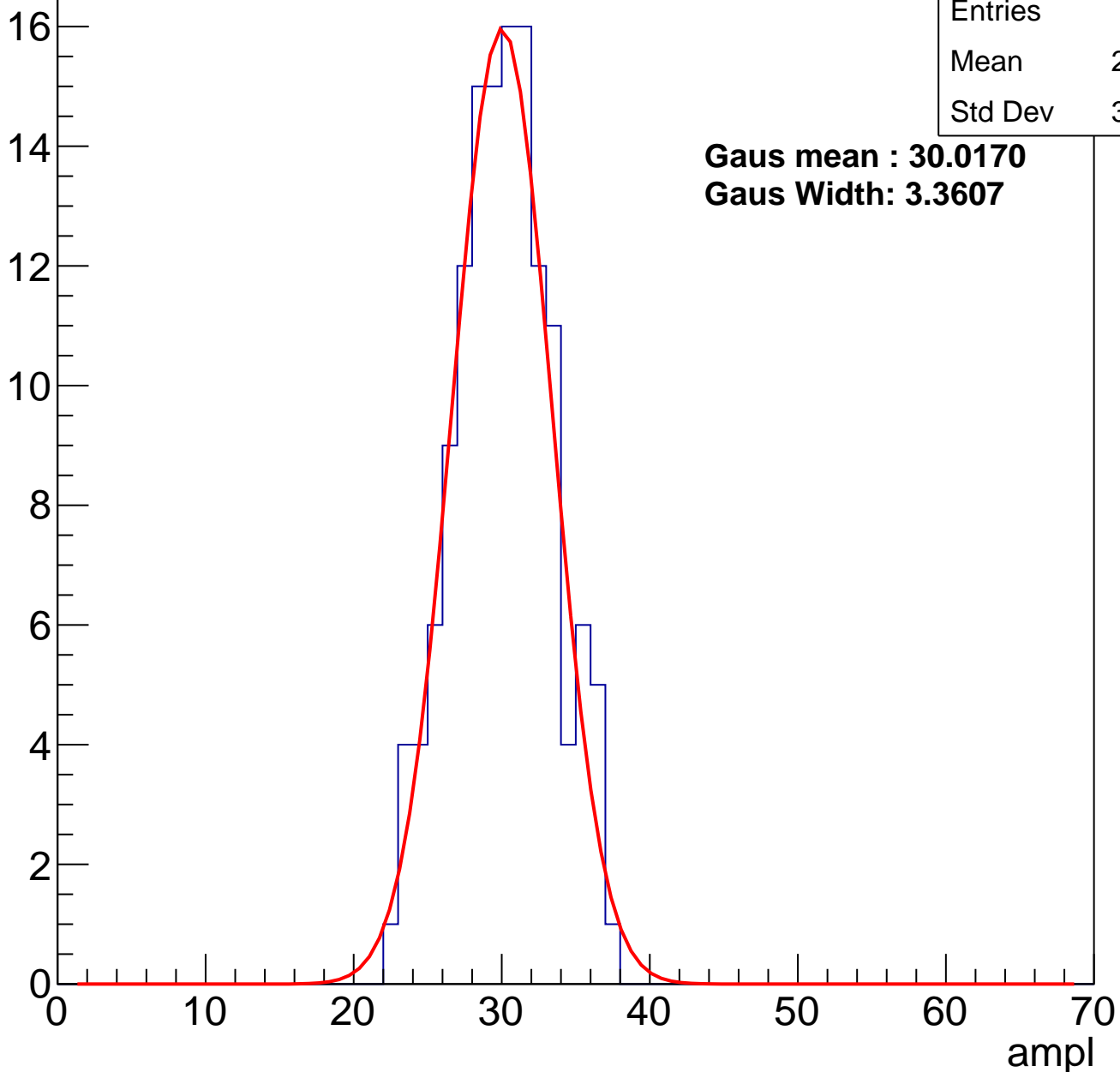
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.63
Std Dev	3.273

Gaus mean : 30.0170

Gaus Width: 3.3607

Entry



B1L001S, U22-ch41, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	35.86
Std Dev	3.52

Gaus mean : 36.7490

Gaus Width: 3.6240

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch41, adc2

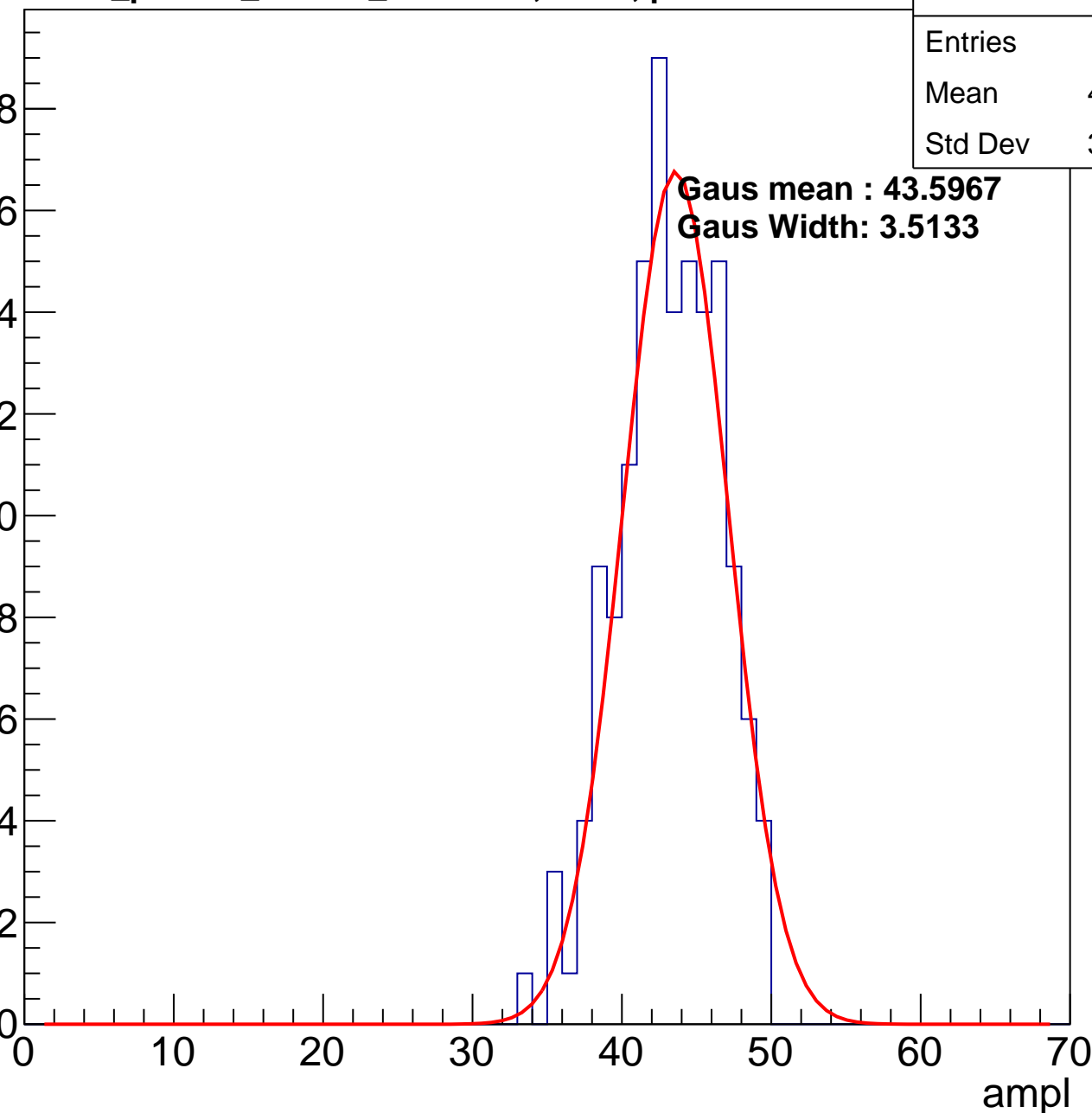
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	148
Mean	42.69
Std Dev	3.335

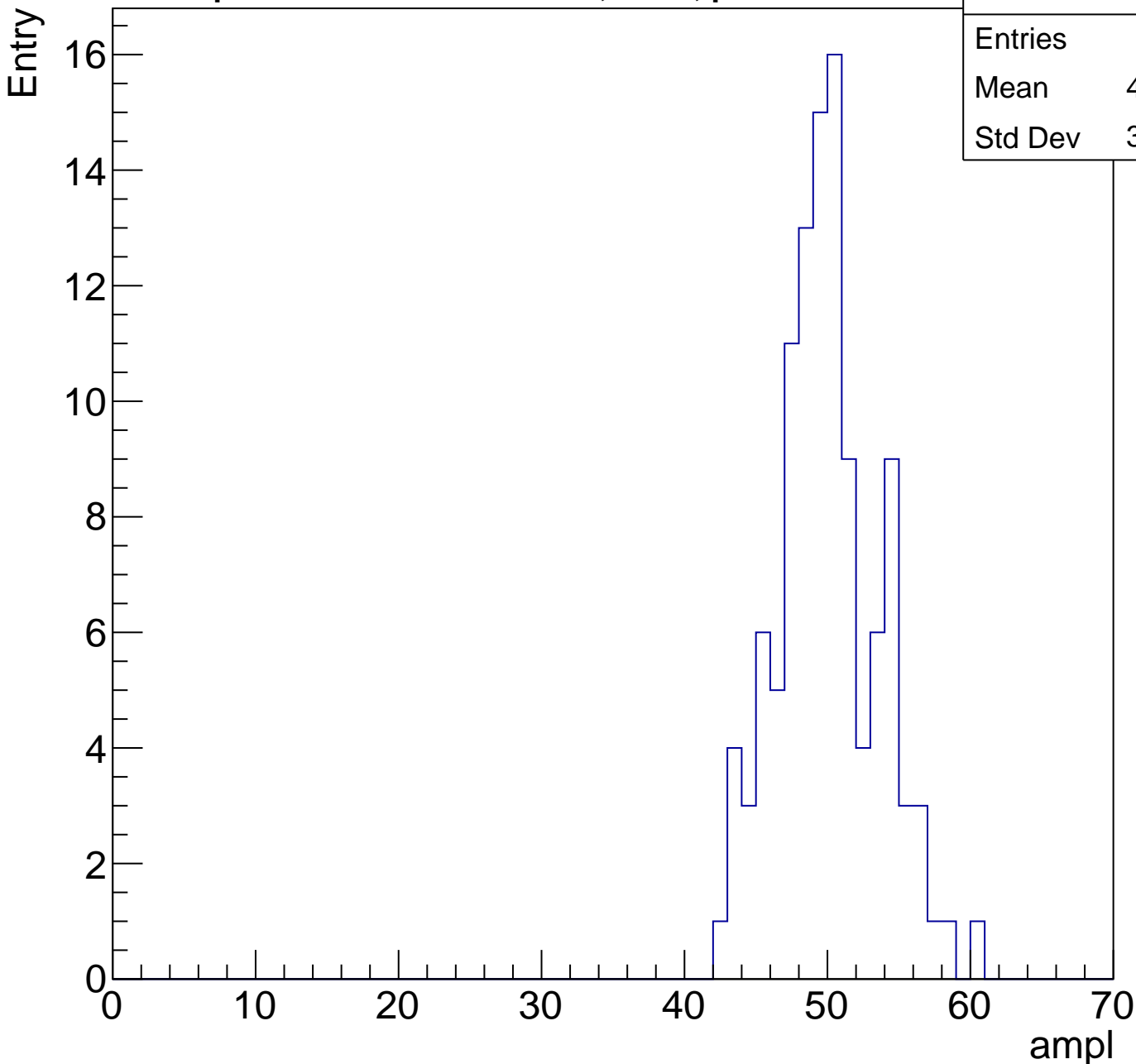
Gaus mean : 43.5967
Gaus Width: 3.5133



B1L001S, U22-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

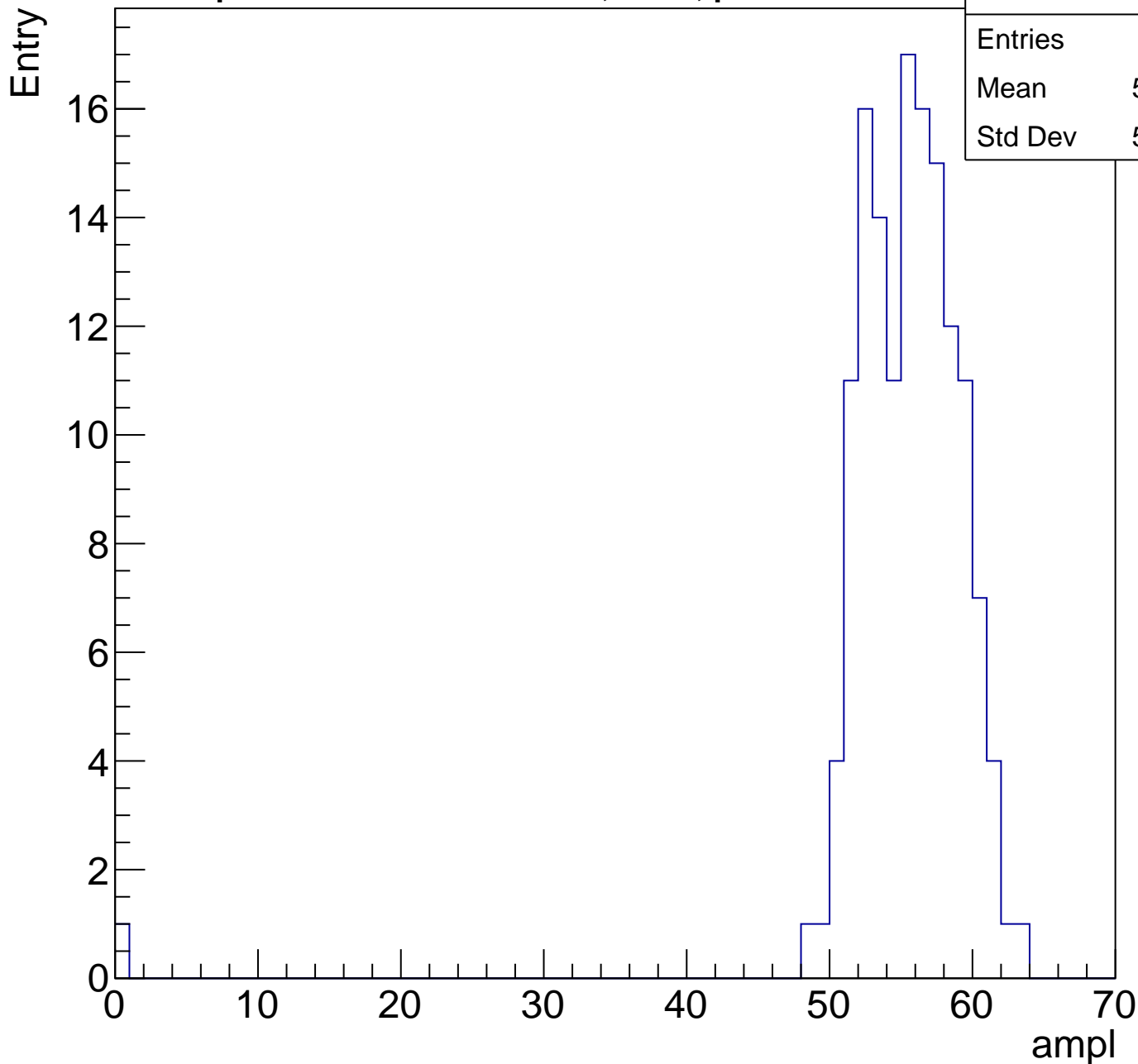
Entries	111
Mean	49.56
Std Dev	3.502



B1L001S, U22-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	54.87
Std Dev	5.538



B1L001S, U22-ch41, adc5

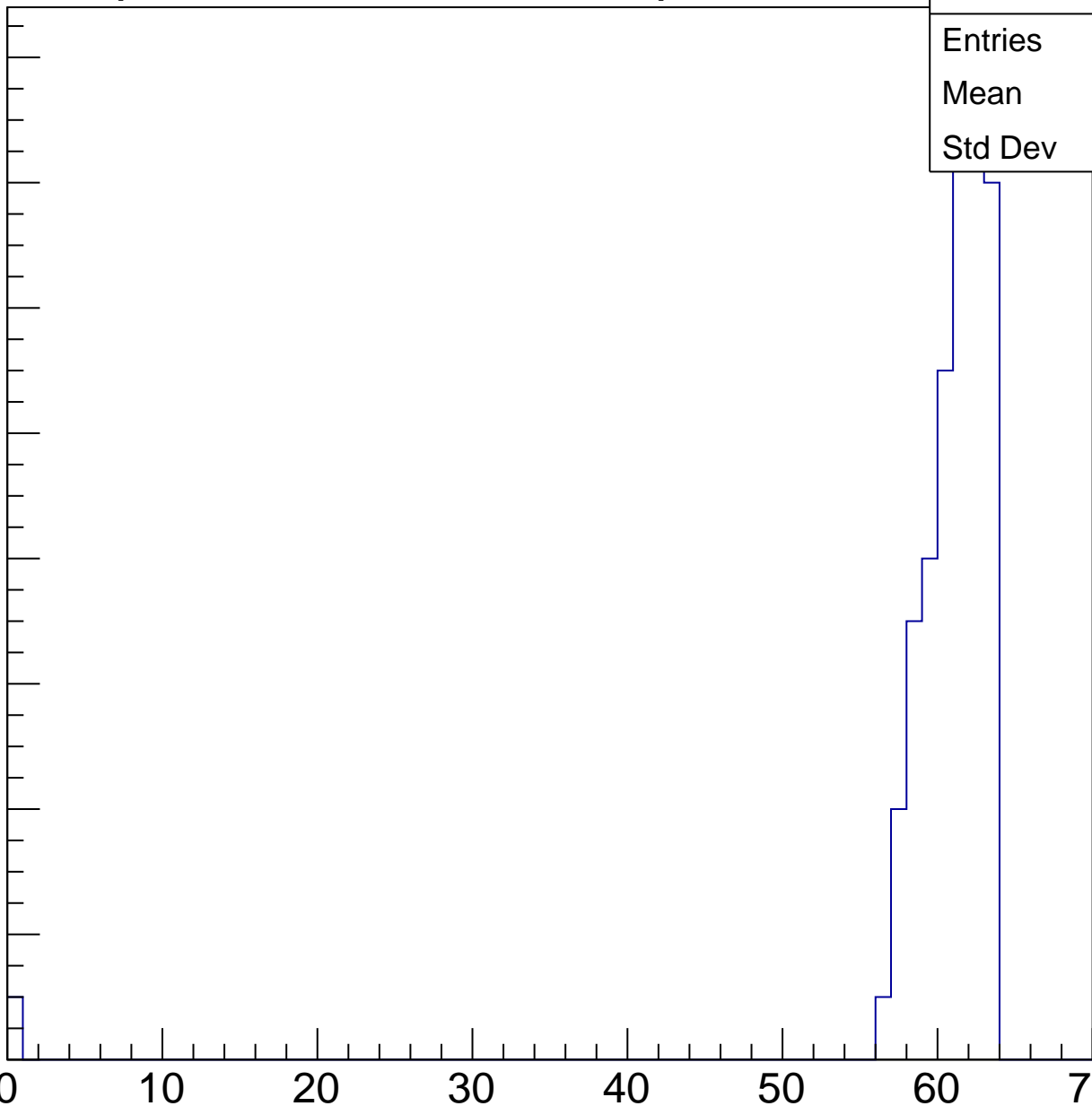
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.9
Std Dev	7.065

ampl

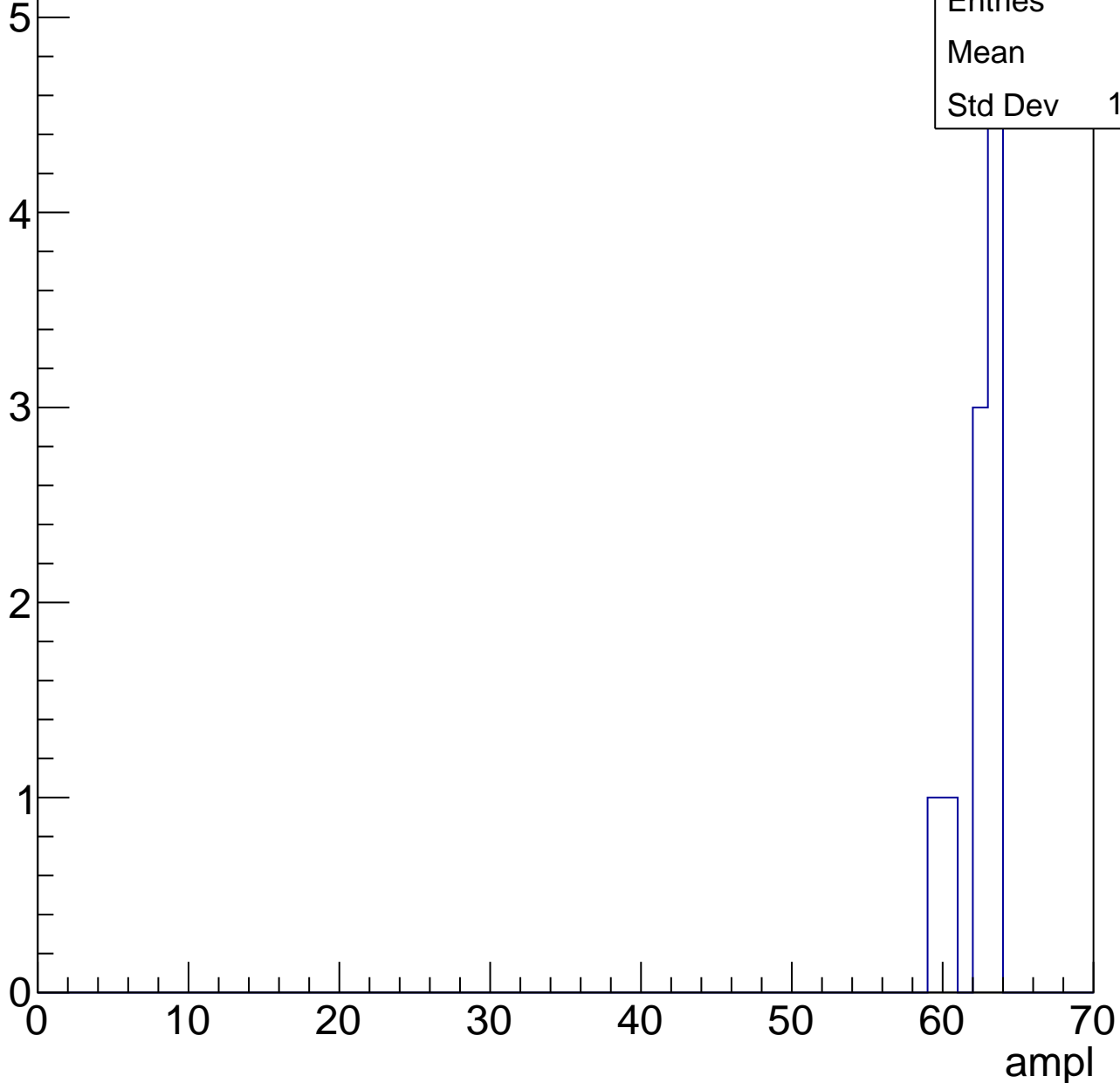


B1L001S, U22-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62
Std Dev	1.342



B1L001S, U22-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch42, adc0

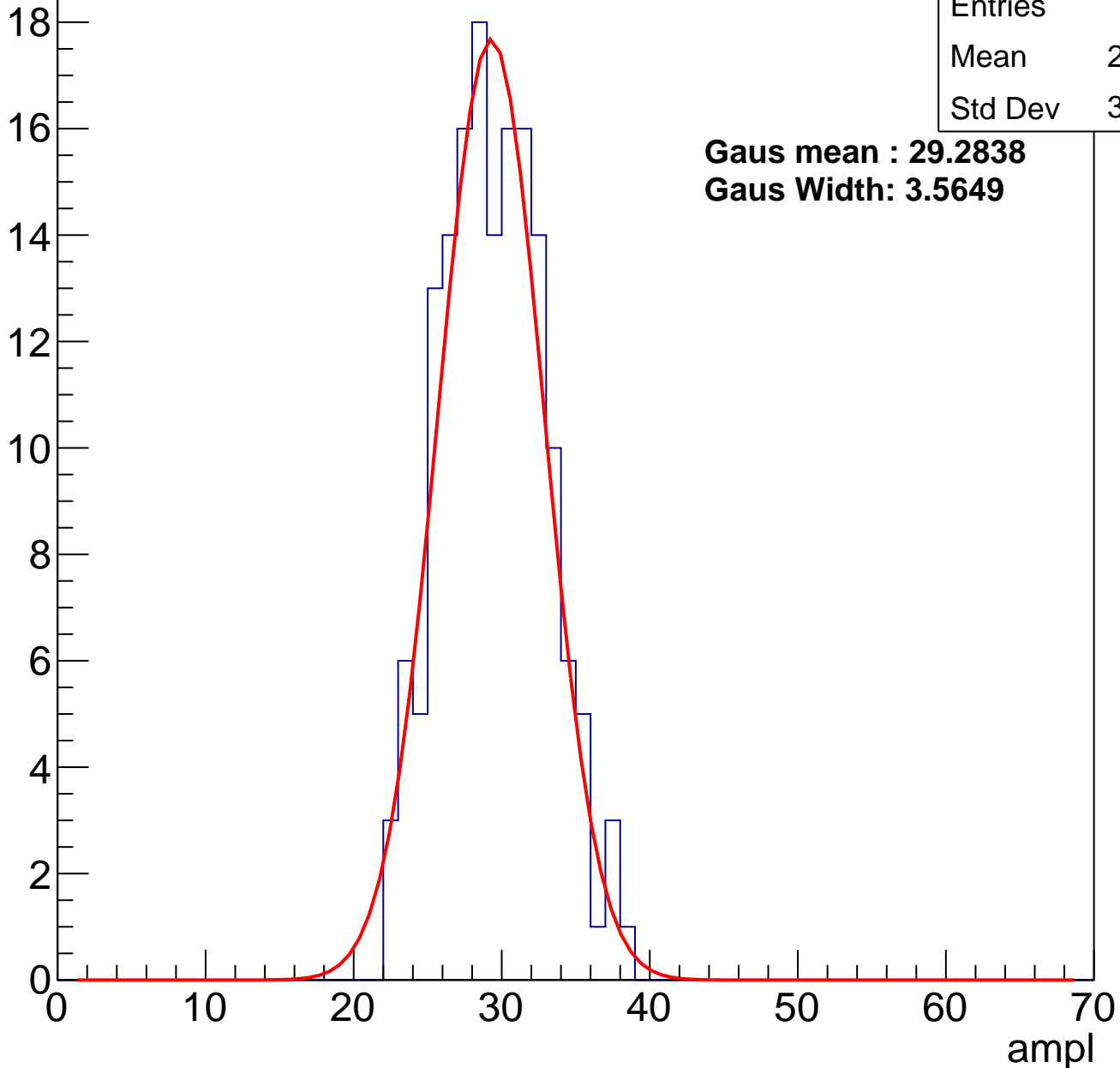
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	29.02
Std Dev	3.455

Gaus mean : 29.2838

Gaus Width: 3.5649

Entry



B1L001S, U22-ch42, adc1

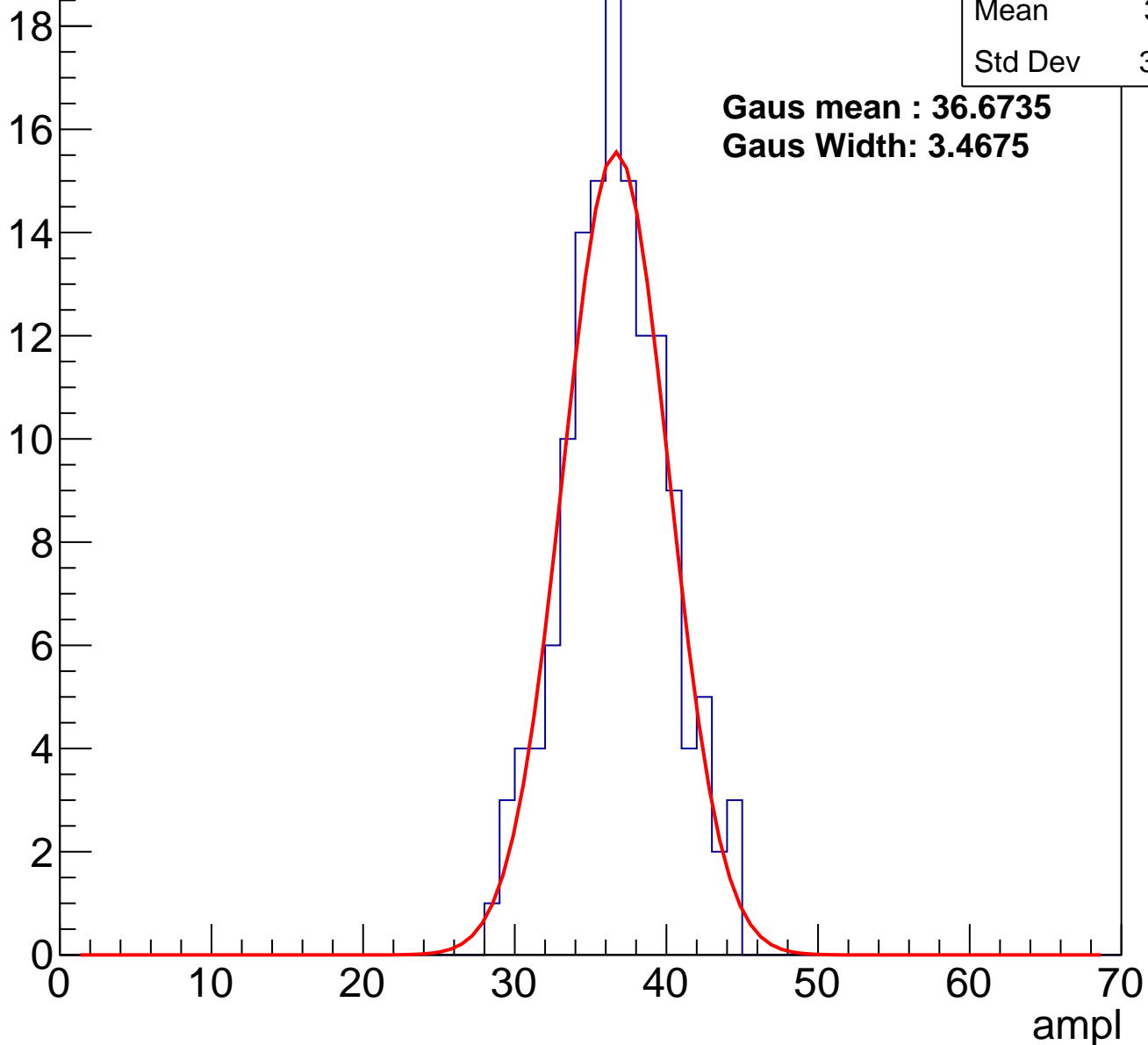
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.21
Std Dev	3.395

Gaus mean : 36.6735

Gaus Width: 3.4675

Entry



B1L001S, U22-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

134

Mean

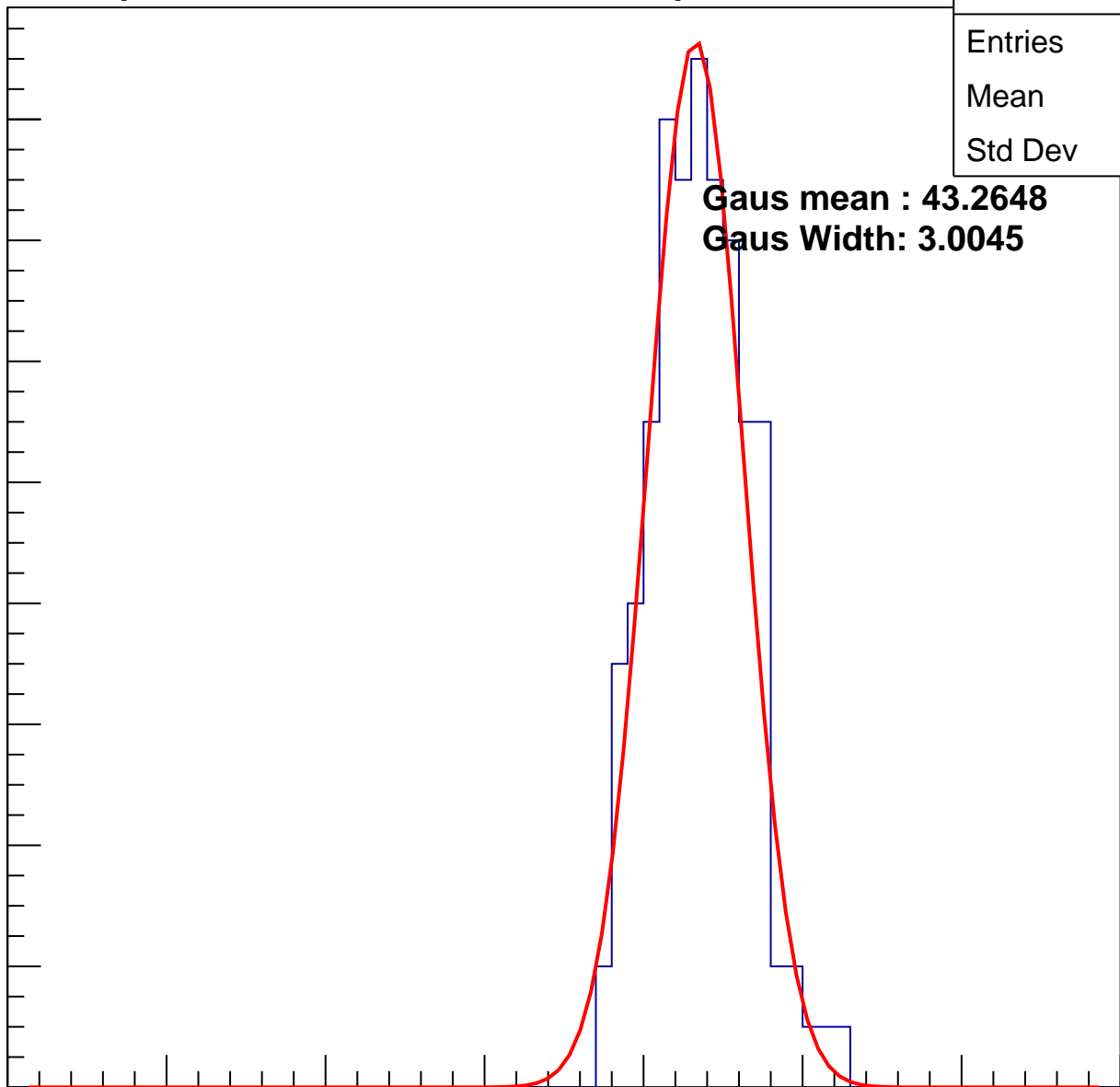
43.05

Std Dev

3.011

Gaus mean : 43.2648

Gaus Width: 3.0045

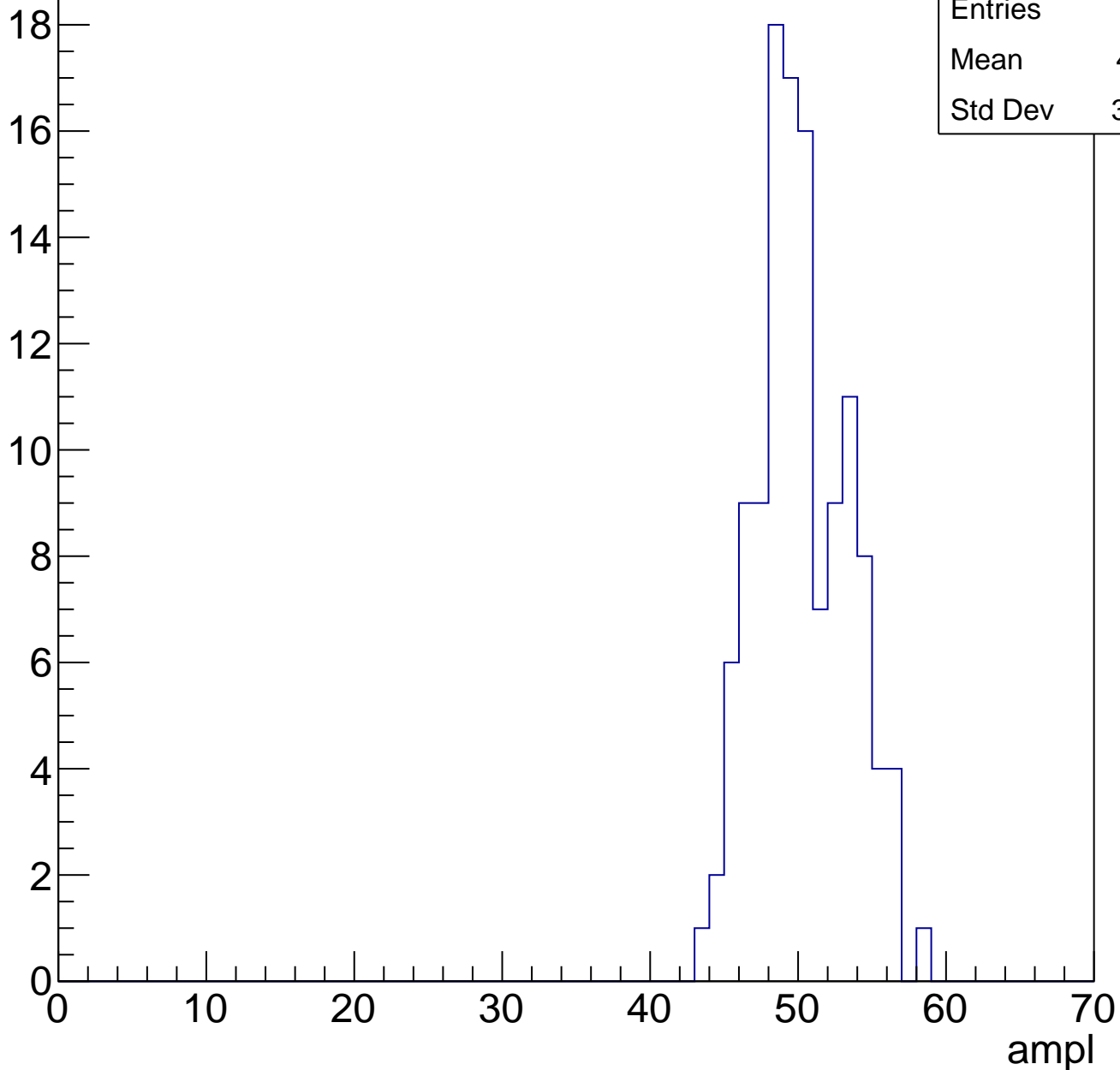


B1L001S, U22-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	49.81
Std Dev	3.082

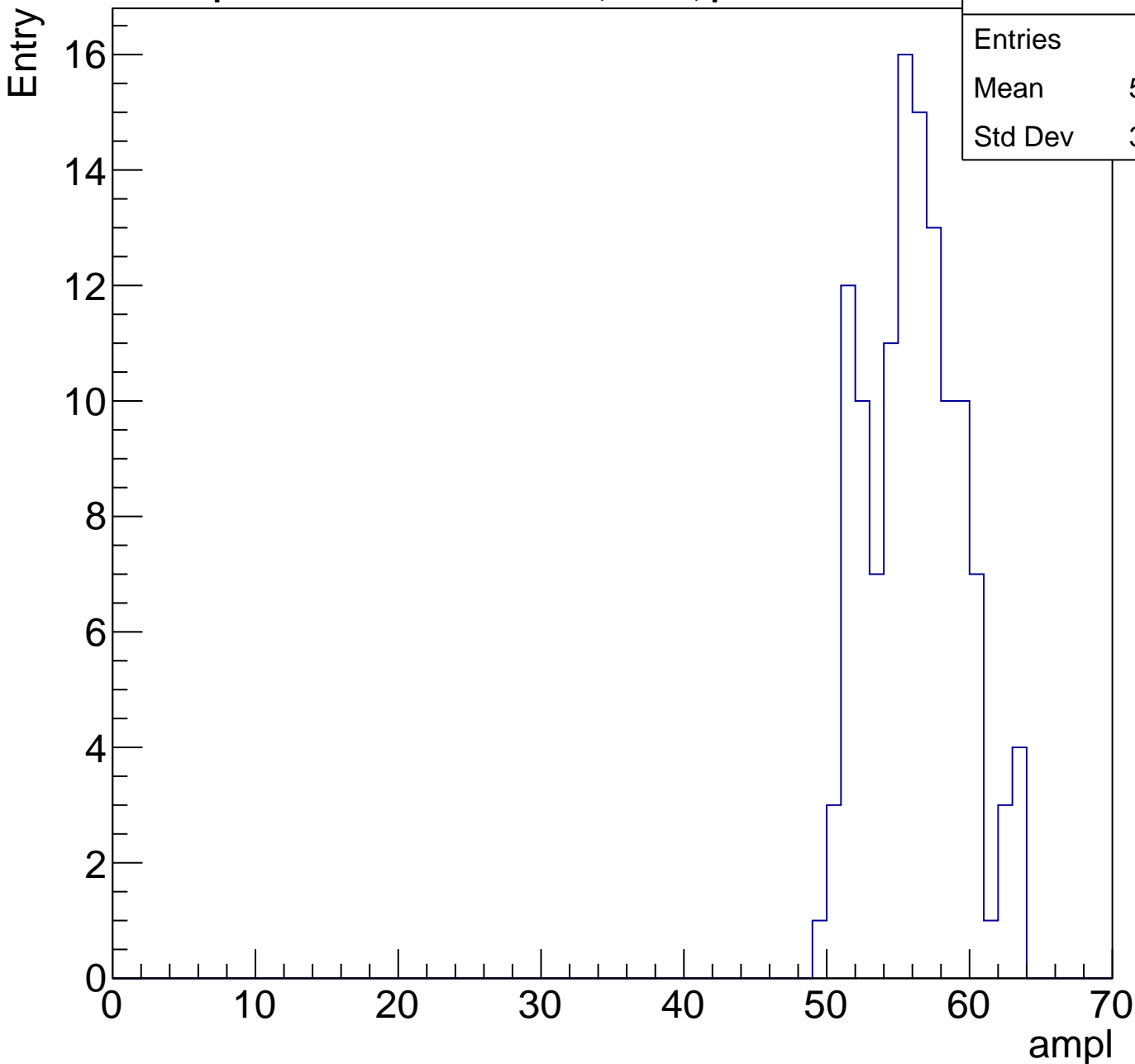
Entry



B1L001S, U22-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	55.66
Std Dev	3.263



B1L001S, U22-ch42, adc5

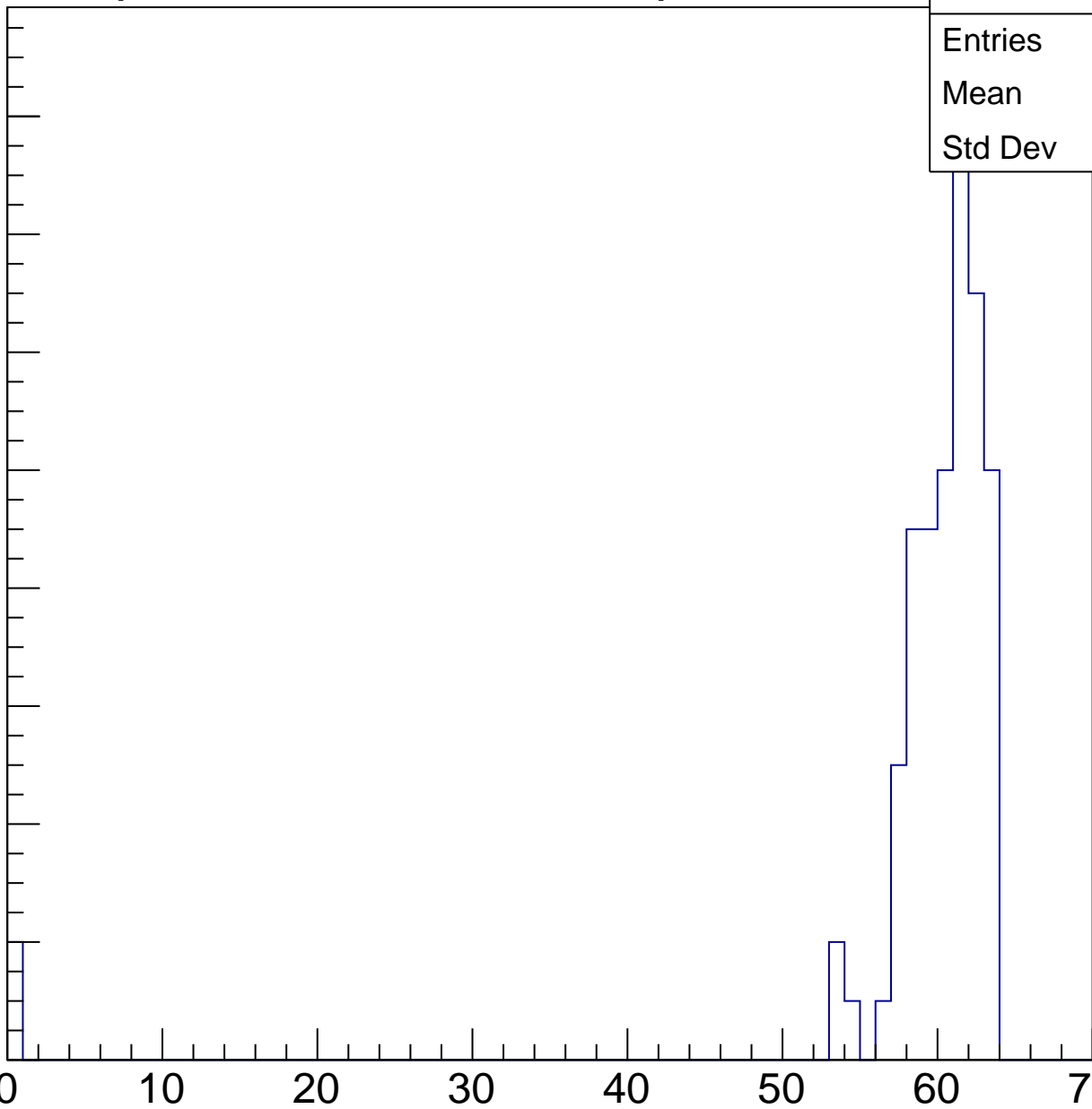
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	58.57
Std Dev	9.703

ampl

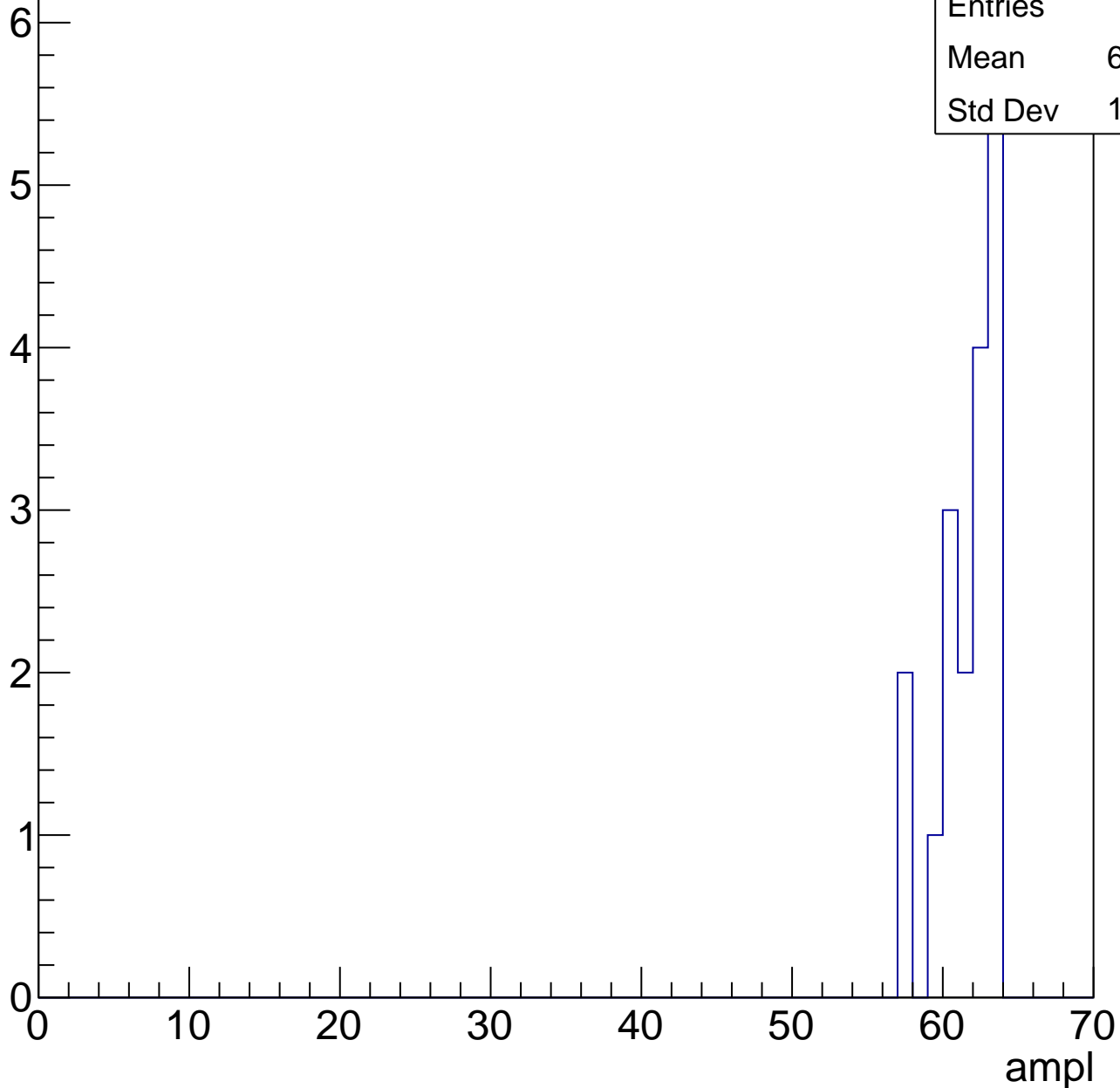


B1L001S, U22-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

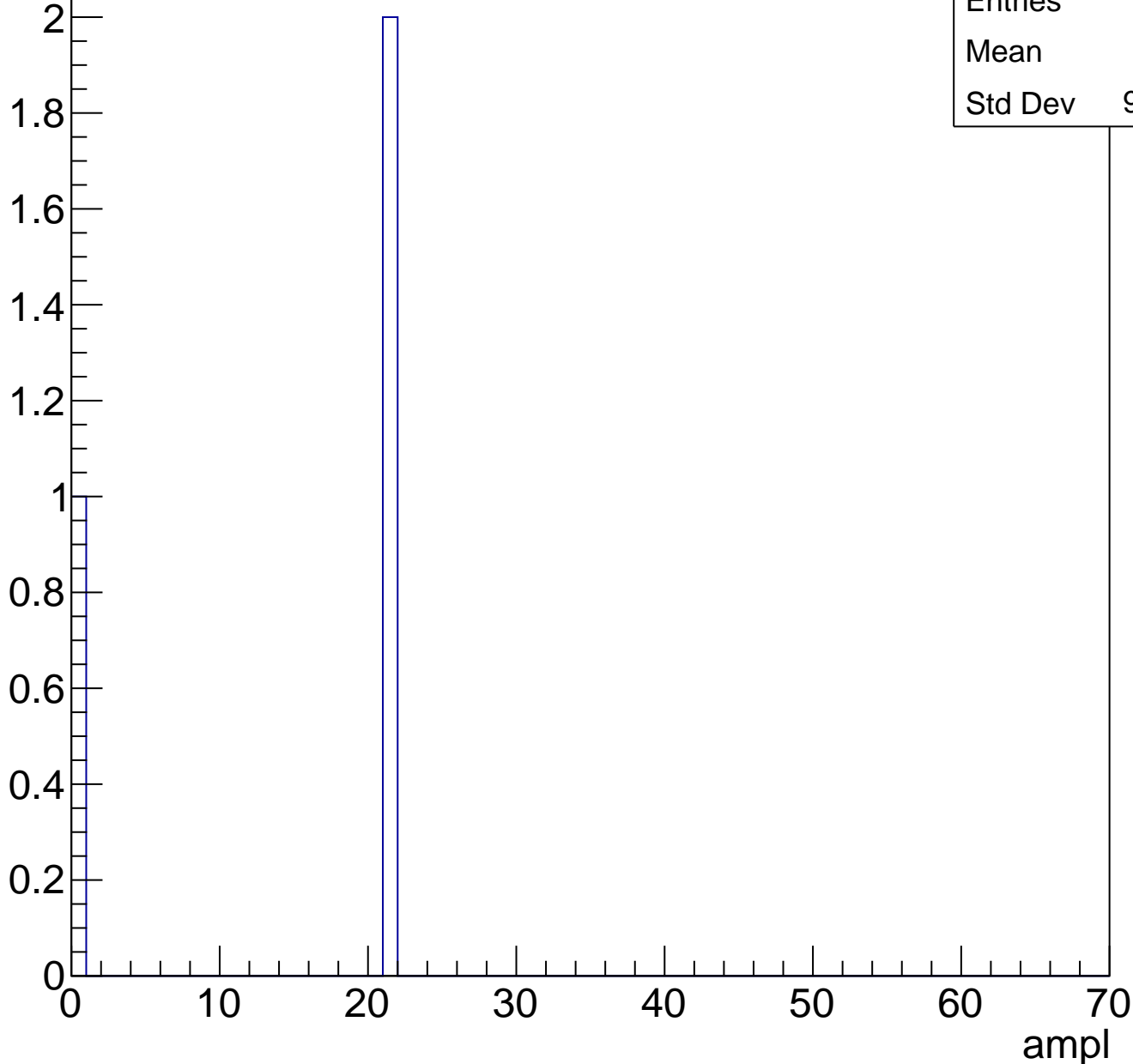
Entries	18
Mean	61.17
Std Dev	1.922



B1L001S, U22-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	14
Std Dev	9.899

B1L001S, U22-ch43, adc0

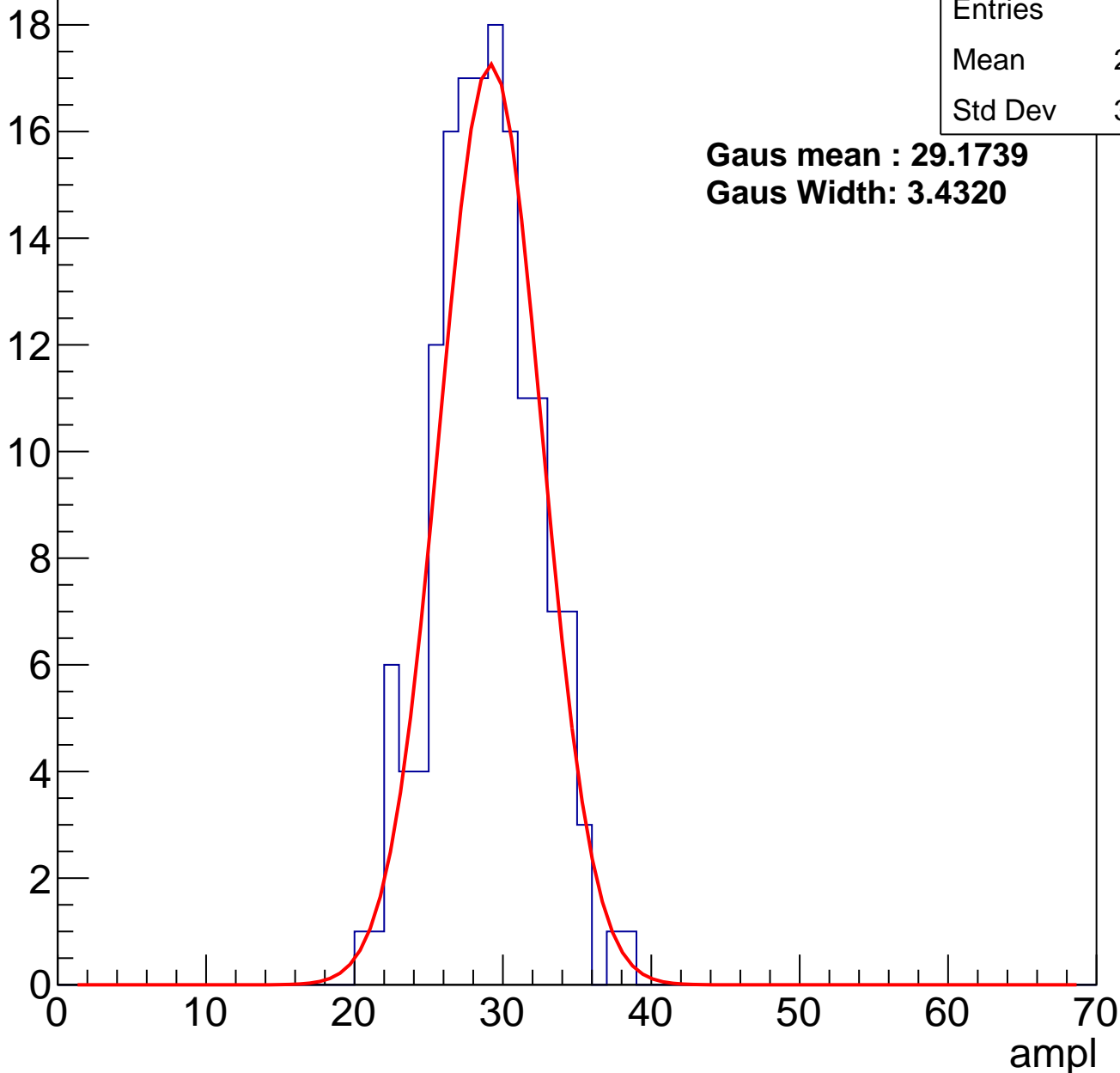
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	28.47
Std Dev	3.392

Gaus mean : 29.1739

Gaus Width: 3.4320

Entry



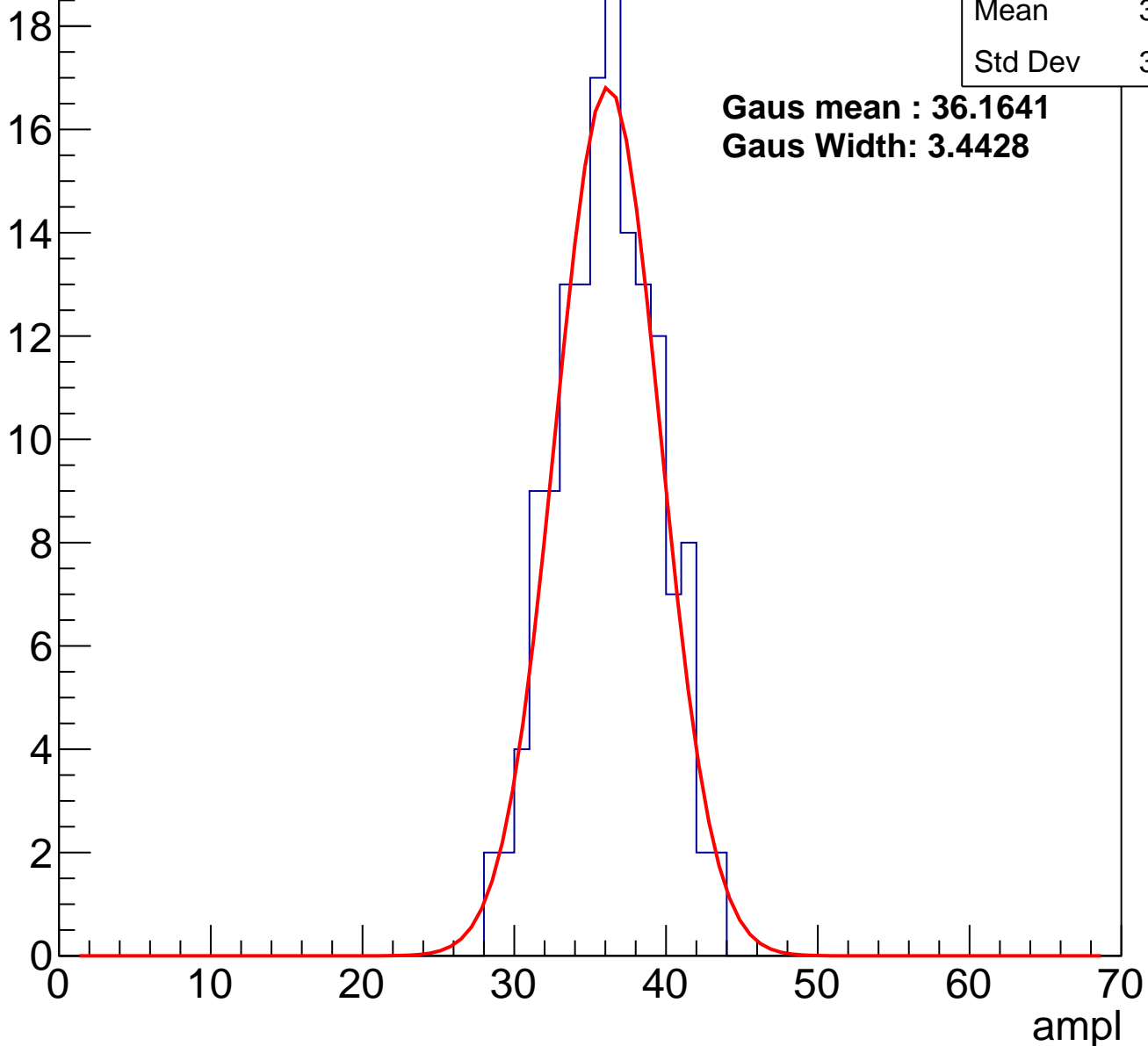
B1L001S, U22-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	35.68
Std Dev	3.275

Gaus mean : 36.1641
Gaus Width: 3.4428

Entry



B1L001S, U22-ch43, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

118

Mean

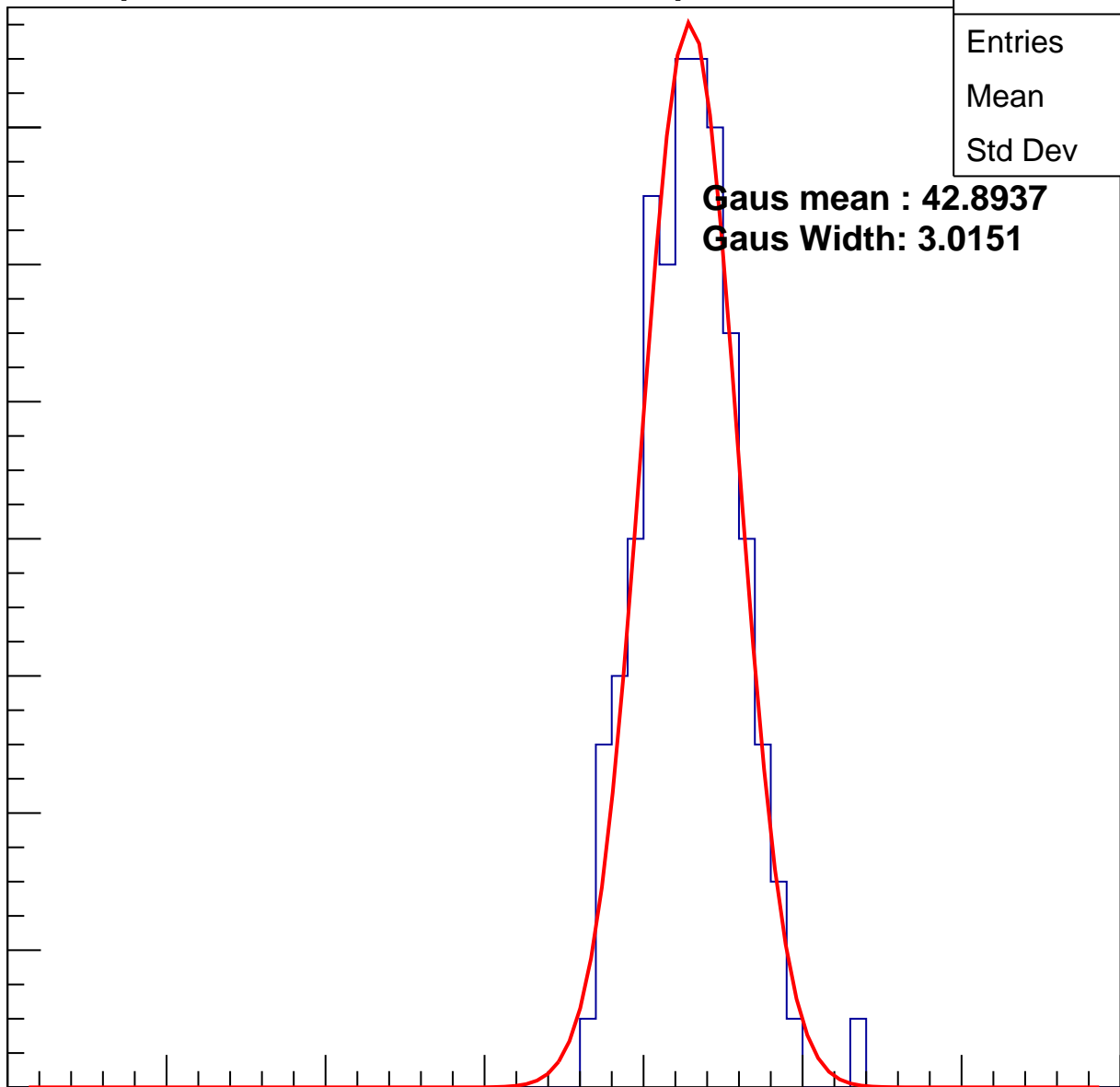
42.44

Std Dev

3.013

Gaus mean : 42.8937

Gaus Width: 3.0151



B1L001S, U22-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	48.8
Std Dev	3.108

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

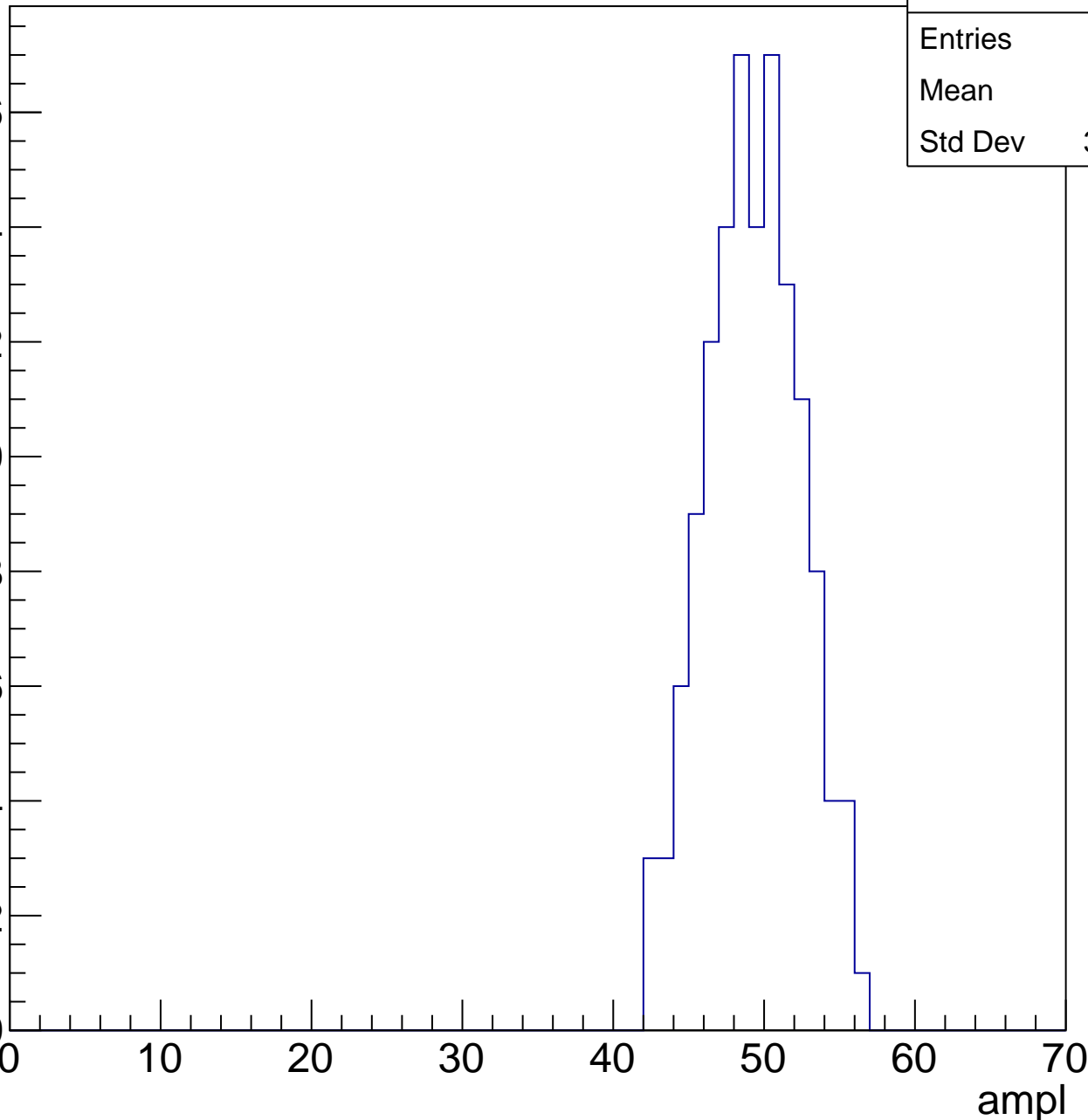
40

50

60

70

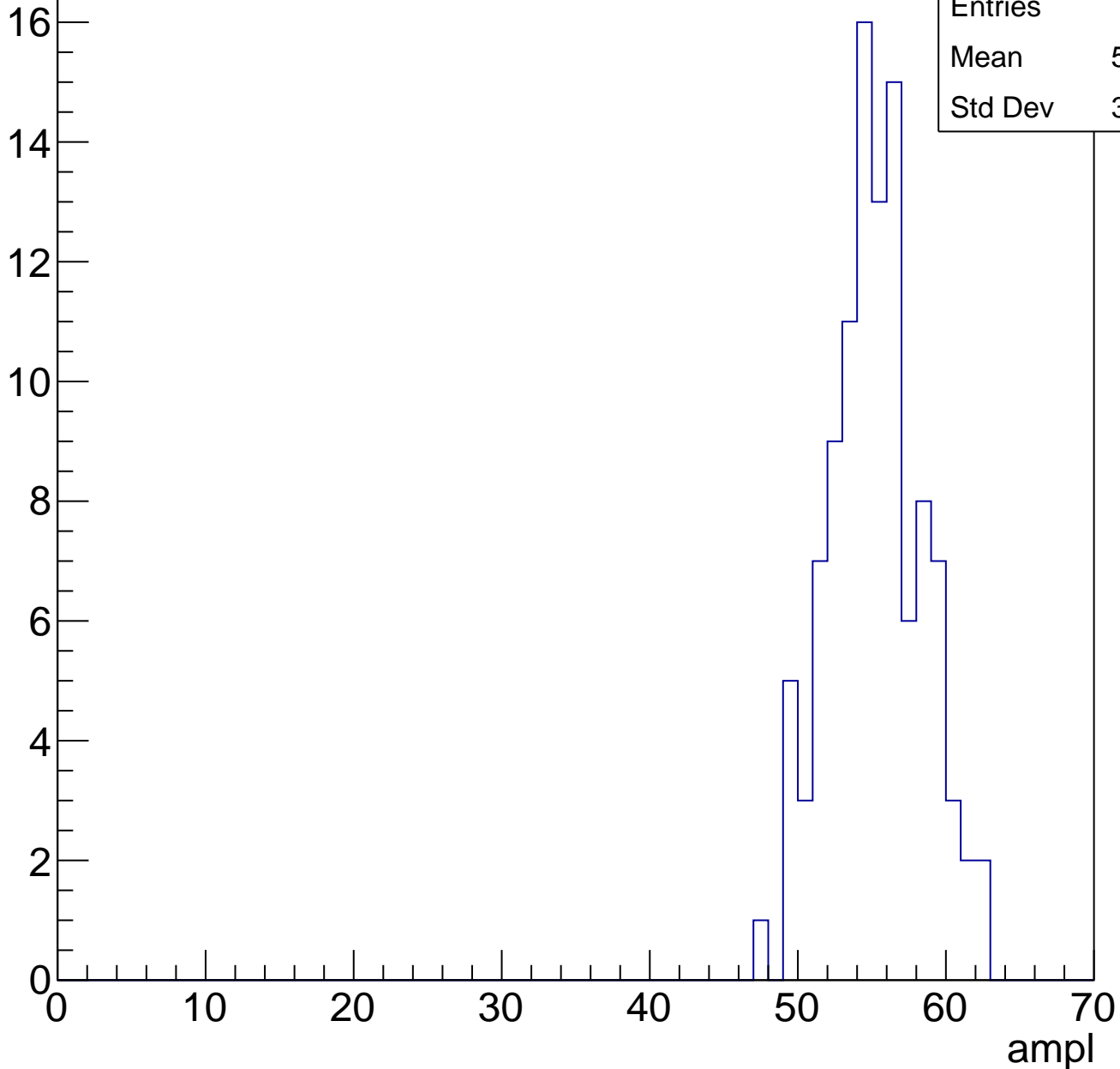
ampl



B1L001S, U22-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch43, adc5

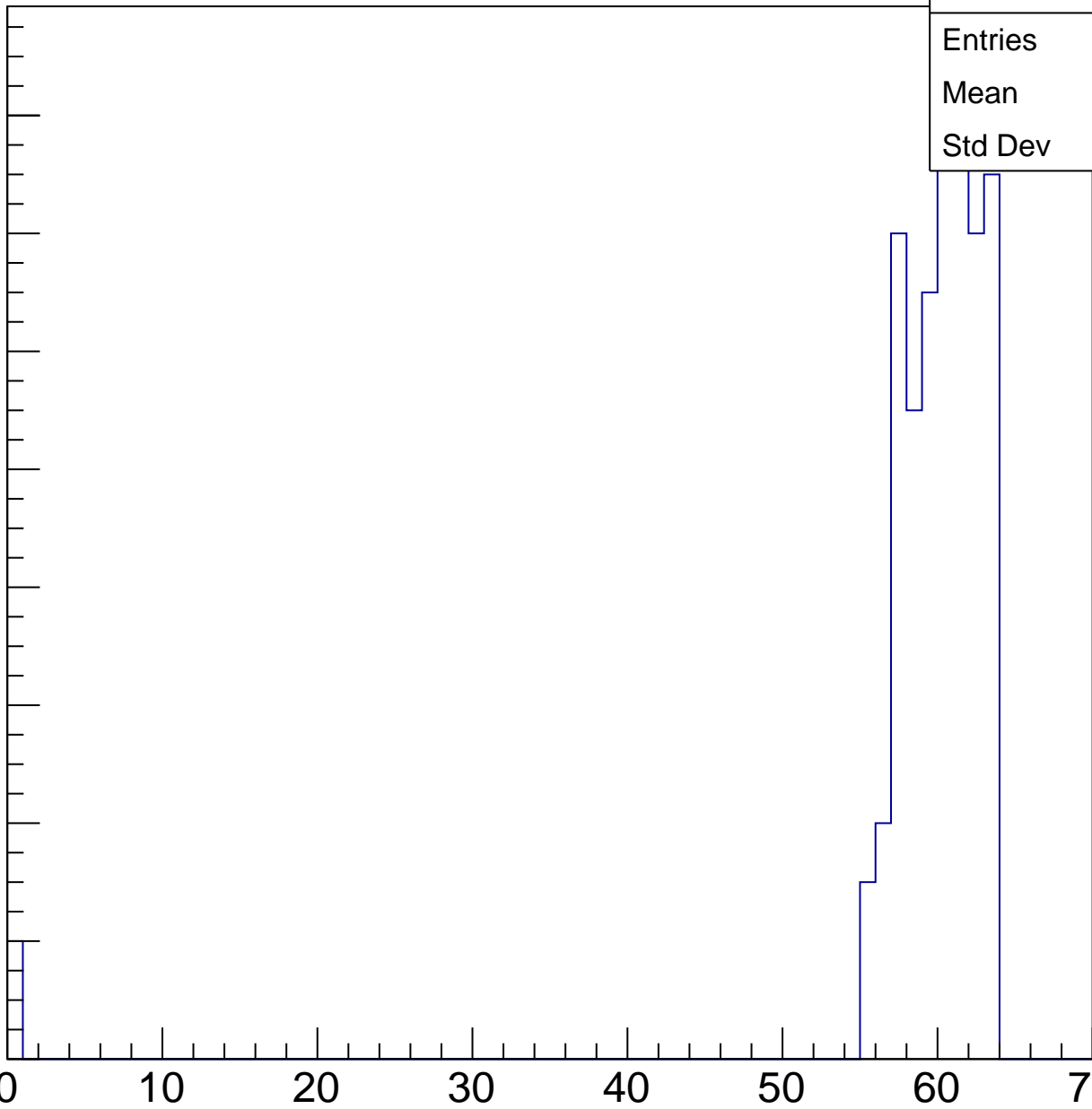
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.75
Std Dev	8.288

ampl

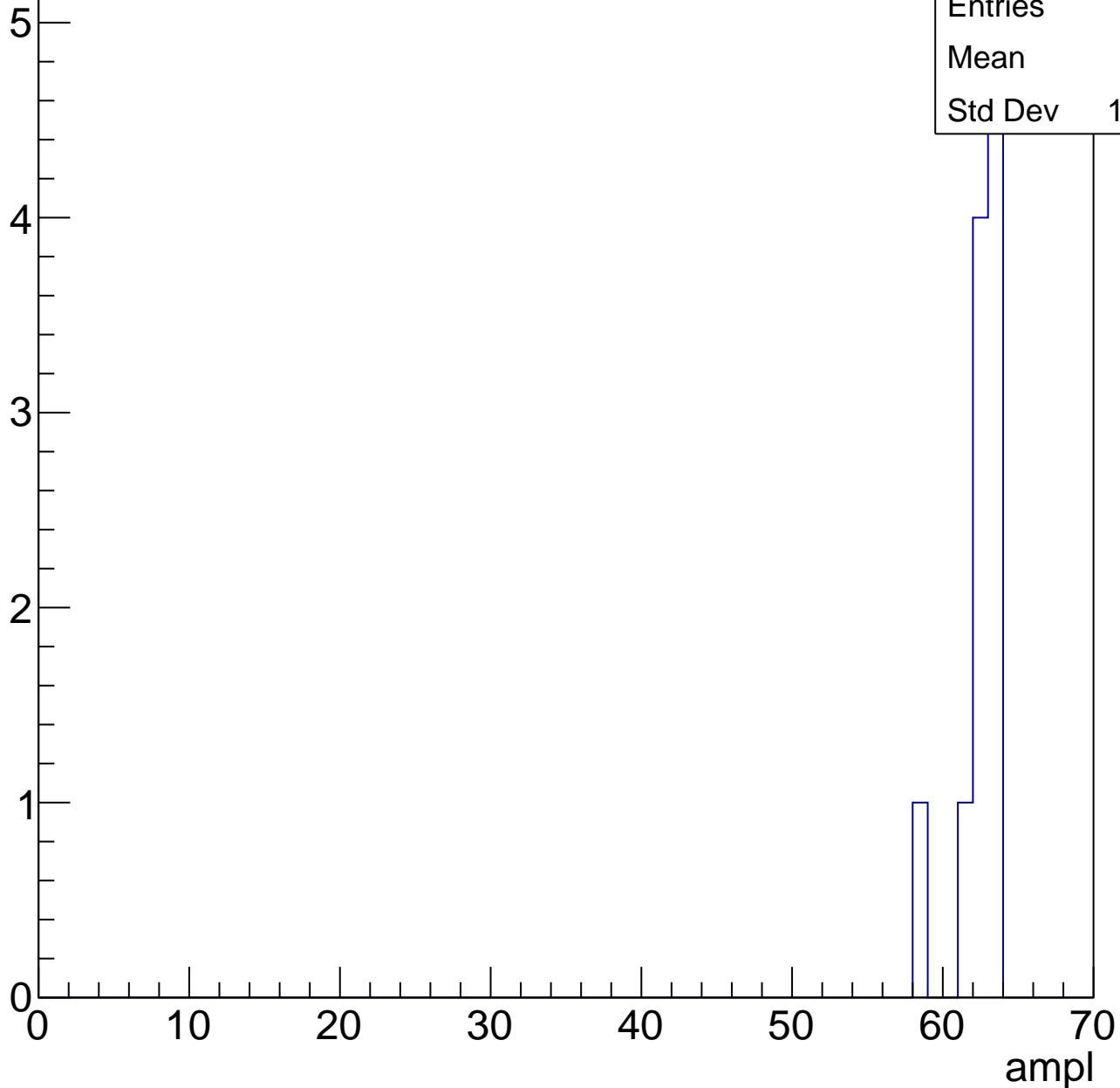


B1L001S, U22-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62
Std Dev	1.414



B1L001S, U22-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch44, adc0

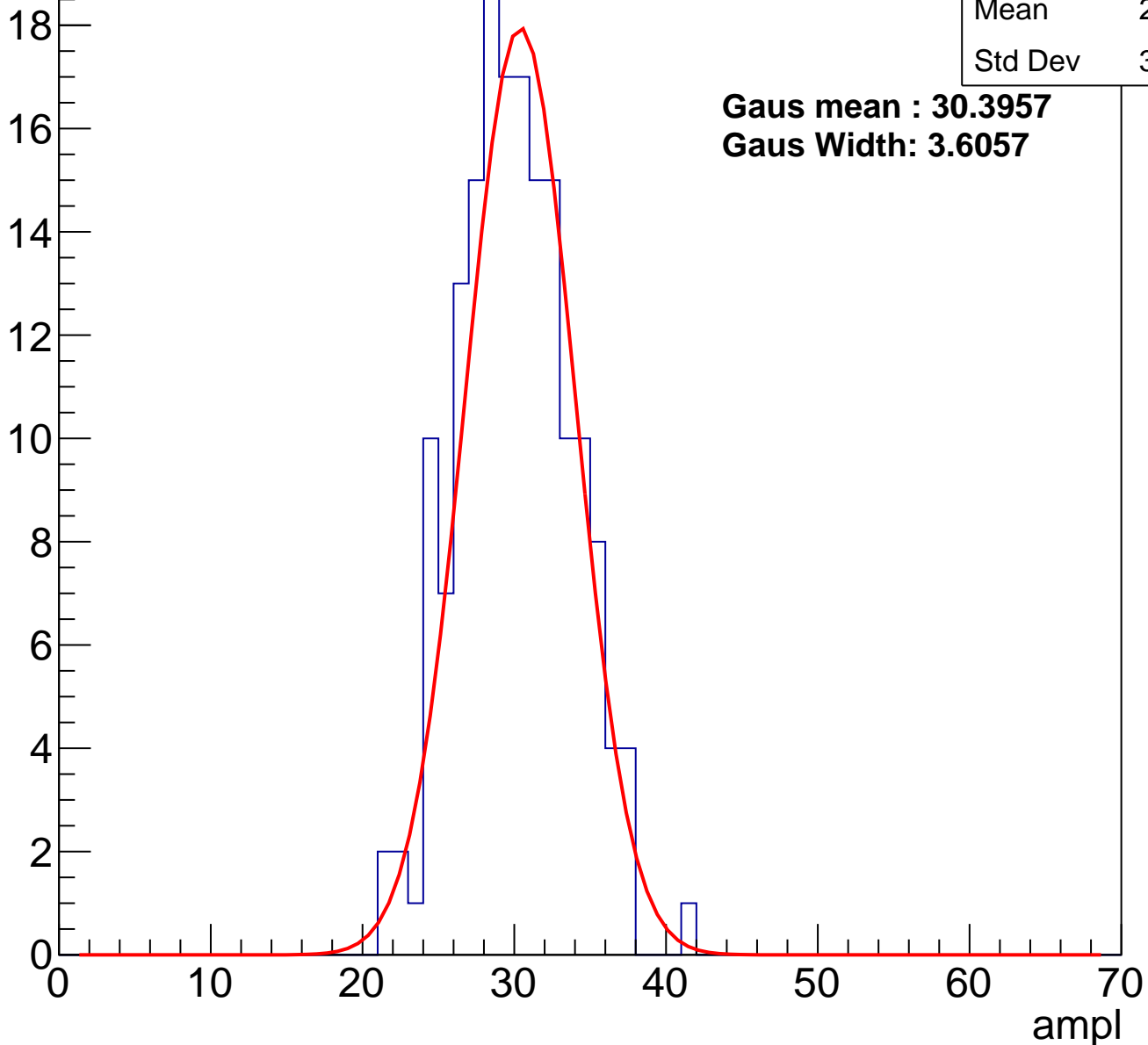
calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	29.59
Std Dev	3.652

Gaus mean : 30.3957

Gaus Width: 3.6057

Entry



B1L001S, U22-ch44, adc1

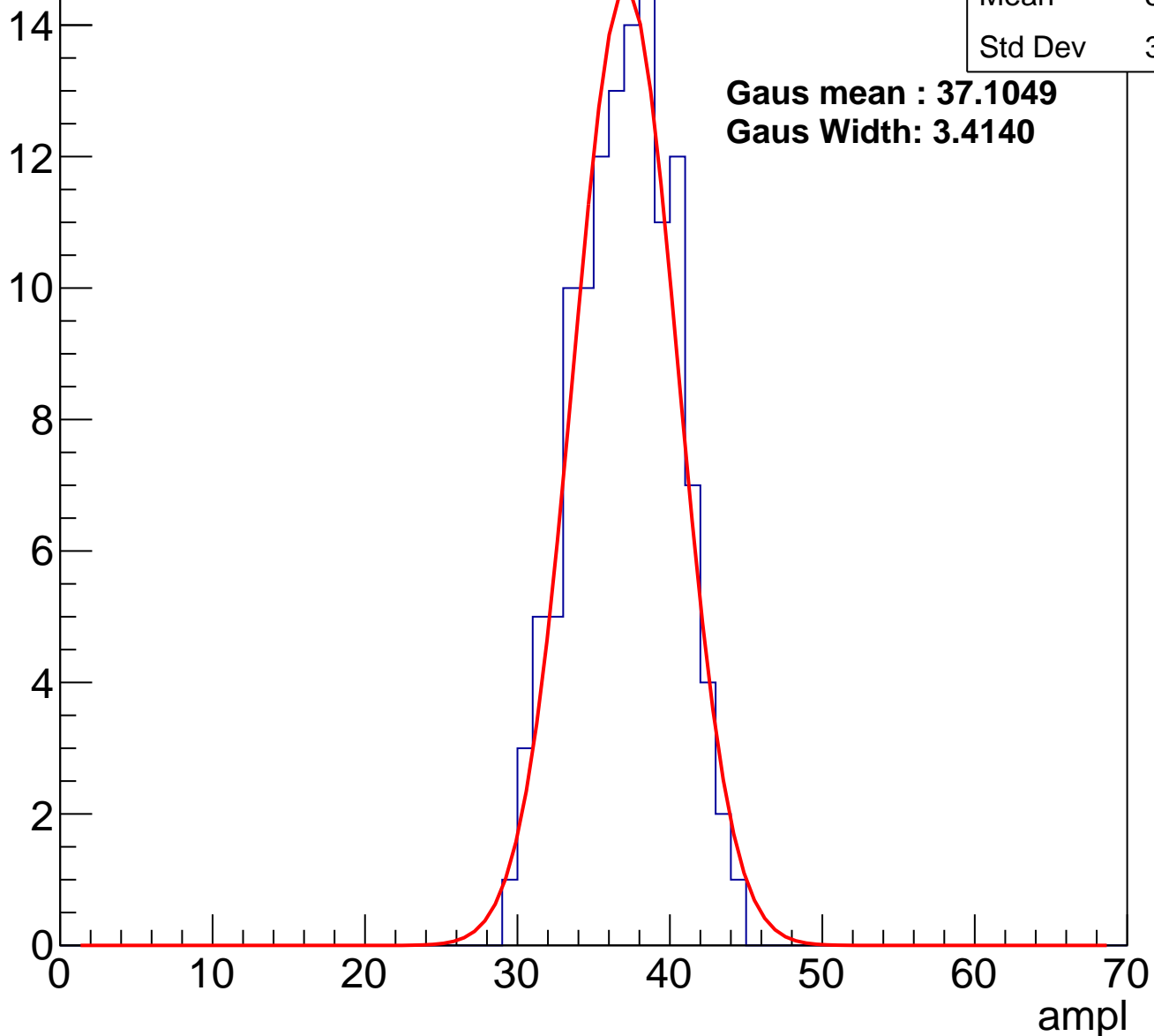
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.59
Std Dev	3.233

Gaus mean : 37.1049

Gaus Width: 3.4140

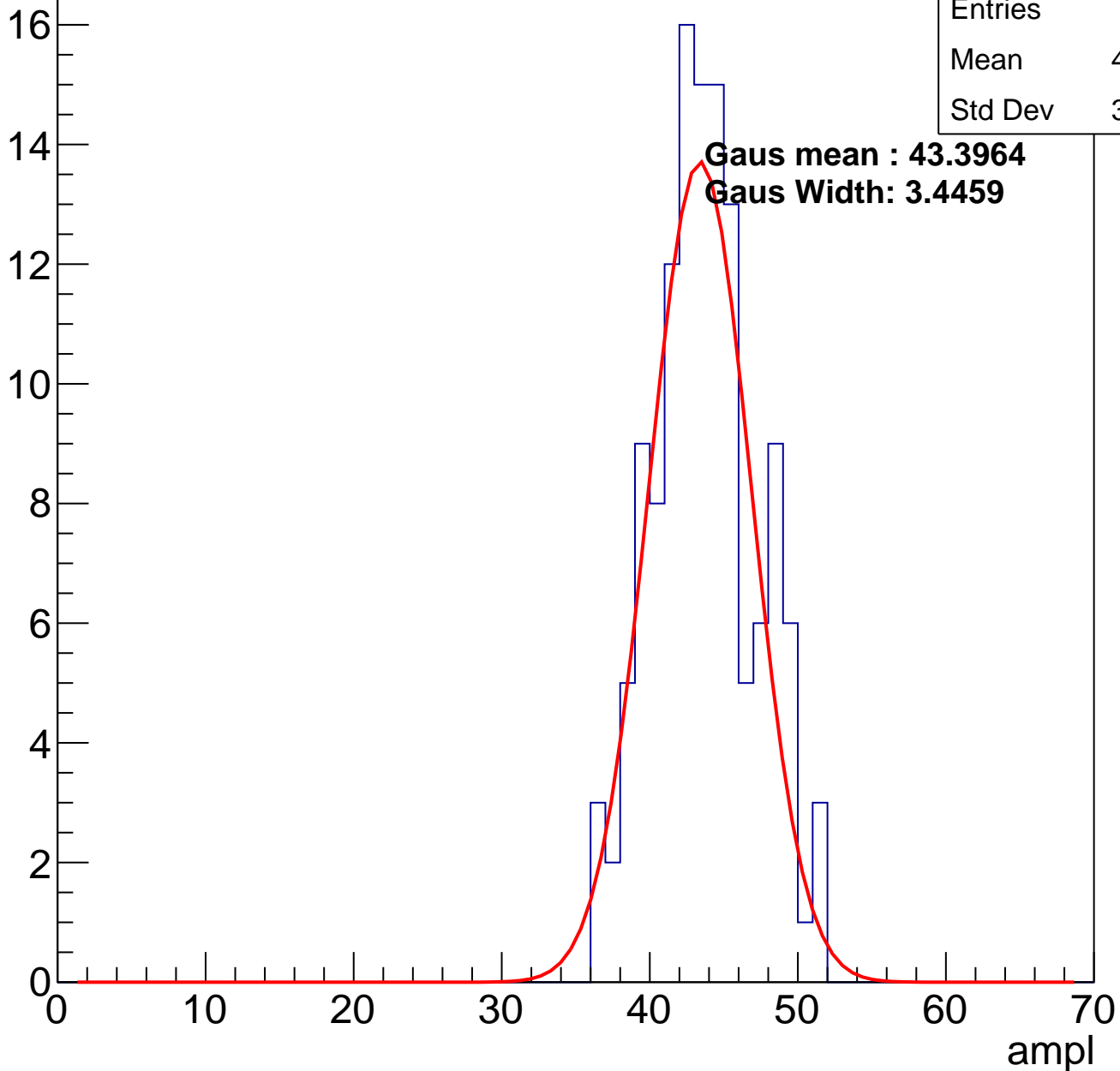
Entry



B1L001S, U22-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	49.97
Std Dev	3.519

Entry

14

12

10

8

6

4

2

0

0

10

20

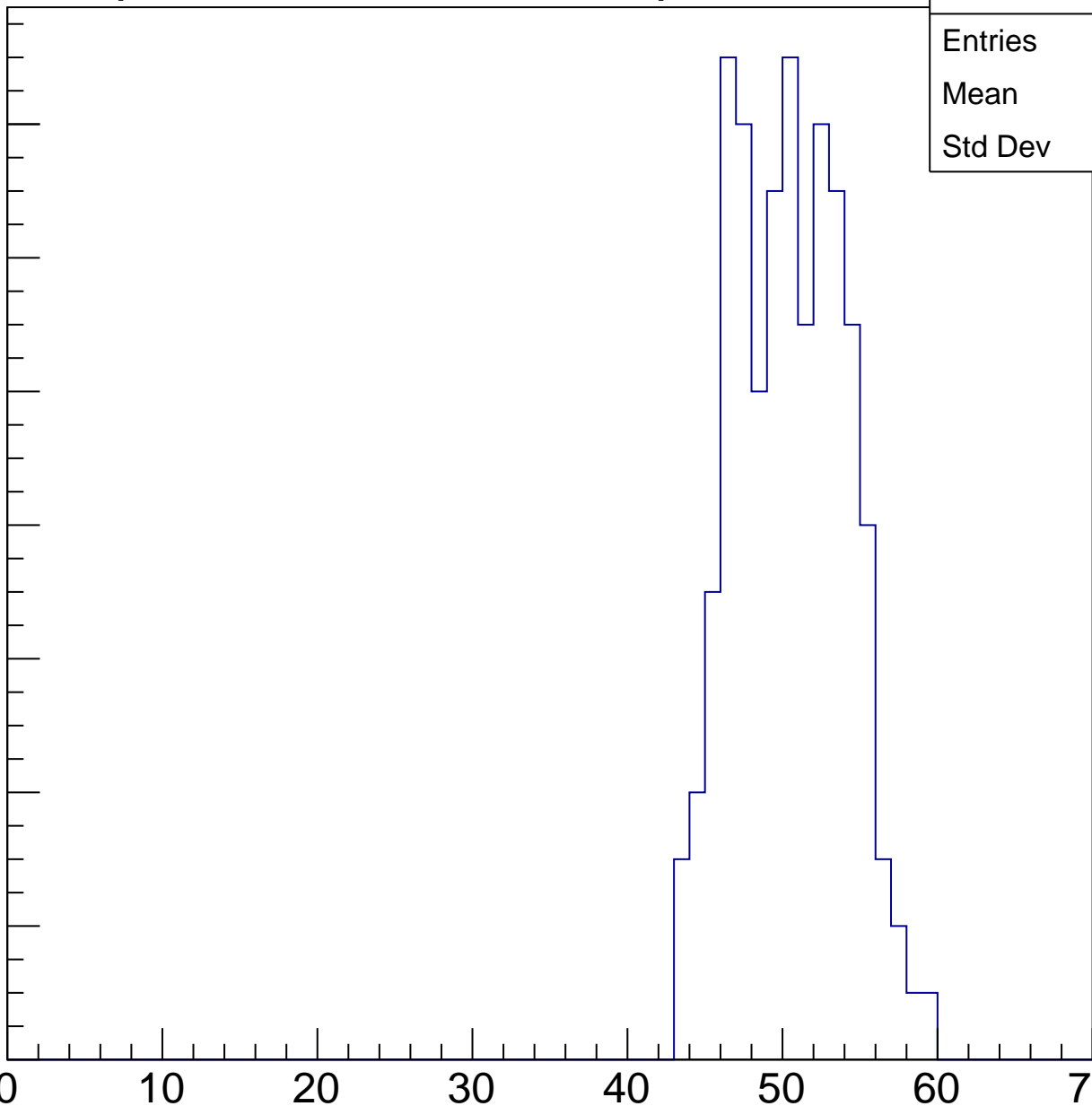
30

40

50

60

ampl



B1L001S, U22-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	88
Mean	55.41
Std Dev	3.118

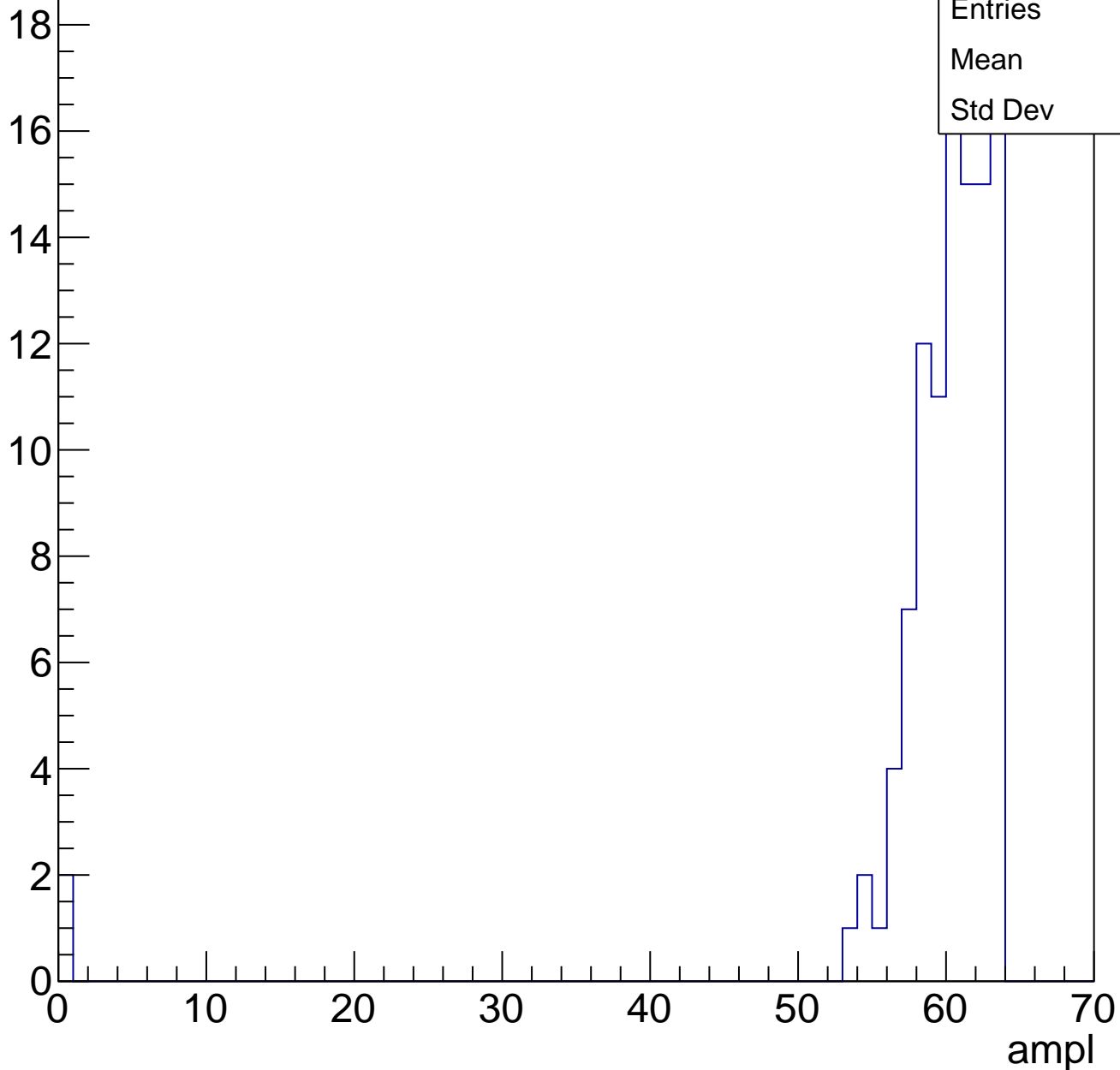
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch44, adc5

calib_packv5_042523_0143.root, FC#2, port C2

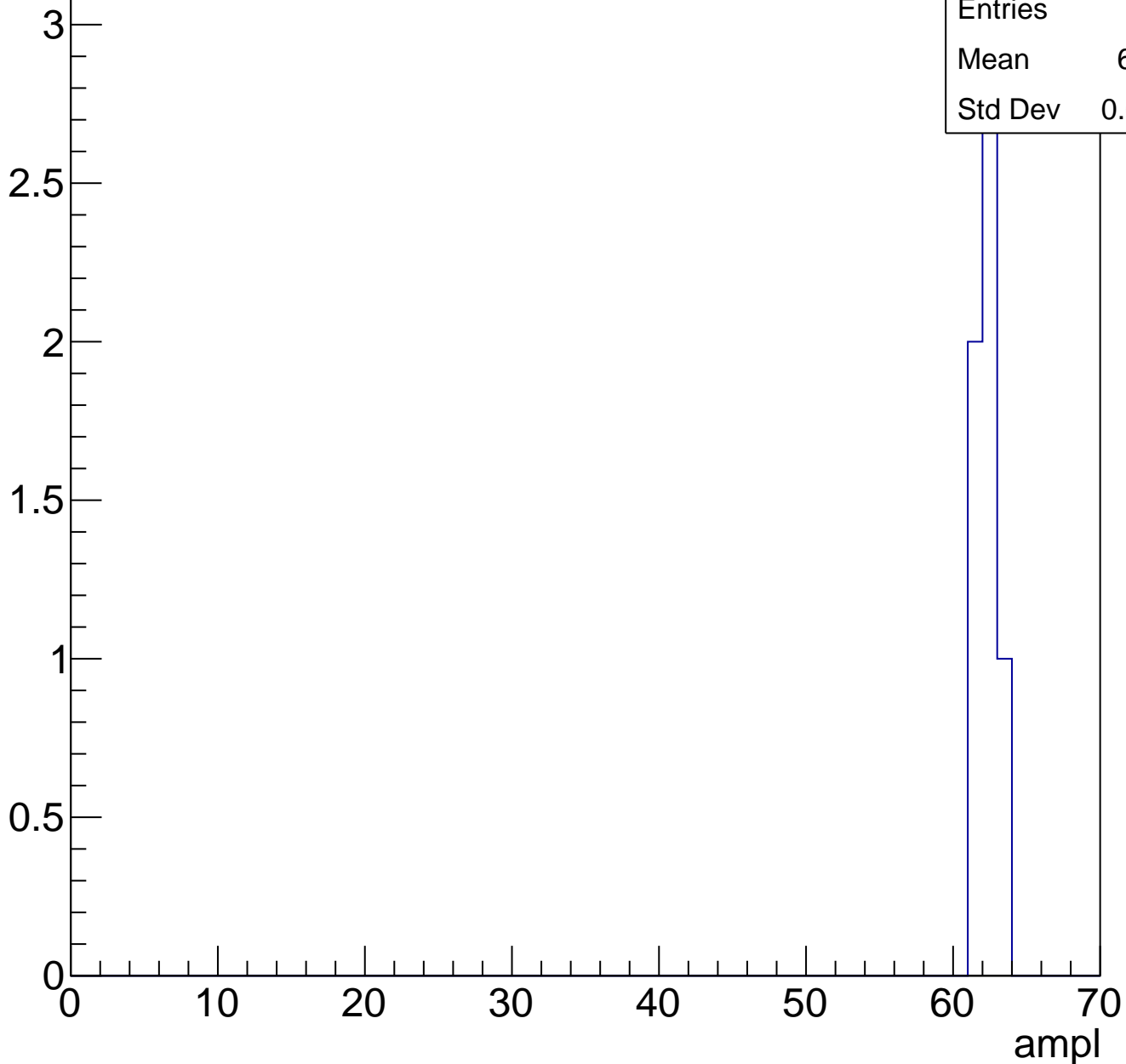
Entry



B1L001S, U22-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

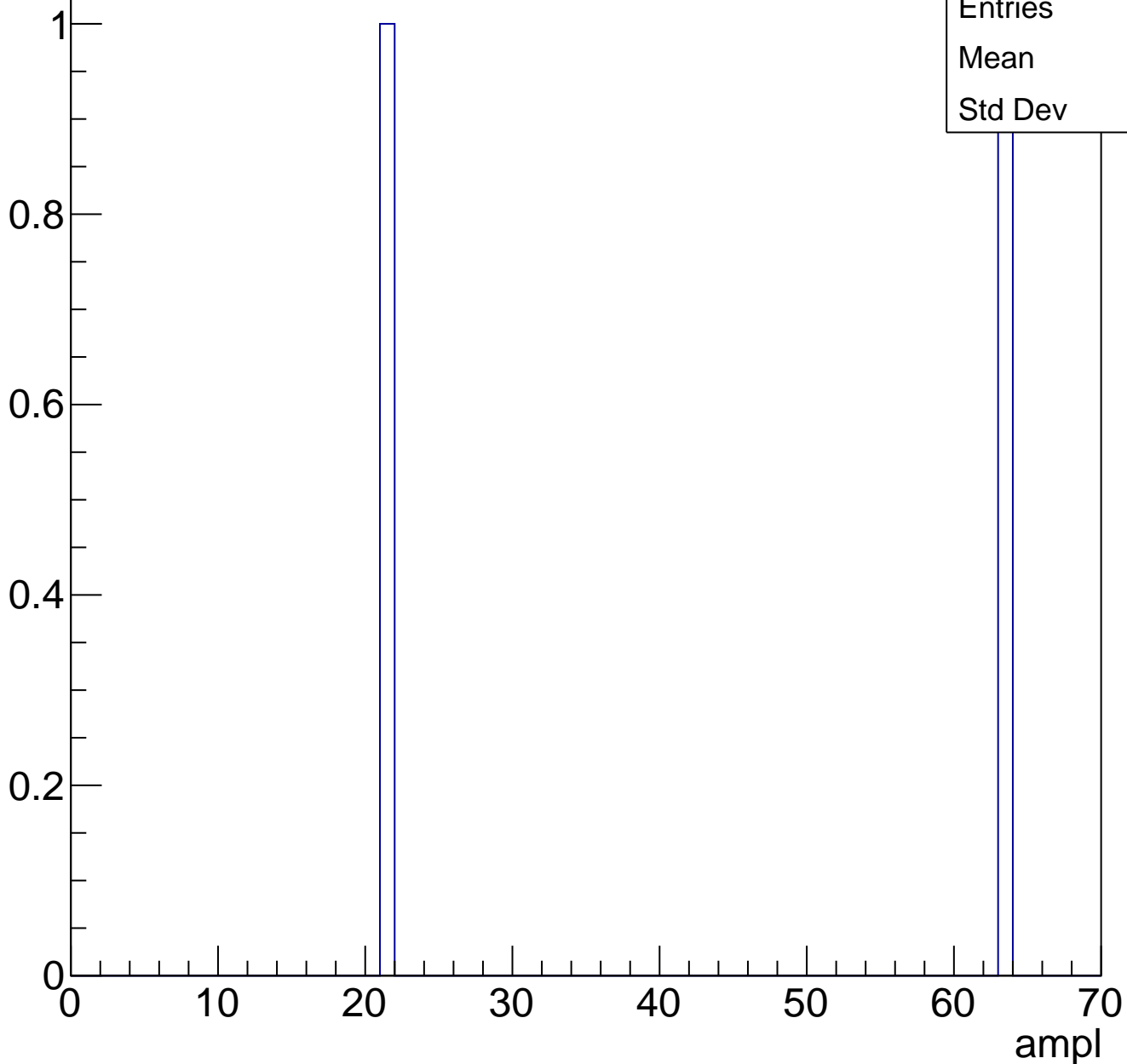
Entry



B1L001S, U22-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	30.06
Std Dev	2.973

Gaus mean : 30.4288

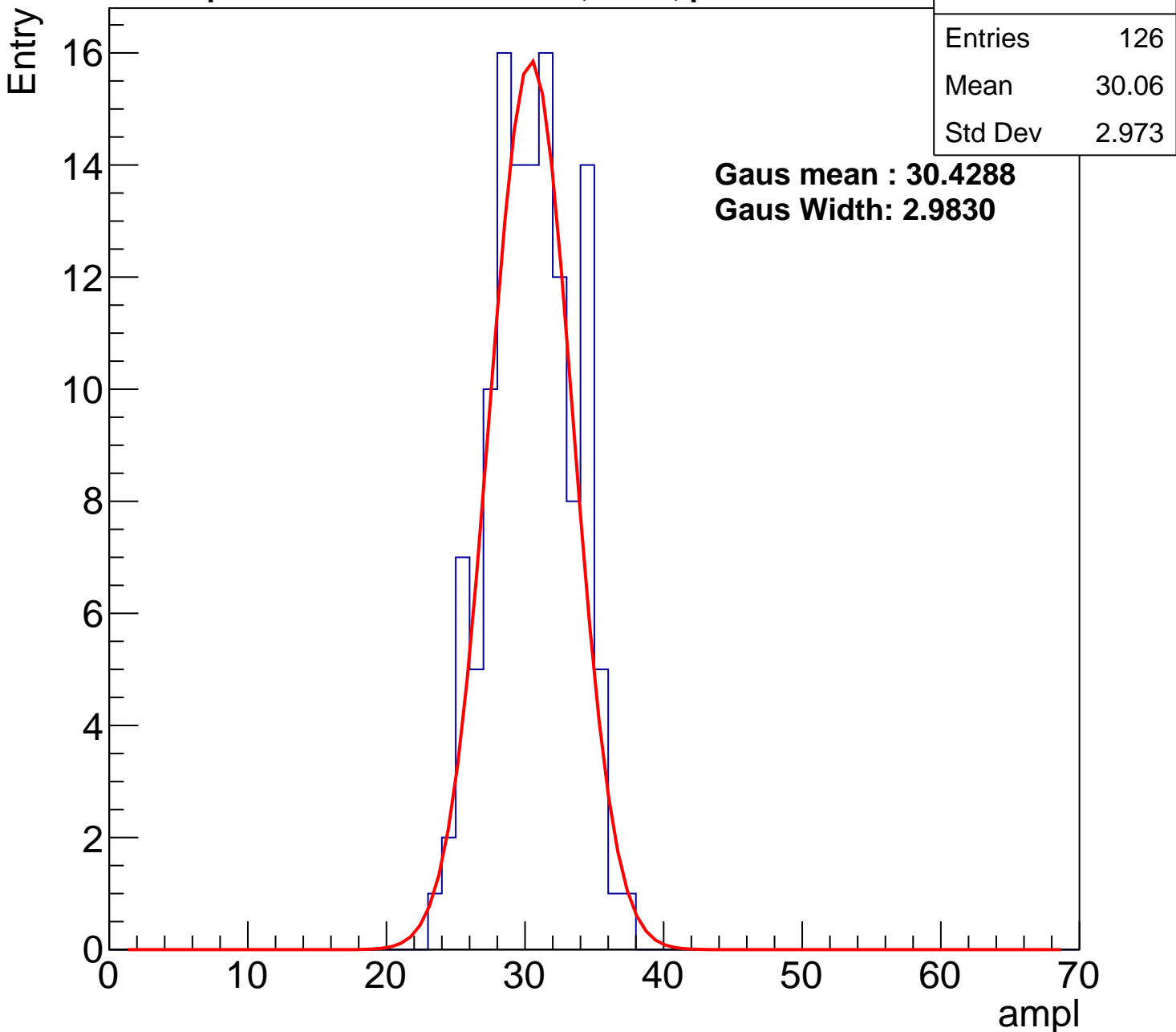
Gaus Width: 2.9830

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch45, adc1

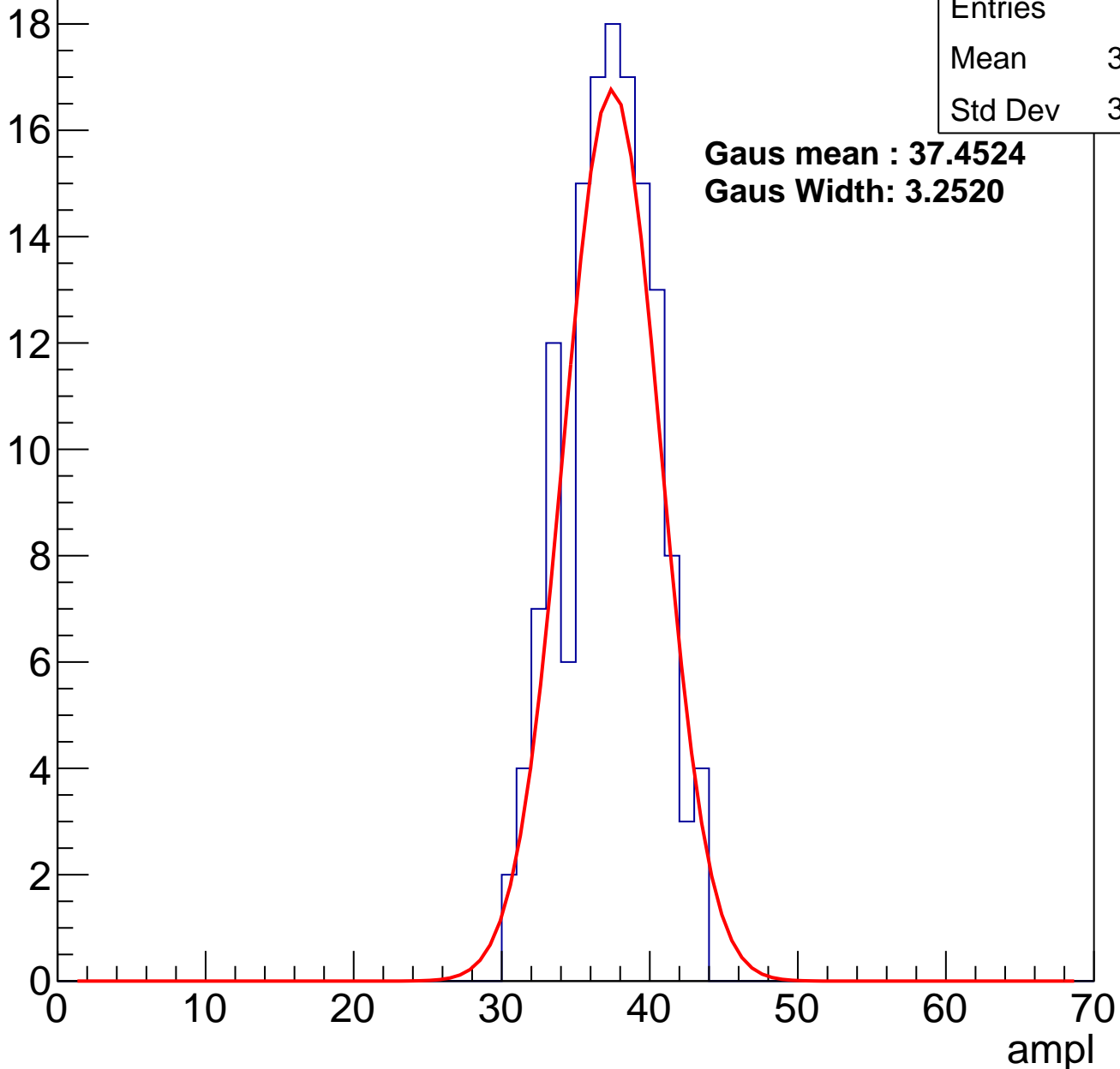
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	36.79
Std Dev	3.007

Gaus mean : 37.4524

Gaus Width: 3.2520

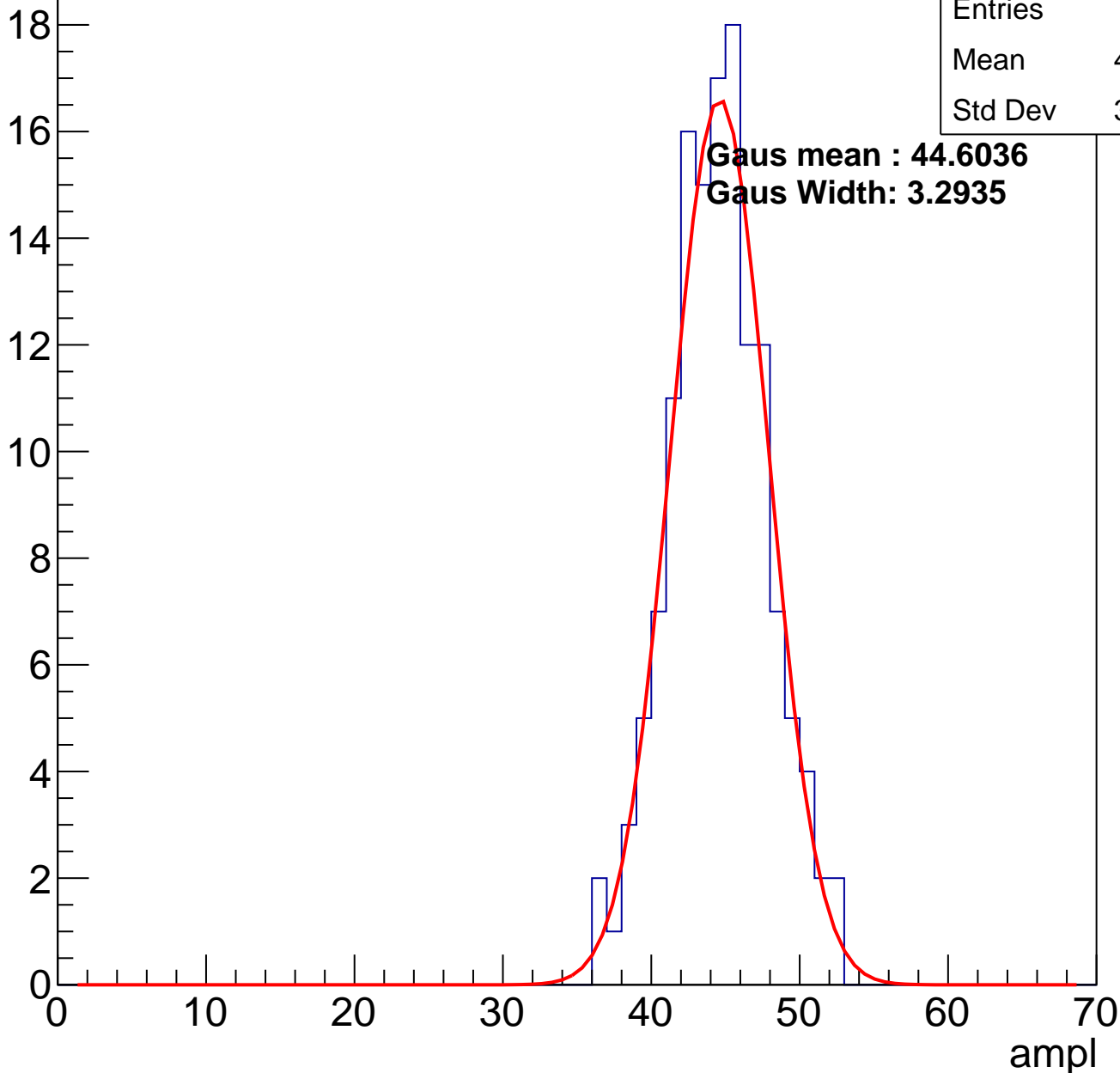
Entry



B1L001S, U22-ch45, adc2

calib_packv5_042523_0143.root, FC#2, port C2

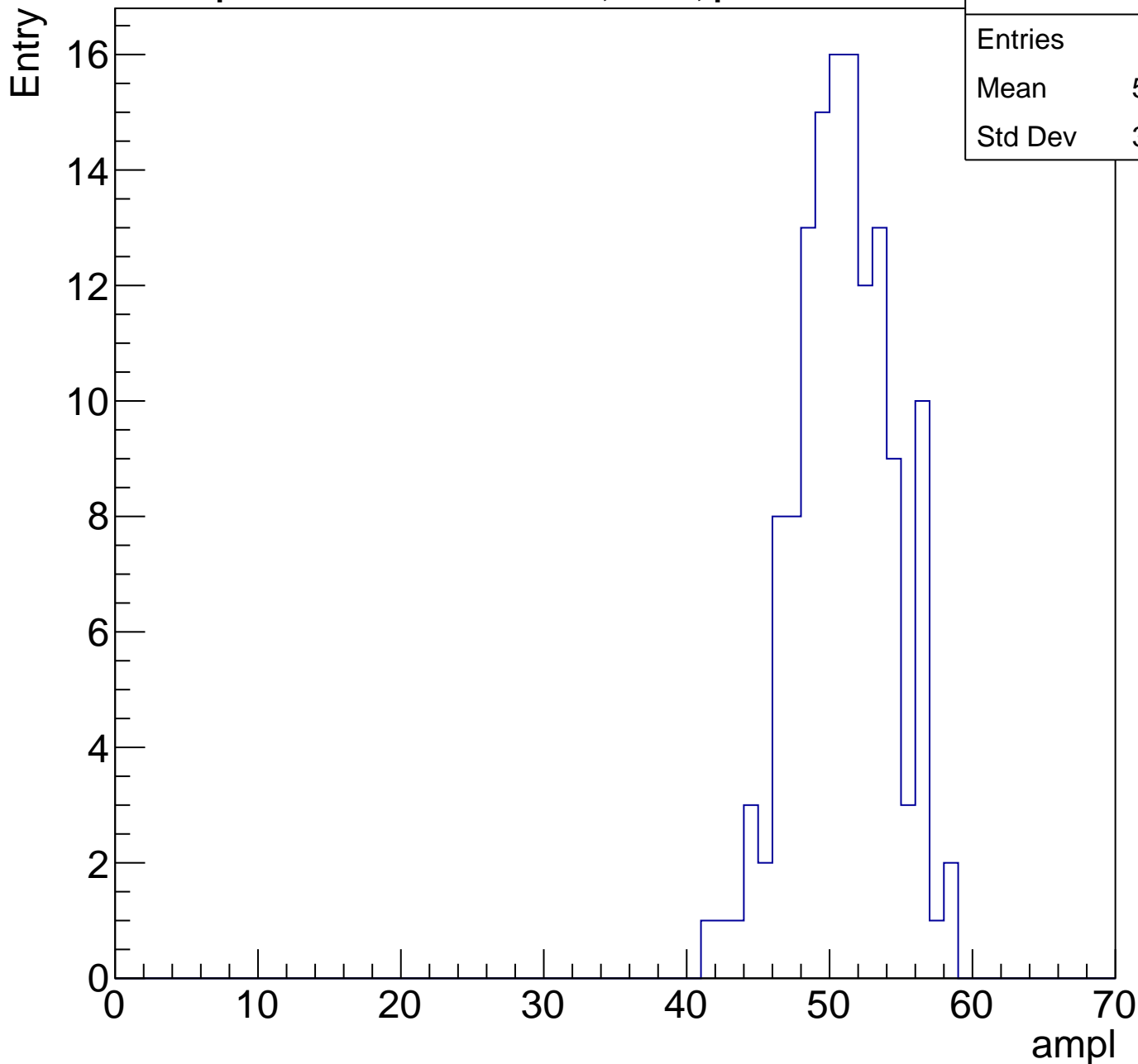
Entry



B1L001S, U22-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	50.48
Std Dev	3.374

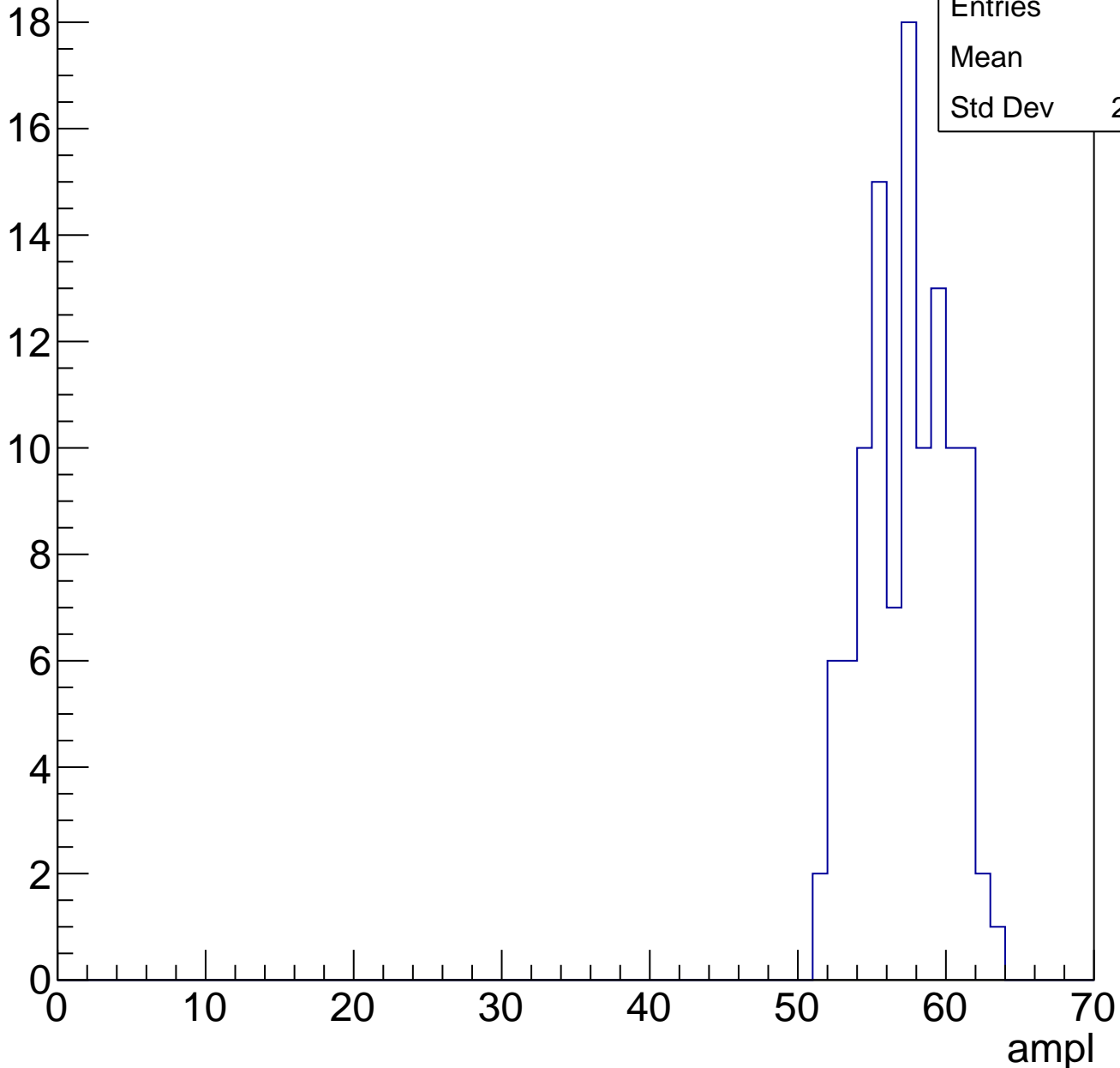


B1L001S, U22-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56.9
Std Dev	2.815

Entry



B1L001S, U22-ch45, adc5

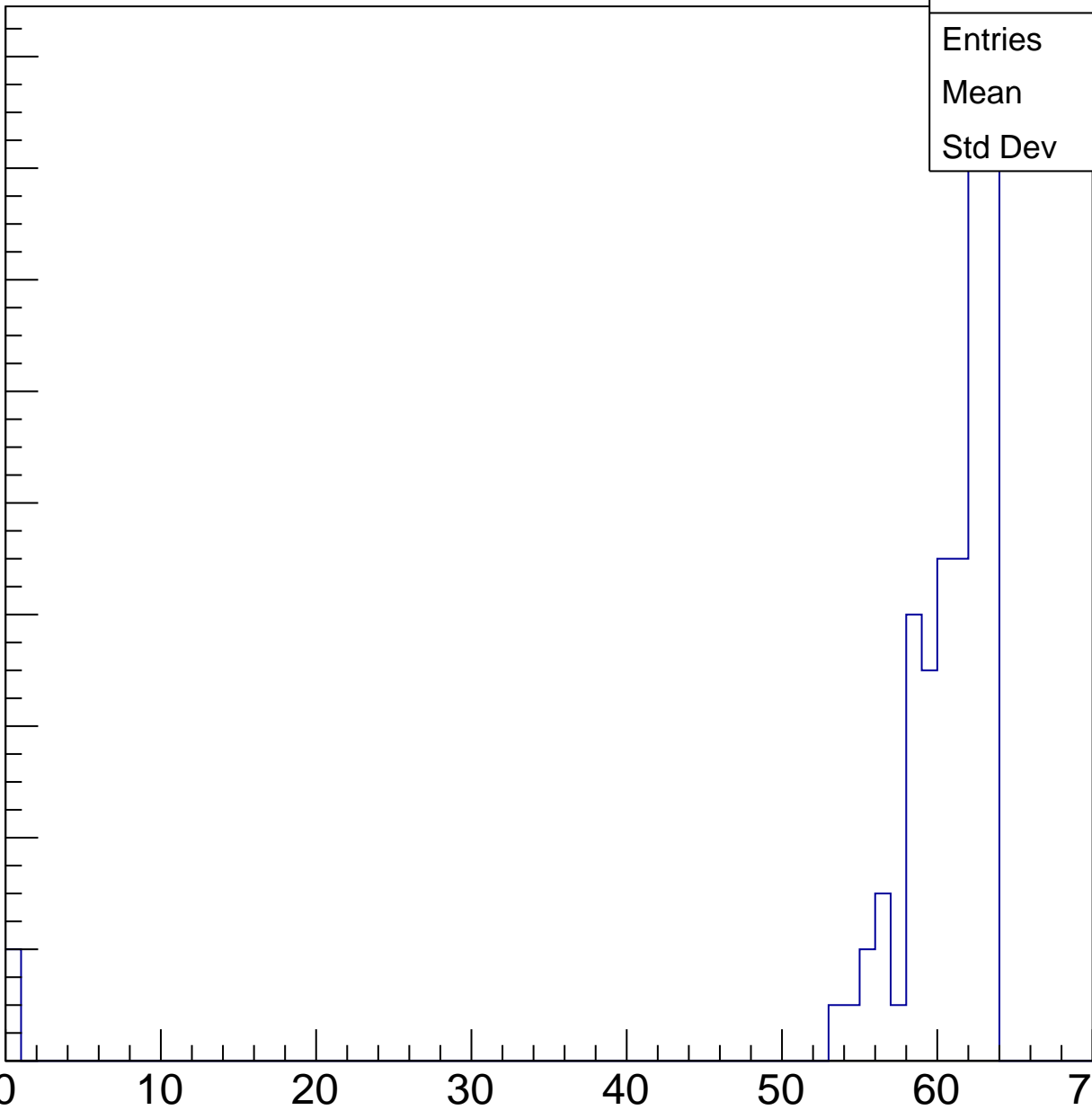
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.91
Std Dev	9.853

ampl



B1L001S, U22-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch46, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	29.81
Std Dev	3.533

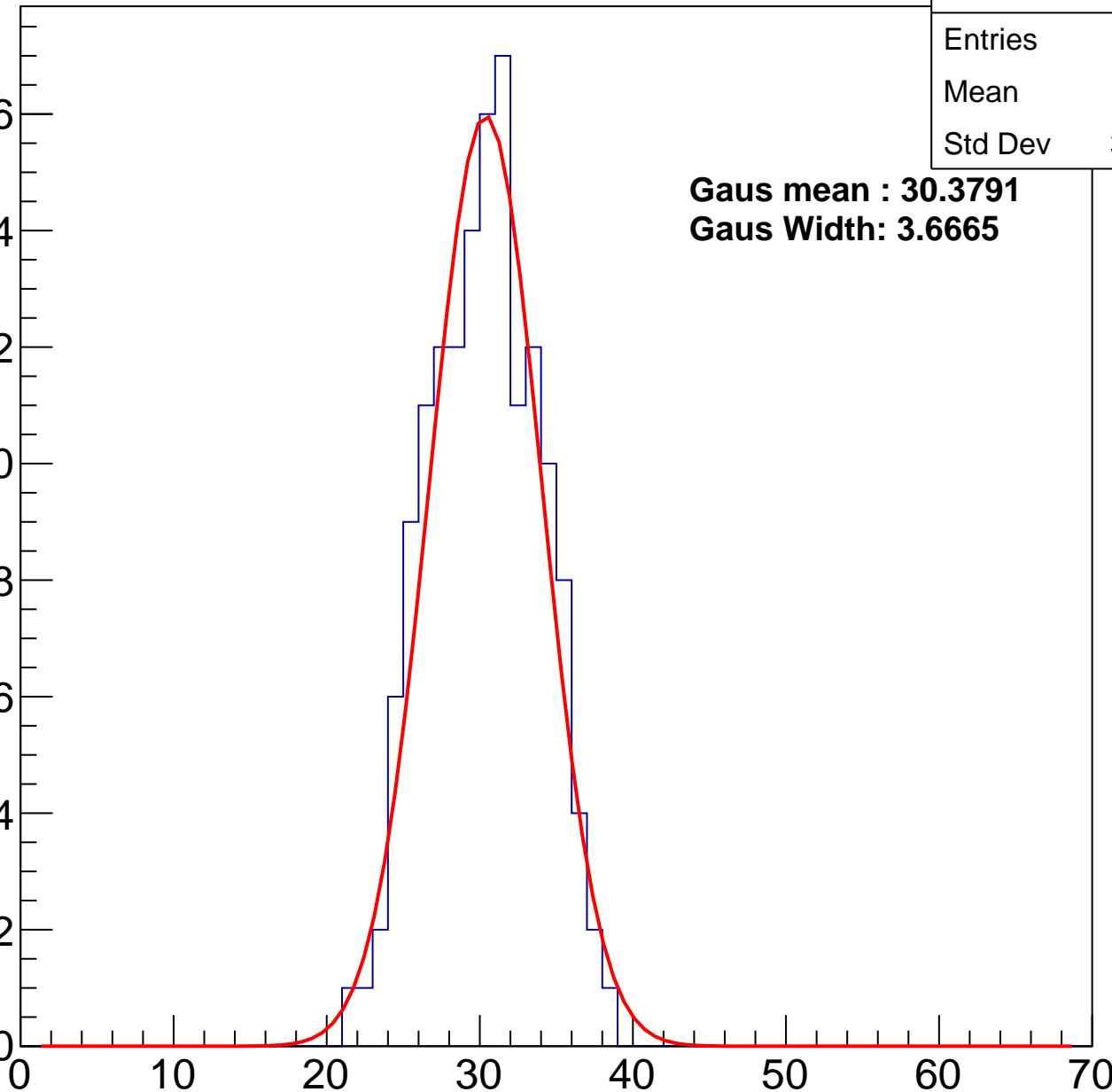
Gaus mean : 30.3791

Gaus Width: 3.6665

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch46, adc1

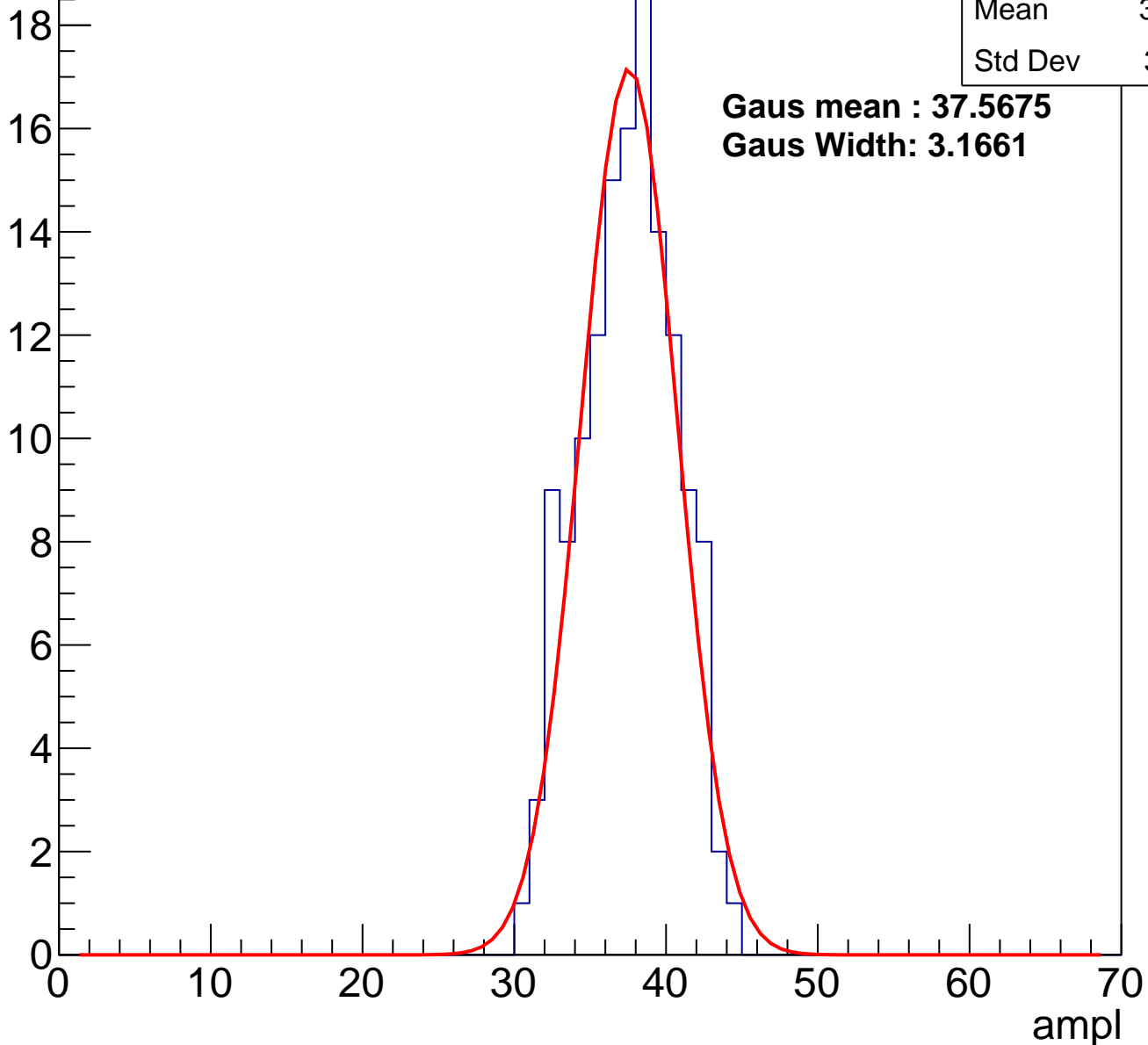
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	37.05
Std Dev	3.071

Gaus mean : 37.5675

Gaus Width: 3.1661

Entry



B1L001S, U22-ch46, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.07
Std Dev	3.064

Gaus mean : 44.4531

Gaus Width: 3.3699

Entry

16

14

12

10

8

6

4

2

0

0

10

20

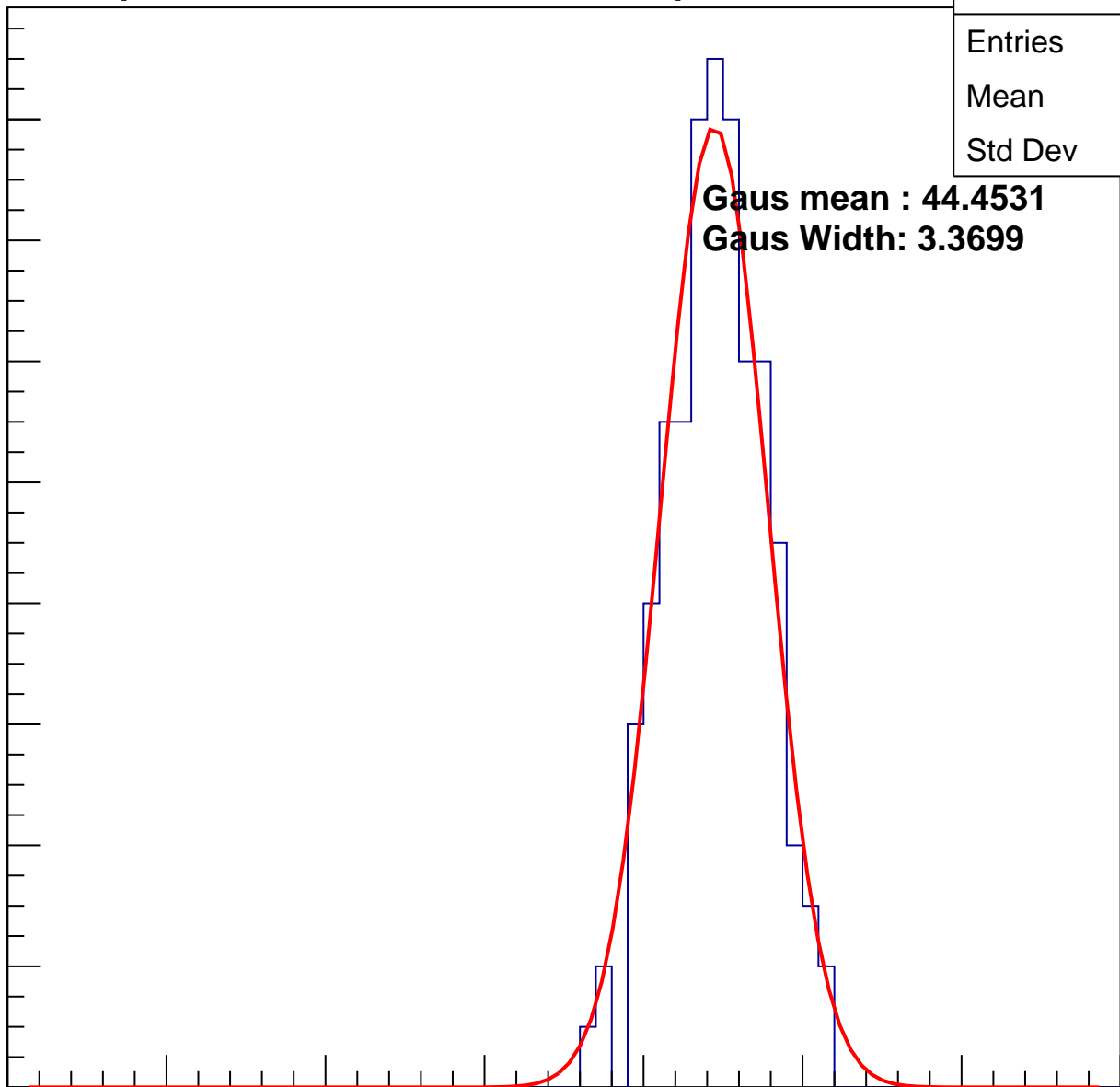
30

40

50

60

ampl



B1L001S, U22-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

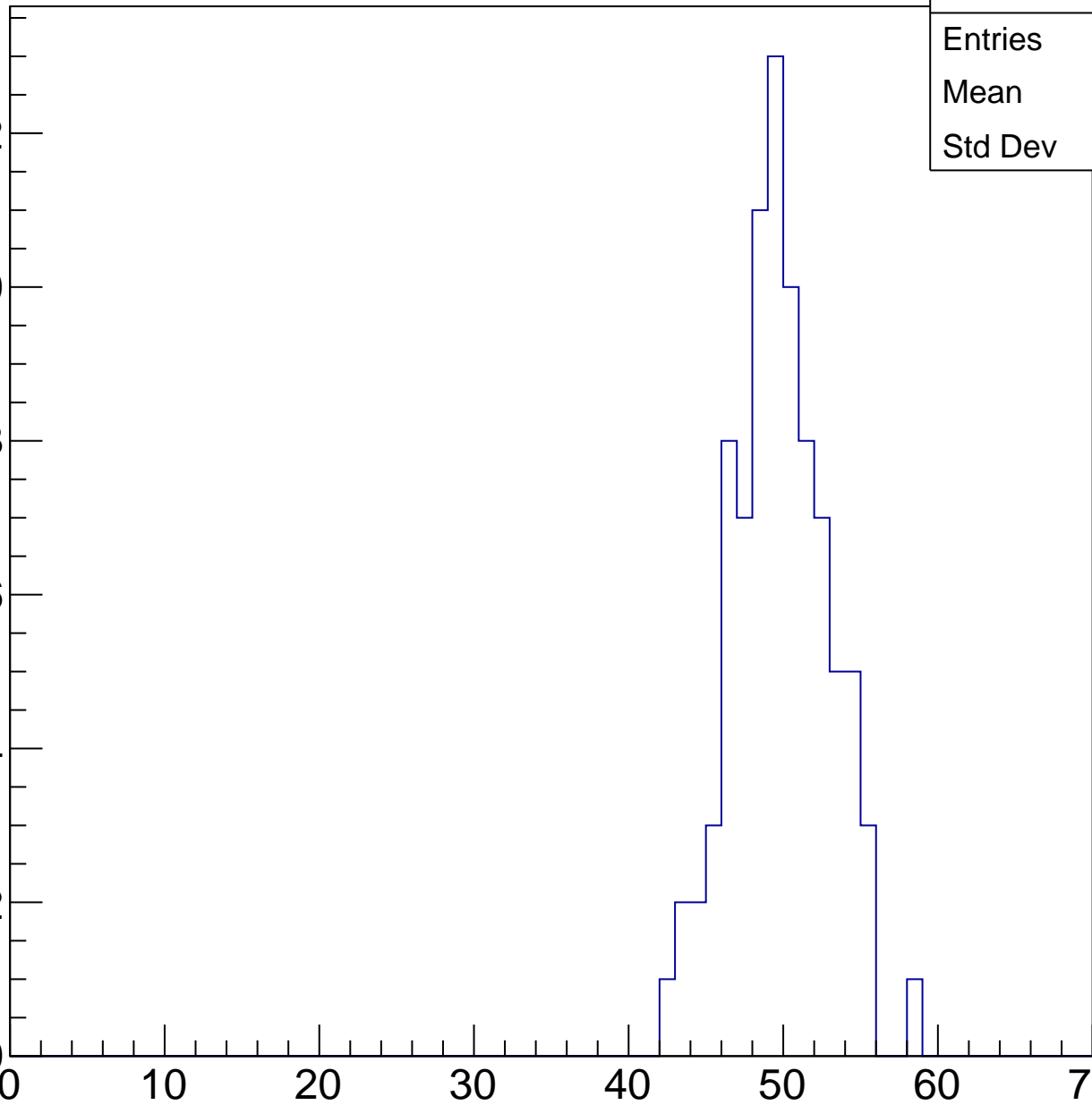
40

50

60

ampl

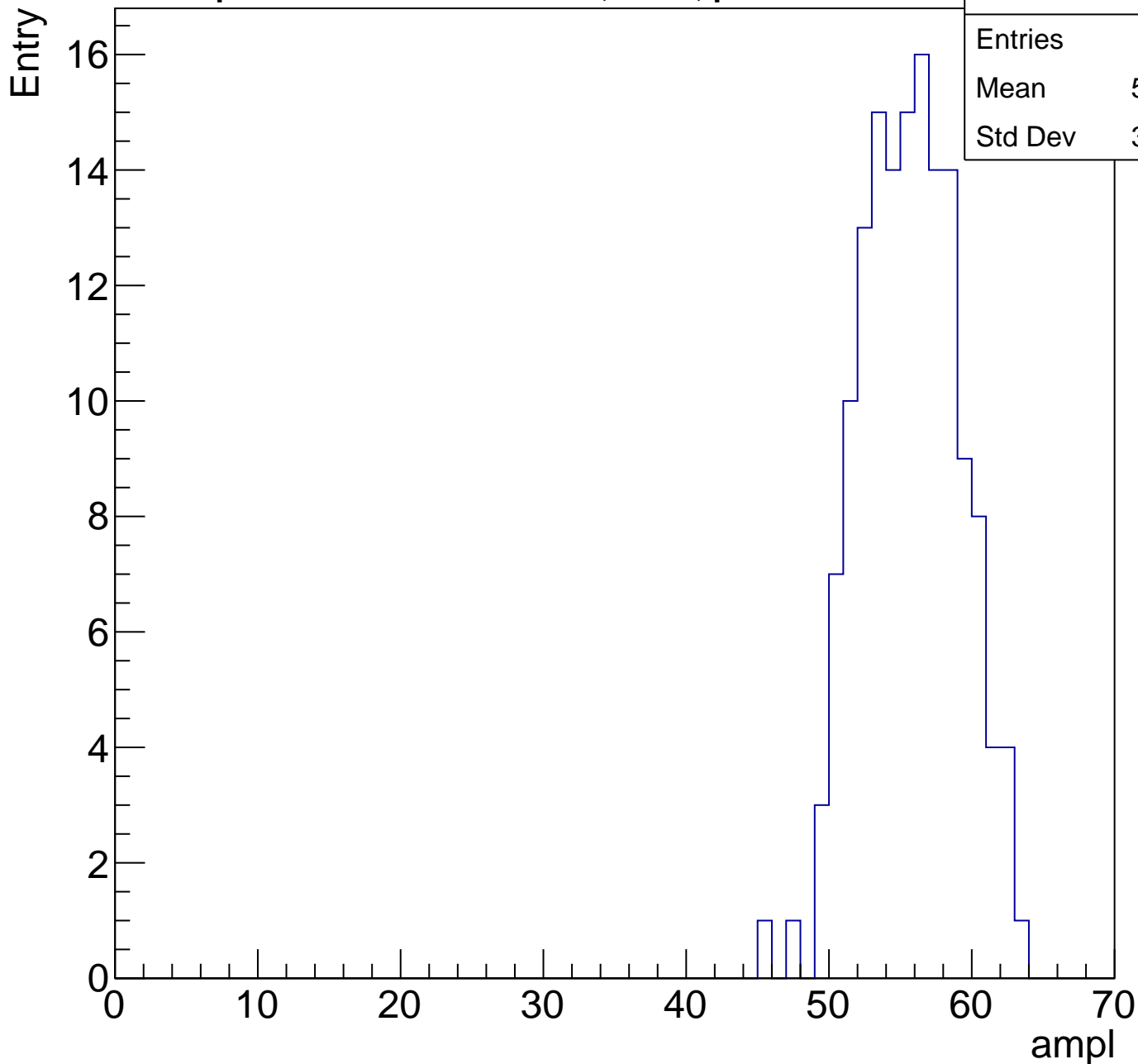
Entries	86
Mean	49.34
Std Dev	3.086



B1L001S, U22-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	55.19
Std Dev	3.416



B1L001S, U22-ch46, adc5

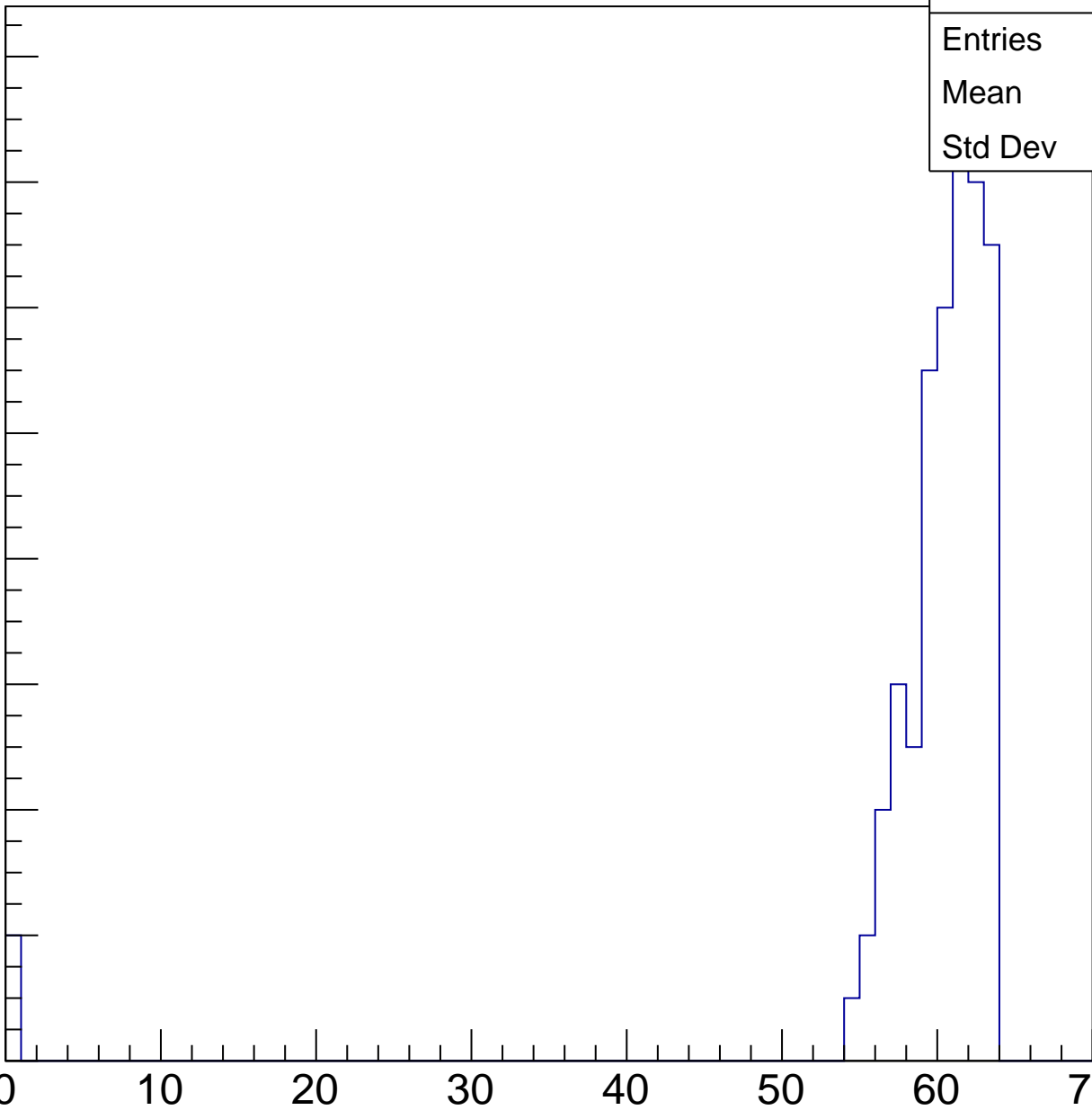
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.74
Std Dev	9.331

ampl

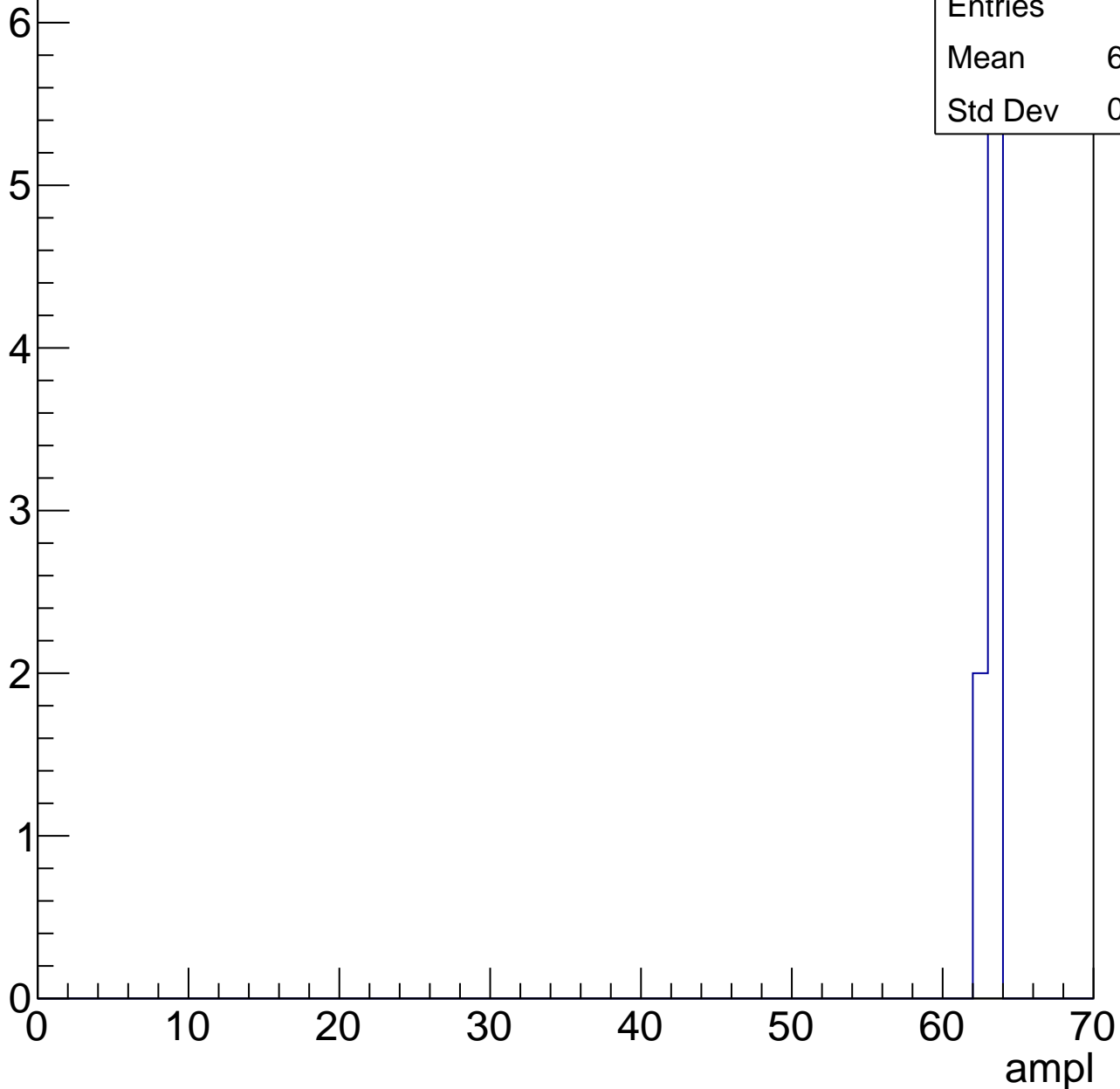


B1L001S, U22-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B1L001S, U22-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U22-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.79
Std Dev	3.447

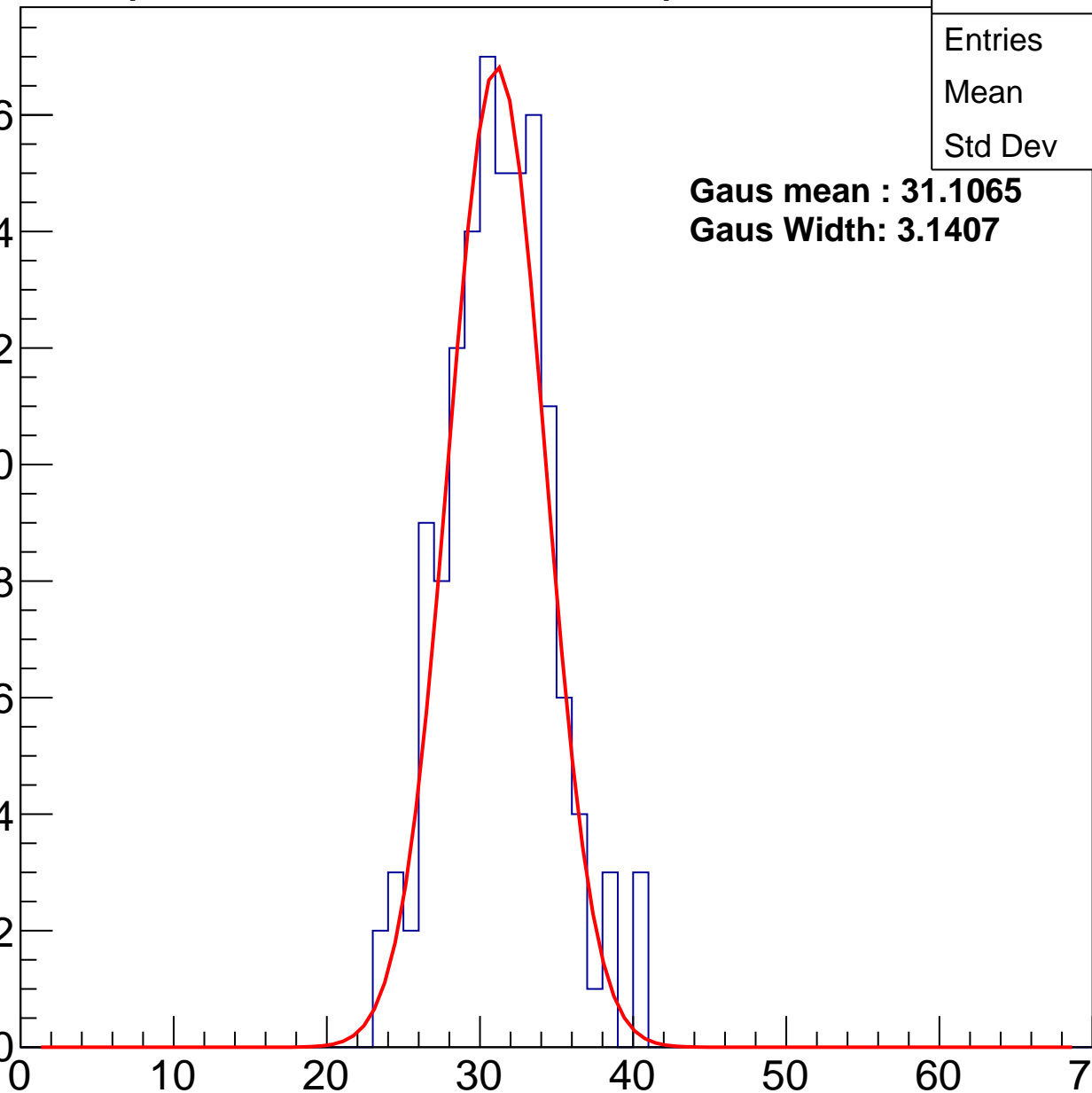
Gaus mean : 31.1065

Gaus Width: 3.1407

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	37.4
Std Dev	3.1

Gaus mean : 38.0267

Gaus Width: 3.4193

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

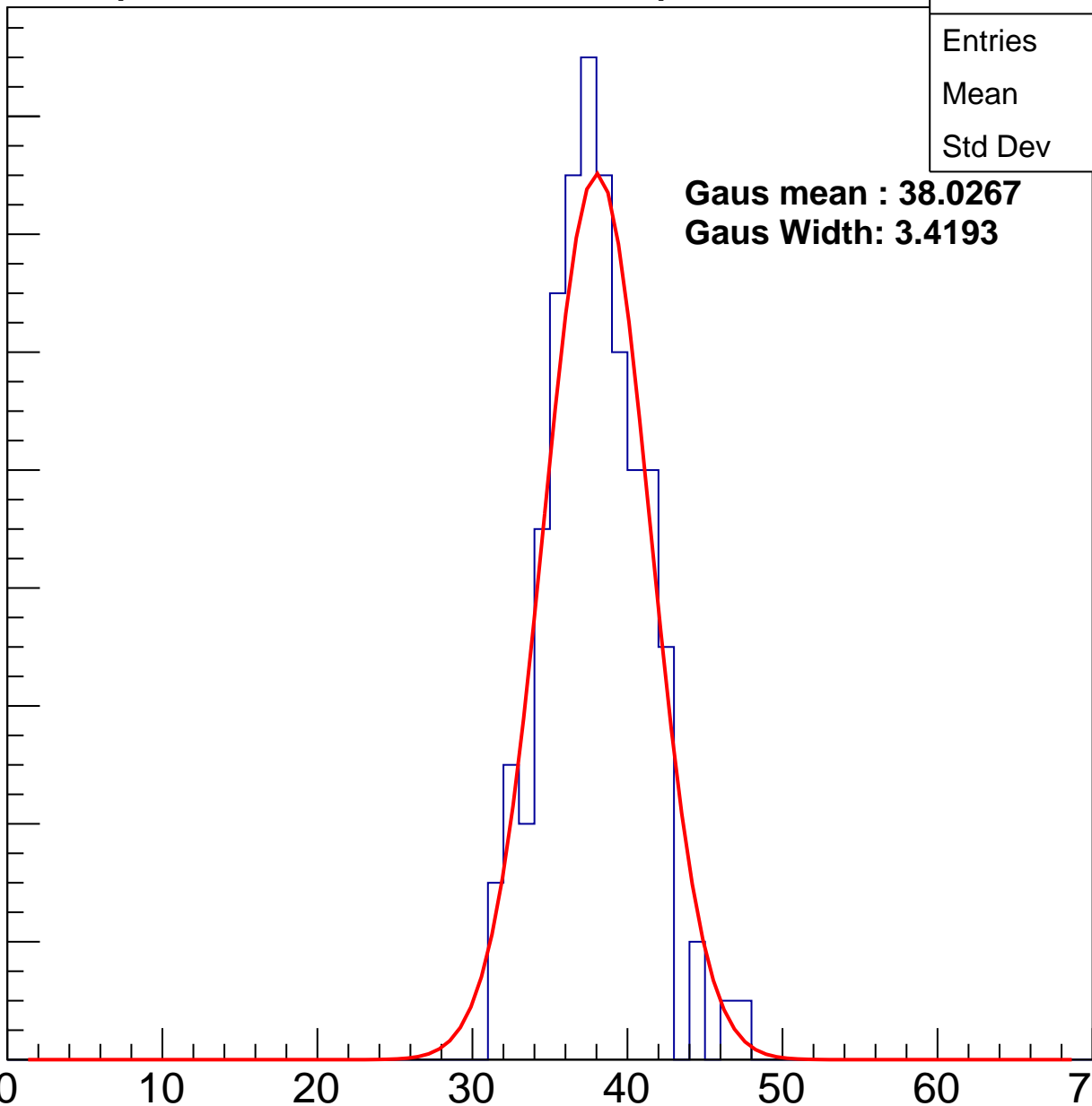
40

50

60

70

ampl



B1L001S, U22-ch47, adc2

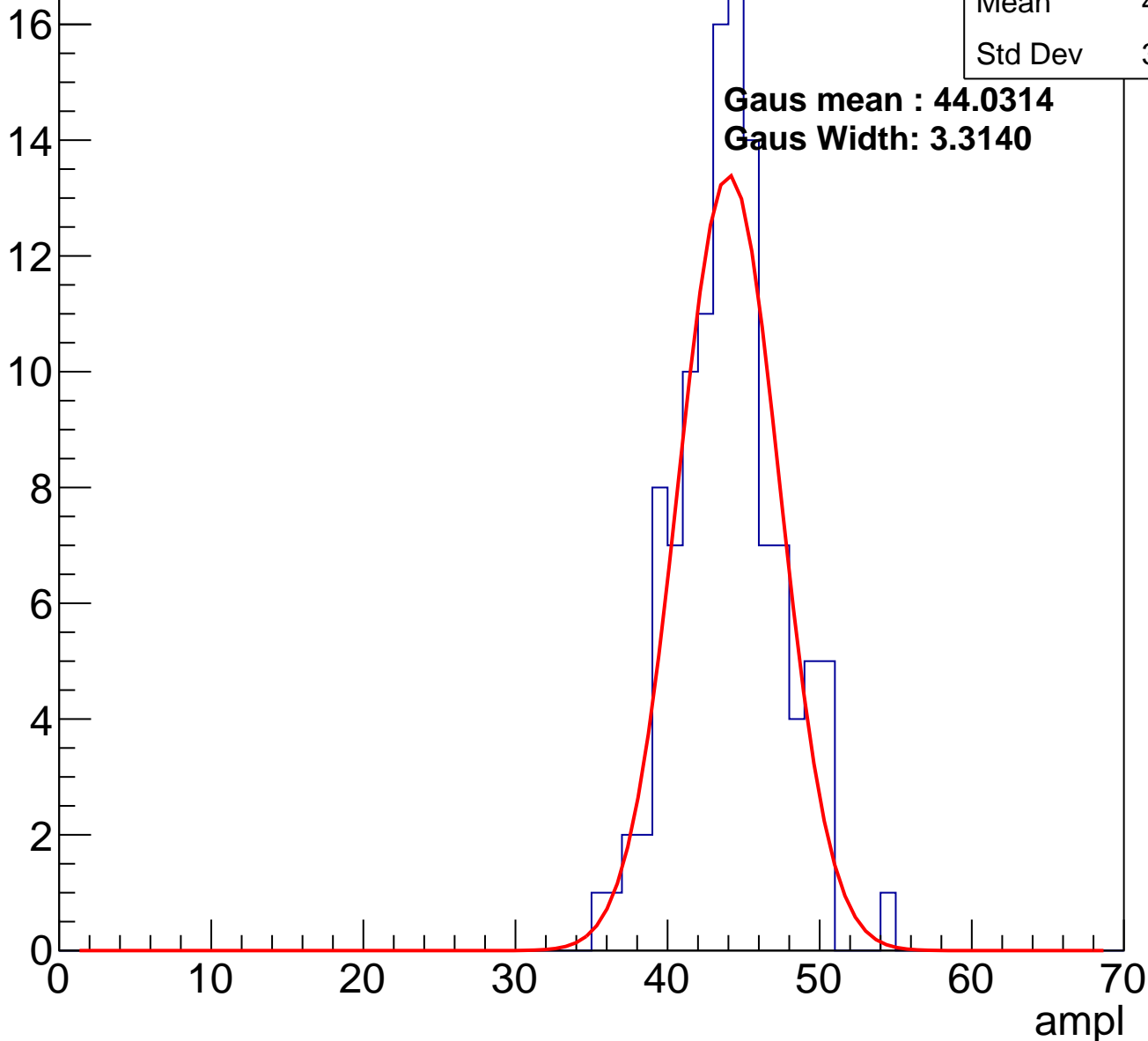
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	118
Mean	43.58
Std Dev	3.358

Gaus mean : 44.0314

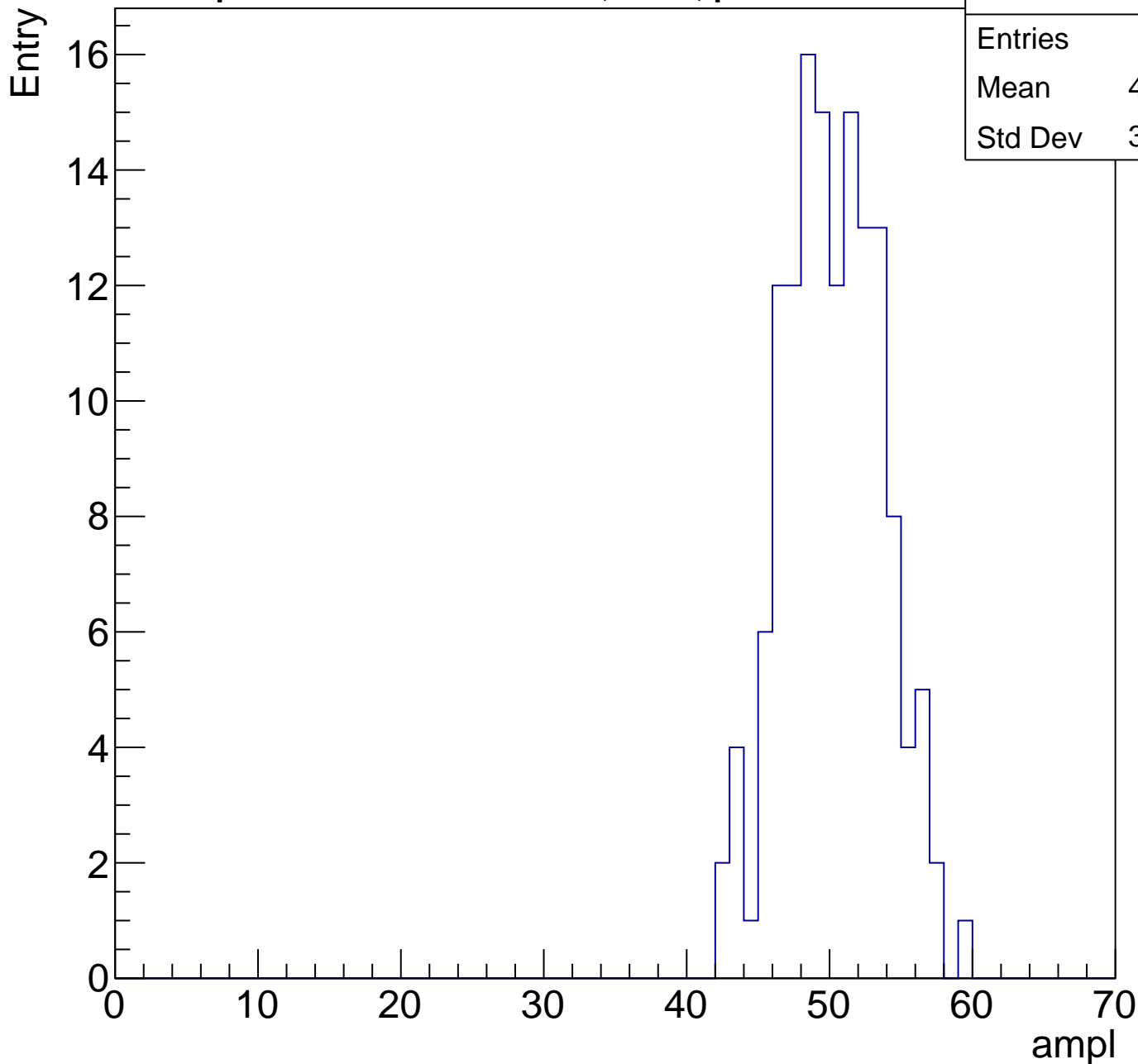
Gaus Width: 3.3140



B1L001S, U22-ch47, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.82
Std Dev	3.426



B1L001S, U22-ch47, adc4

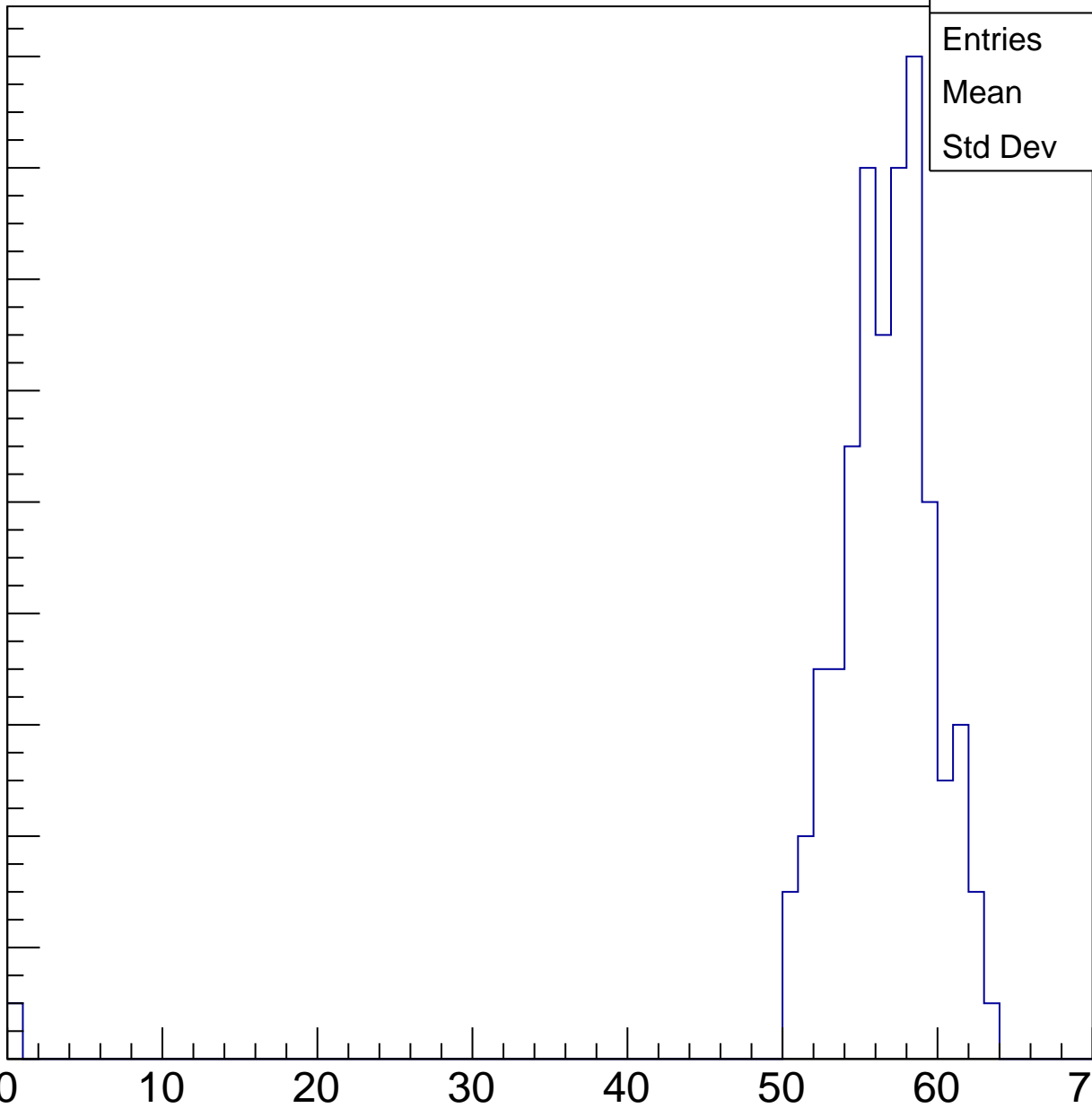
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	121
Mean	55.8
Std Dev	5.849

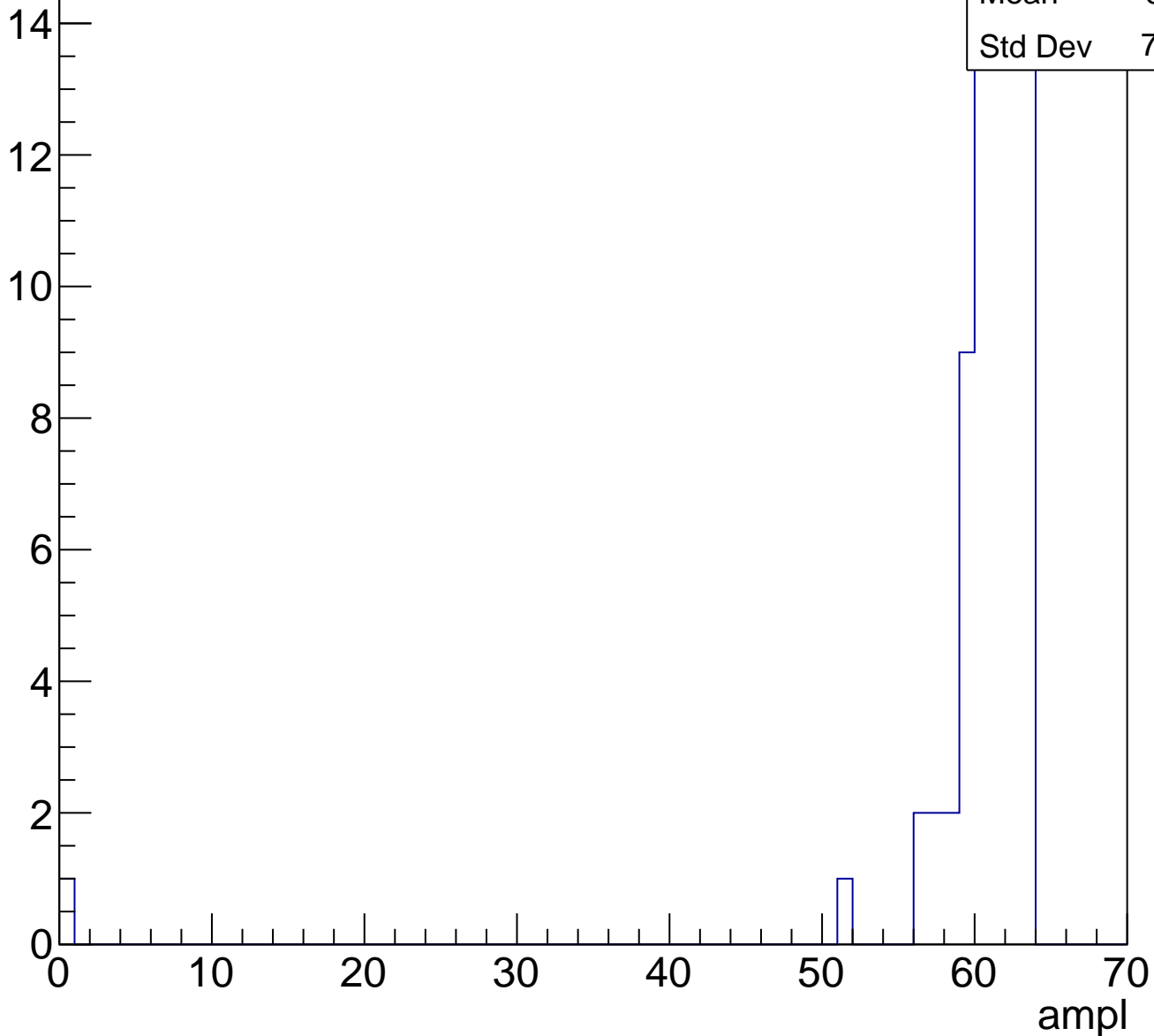
ampl



B1L001S, U22-ch47, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch48, adc0

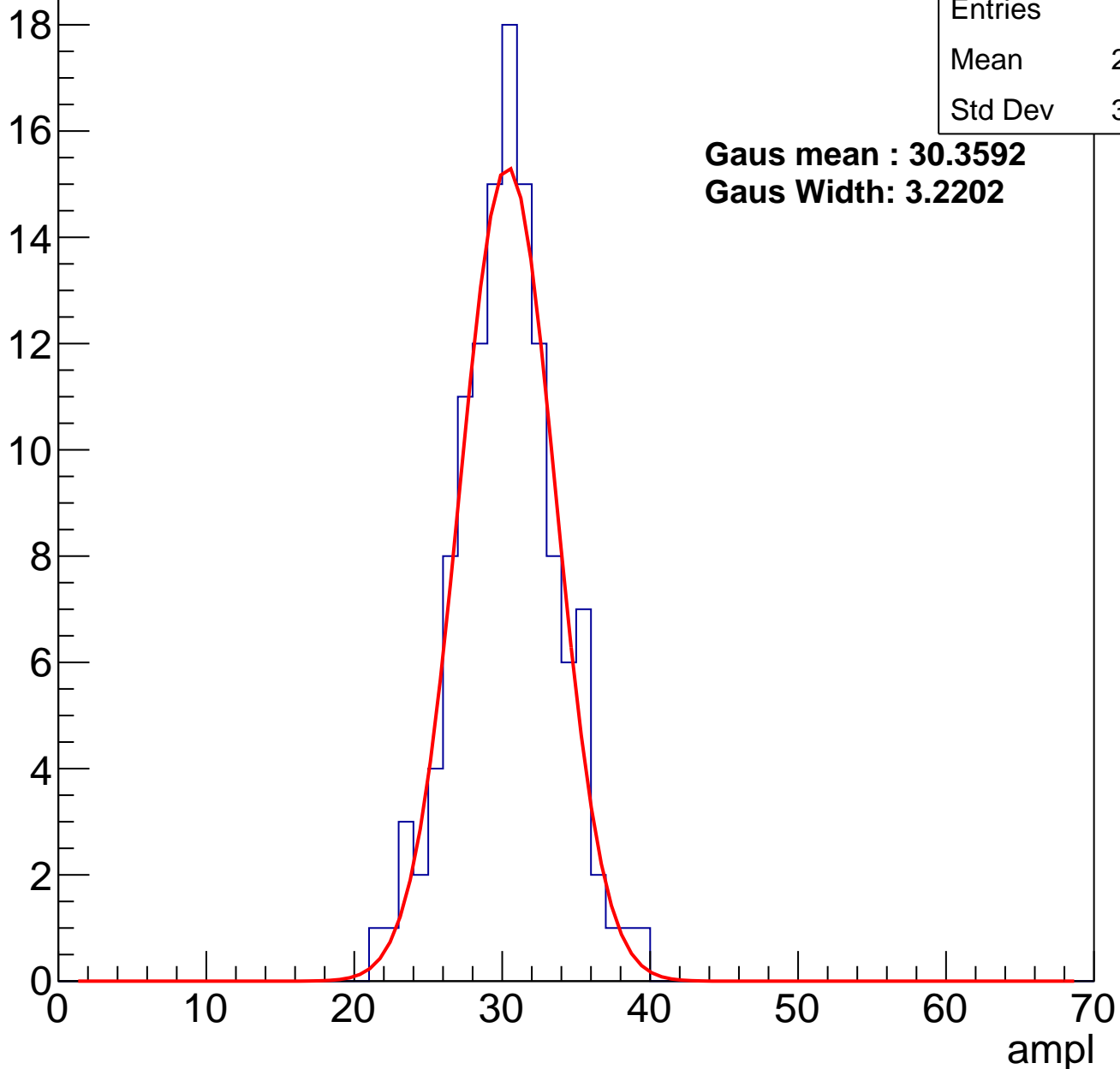
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.88
Std Dev	3.335

Gaus mean : 30.3592

Gaus Width: 3.2202

Entry



B1L001S, U22-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.22
Std Dev	3.212

Gaus mean : 37.0399

Gaus Width: 3.2345

Entry

16

14

12

10

8

6

4

2

0

0

10

20

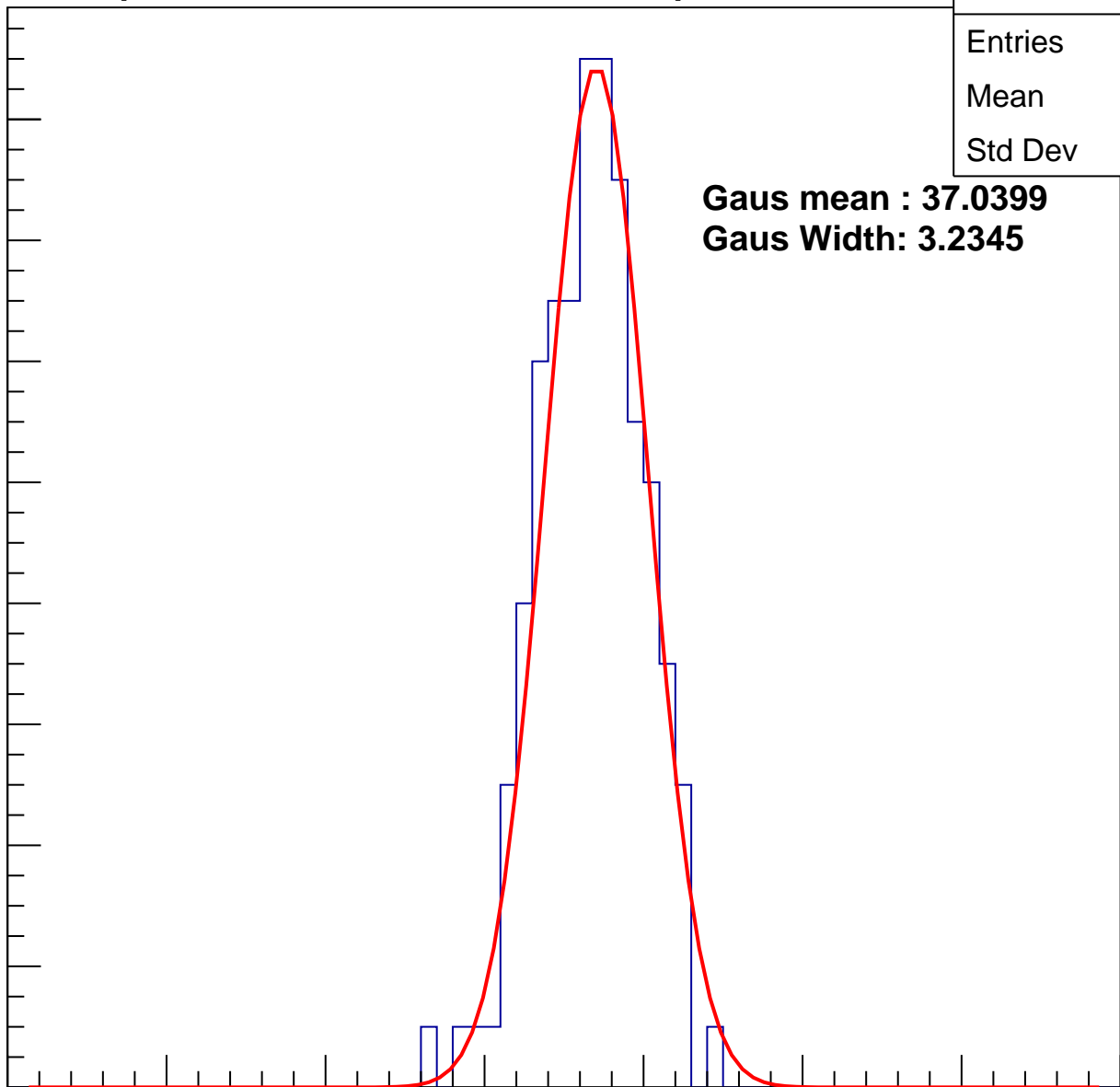
30

40

50

60

ampl



B1L001S, U22-ch48, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

120

Mean

42.88

Std Dev

2.99

Gaus mean : 43.4082

Gaus Width: 3.0279

0

10

20

30

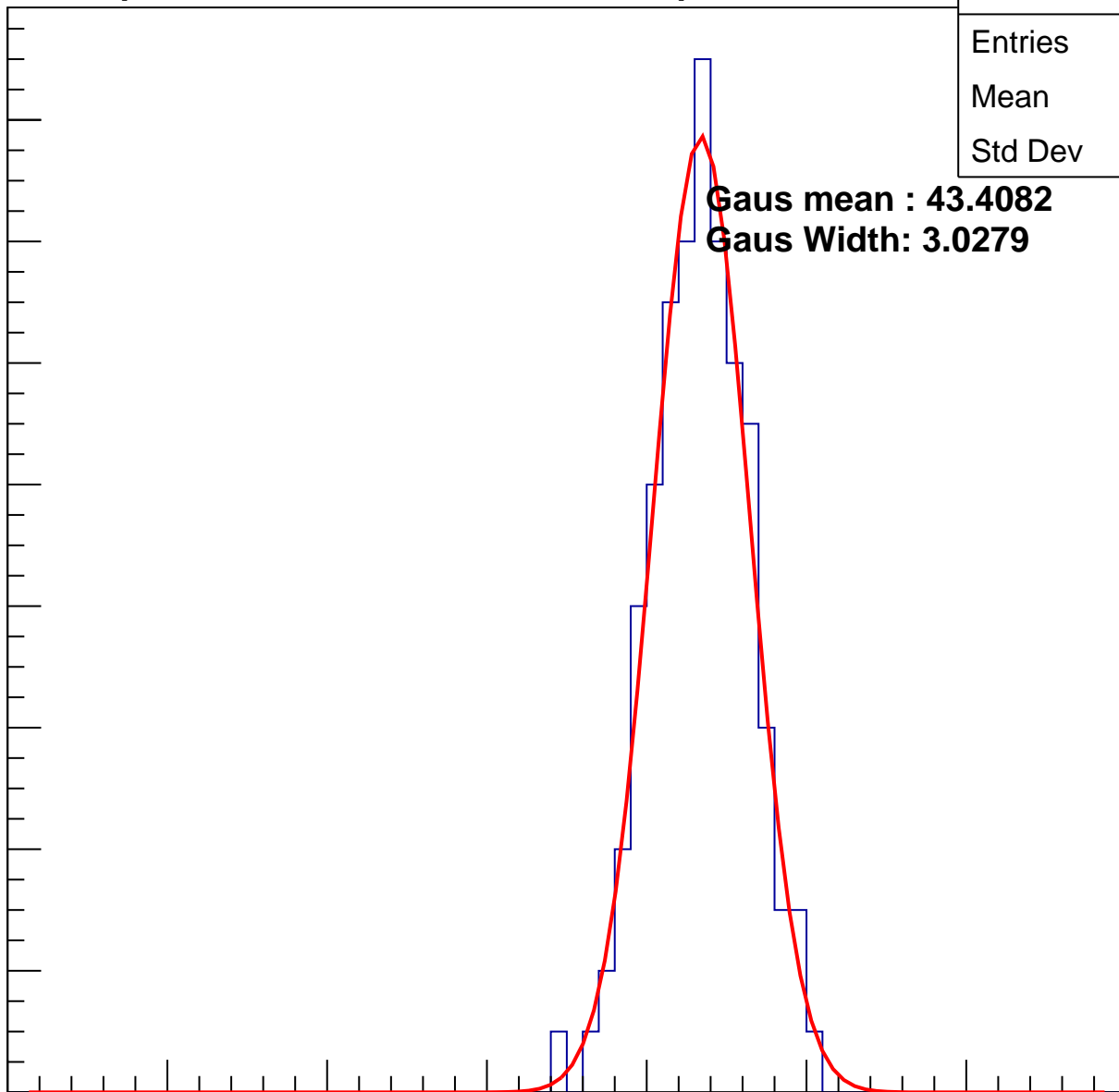
40

50

60

70

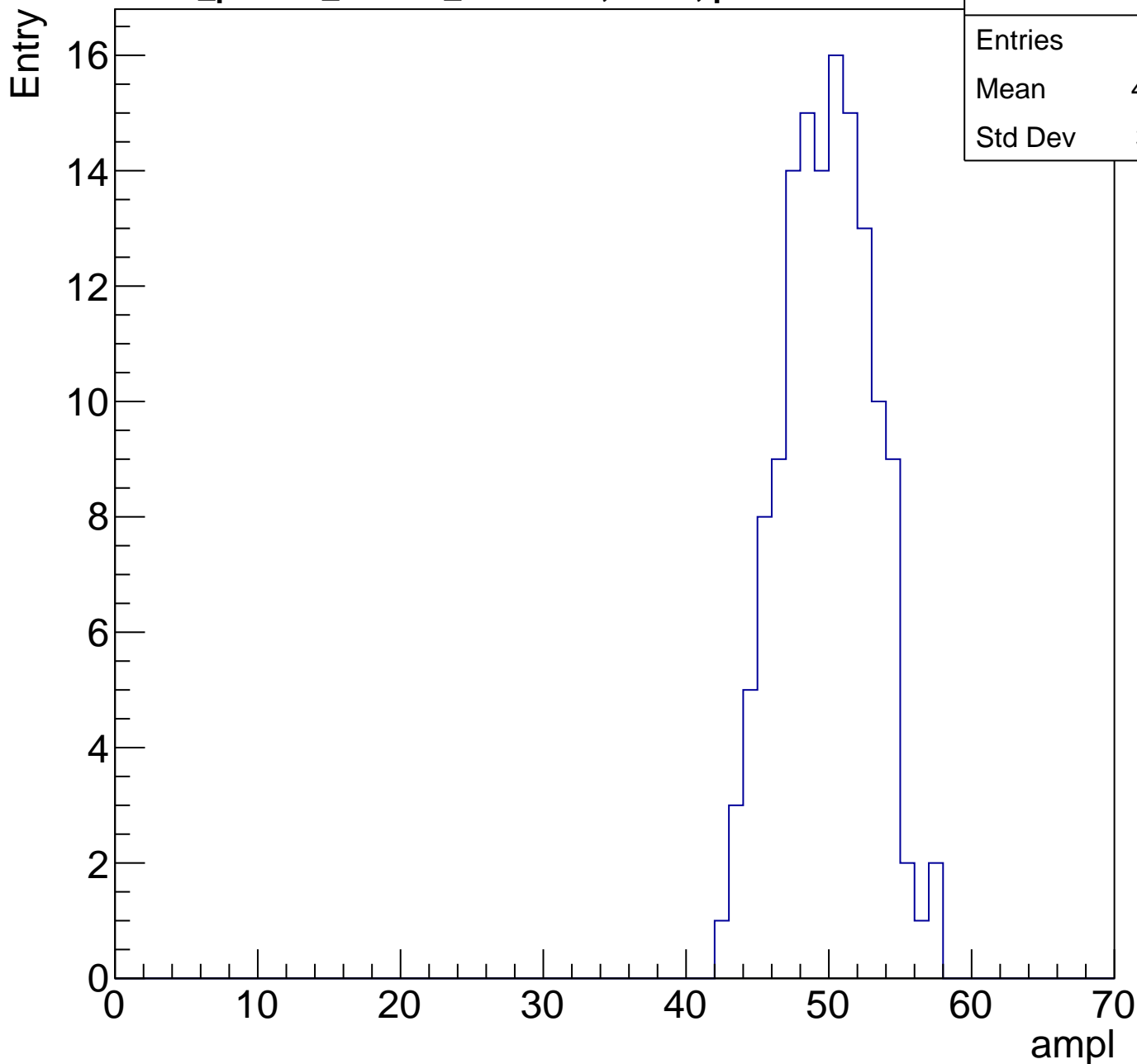
ampl



B1L001S, U22-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	49.39
Std Dev	3.161



B1L001S, U22-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

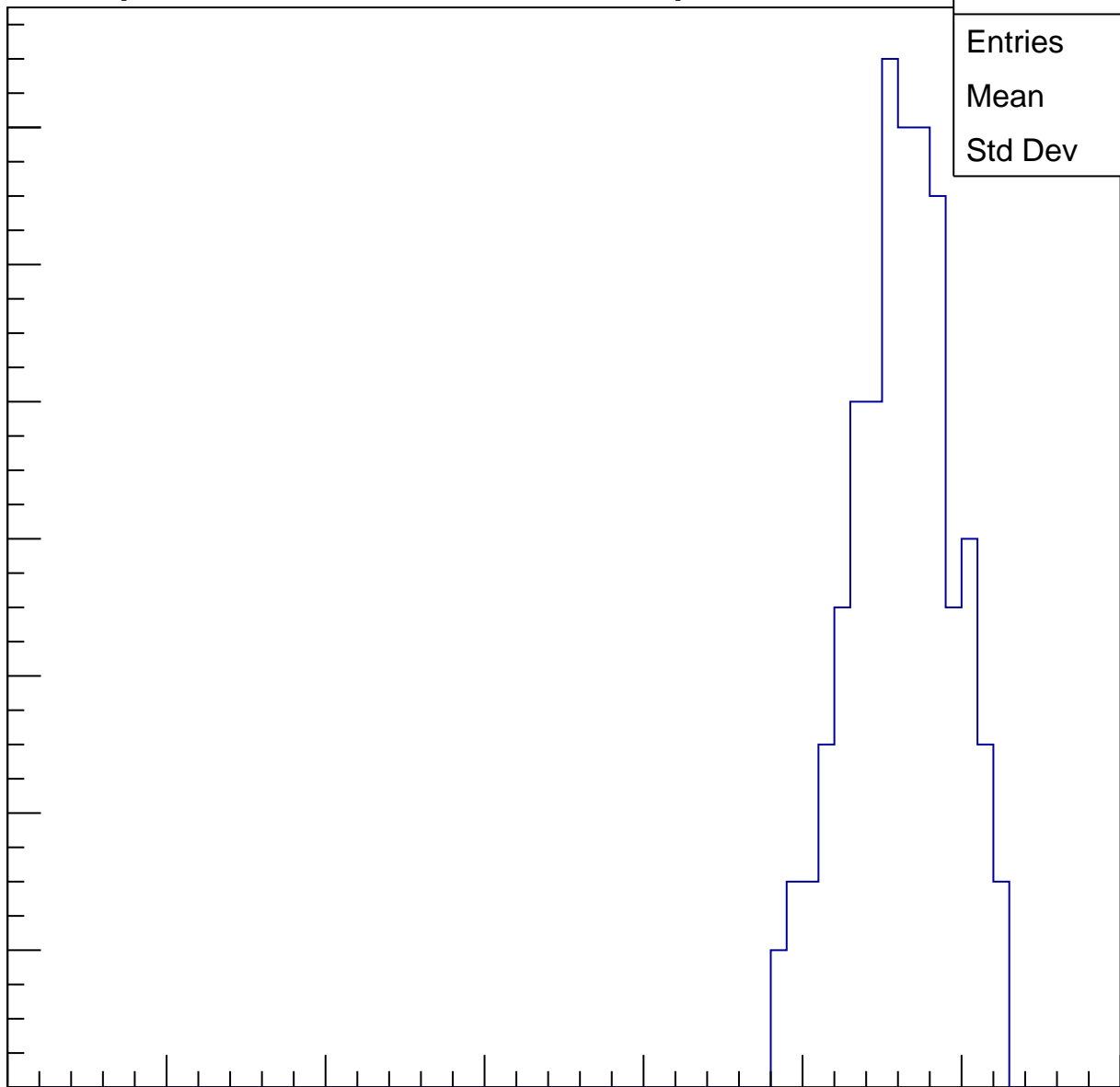
Entries	119
Mean	55.69
Std Dev	3.233

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch48, adc5

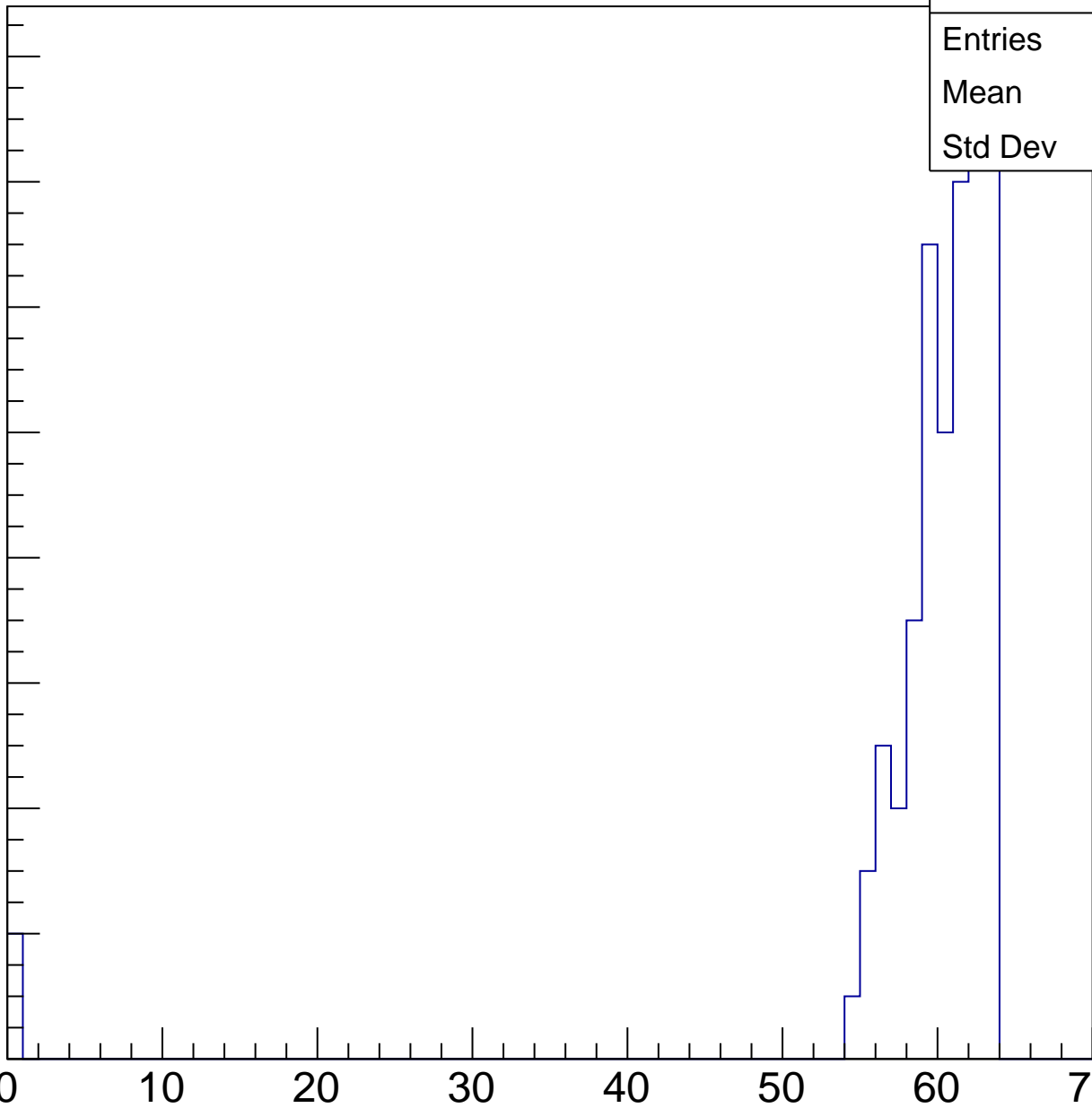
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.84
Std Dev	9.119

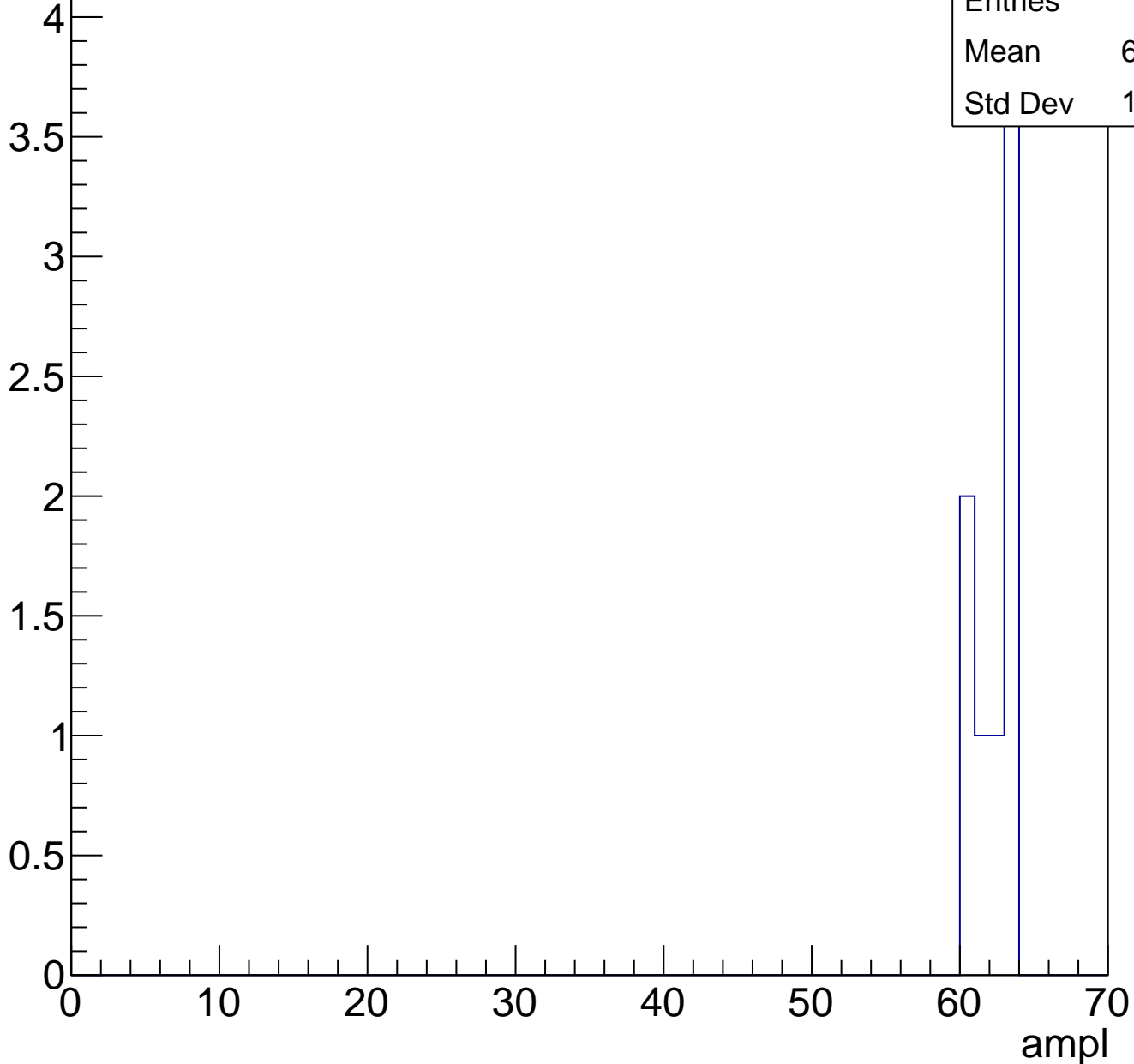
ampl



B1L001S, U22-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch49, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	30.82
Std Dev	3.397

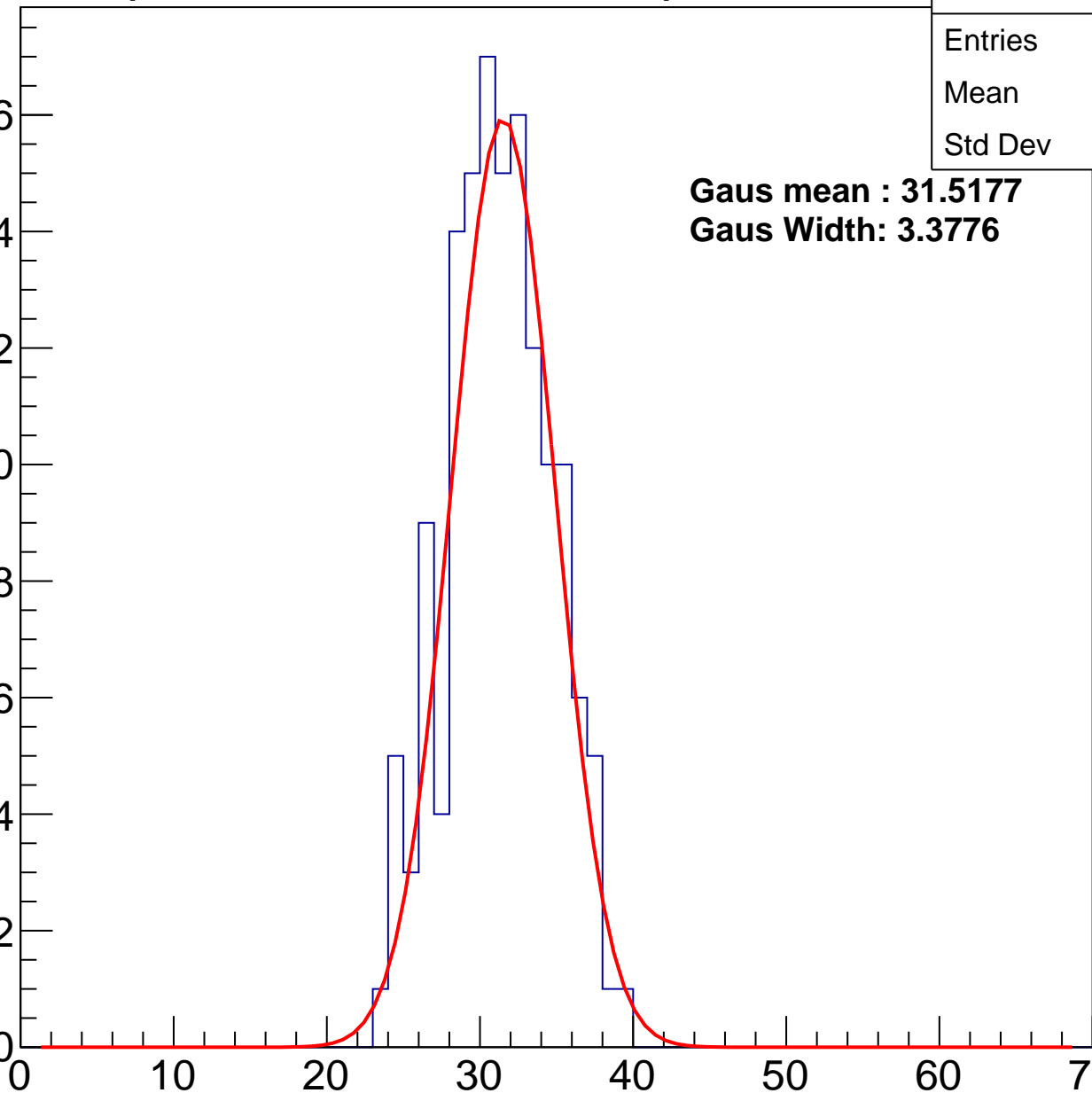
Gaus mean : 31.5177

Gaus Width: 3.3776

Entry

16
14
12
10
8
6
4
2
0

ampl



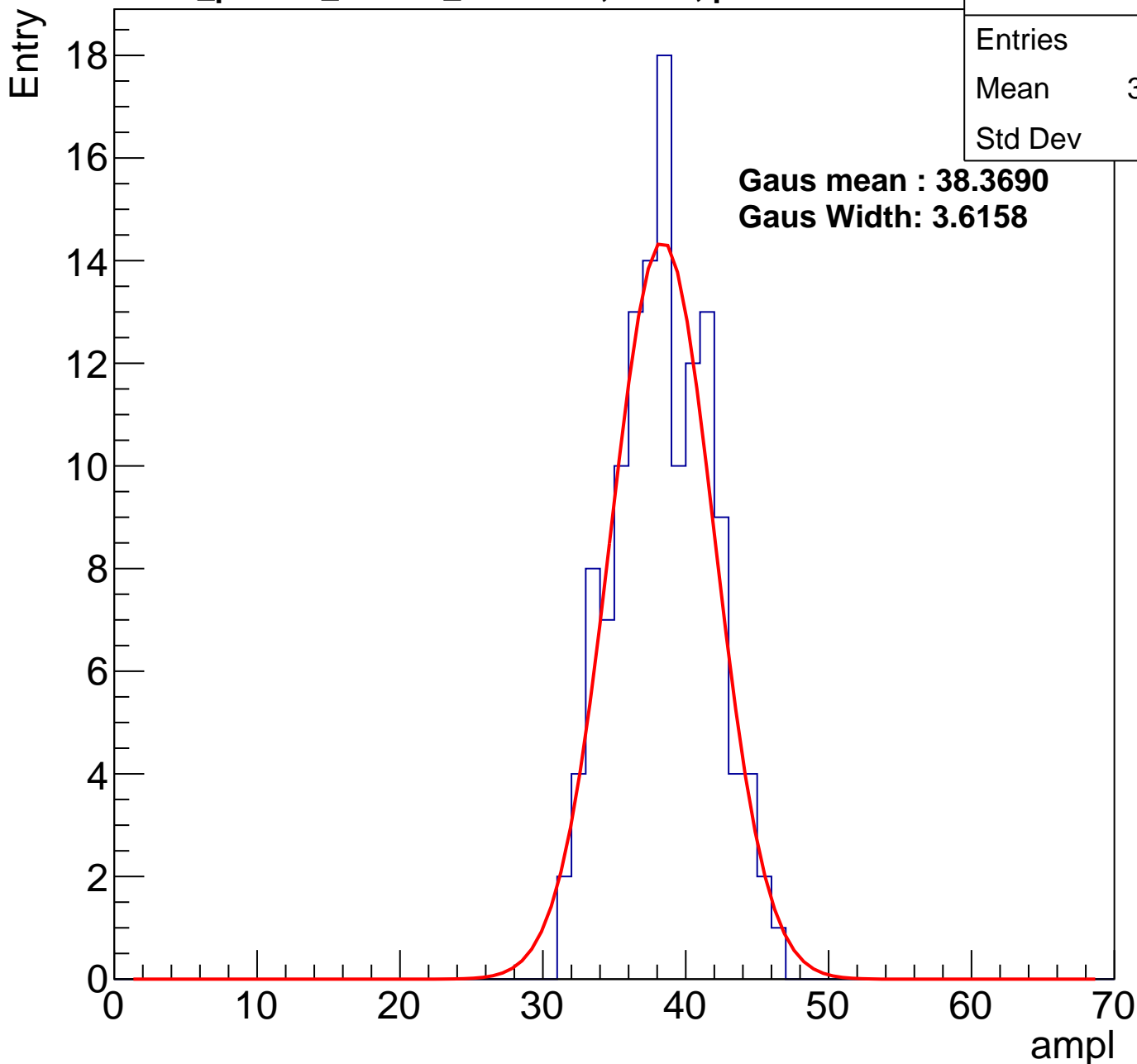
B1L001S, U22-ch49, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.99
Std Dev	3.31

Gaus mean : 38.3690

Gaus Width: 3.6158



B1L001S, U22-ch49, adc2

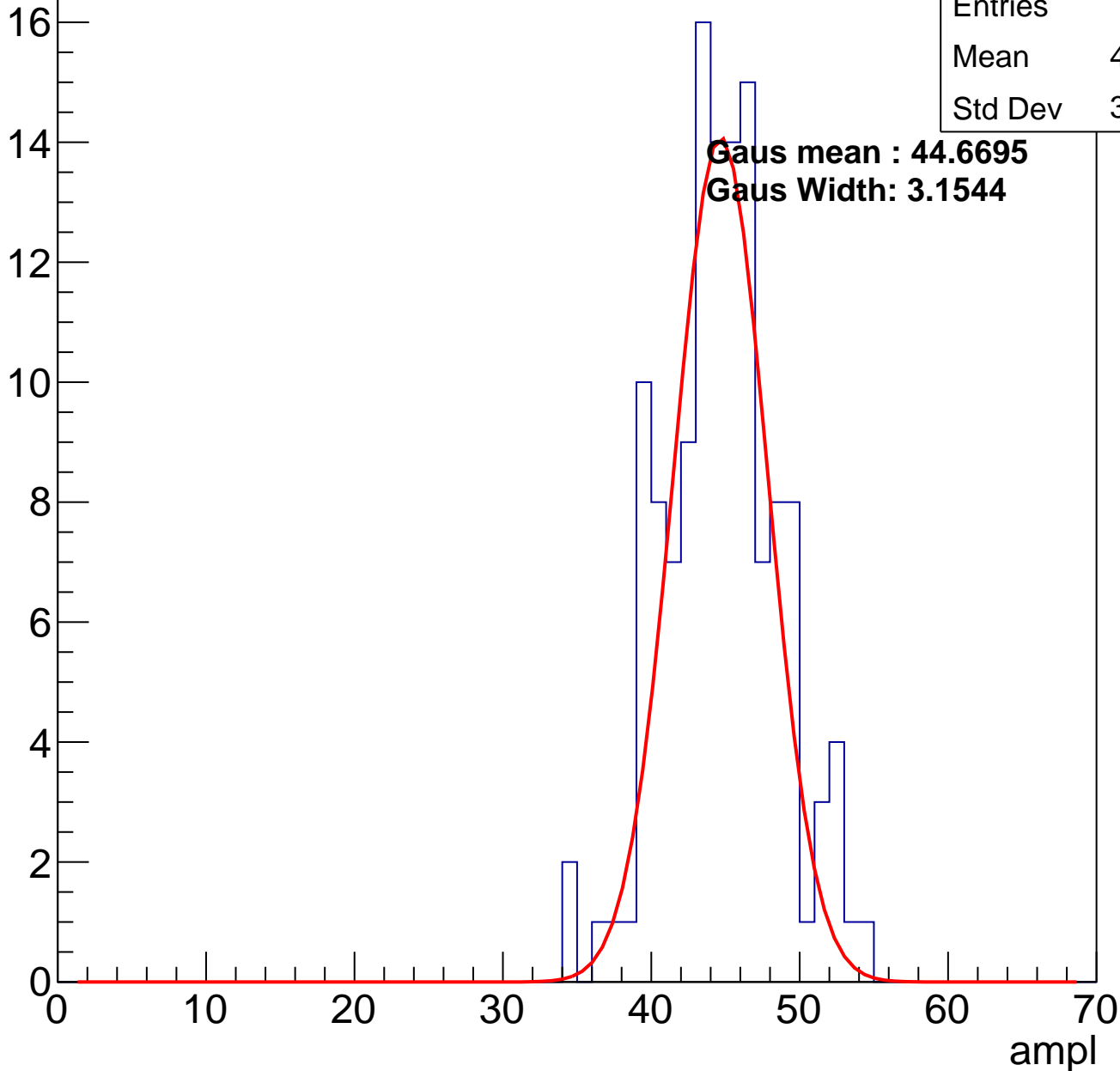
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	44.28
Std Dev	3.825

Gaus mean : 44.6695

Gaus Width: 3.1544

Entry



B1L001S, U22-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

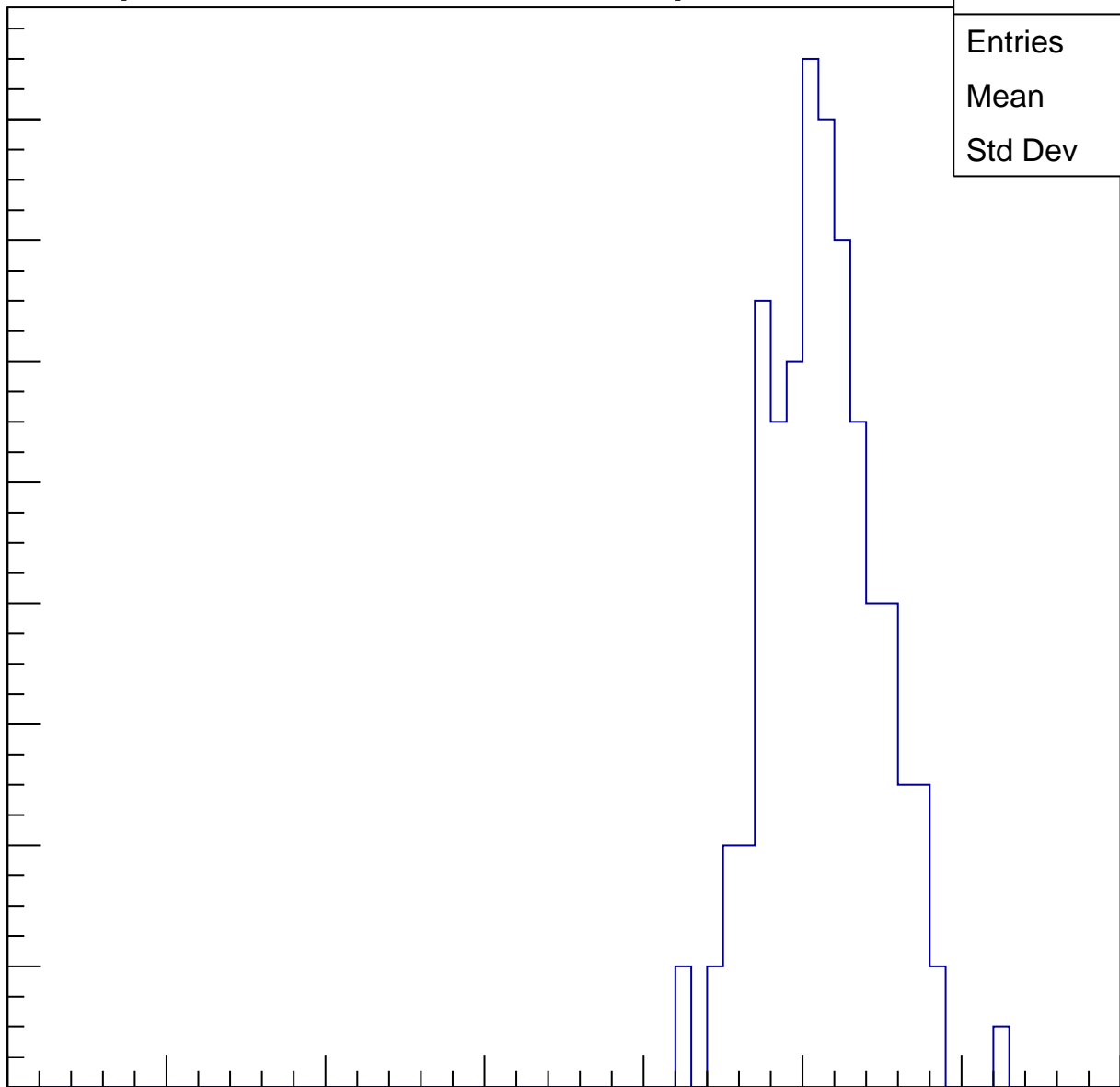
Entries	135
Mean	50.78
Std Dev	3.497

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch49, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	56.65
Std Dev	5.905

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

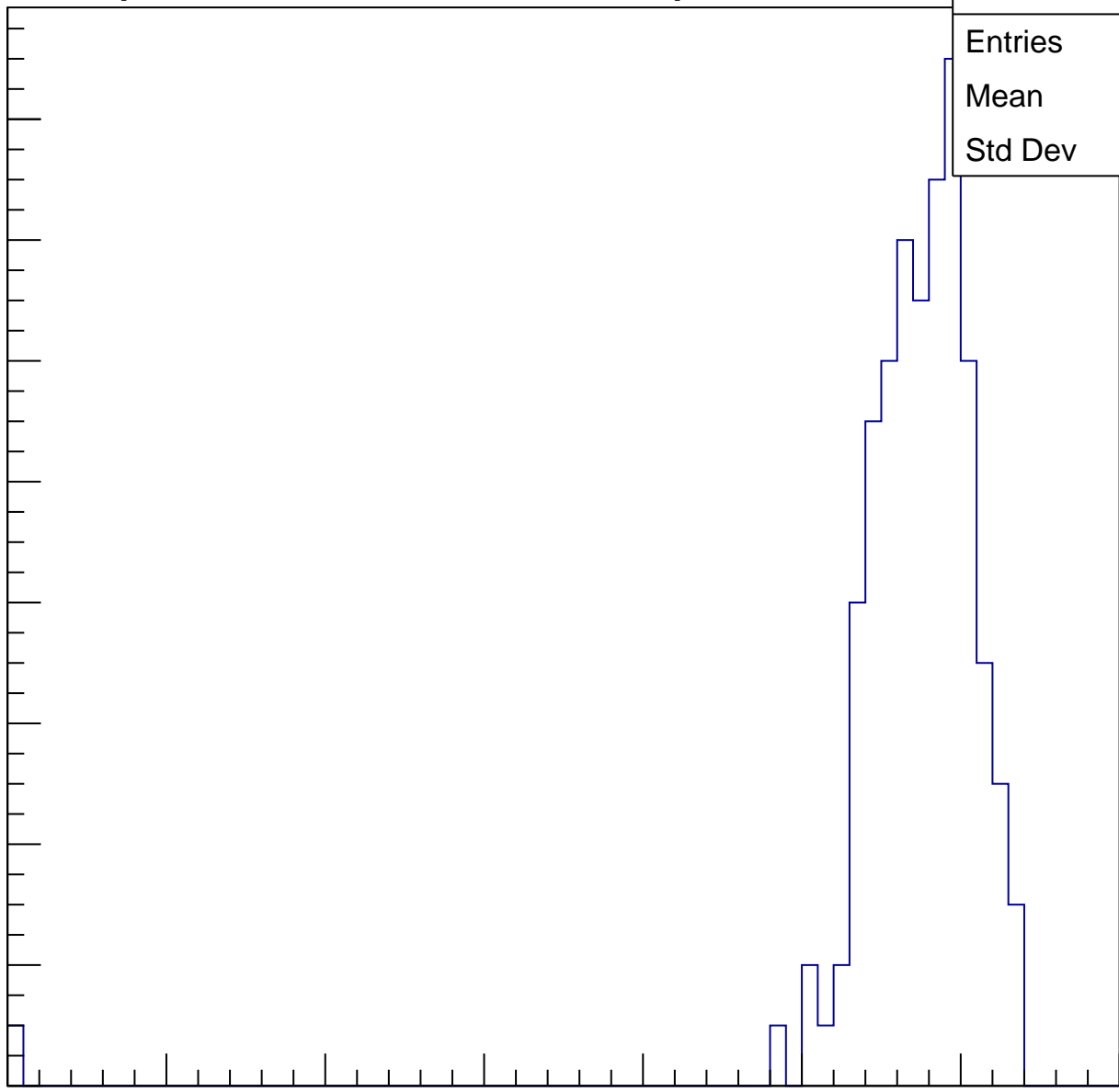
40

50

60

70

ampl



B1L001S, U22-ch49, adc5

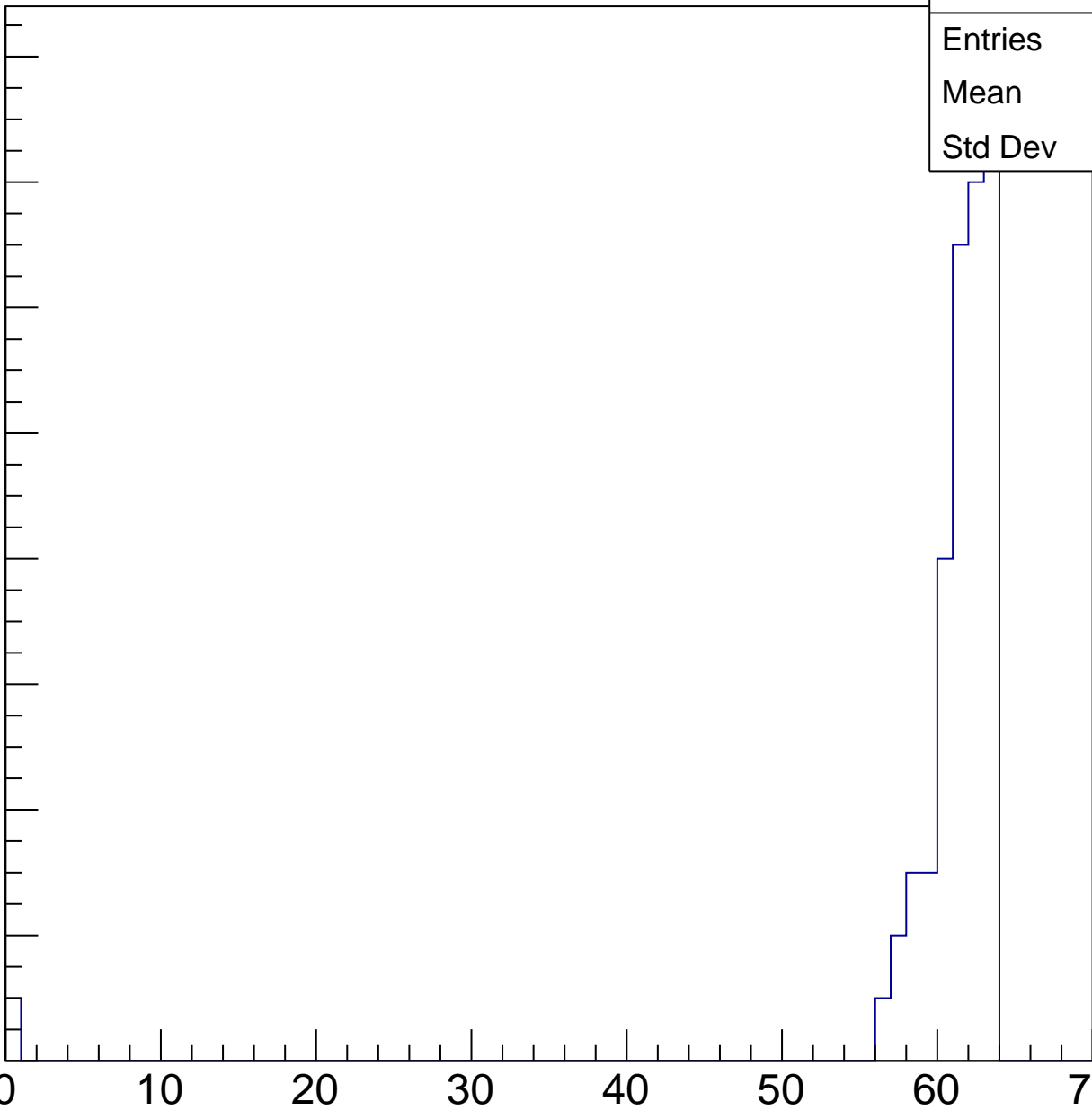
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	61
Mean	60.16
Std Dev	7.955

ampl



B1L001S, U22-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch50, adc0

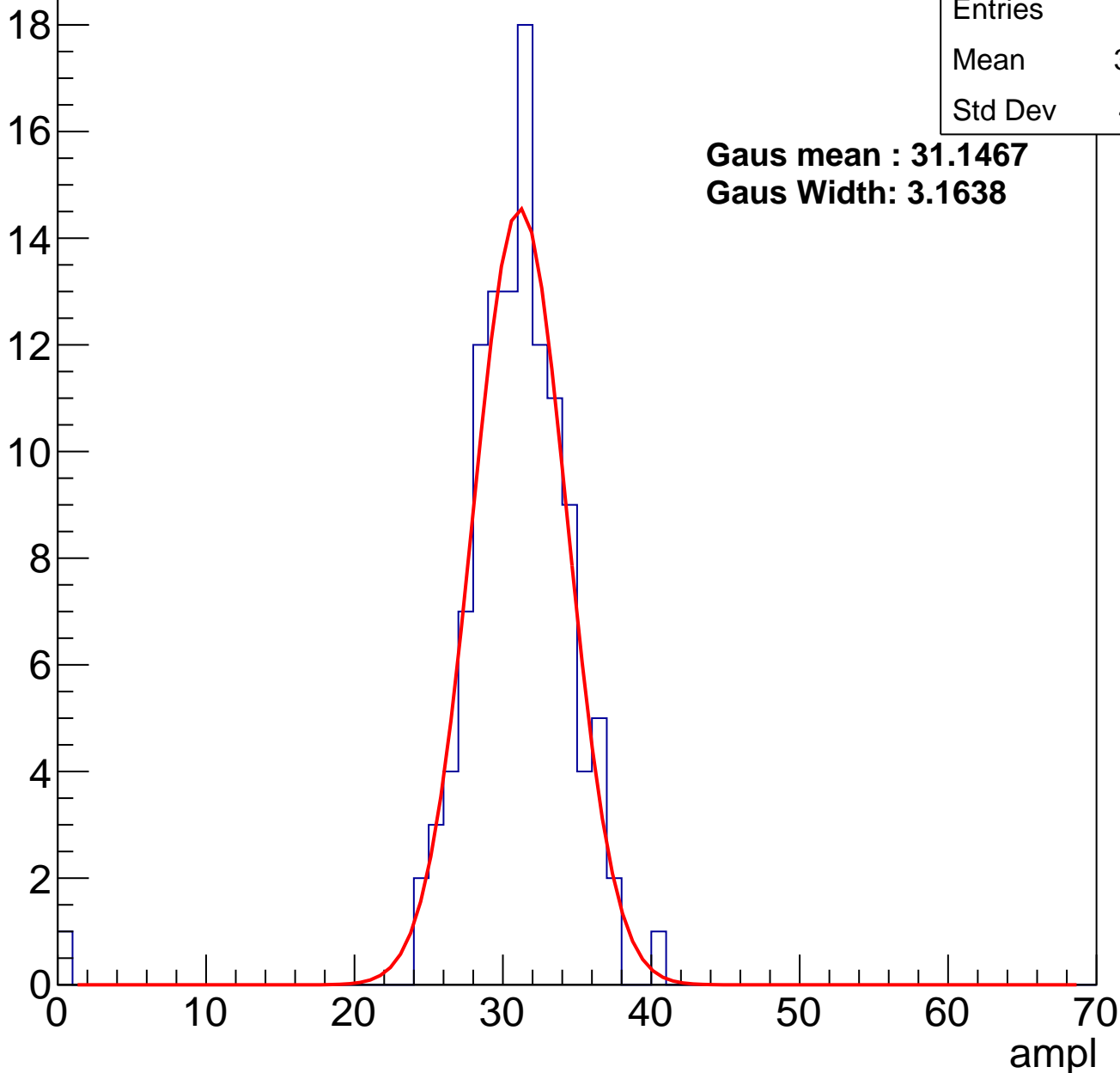
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	30.46
Std Dev	4.131

Gaus mean : 31.1467

Gaus Width: 3.1638

Entry



B1L001S, U22-ch50, adc1

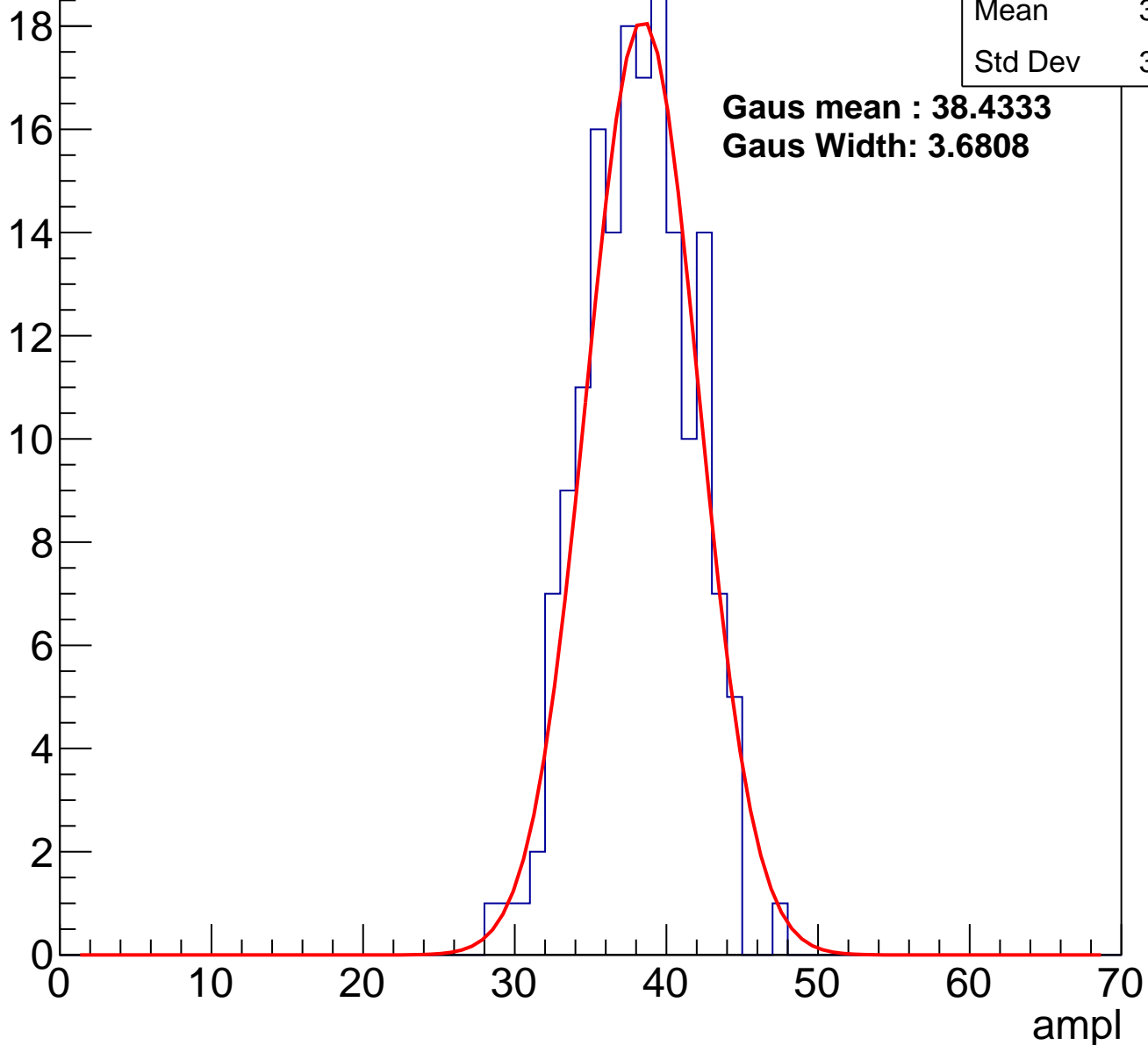
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	37.65
Std Dev	3.476

Gaus mean : 38.4333

Gaus Width: 3.6808

Entry



B1L001S, U22-ch50, adc2

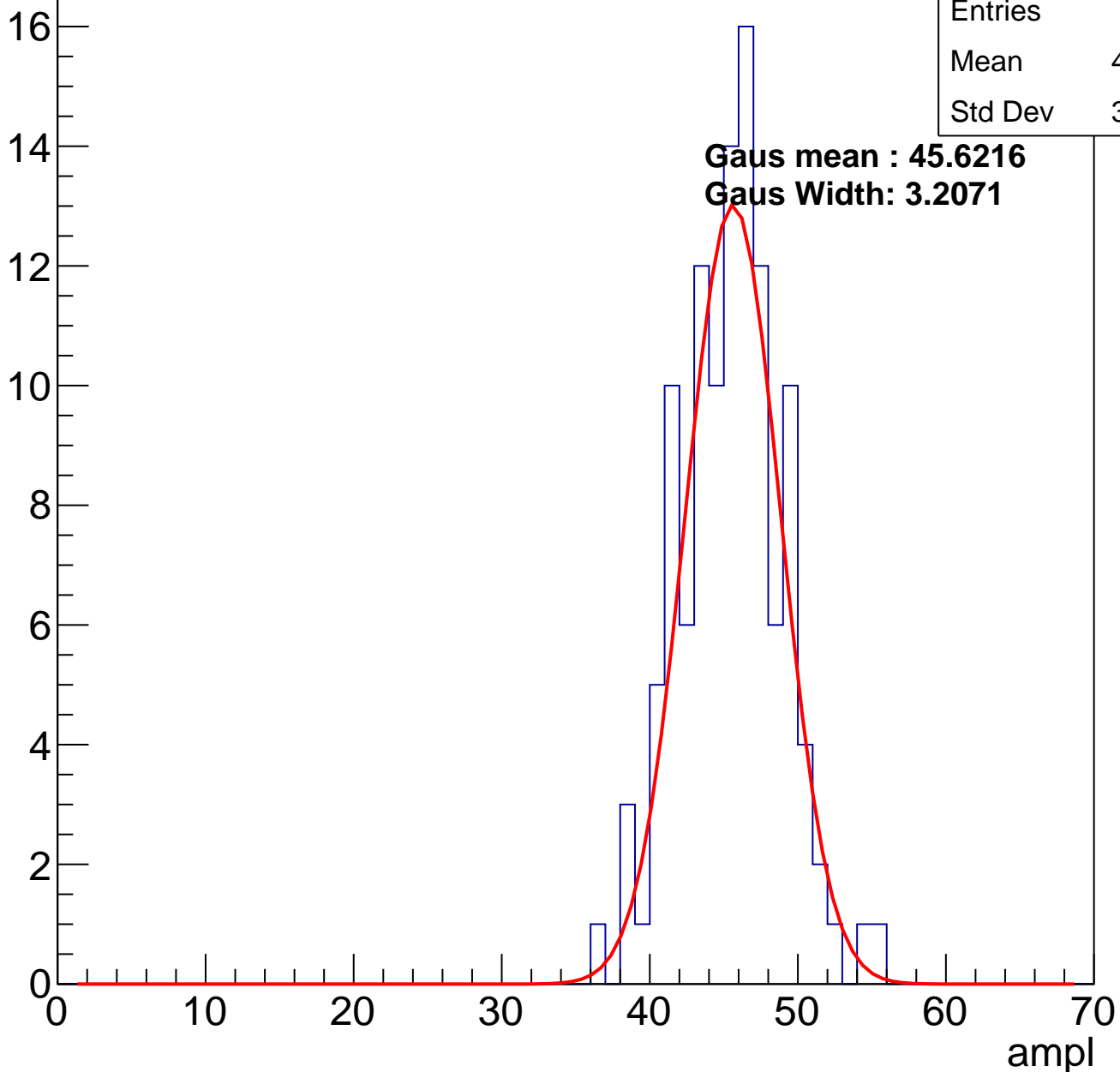
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	45.03
Std Dev	3.415

Gaus mean : 45.6216

Gaus Width: 3.2071

Entry



B1L001S, U22-ch50, adc3

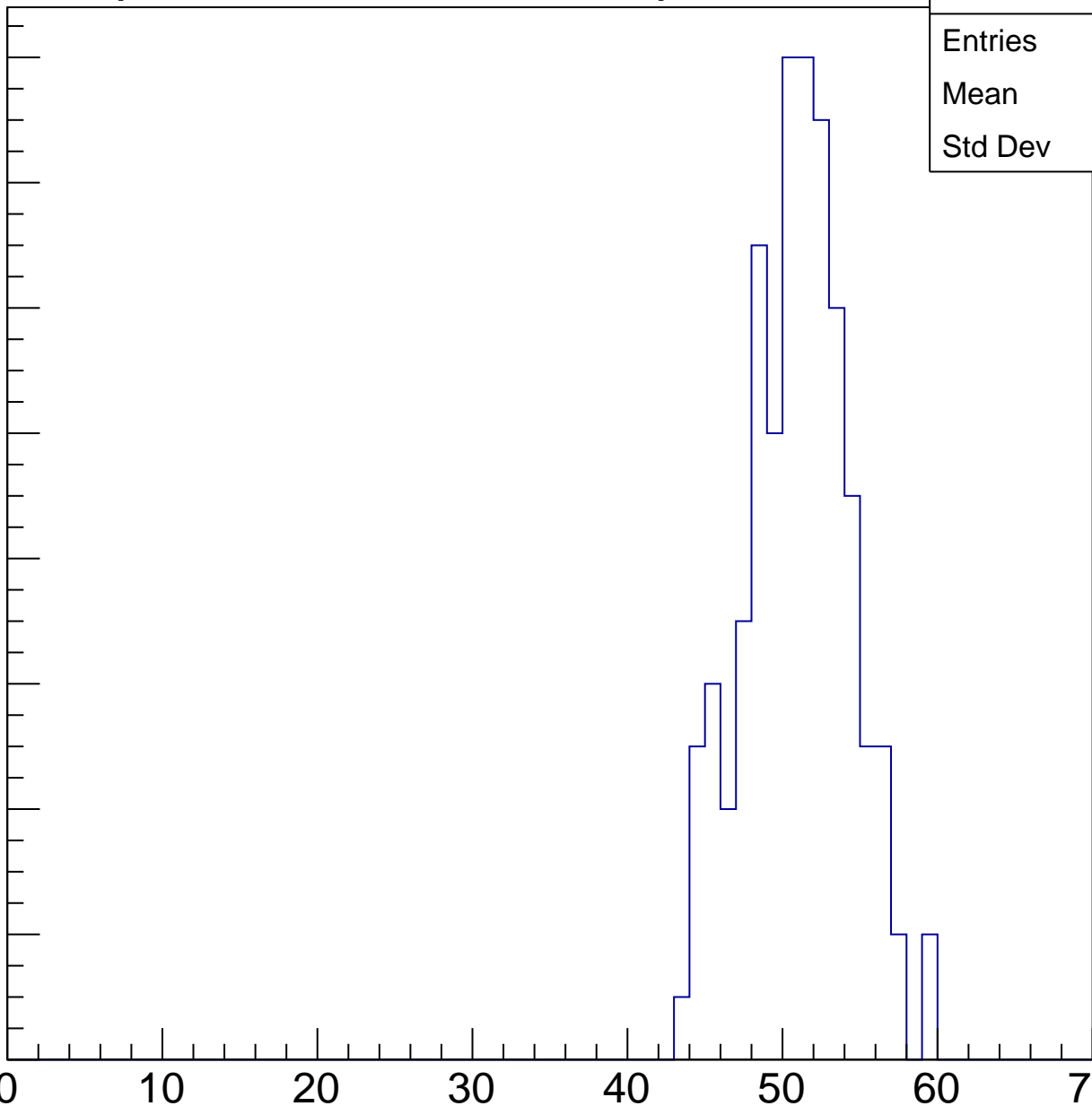
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	50.51
Std Dev	3.34

ampl



B1L001S, U22-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

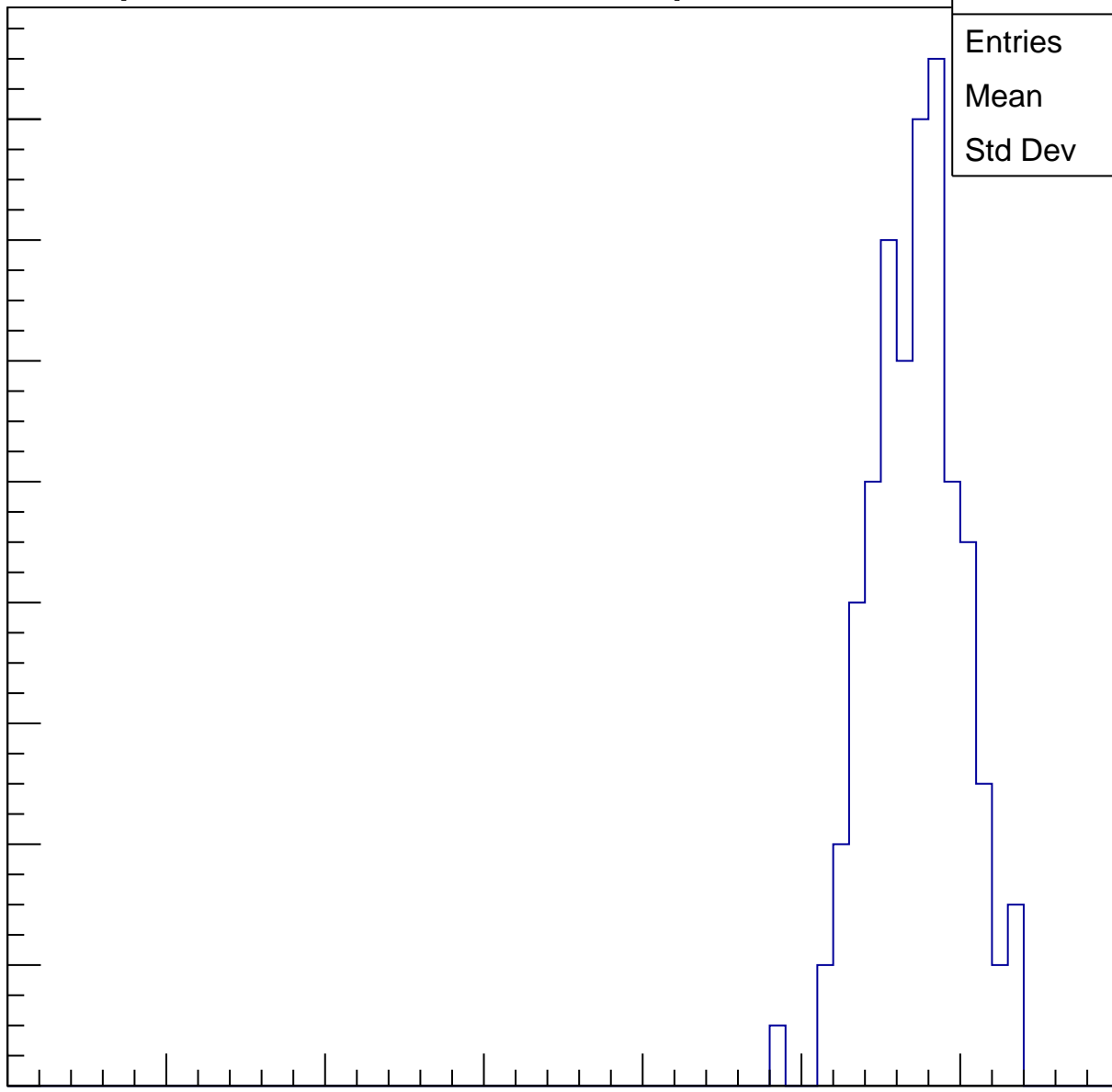
Entries	113
Mean	56.73
Std Dev	2.832

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch50, adc5

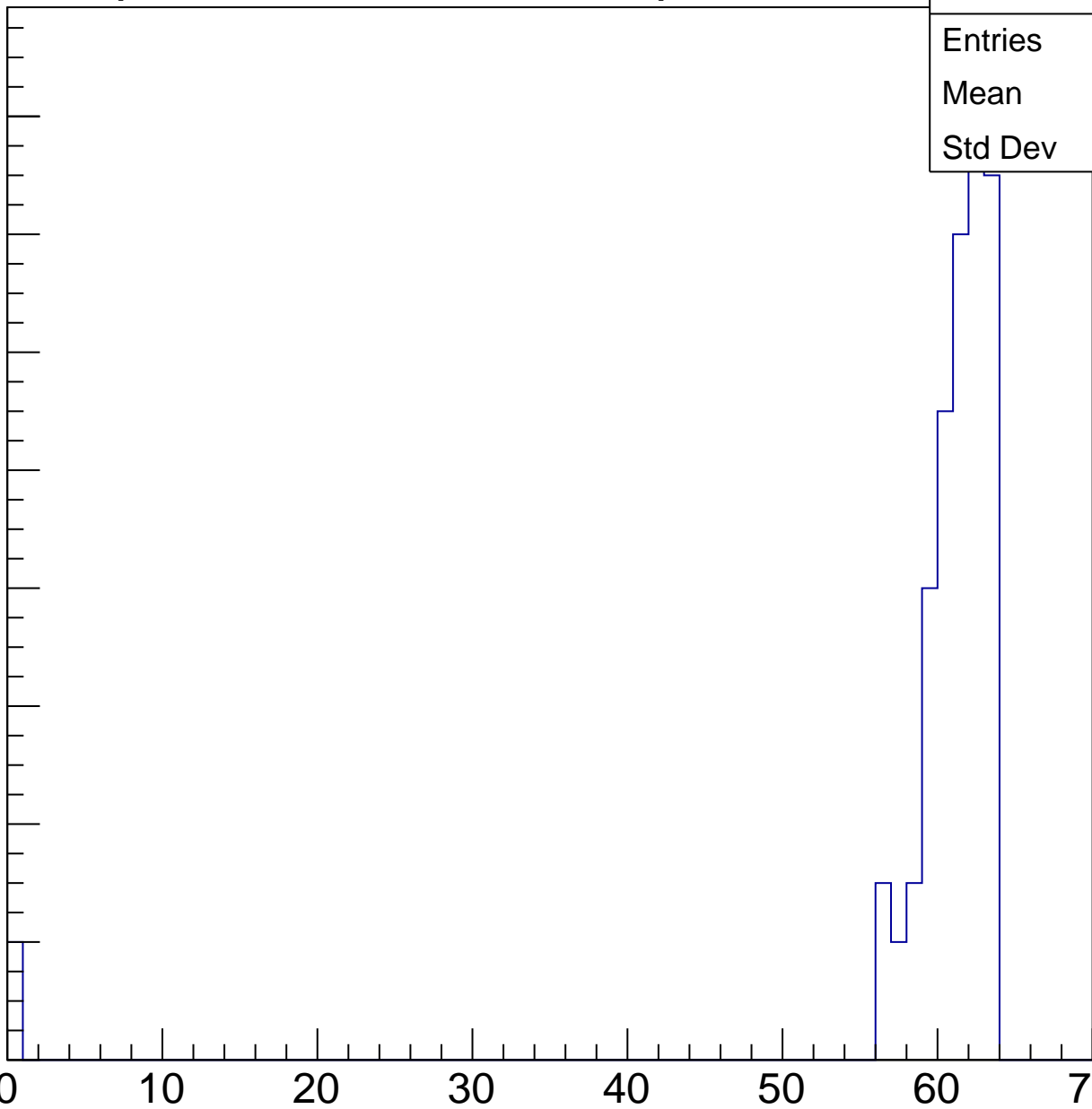
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.21
Std Dev	9.971

ampl



B1L001S, U22-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

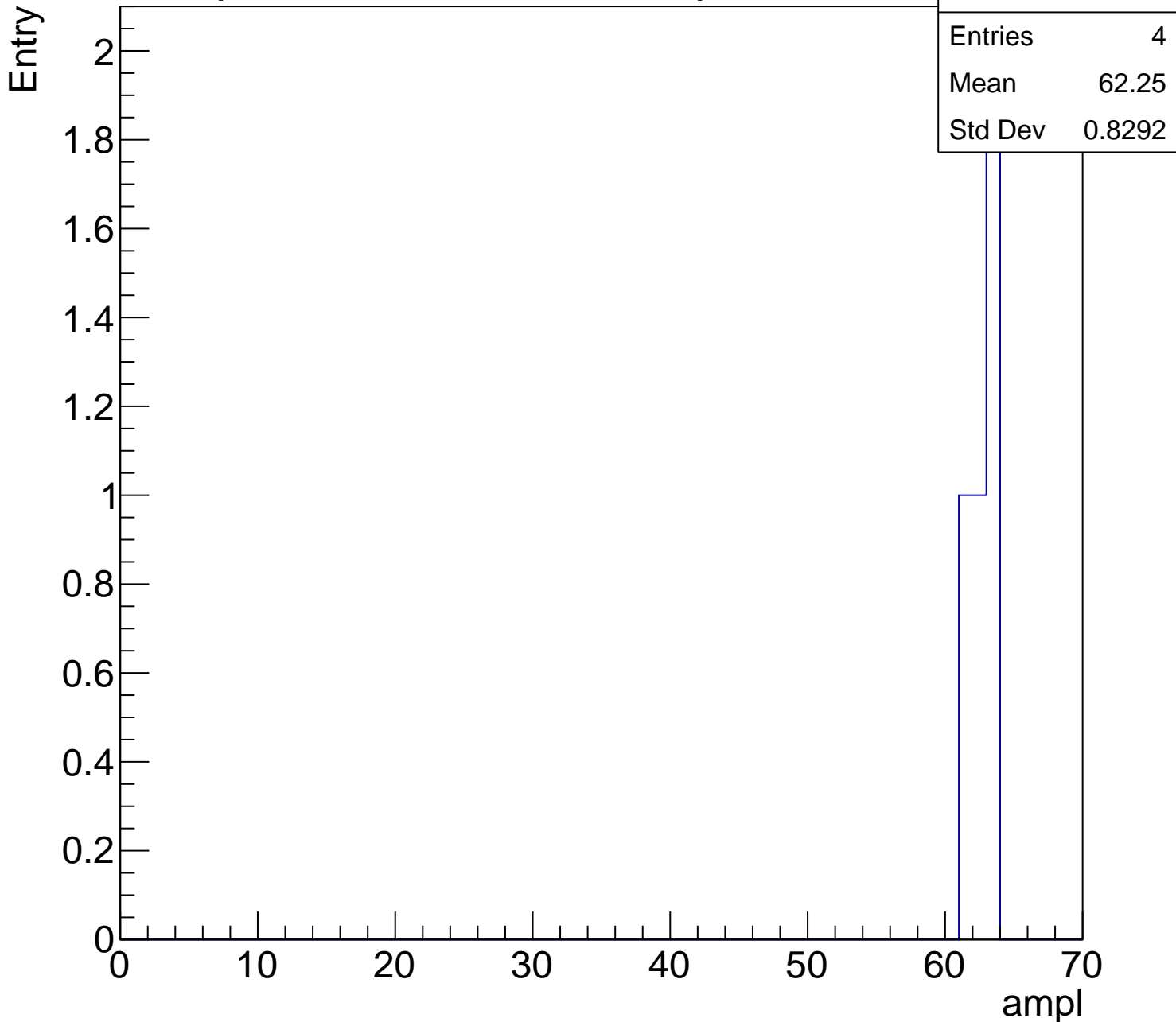
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

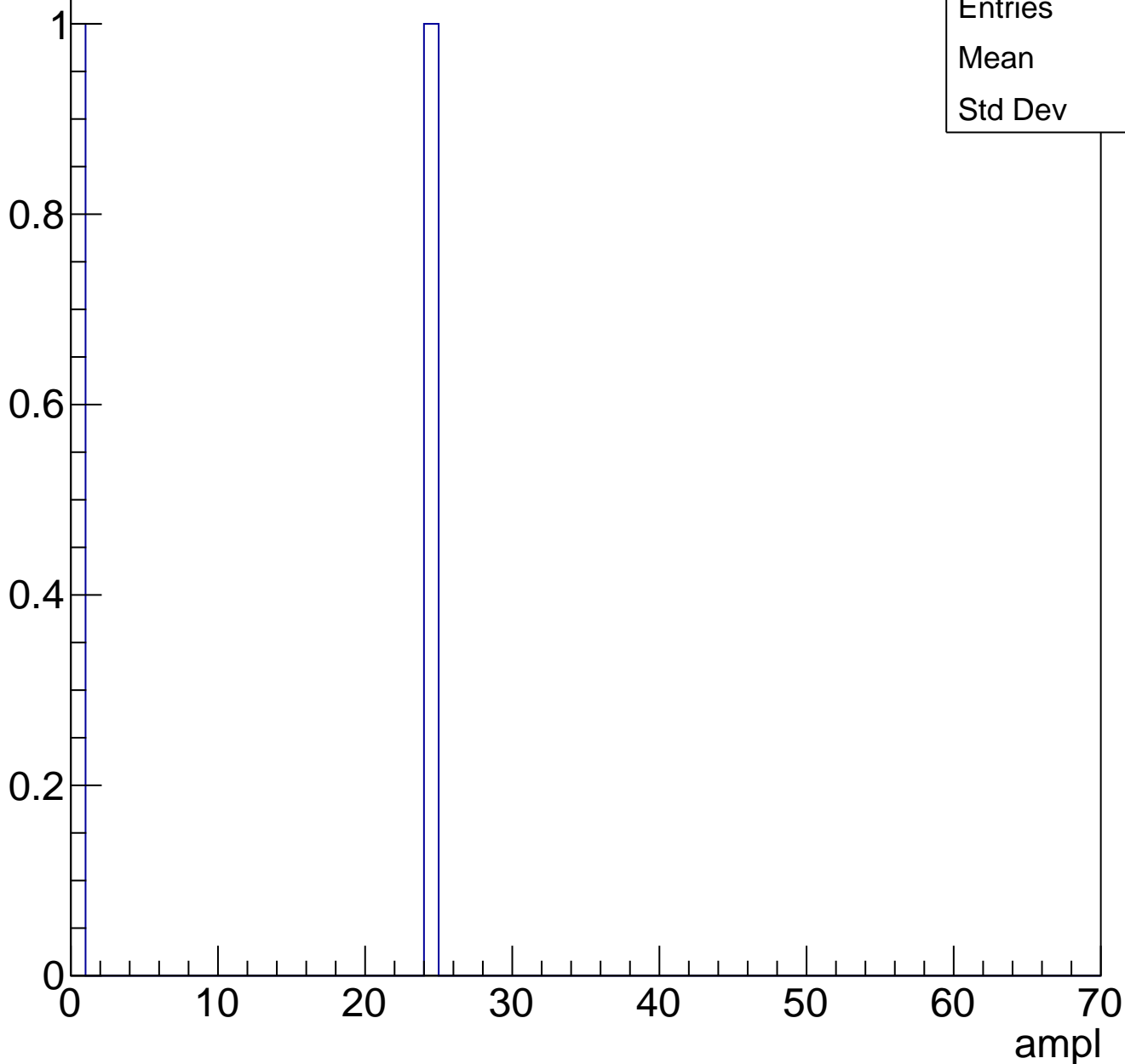
0 10 20 30 40 50 60 70



B1L001S, U22-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch51, adc0

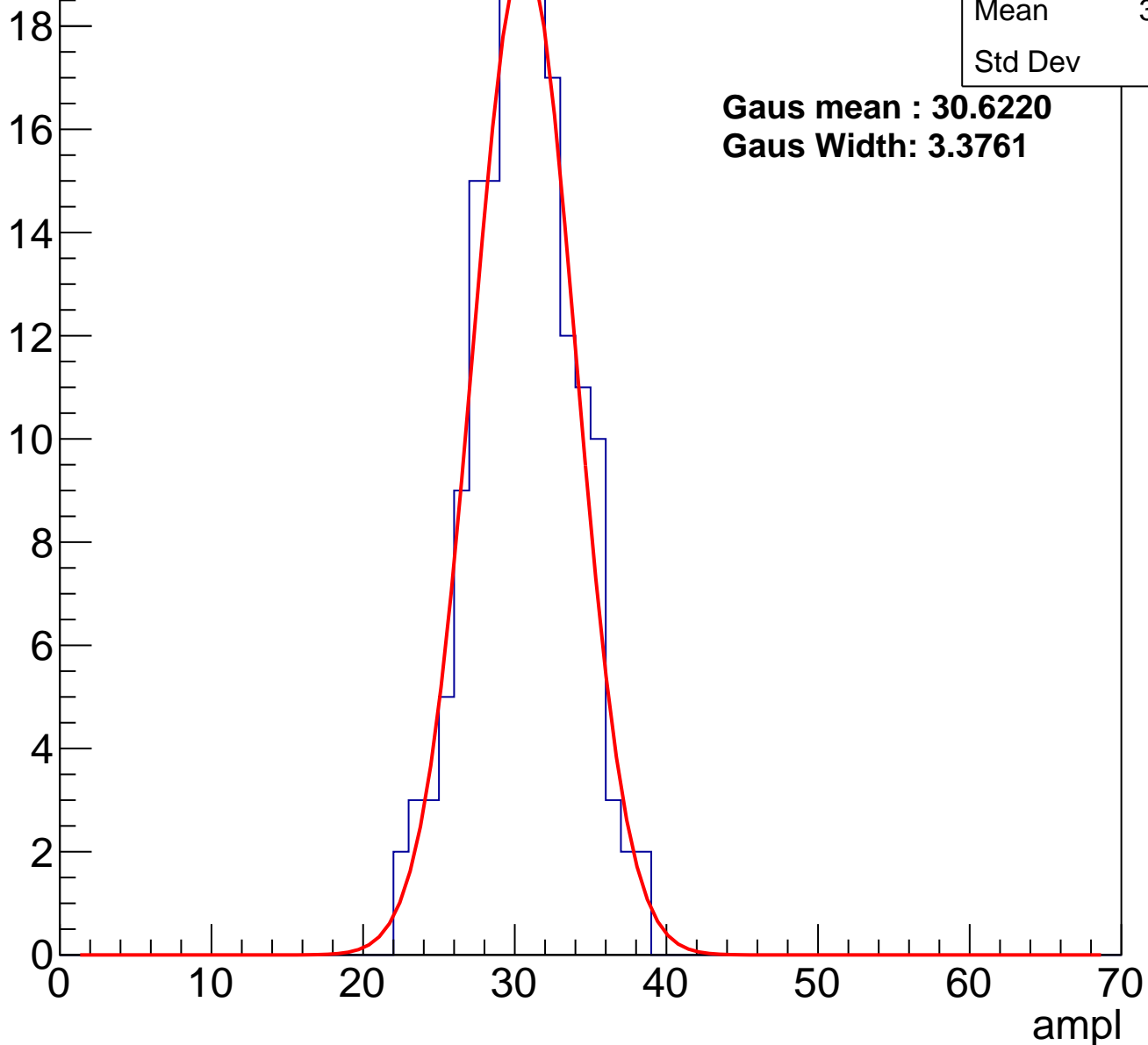
calib_packv5_042523_0143.root, FC#2, port C2

Entries	166
Mean	30.13
Std Dev	3.31

Gaus mean : 30.6220

Gaus Width: 3.3761

Entry



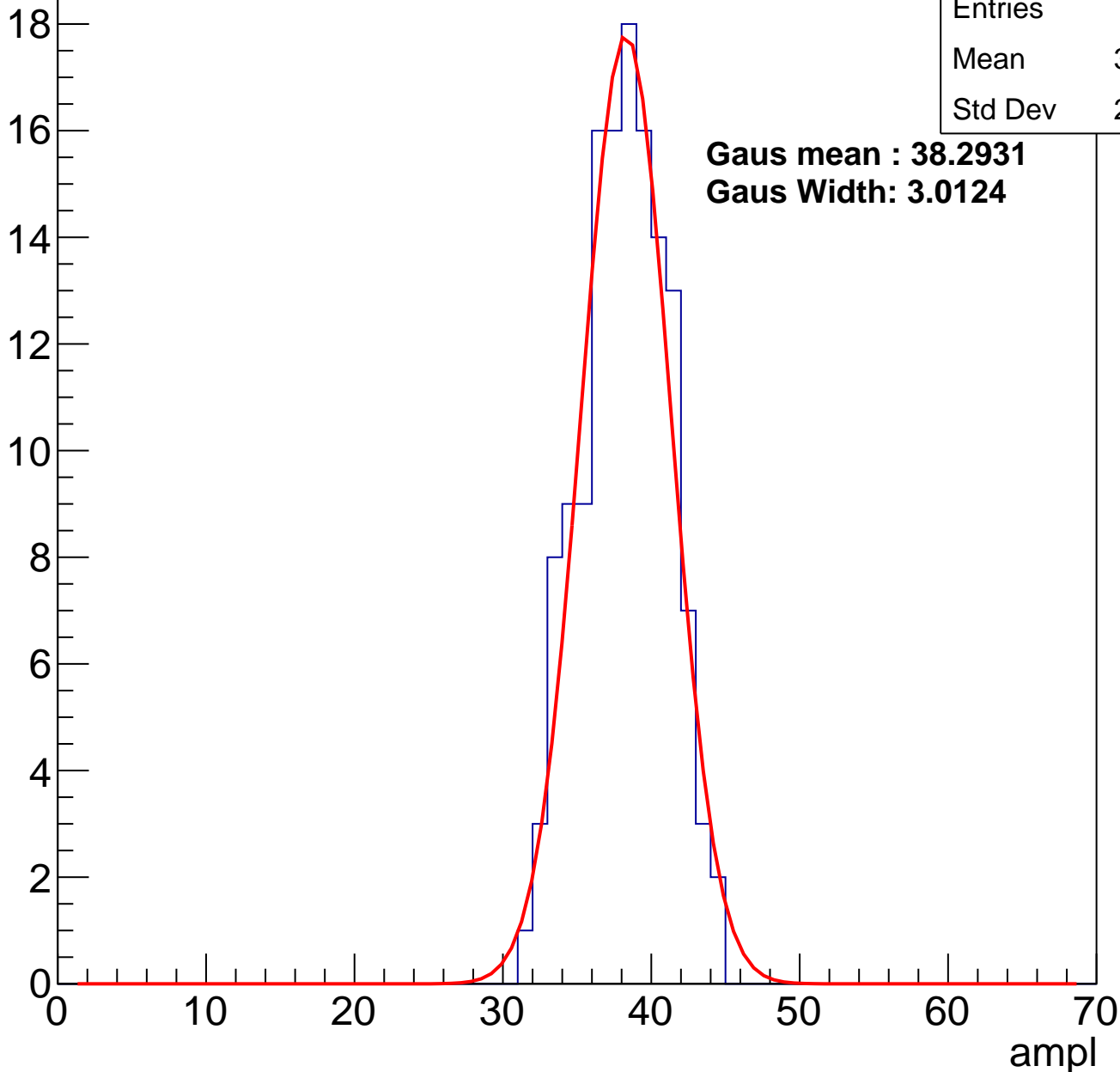
B1L001S, U22-ch51, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	37.72
Std Dev	2.854

Gaus mean : 38.2931
Gaus Width: 3.0124

Entry



B1L001S, U22-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	44.15
Std Dev	3.001

Gaus mean : 44.7986

Gaus Width: 3.2596

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

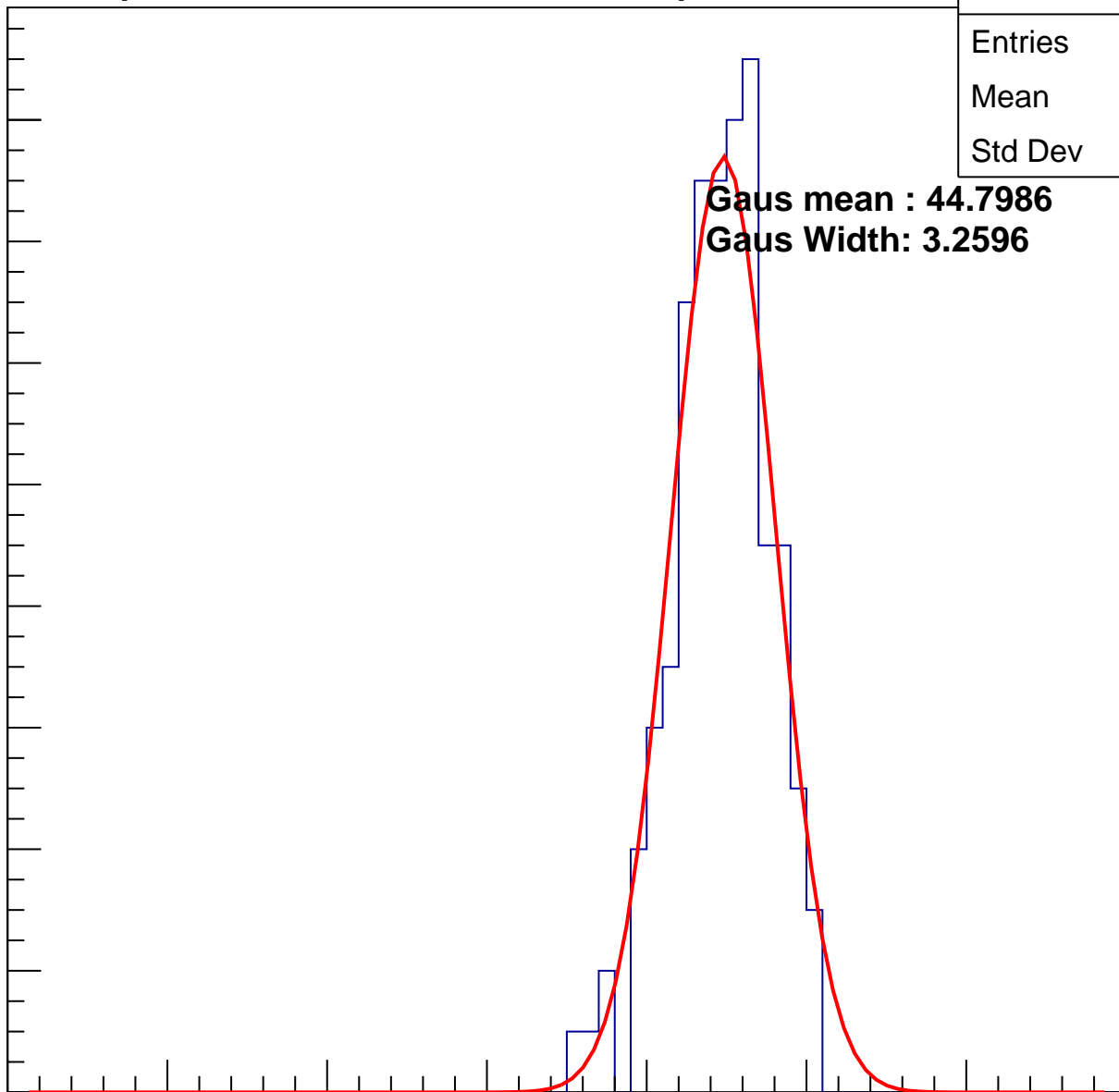
40

50

60

70

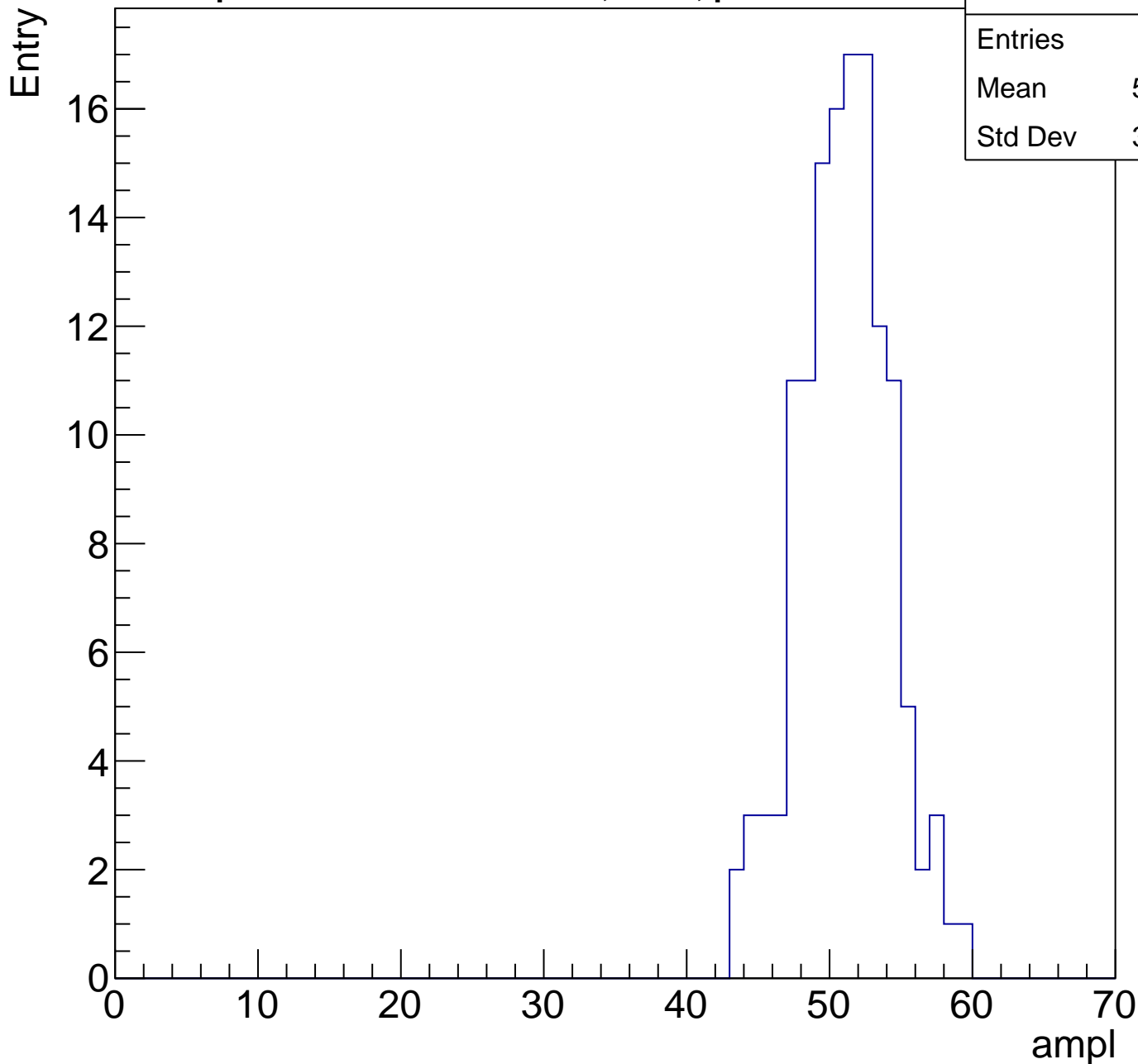
ampl



B1L001S, U22-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	50.58
Std Dev	3.127

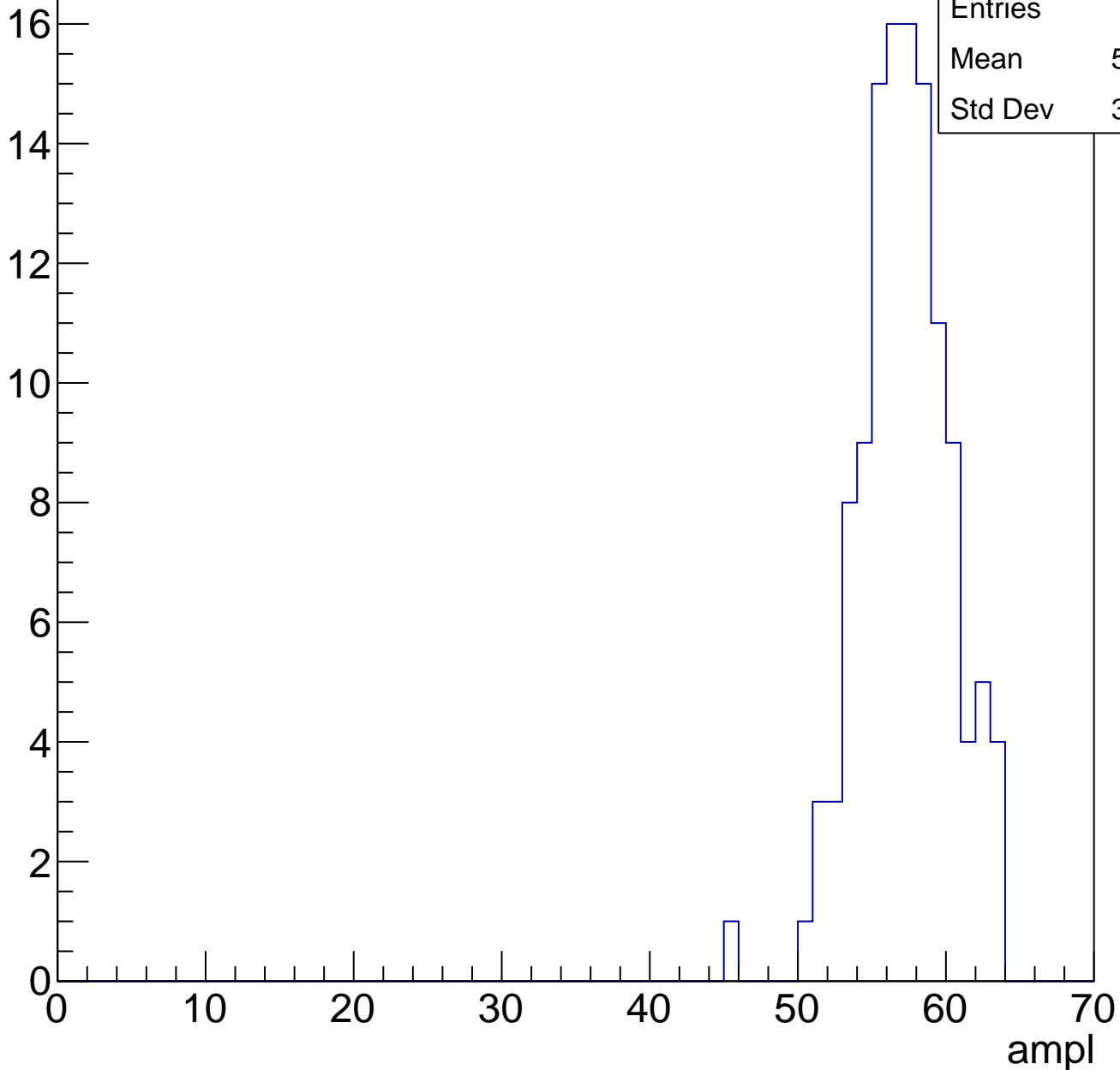


B1L001S, U22-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	56.77
Std Dev	3.065

Entry



B1L001S, U22-ch51, adc5

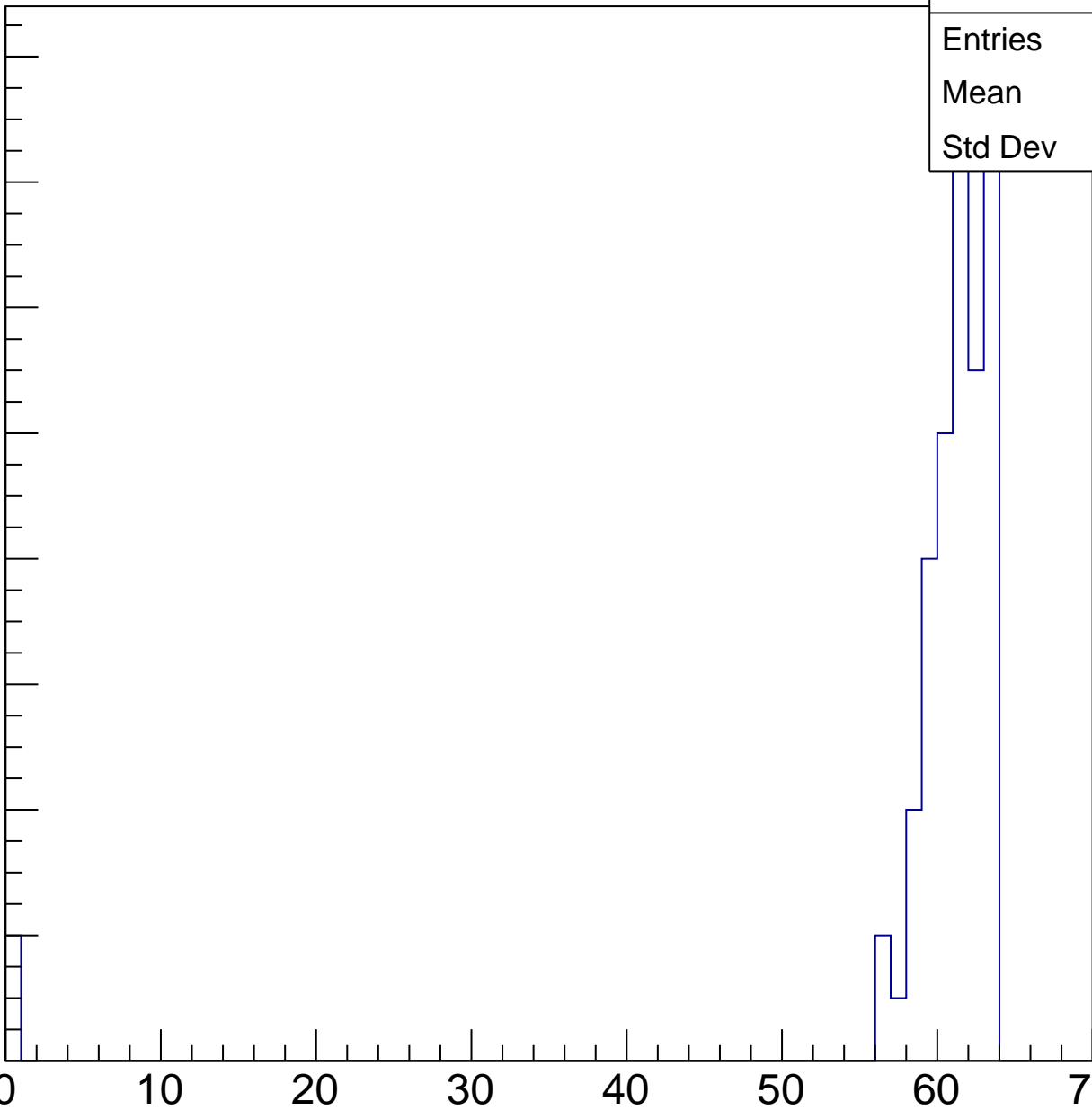
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	59.07
Std Dev	10.36

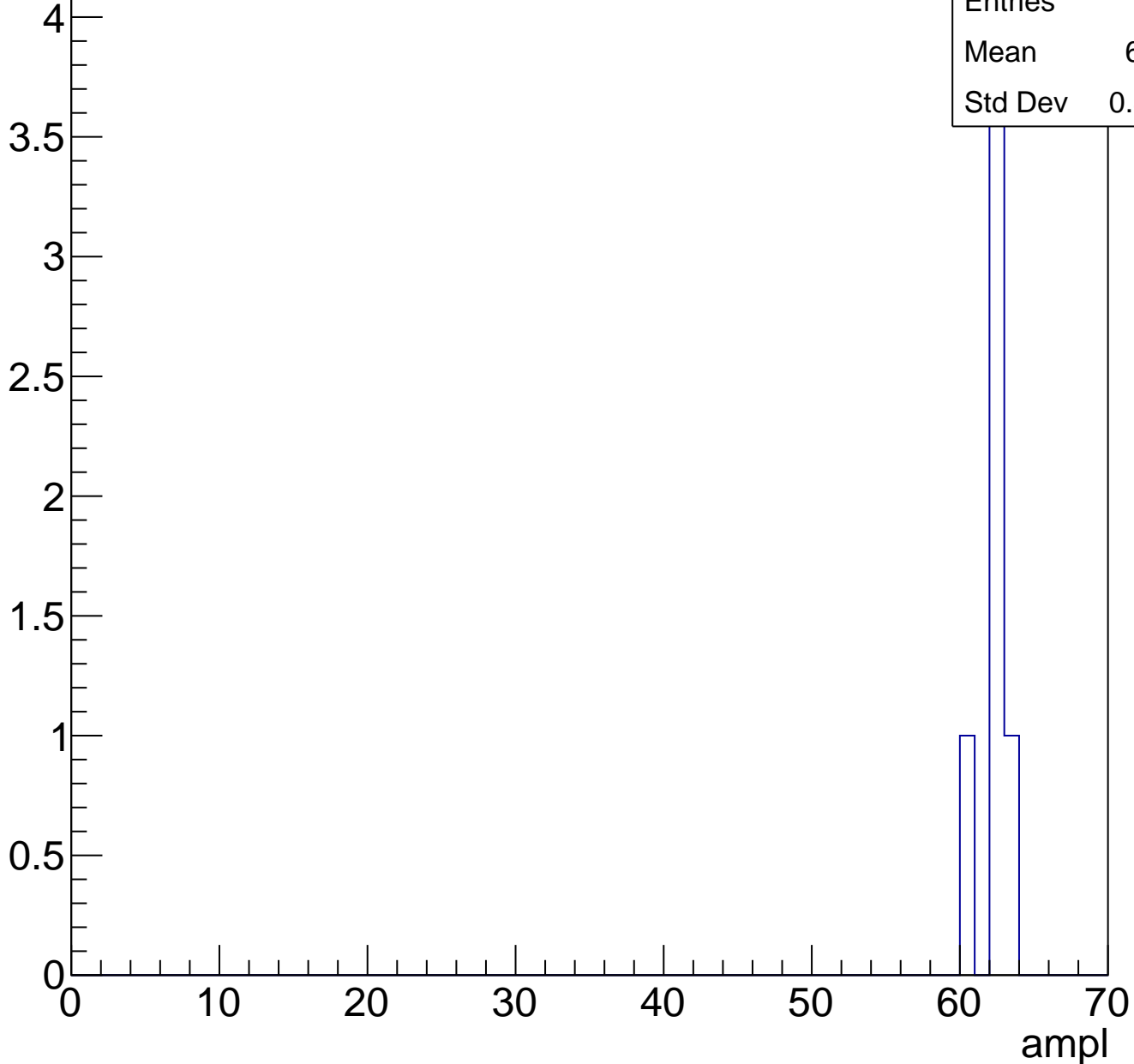
ampl



B1L001S, U22-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	29.3
Std Dev	4.232

Gaus mean : 30.0233

Gaus Width: 3.6619

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

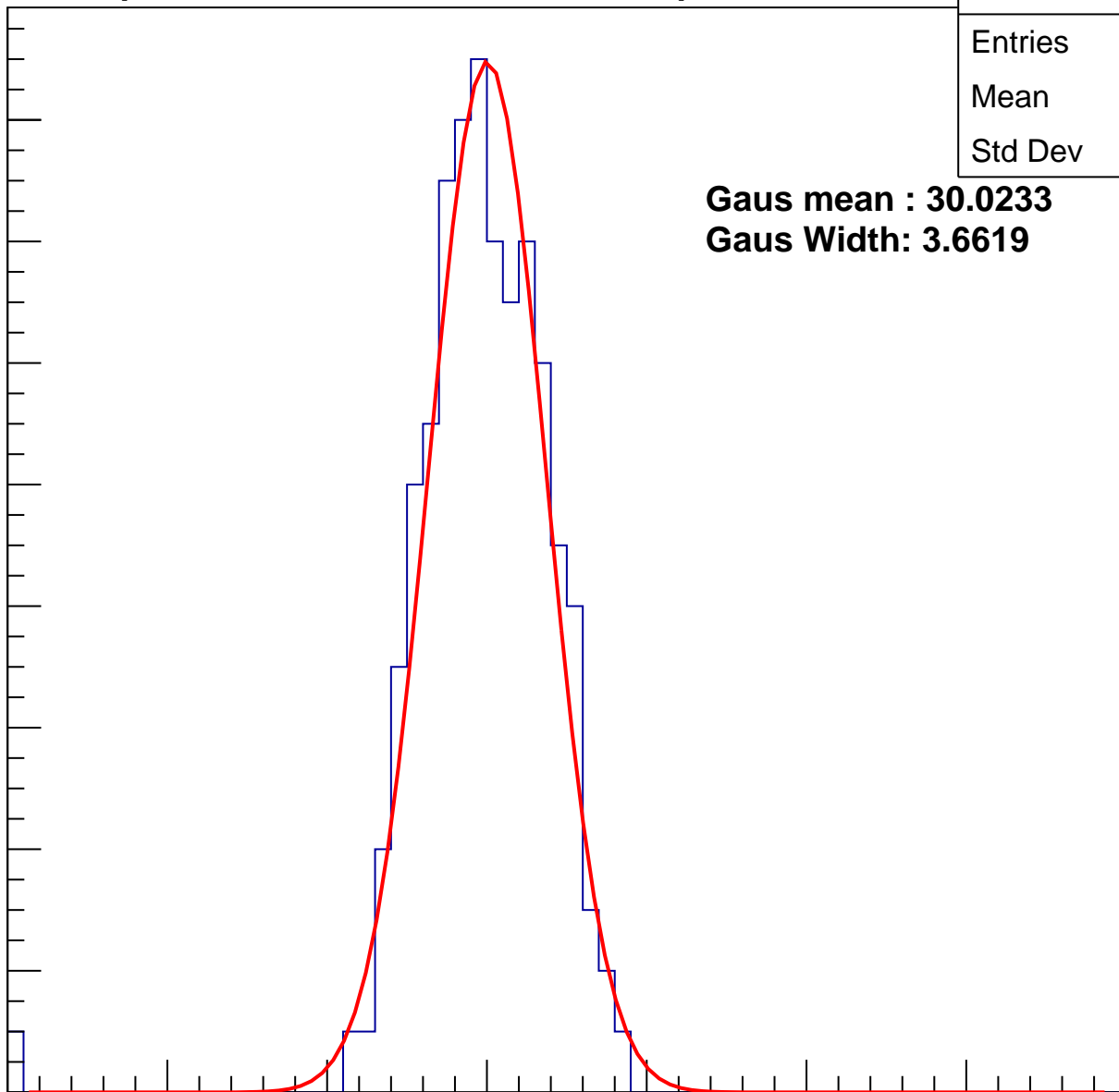
40

50

60

70

ampl



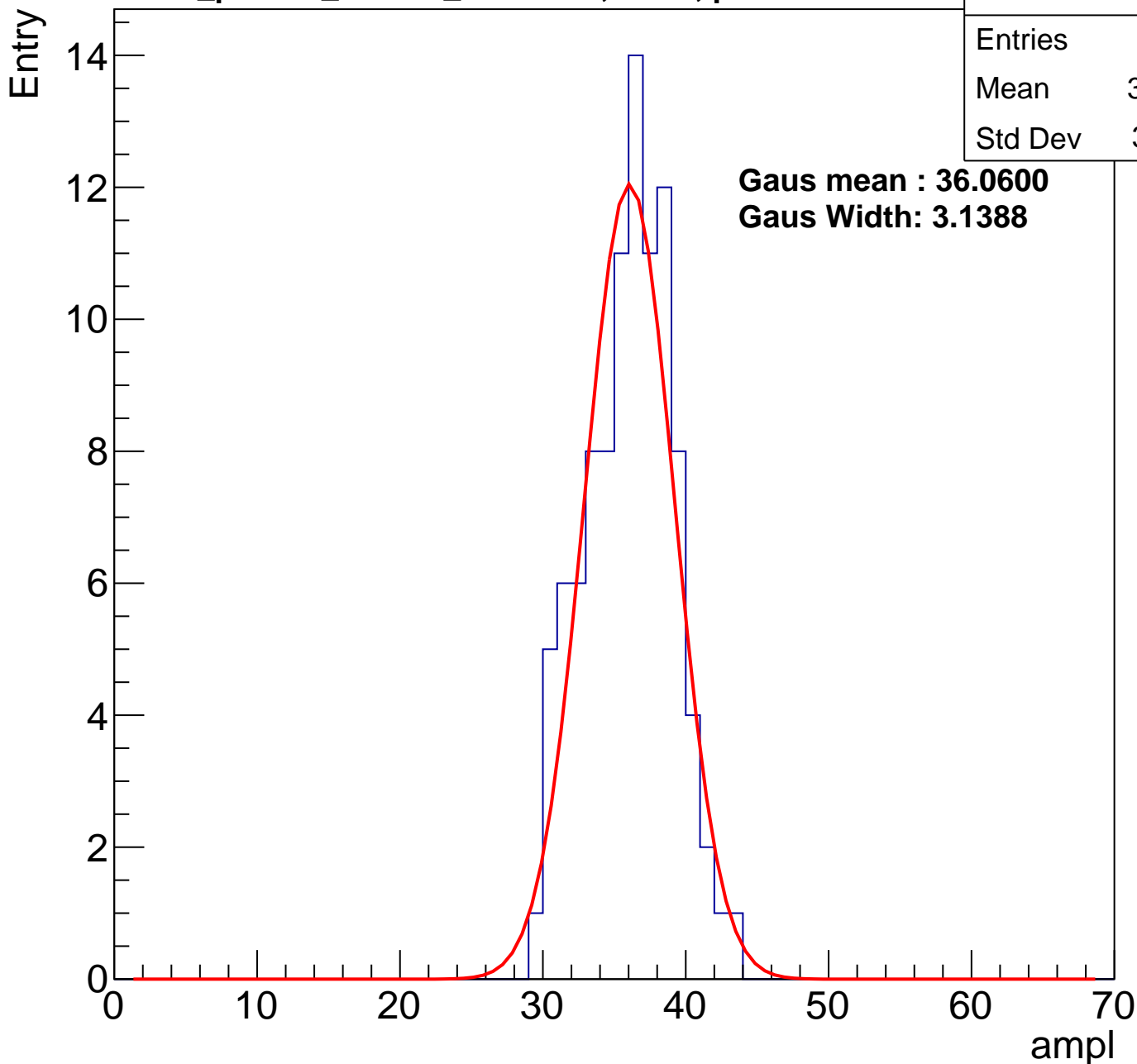
B1L001S, U22-ch52, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	35.55
Std Dev	3.031

Gaus mean : 36.0600

Gaus Width: 3.1388



B1L001S, U22-ch52, adc2

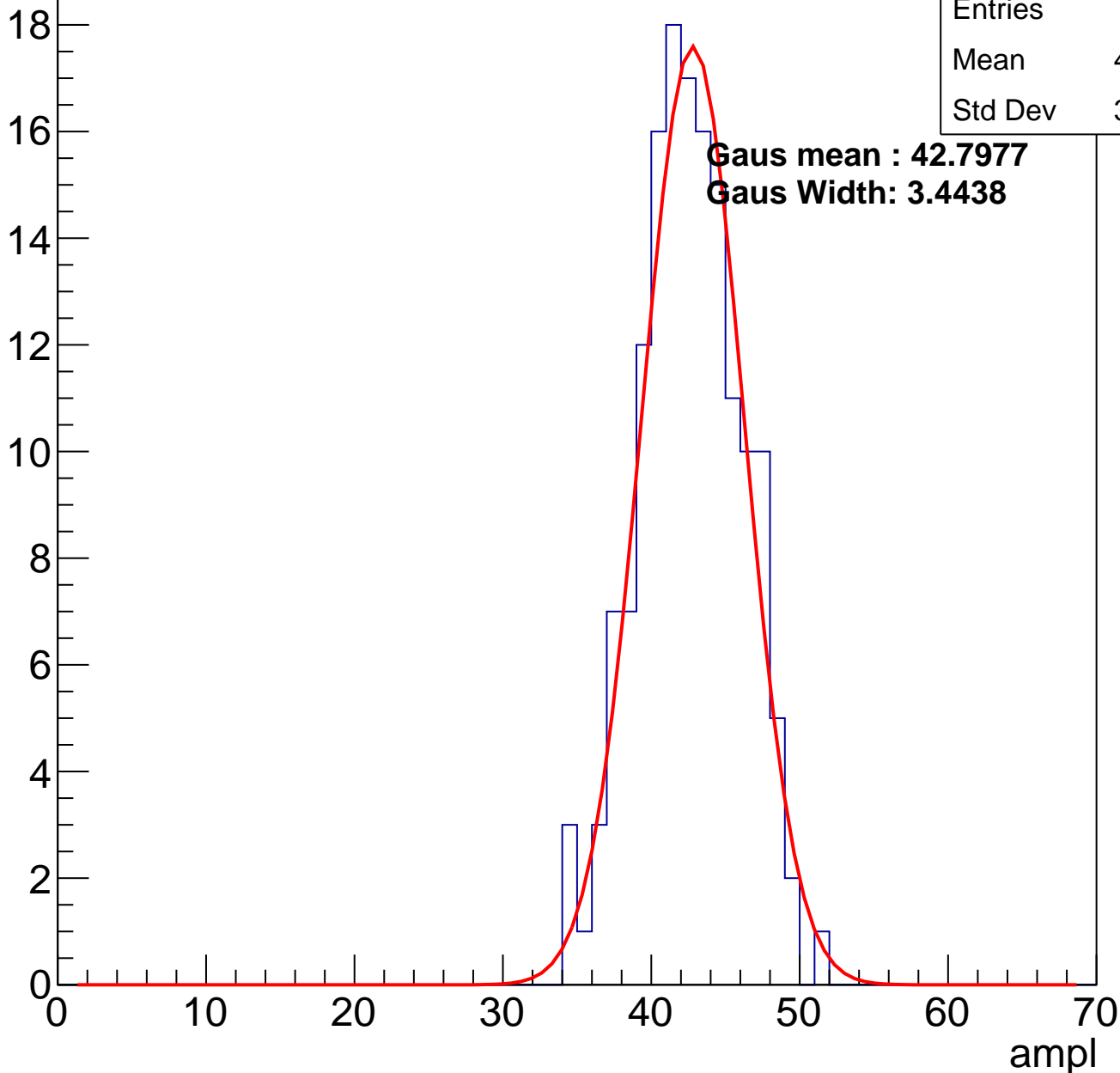
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	42.16
Std Dev	3.387

Gaus mean : 42.7977

Gaus Width: 3.4438

Entry



B1L001S, U22-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	49.3
Std Dev	3.246

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

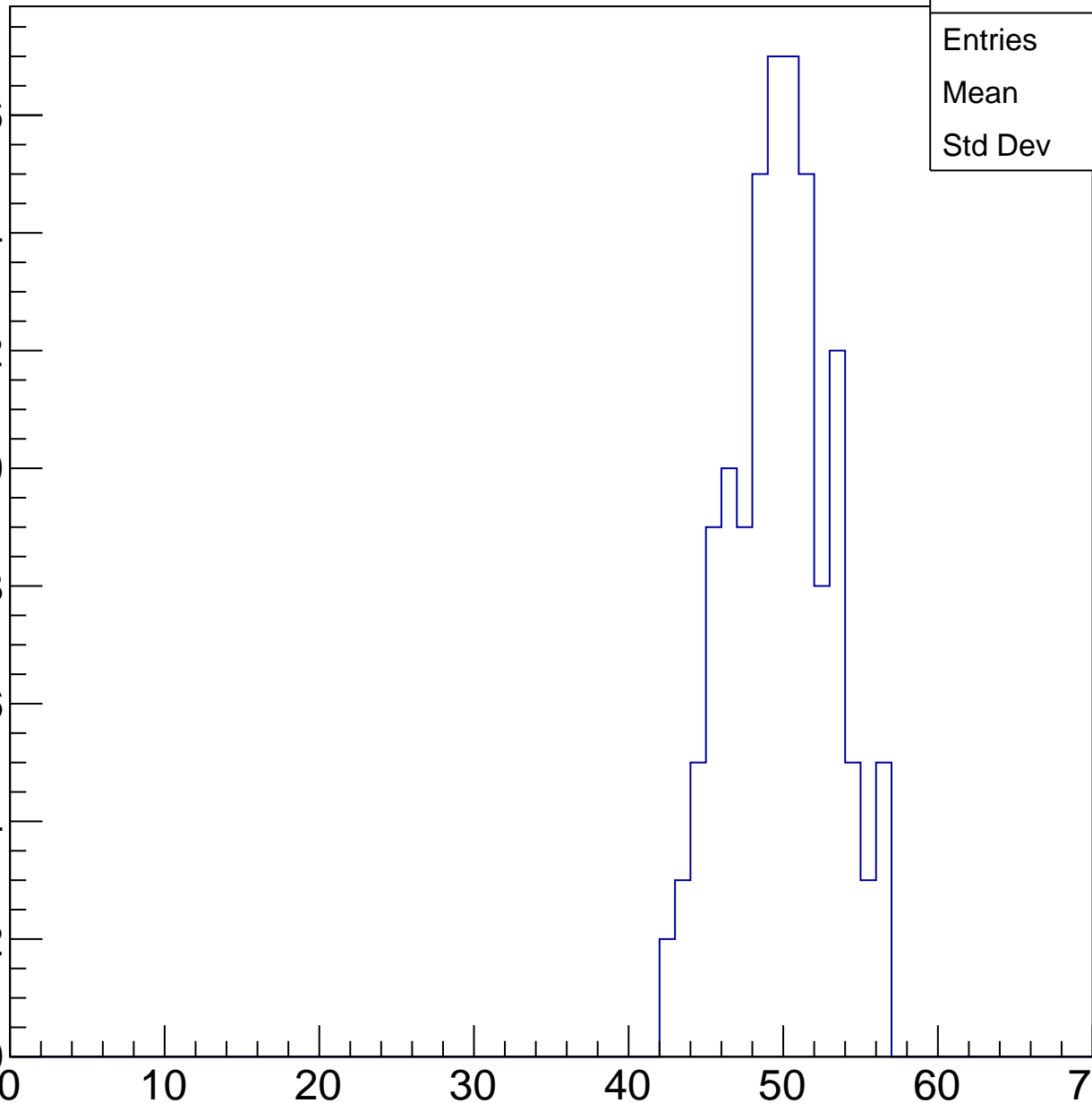
40

50

60

70

ampl

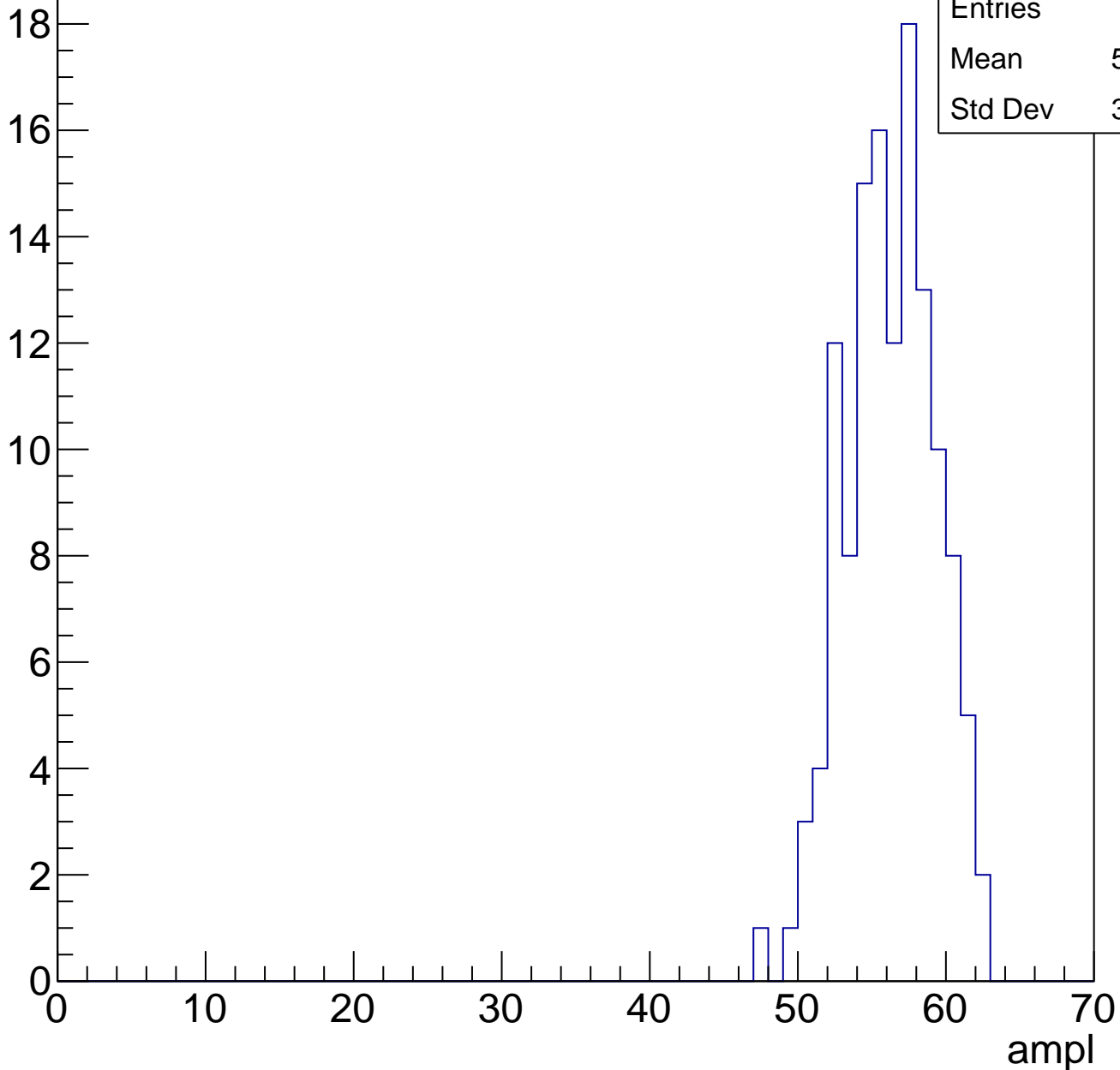


B1L001S, U22-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	55.77
Std Dev	3.006

Entry



B1L001S, U22-ch52, adc5

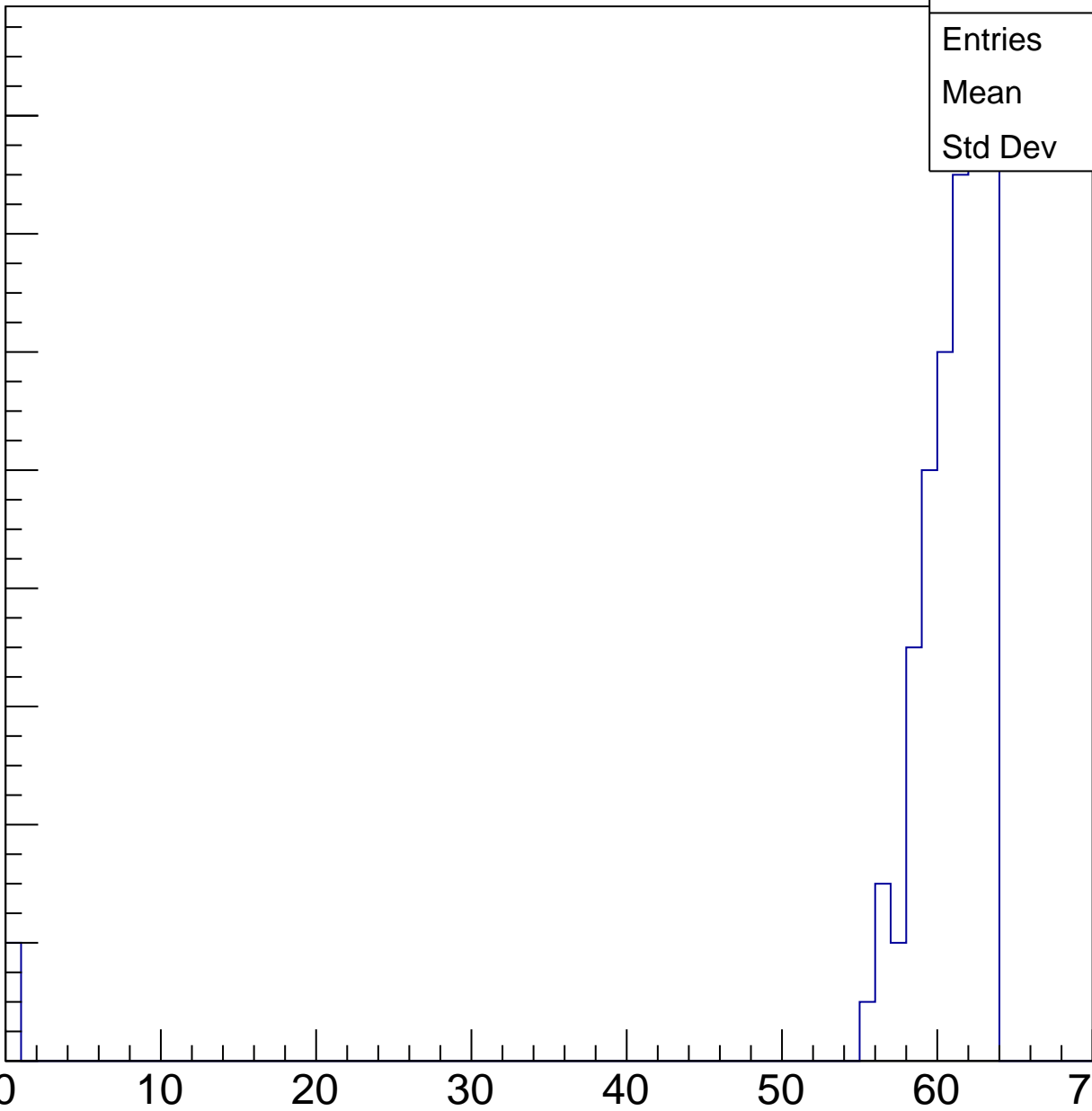
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.22
Std Dev	9.347

ampl



B1L001S, U22-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

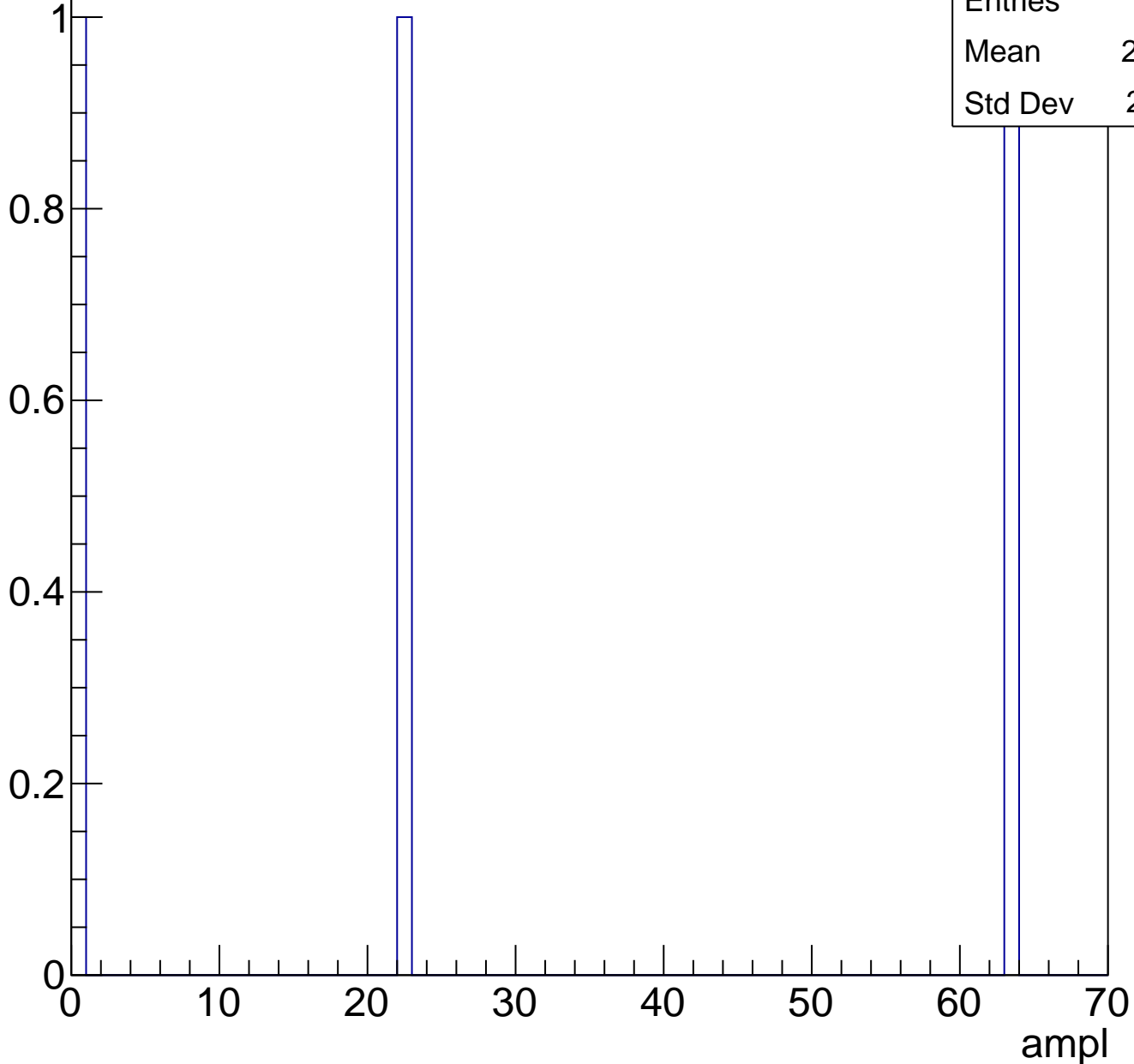
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L001S, U22-ch53, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	31.49
Std Dev	3.274

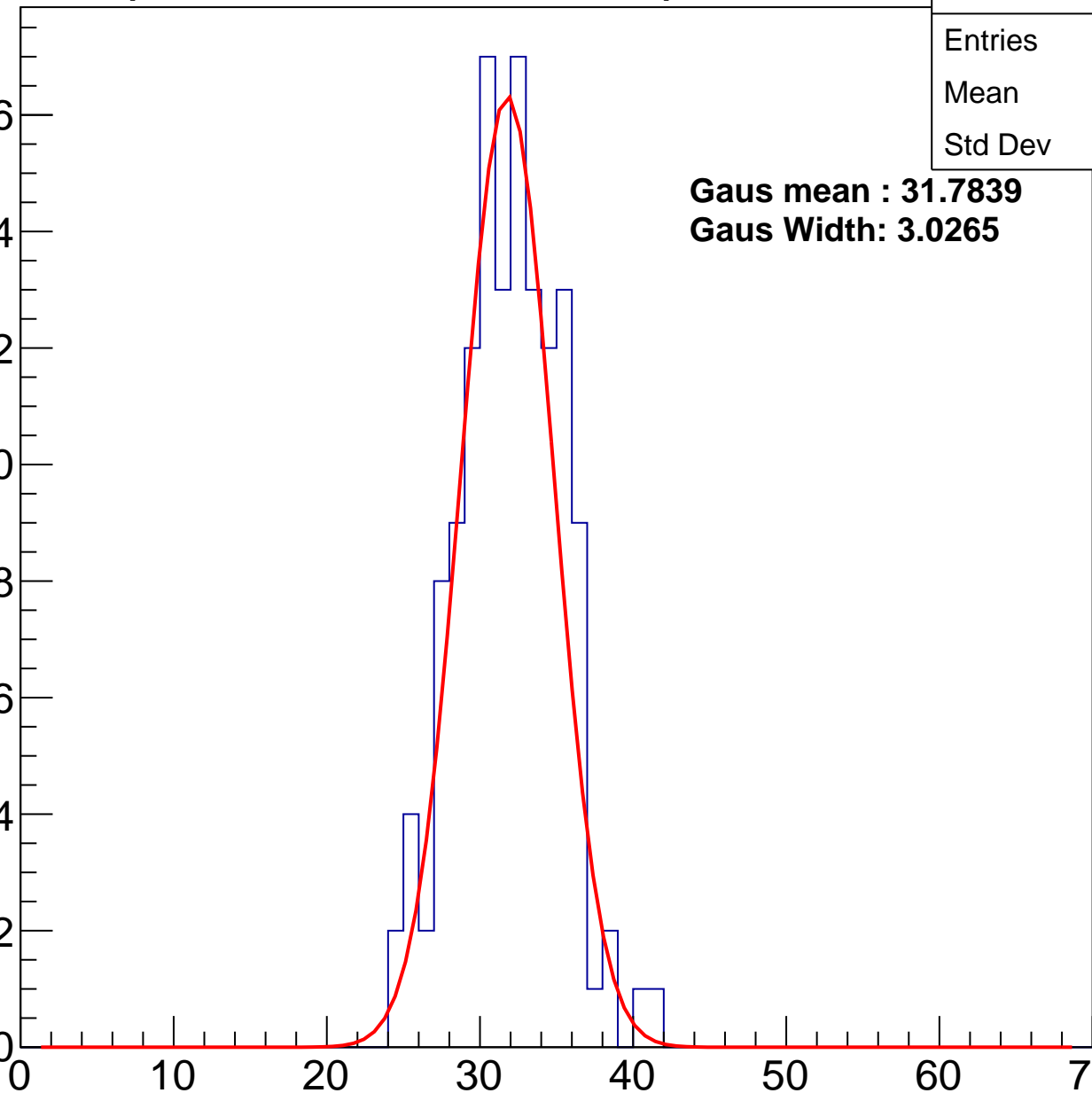
Gaus mean : 31.7839

Gaus Width: 3.0265

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	37.78
Std Dev	3.153

Gaus mean : 38.2087

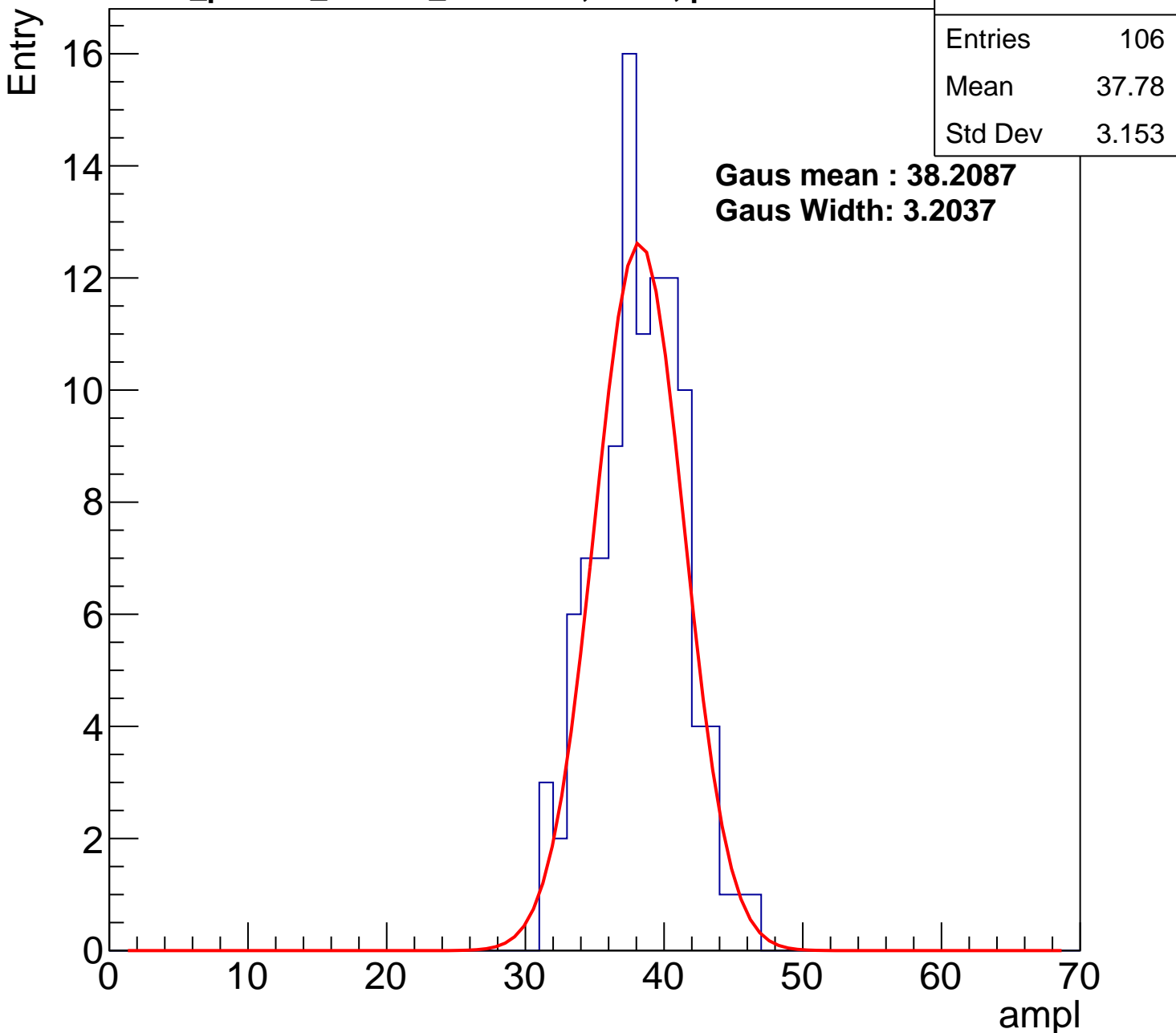
Gaus Width: 3.2037

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch53, adc2

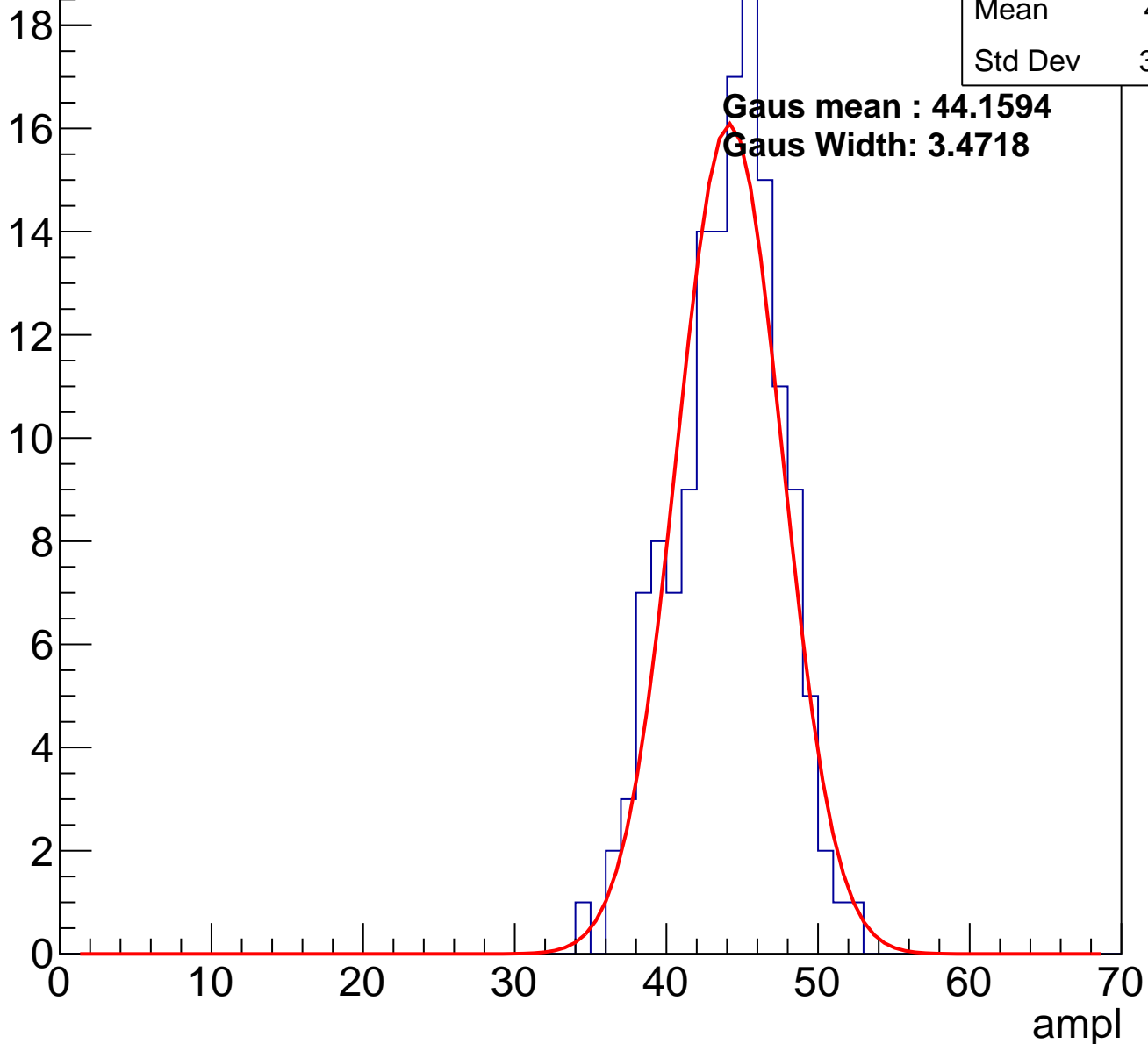
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	43.61
Std Dev	3.424

Gaus mean : 44.1594

Gaus Width: 3.4718

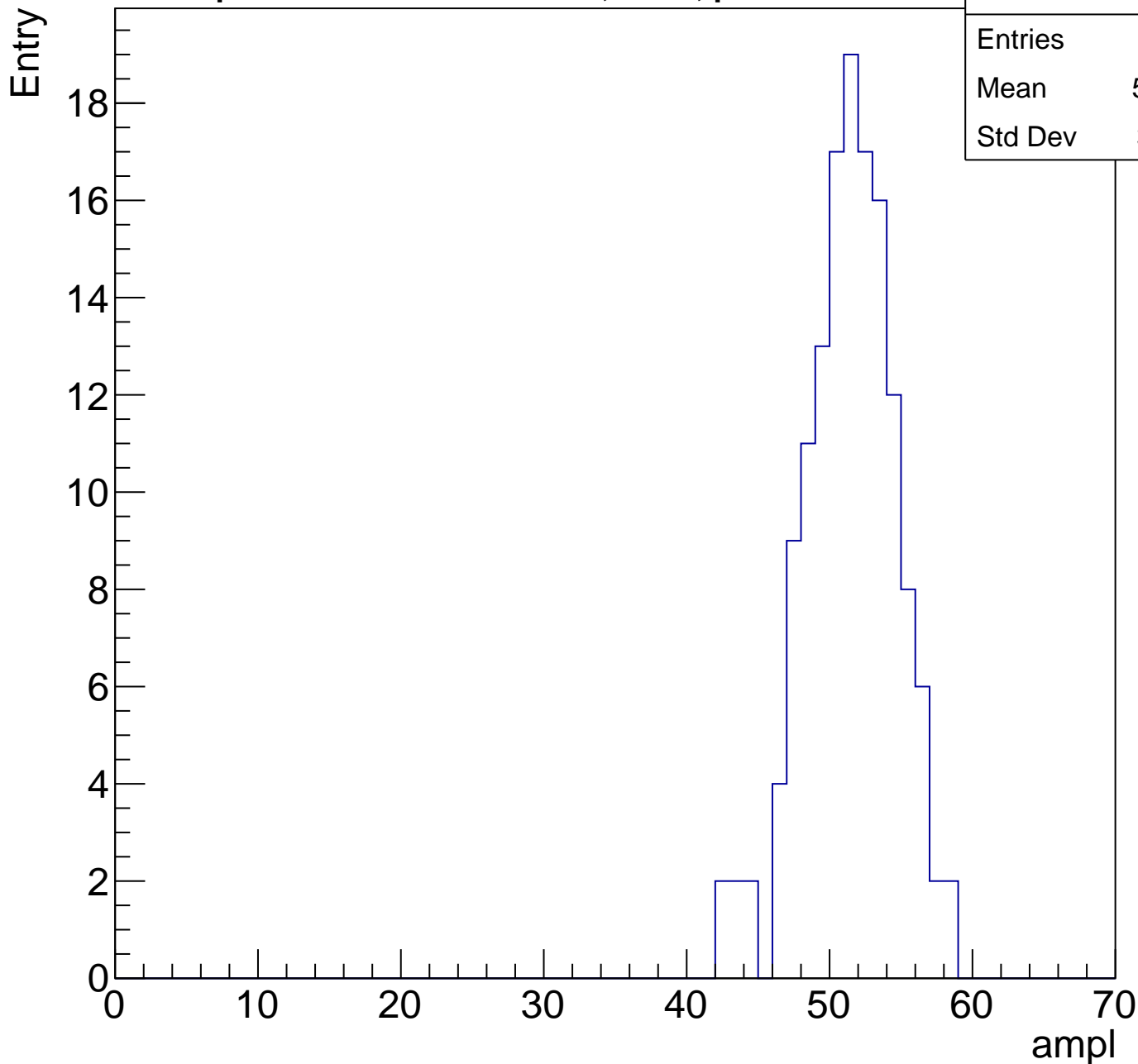
Entry



B1L001S, U22-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	50.95
Std Dev	3.181



B1L001S, U22-ch53, adc4

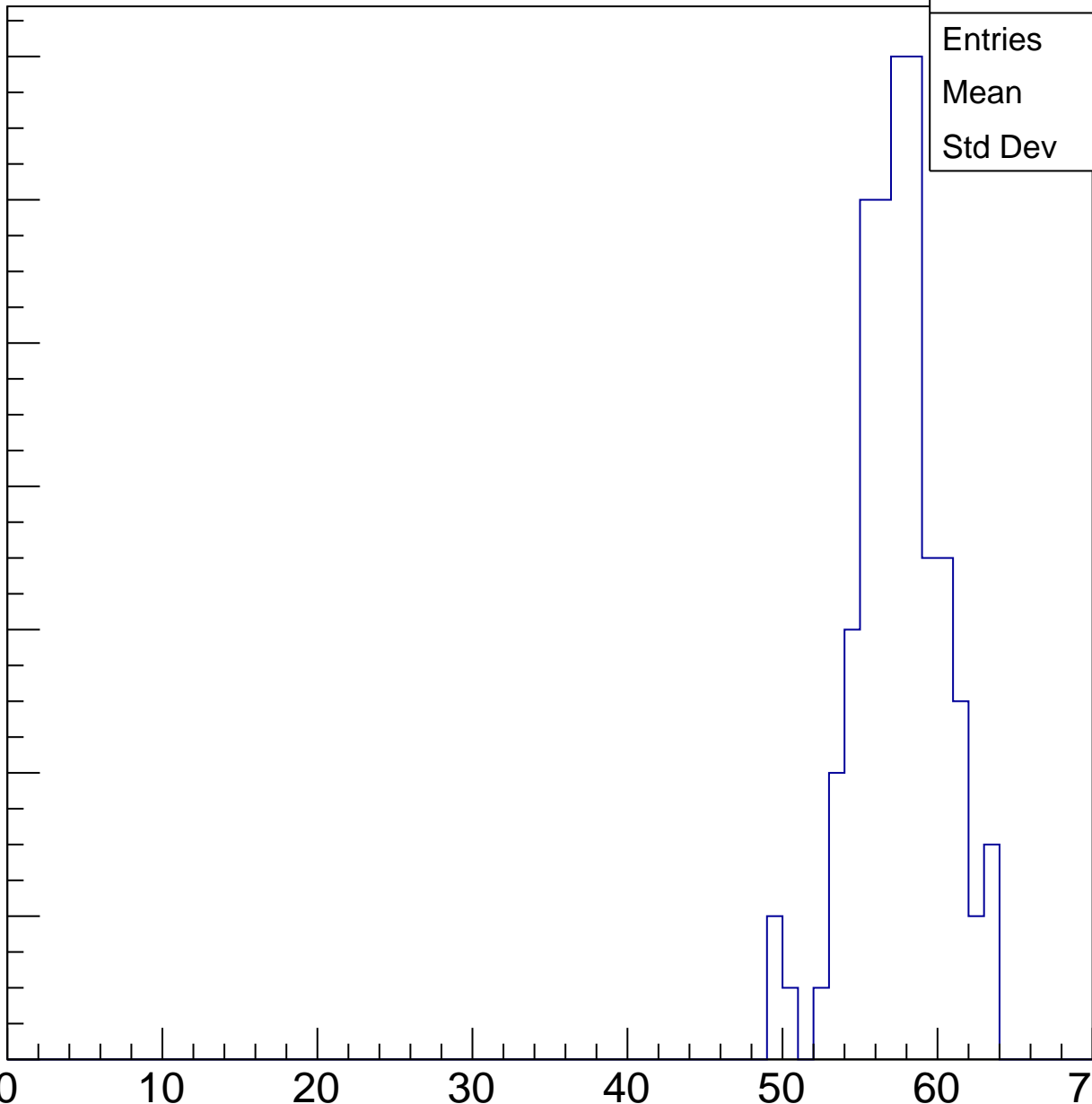
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	56.99
Std Dev	2.834

ampl



B1L001S, U22-ch53, adc5

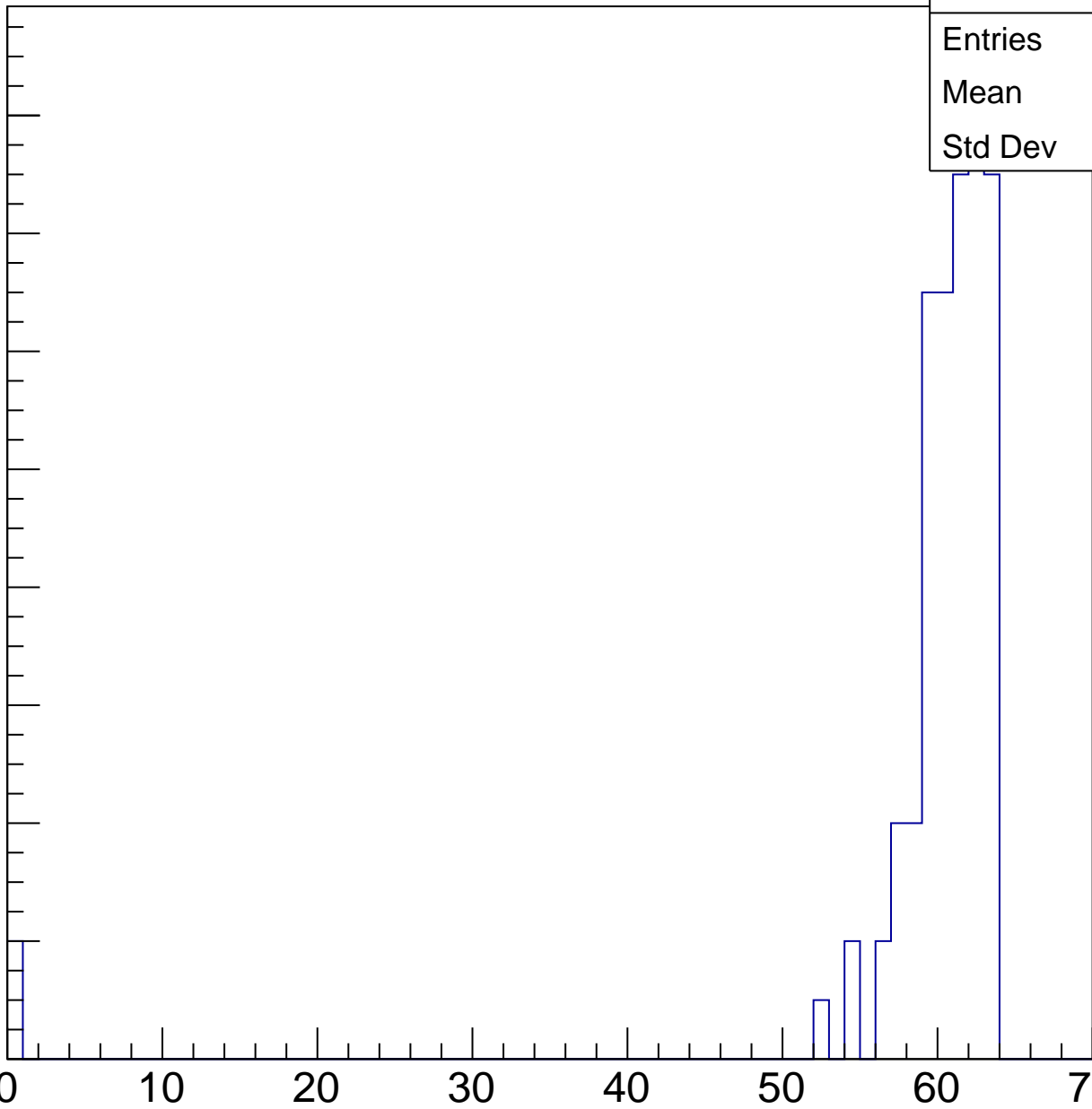
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.01
Std Dev	9.272

ampl



B1L001S, U22-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

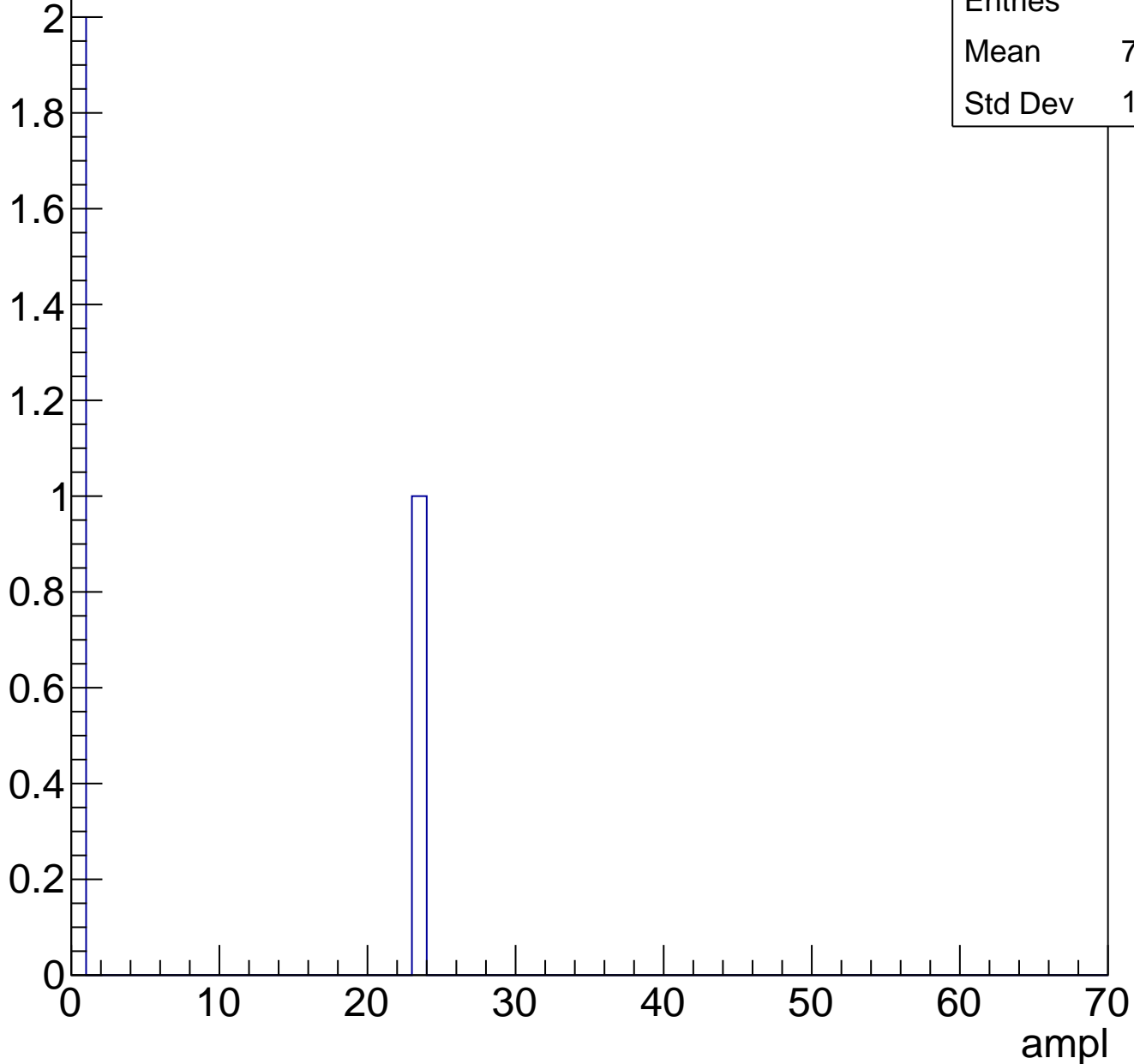
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L001S, U22-ch54, adc0

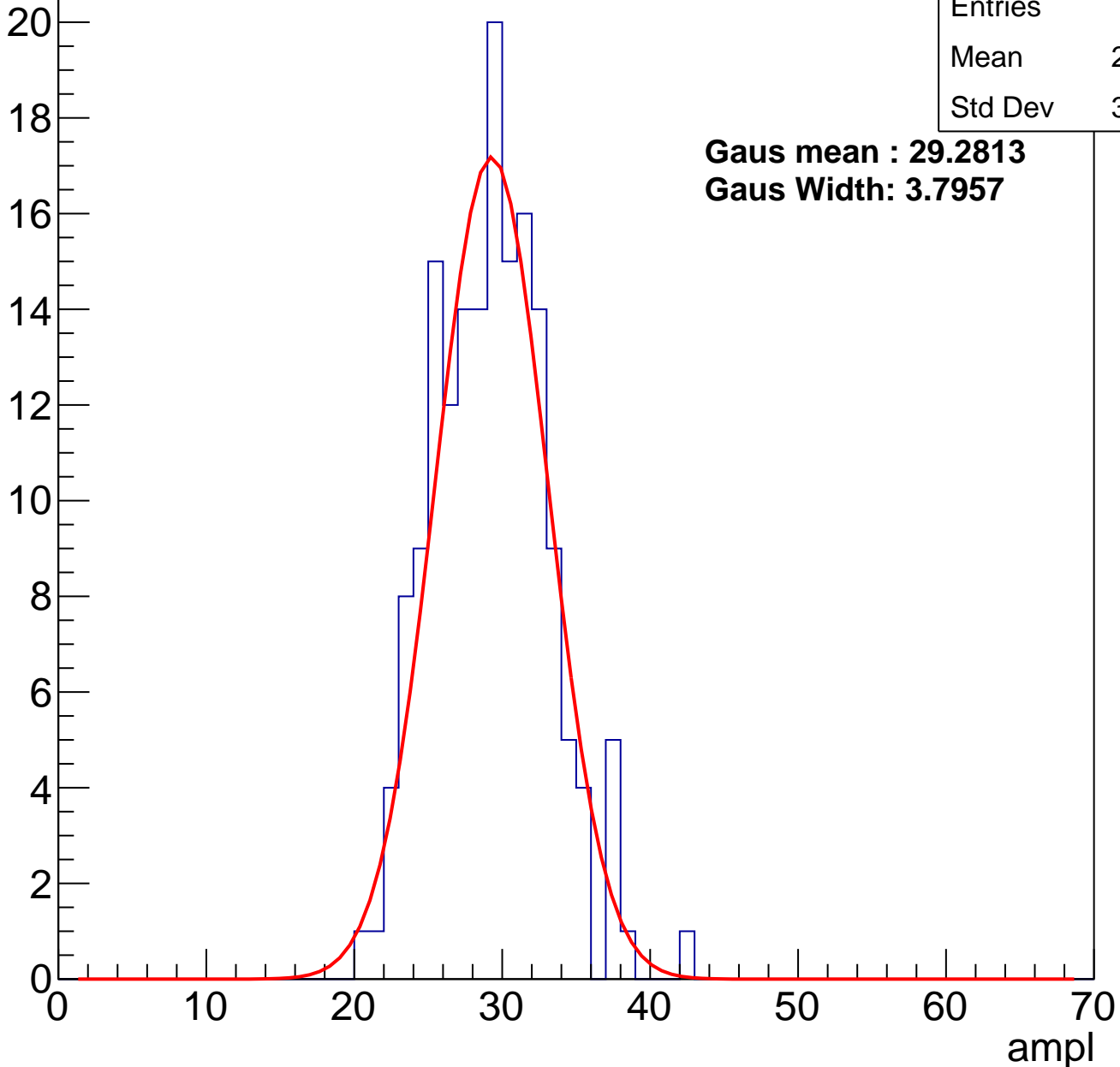
calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	28.76
Std Dev	3.819

Gaus mean : 29.2813

Gaus Width: 3.7957

Entry



B1L001S, U22-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.47
Std Dev	3.115

Gaus mean : 36.5467

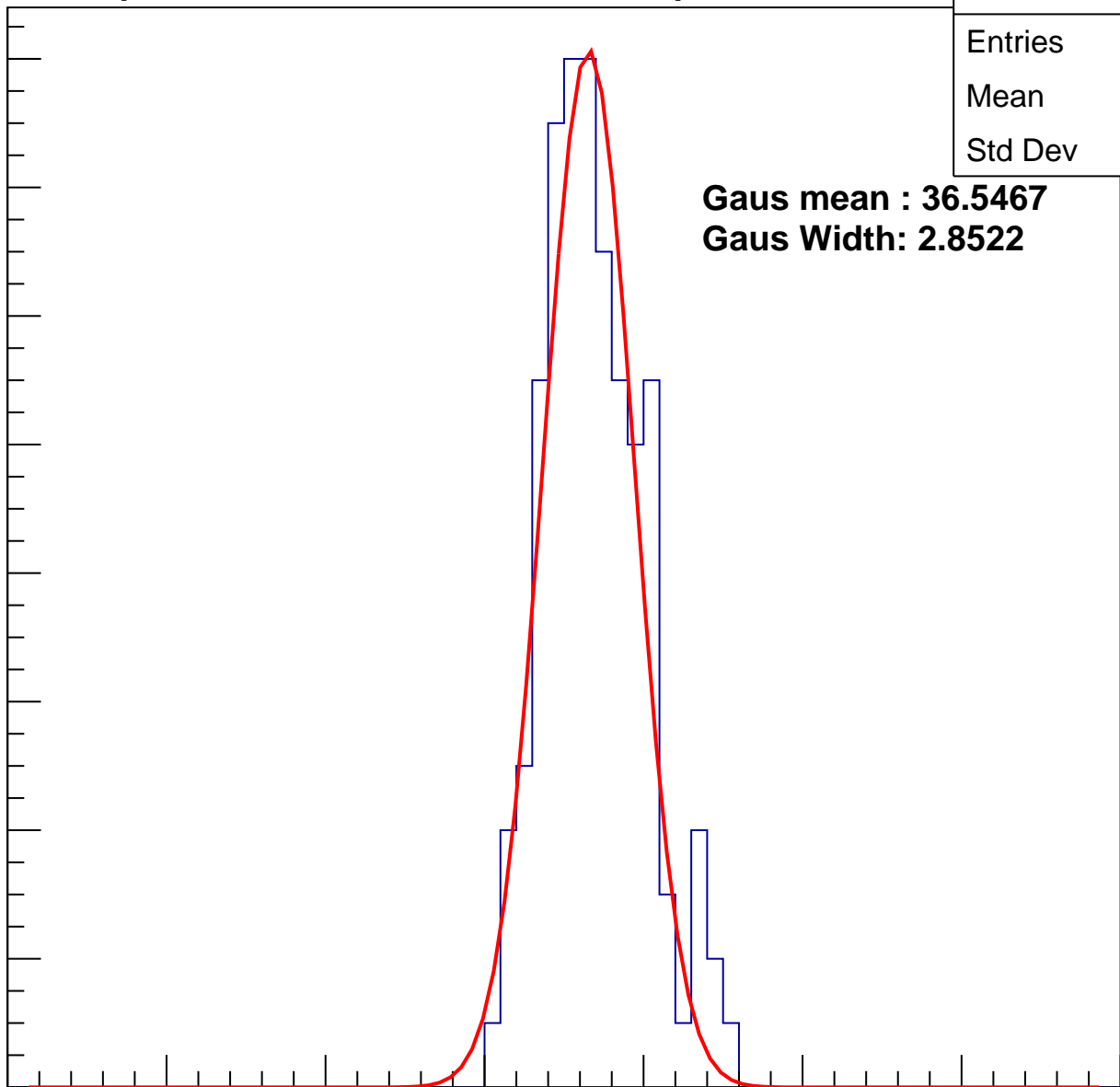
Gaus Width: 2.8522

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch54, adc2

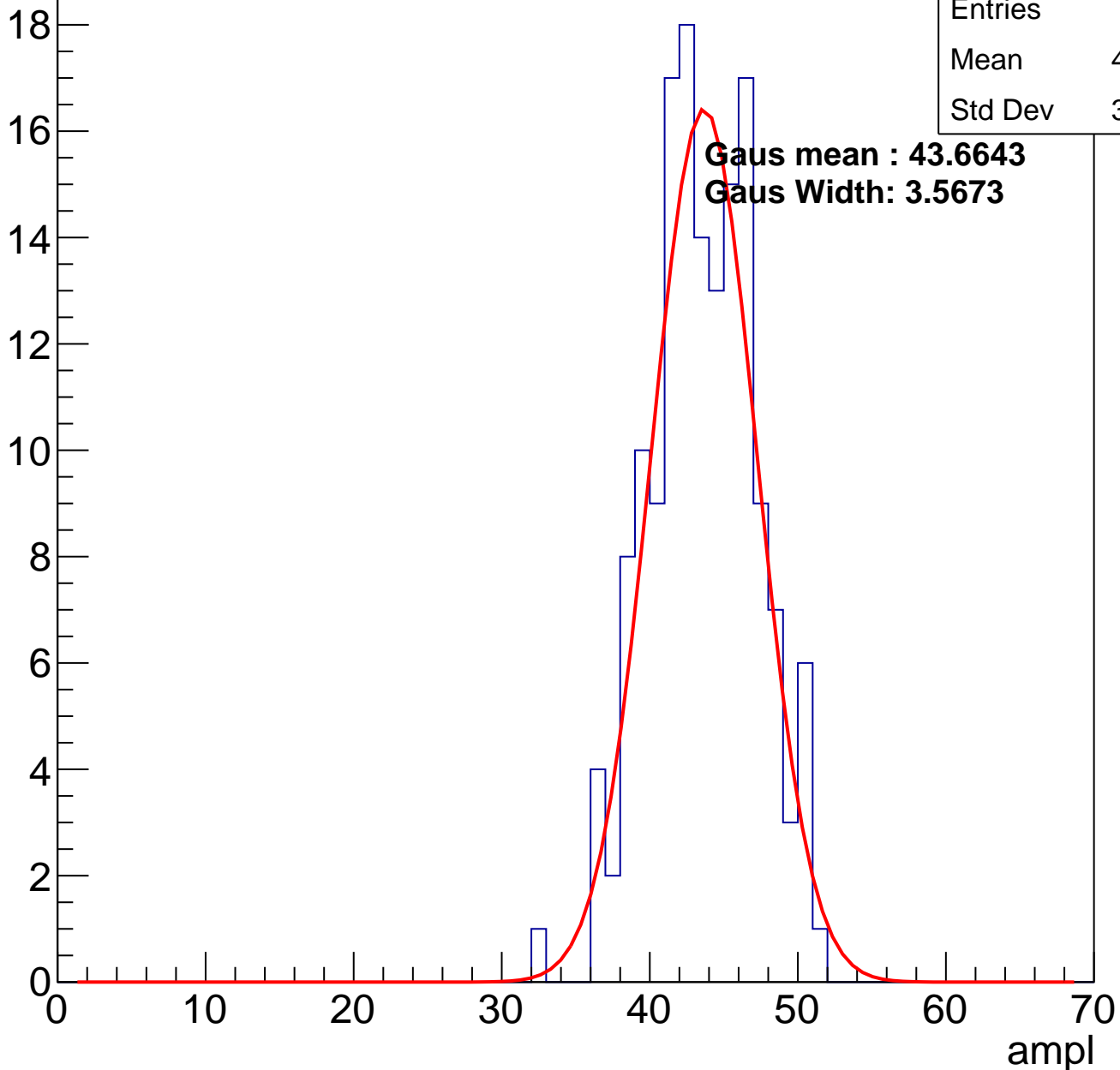
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	43.15
Std Dev	3.534

Gaus mean : 43.6643

Gaus Width: 3.5673

Entry



B1L001S, U22-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

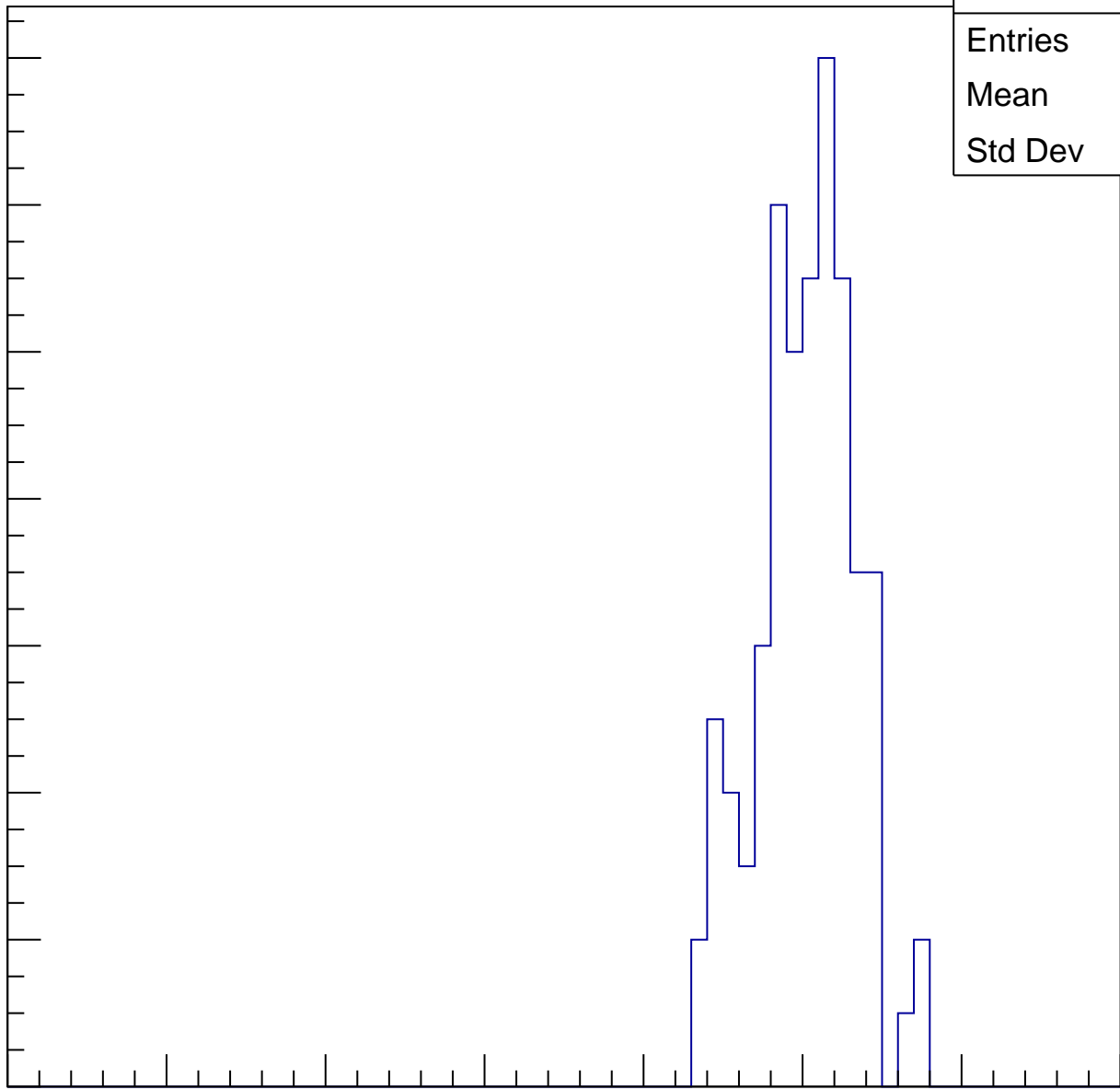
Entries	95
Mean	49.76
Std Dev	3.077

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch54, adc4

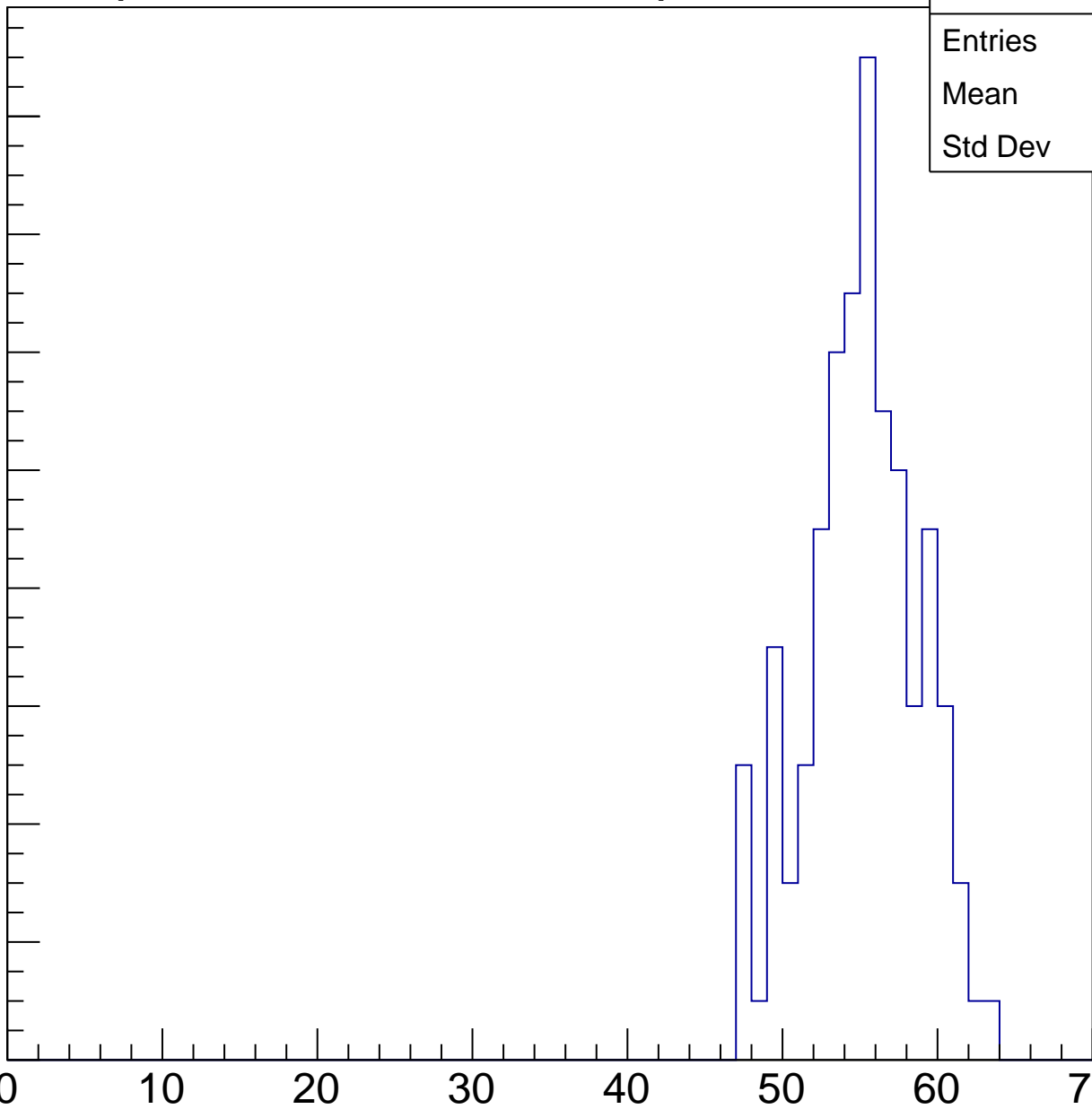
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	54.66
Std Dev	3.579

ampl



B1L001S, U22-ch54, adc5

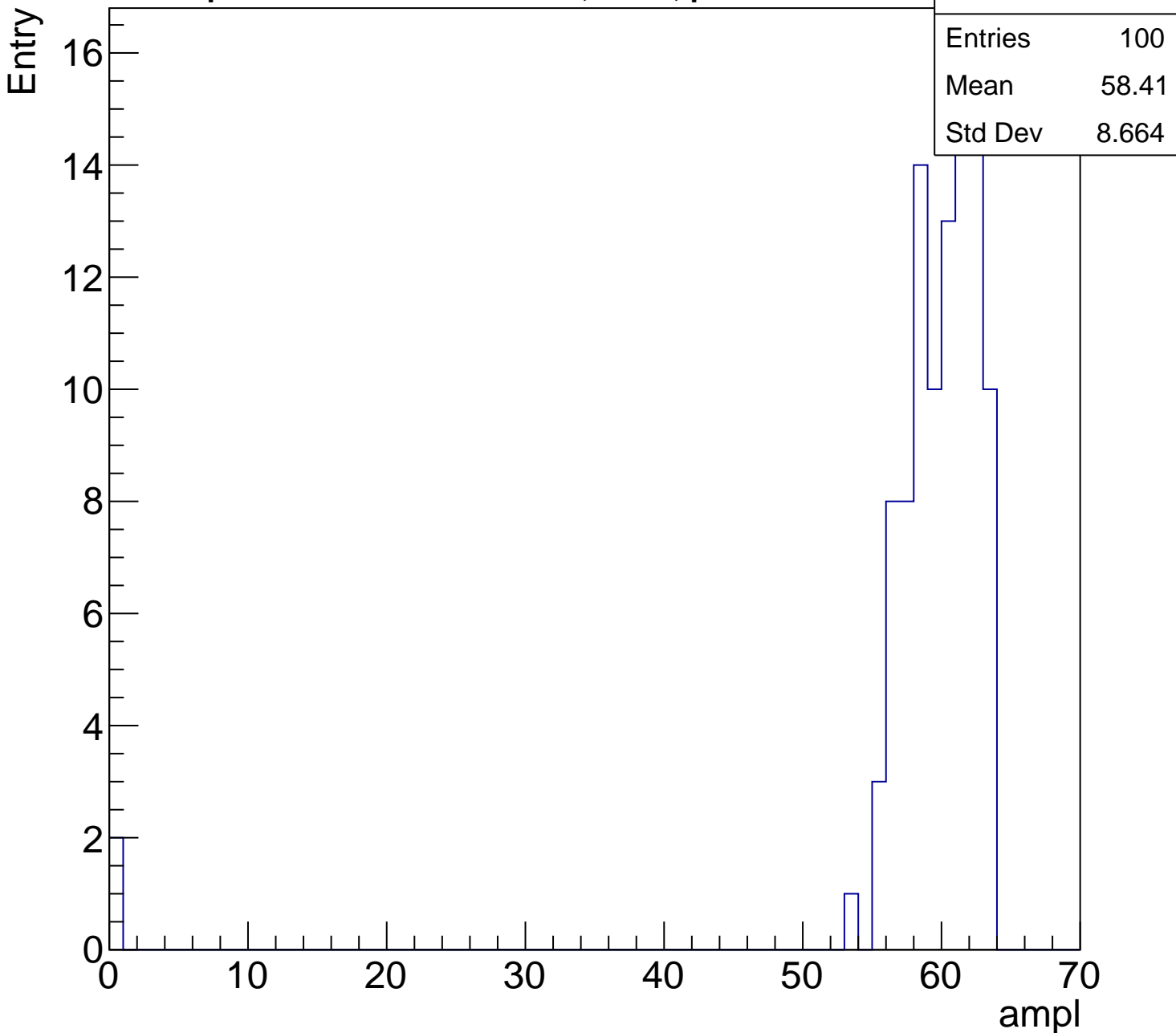
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	58.41
Std Dev	8.664

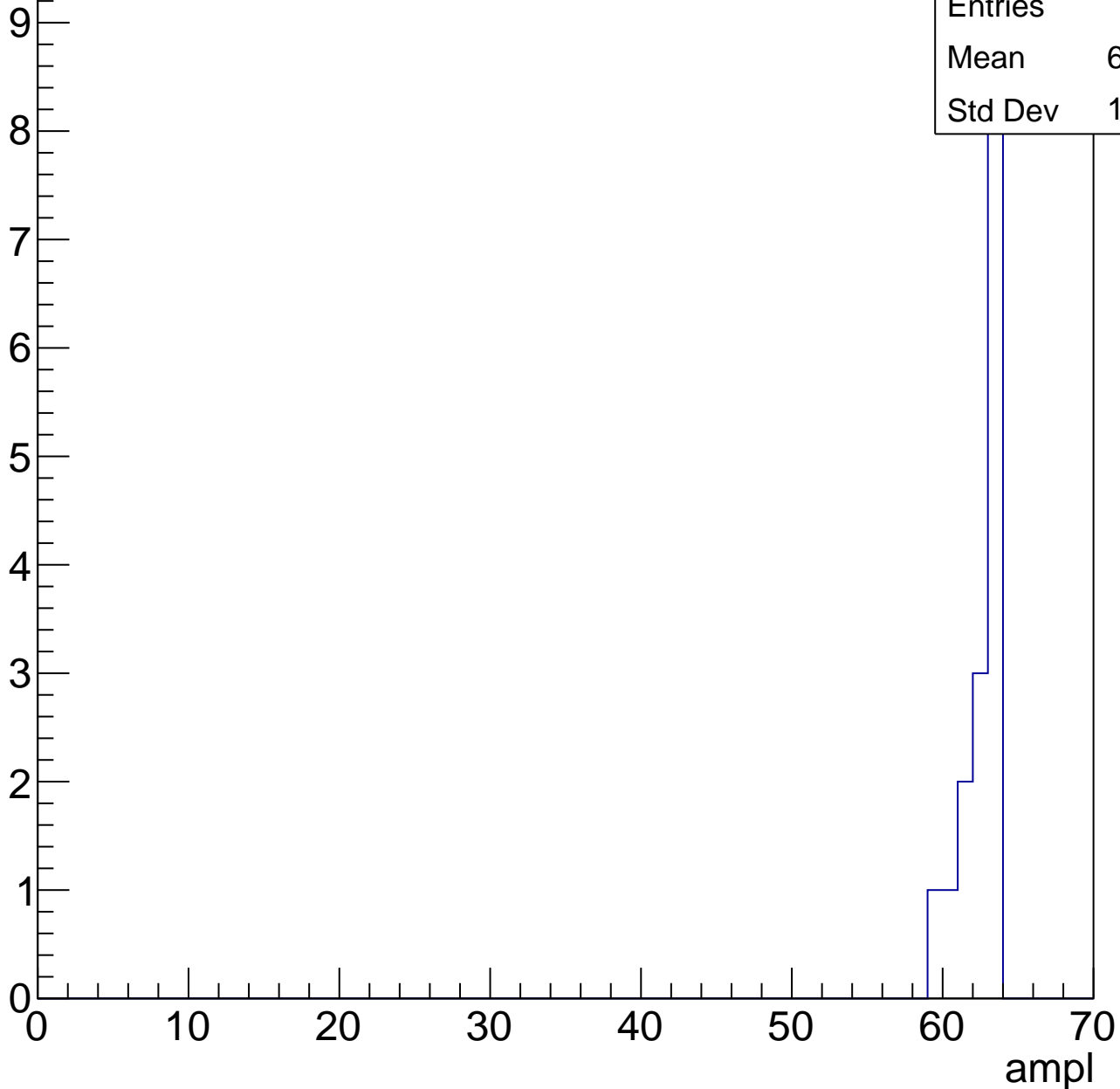
ampl



B1L001S, U22-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch55, adc0

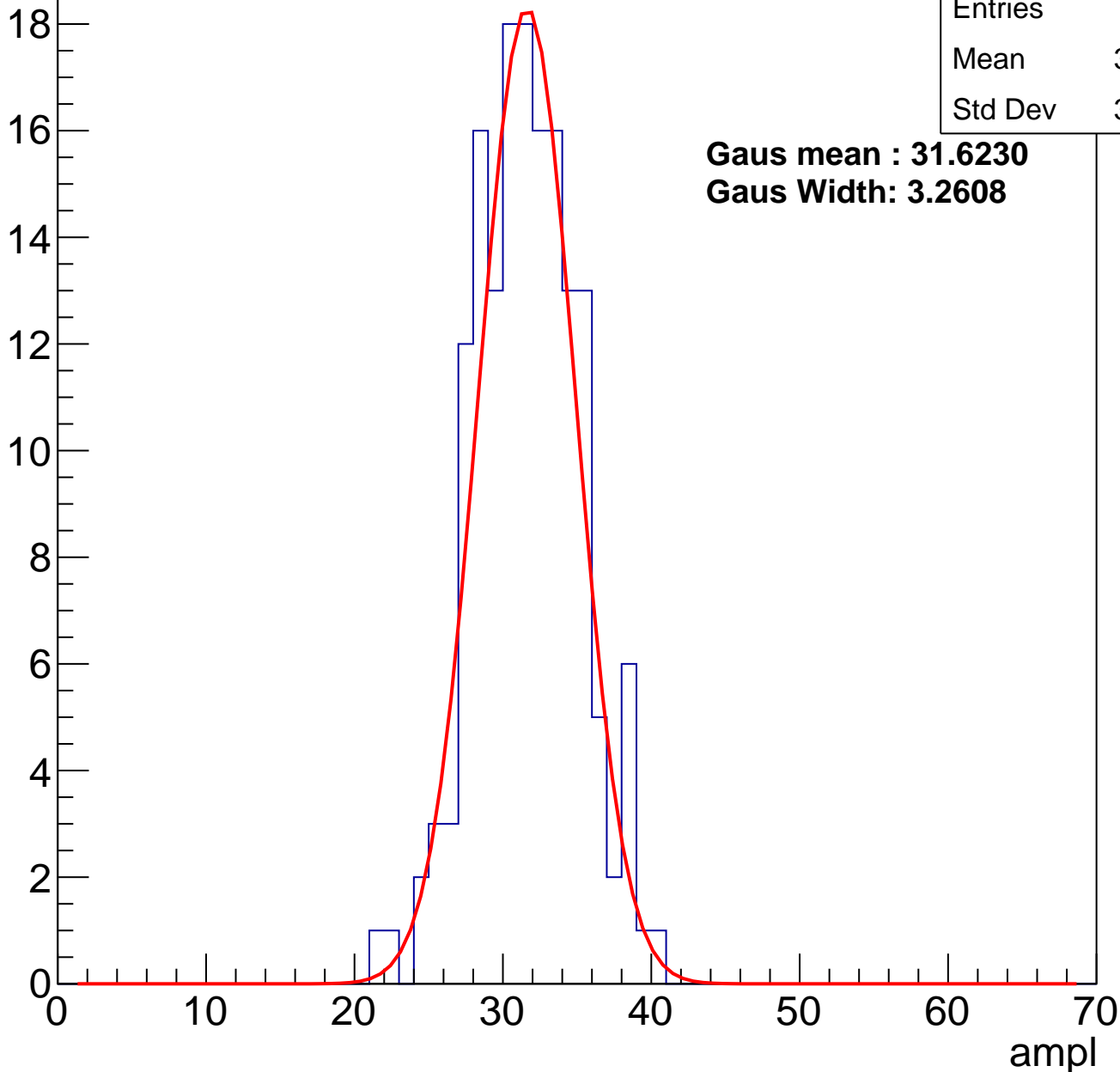
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	31.18
Std Dev	3.449

Gaus mean : 31.6230

Gaus Width: 3.2608

Entry



B1L001S, U22-ch55, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	38.2
Std Dev	3.216

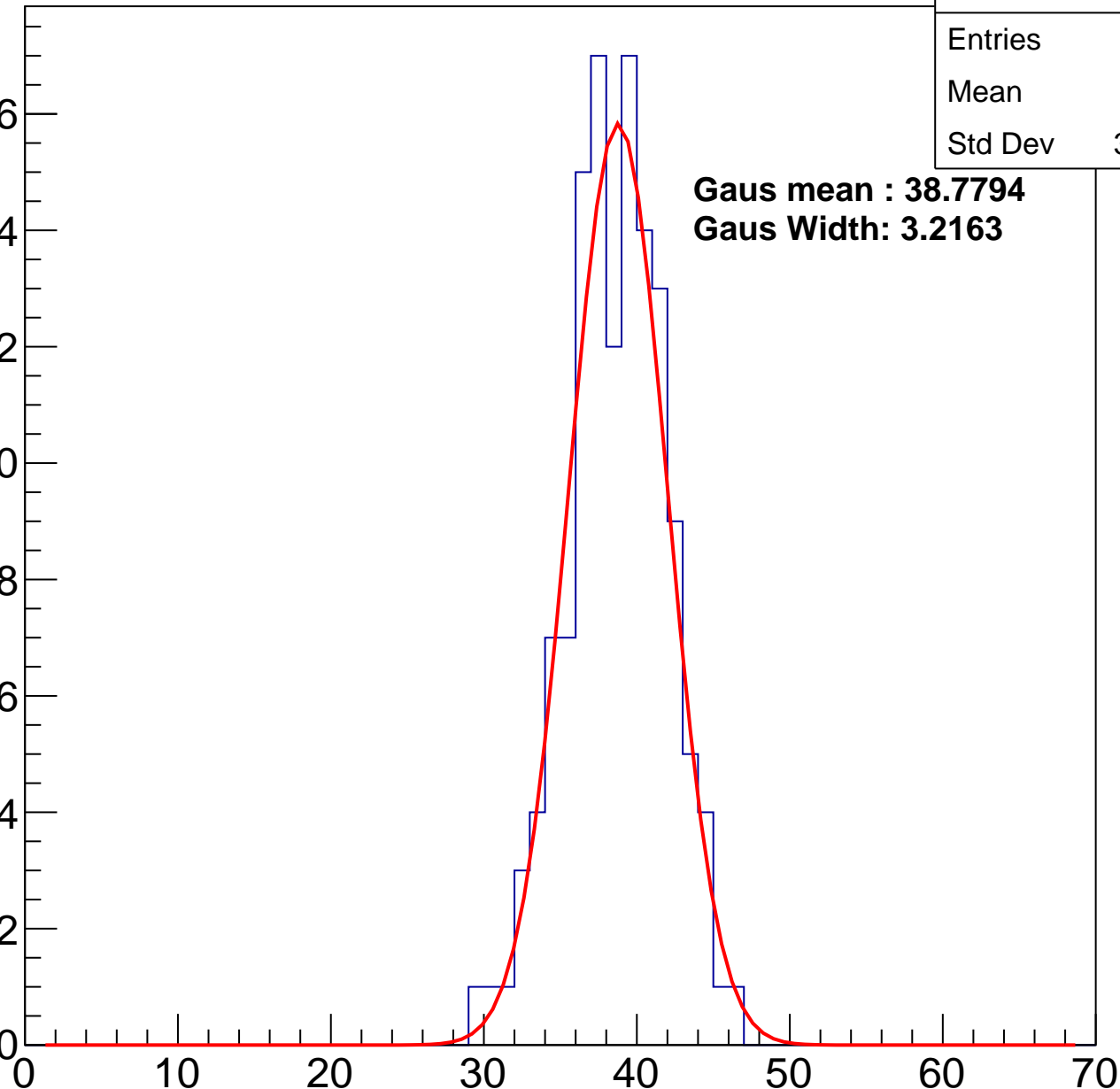
Gaus mean : 38.7794

Gaus Width: 3.2163

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch55, adc2

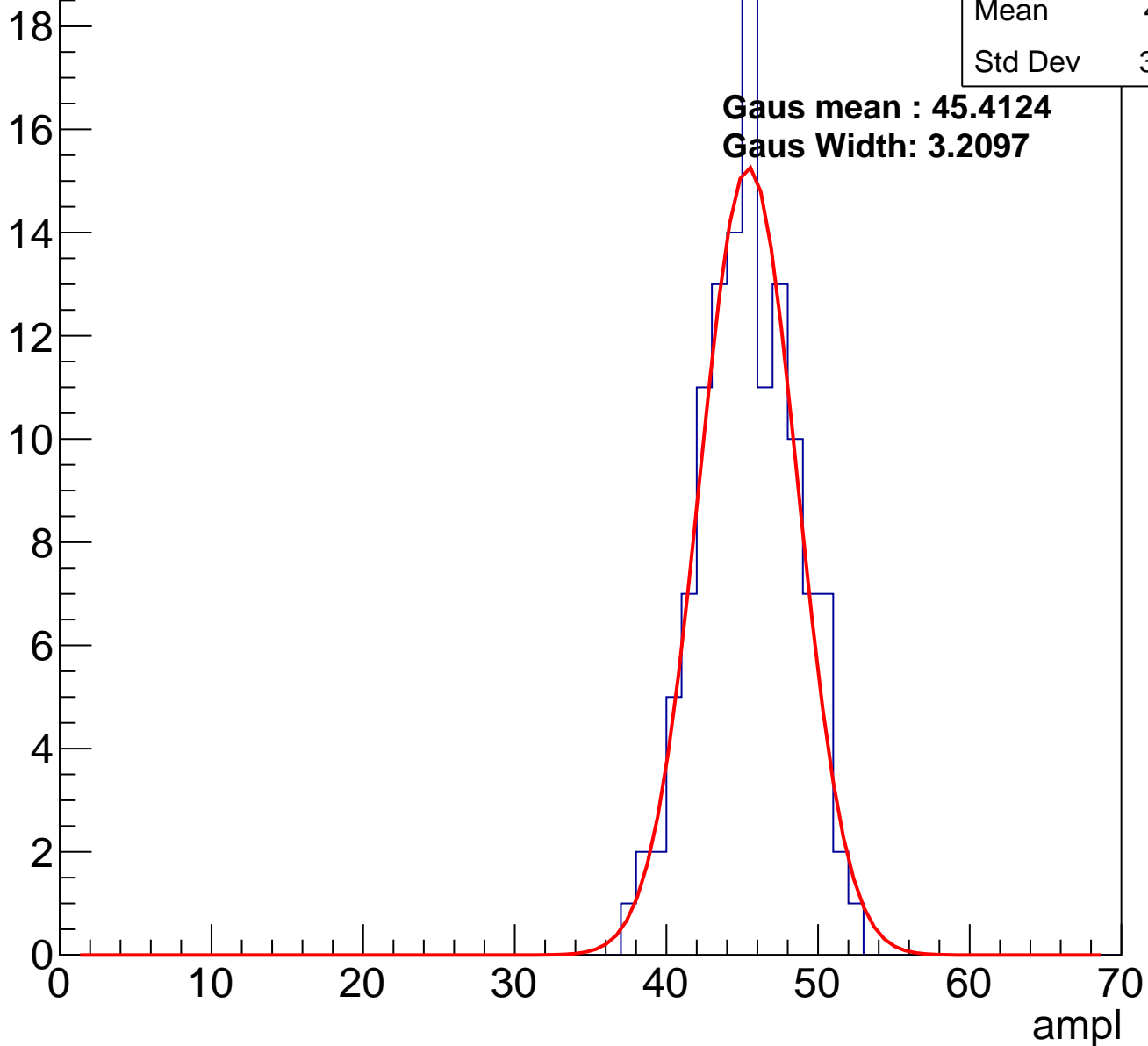
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	44.91
Std Dev	3.093

Entry

Gaus mean : 45.4124

Gaus Width: 3.2097

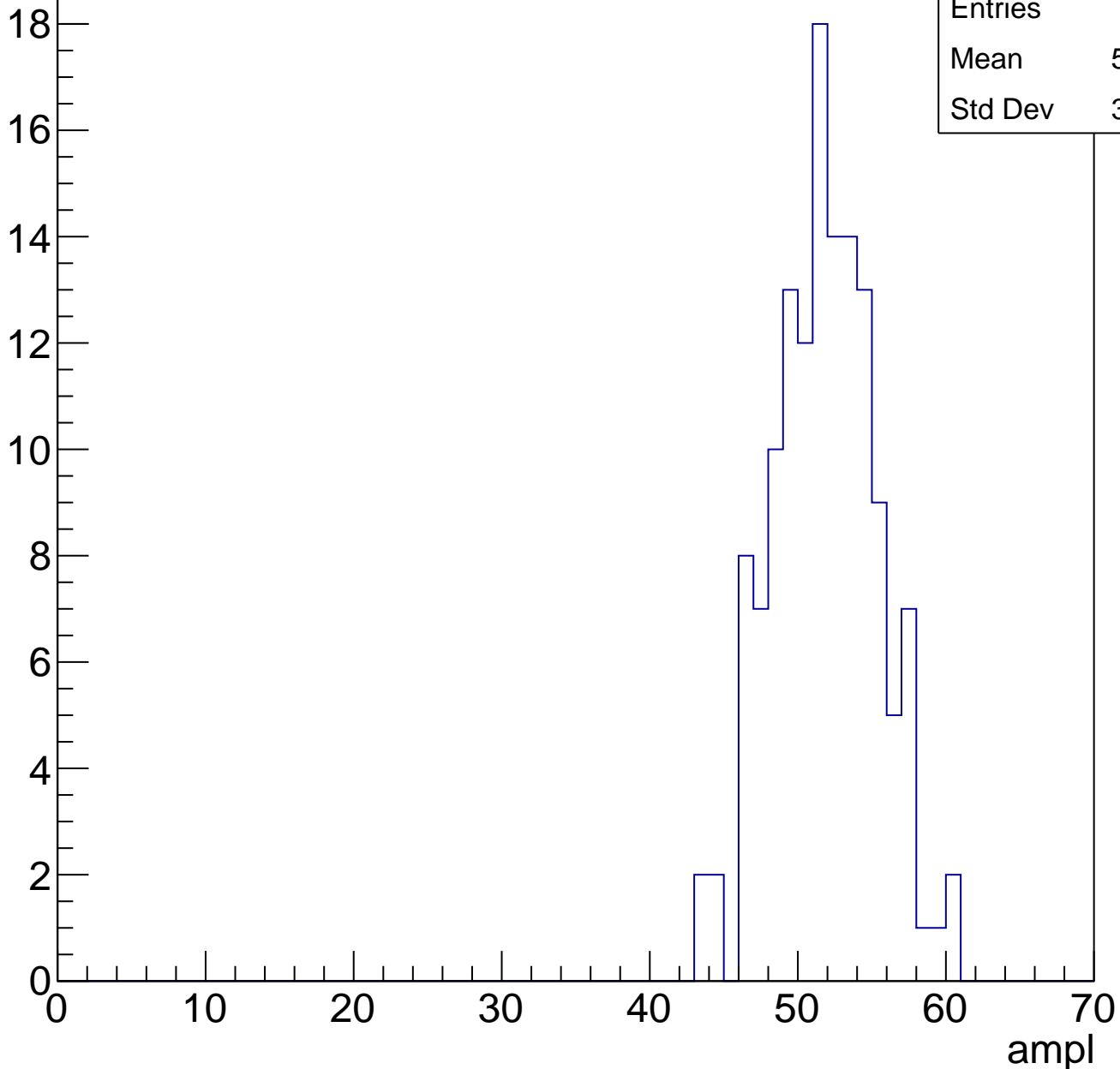


B1L001S, U22-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	51.37
Std Dev	3.464

Entry

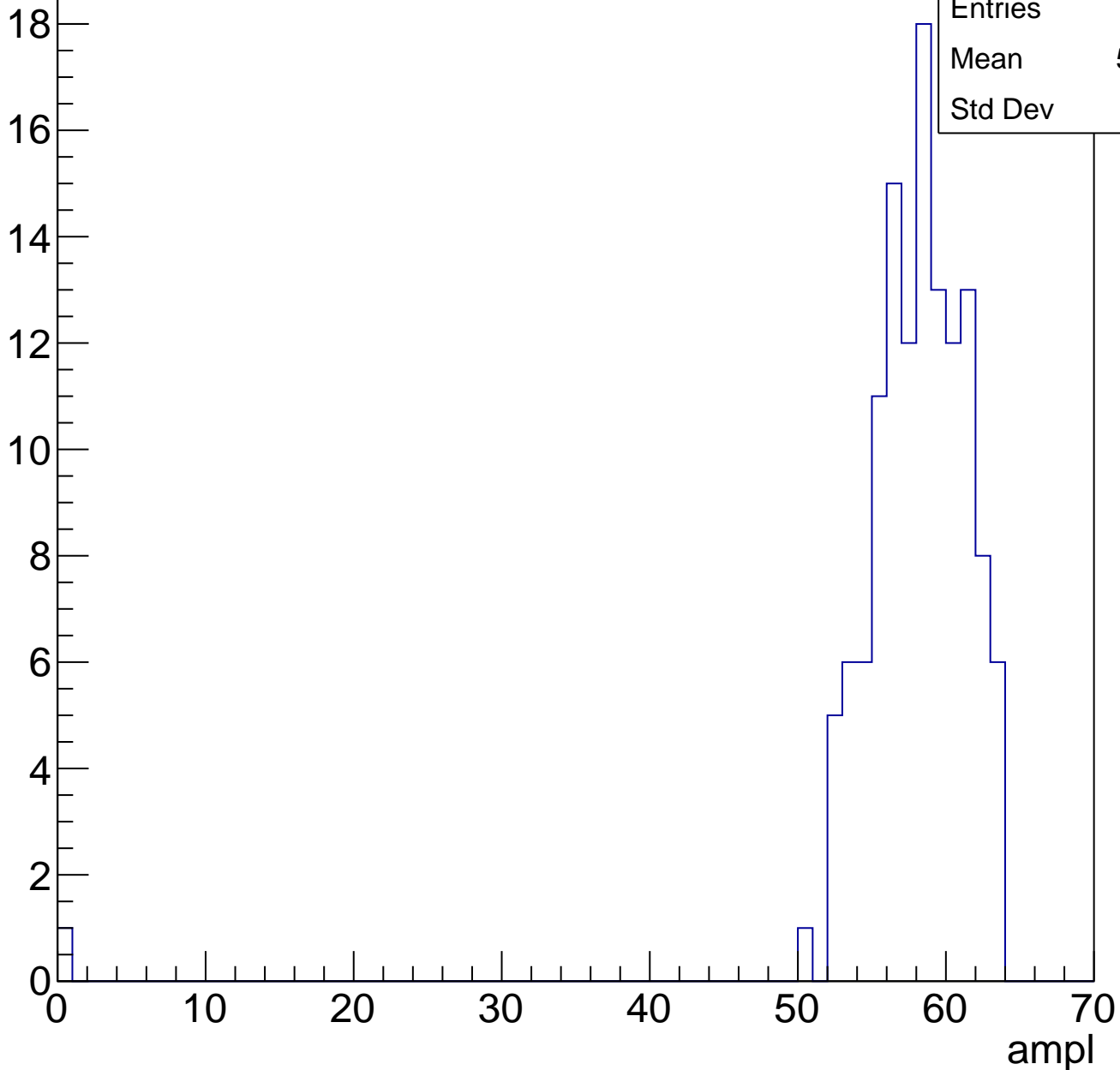


B1L001S, U22-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	57.31
Std Dev	5.9

Entry



B1L001S, U22-ch55, adc5

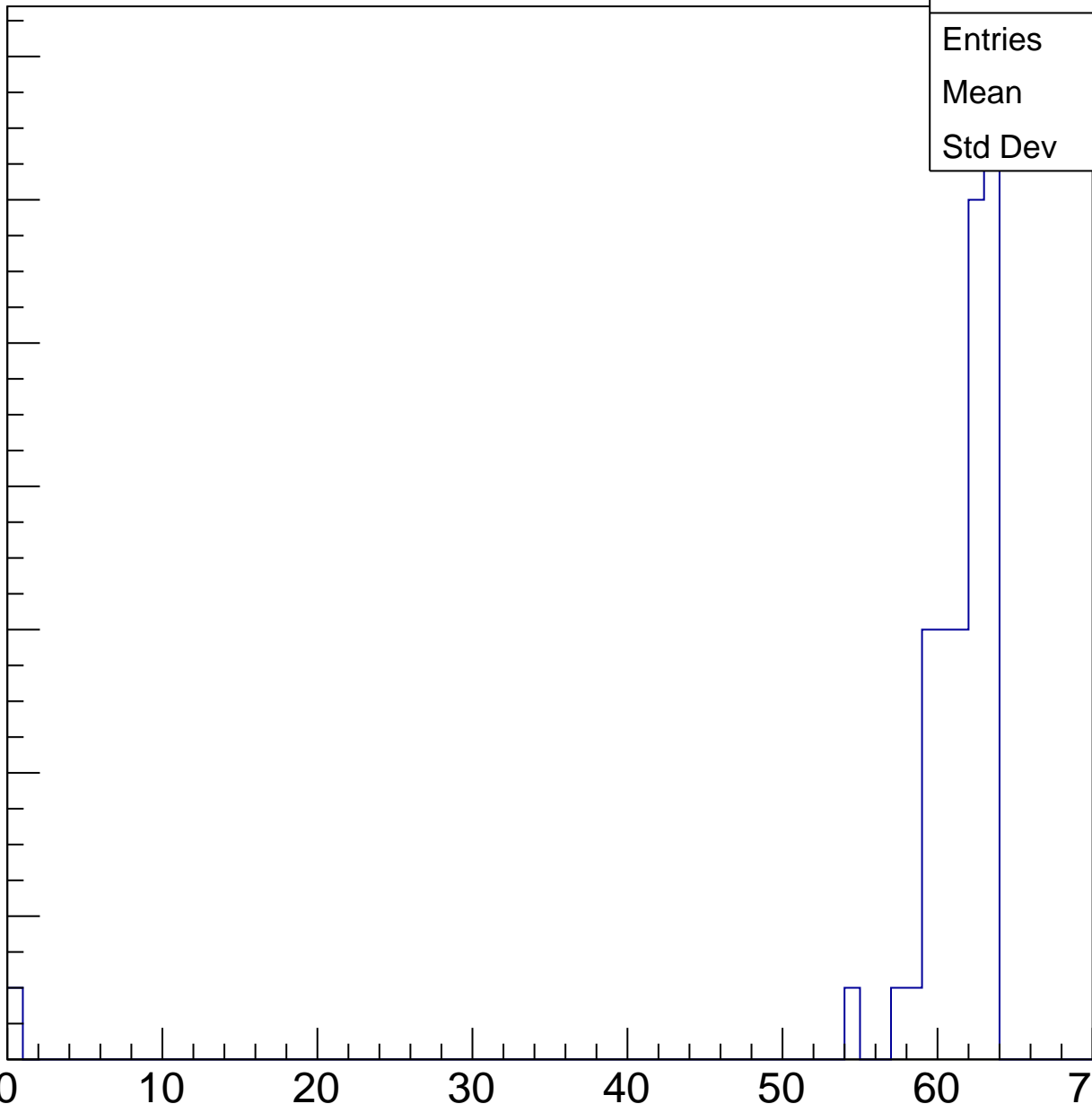
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	48
Mean	59.9
Std Dev	8.938

ampl



B1L001S, U22-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch55, adc7

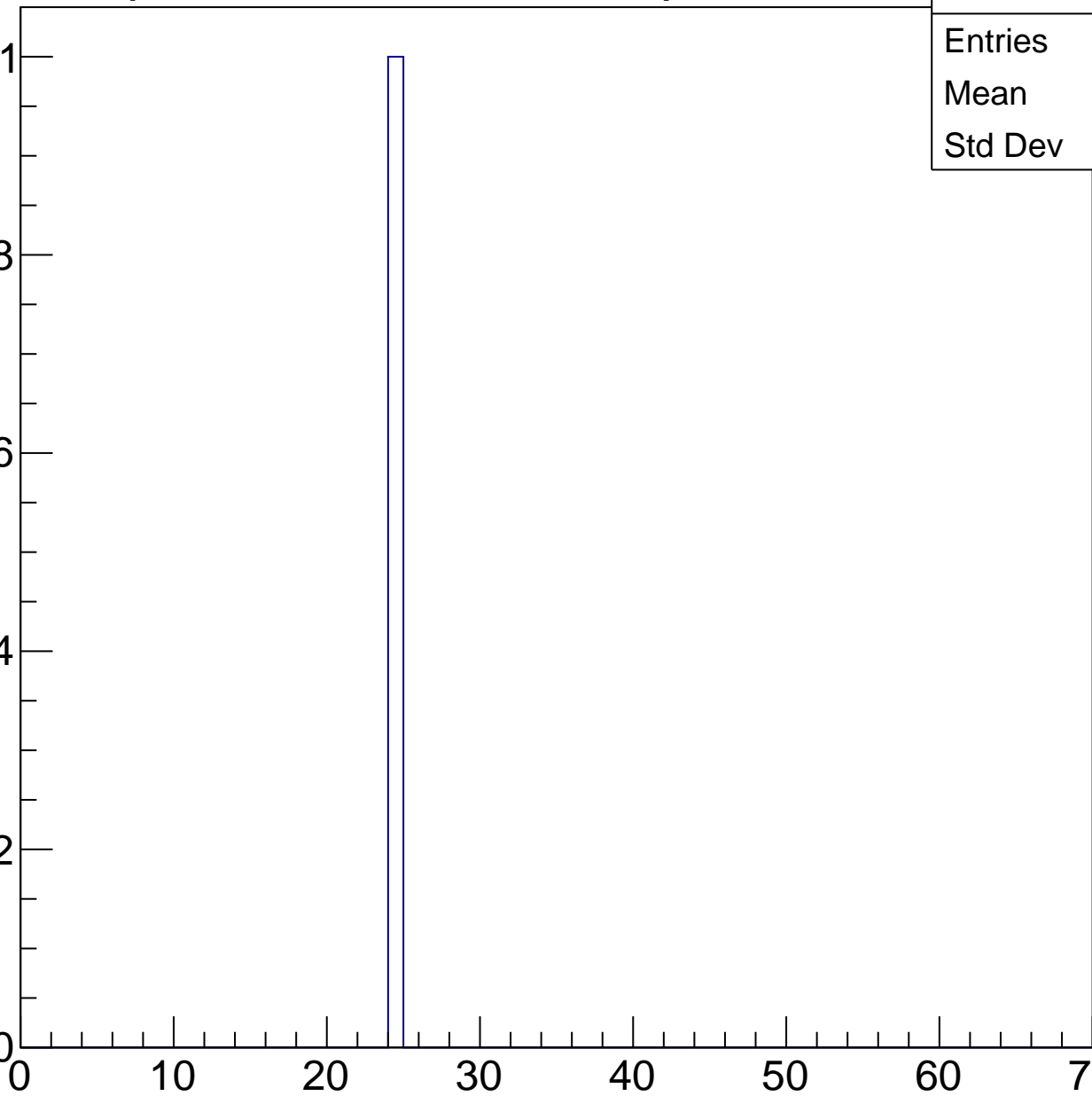
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U22-ch56, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	30.01
Std Dev	4.281

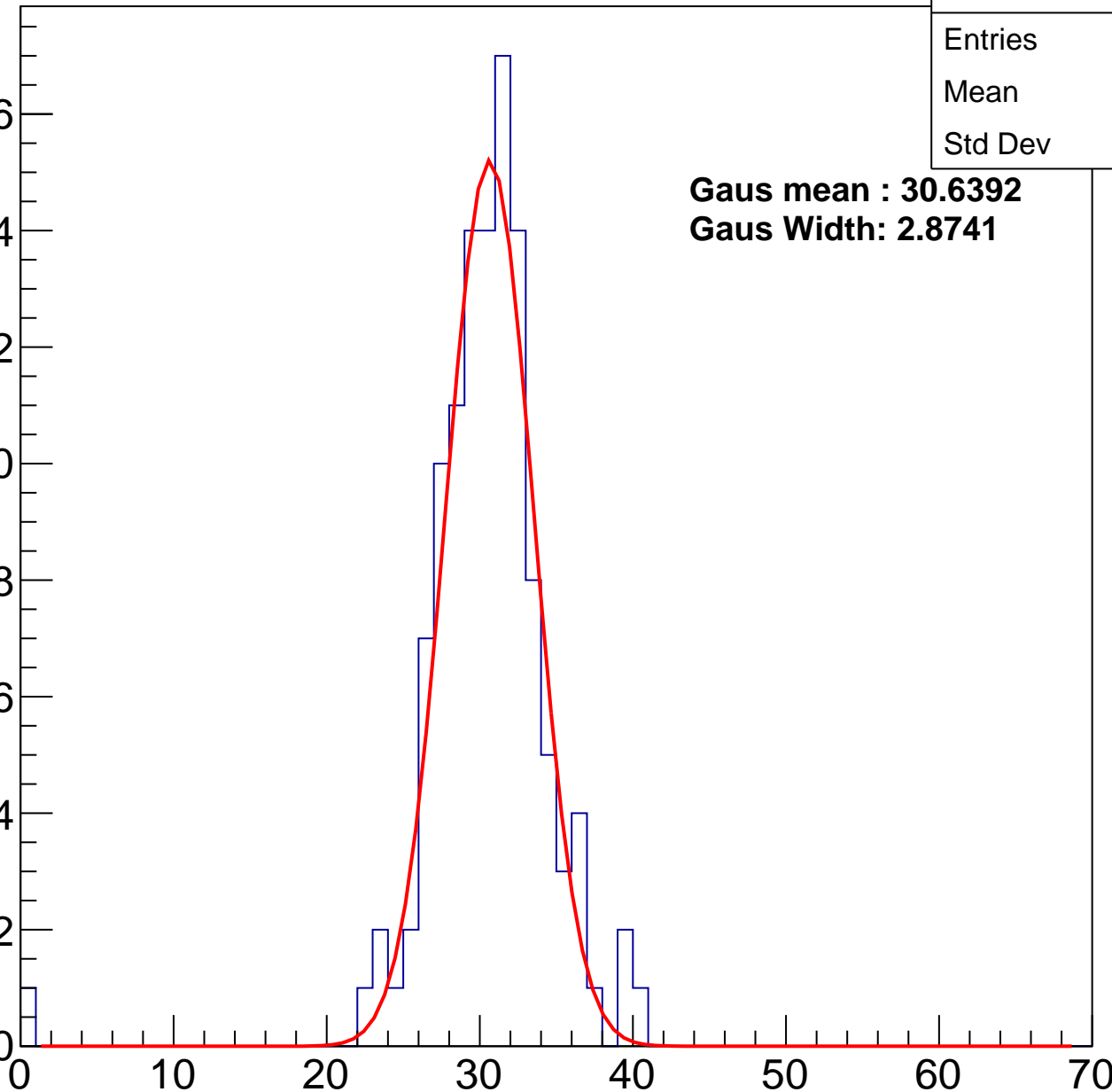
Gaus mean : 30.6392

Gaus Width: 2.8741

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch56, adc1

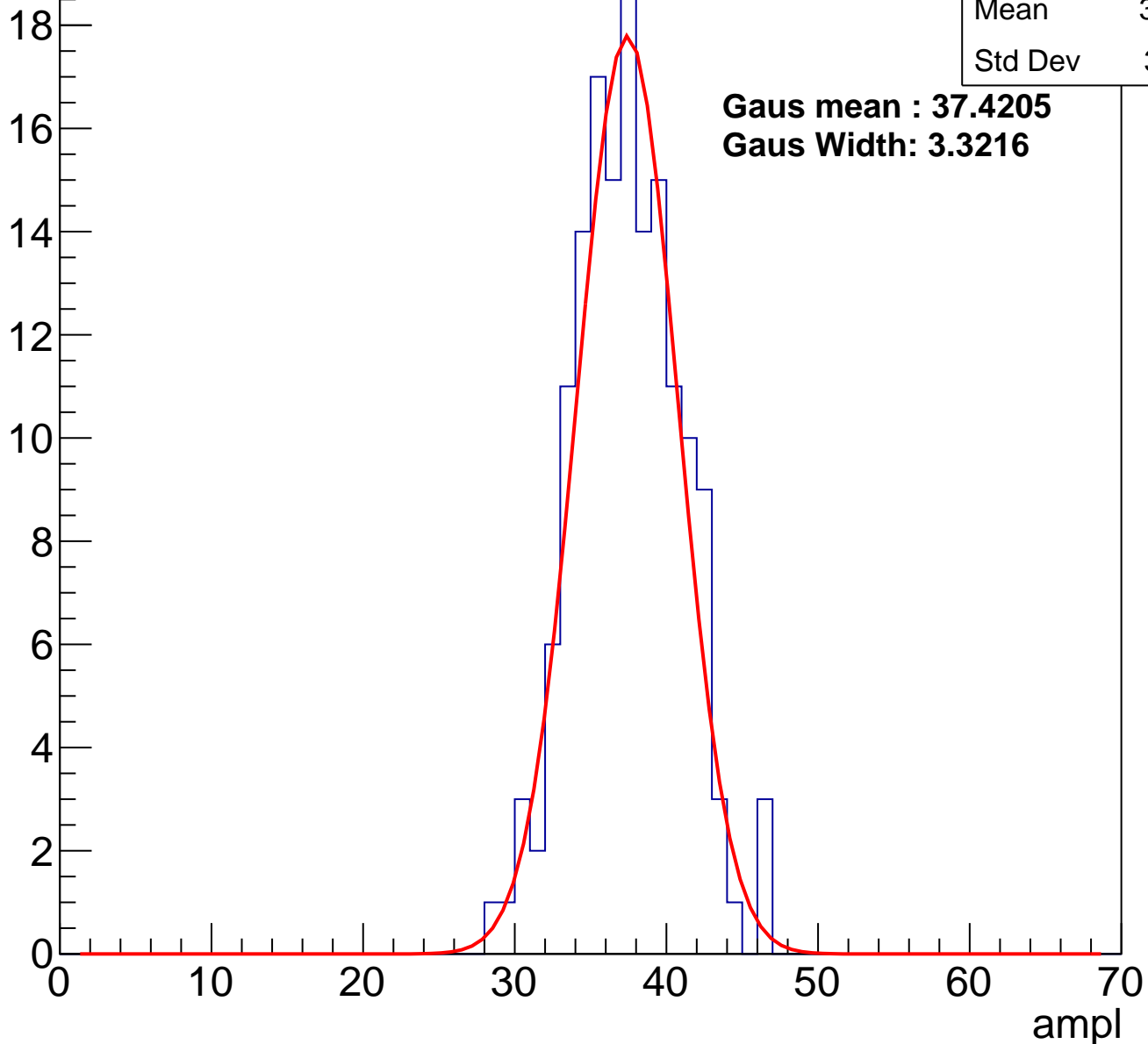
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.99
Std Dev	3.461

Gaus mean : 37.4205

Gaus Width: 3.3216

Entry



B1L001S, U22-ch56, adc2

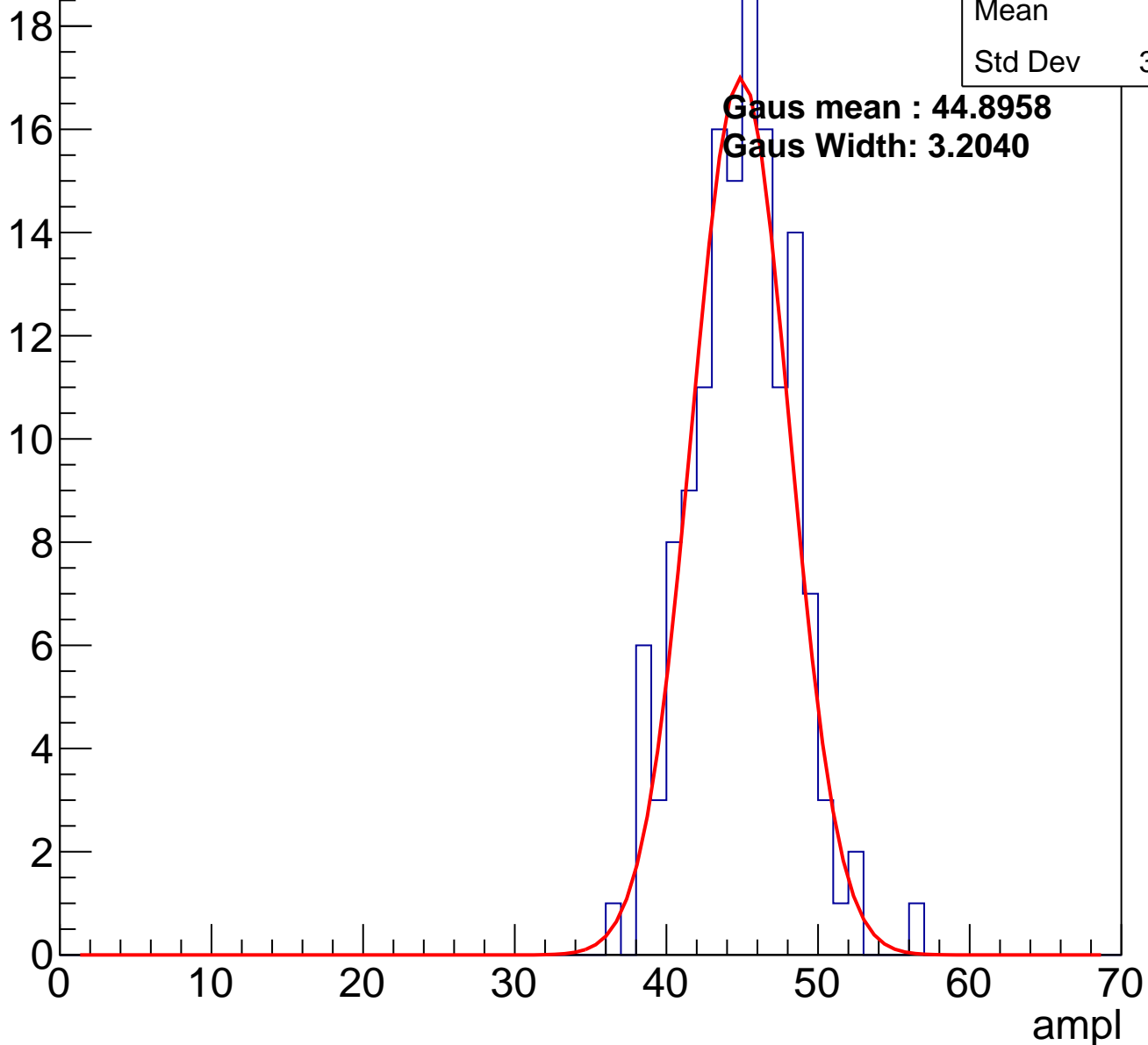
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	44.5
Std Dev	3.343

Gaus mean : 44.8958

Gaus Width: 3.2040

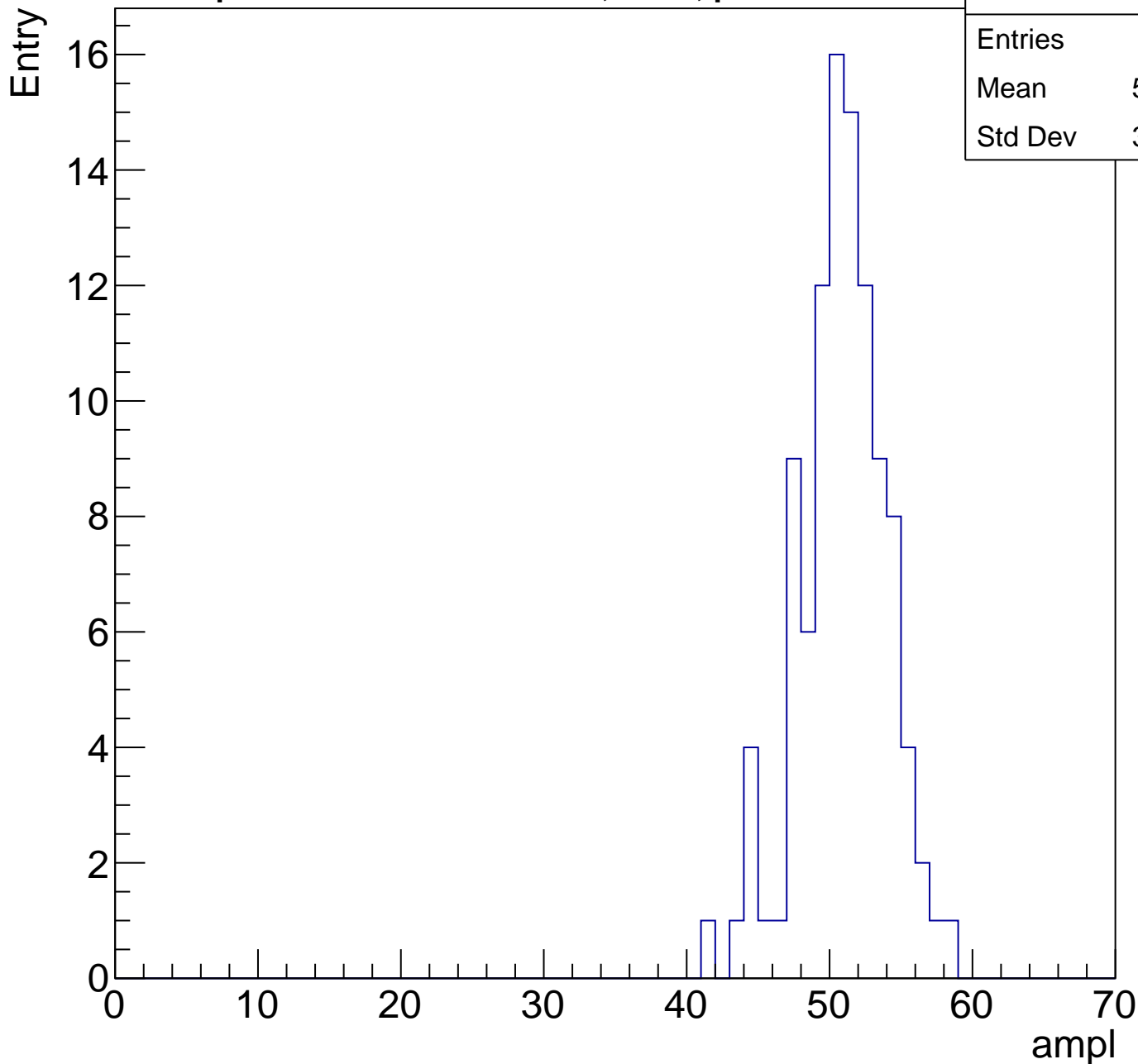
Entry



B1L001S, U22-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

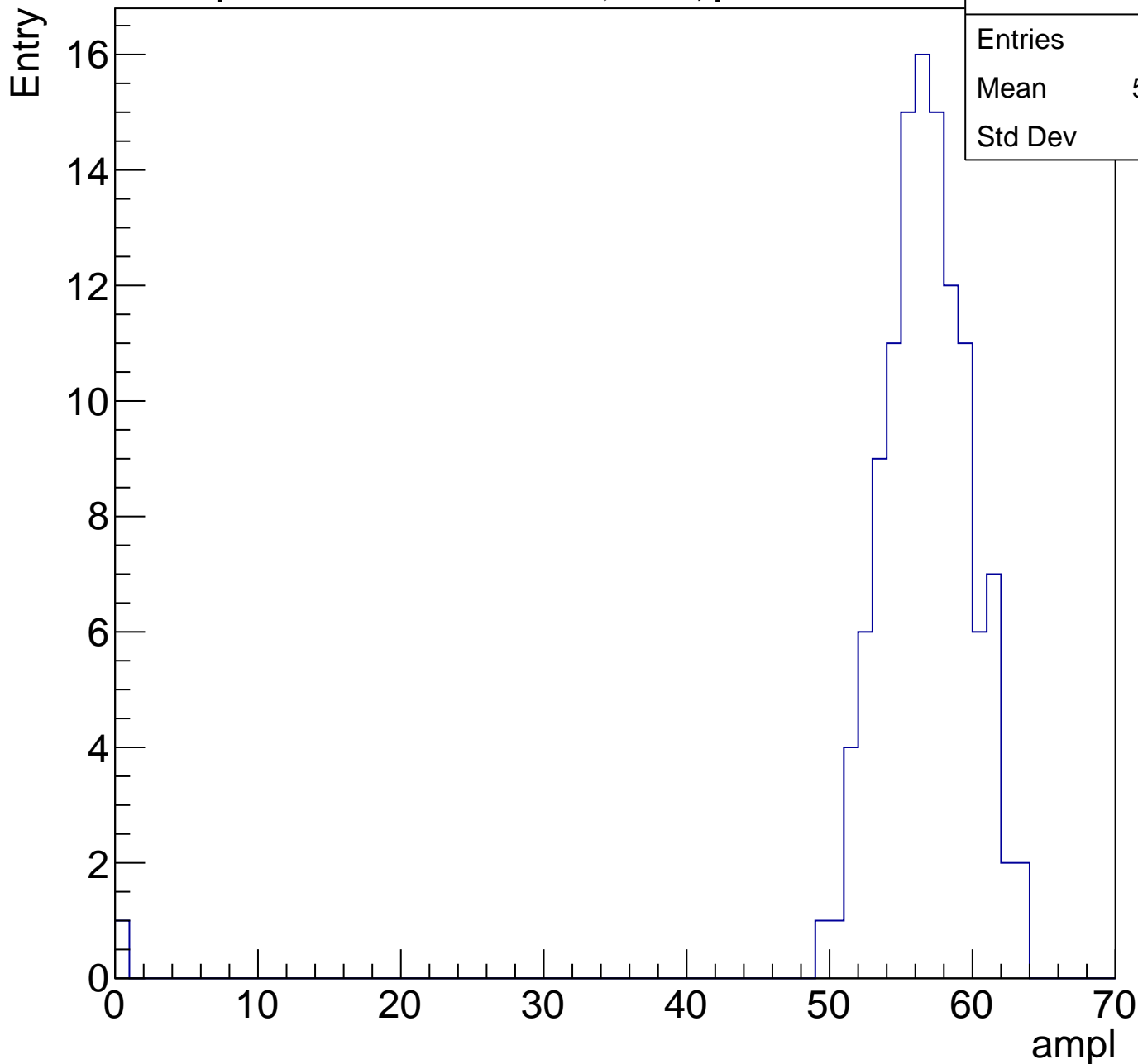
Entries	103
Mean	50.44
Std Dev	3.068



B1L001S, U22-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.83
Std Dev	5.91



B1L001S, U22-ch56, adc5

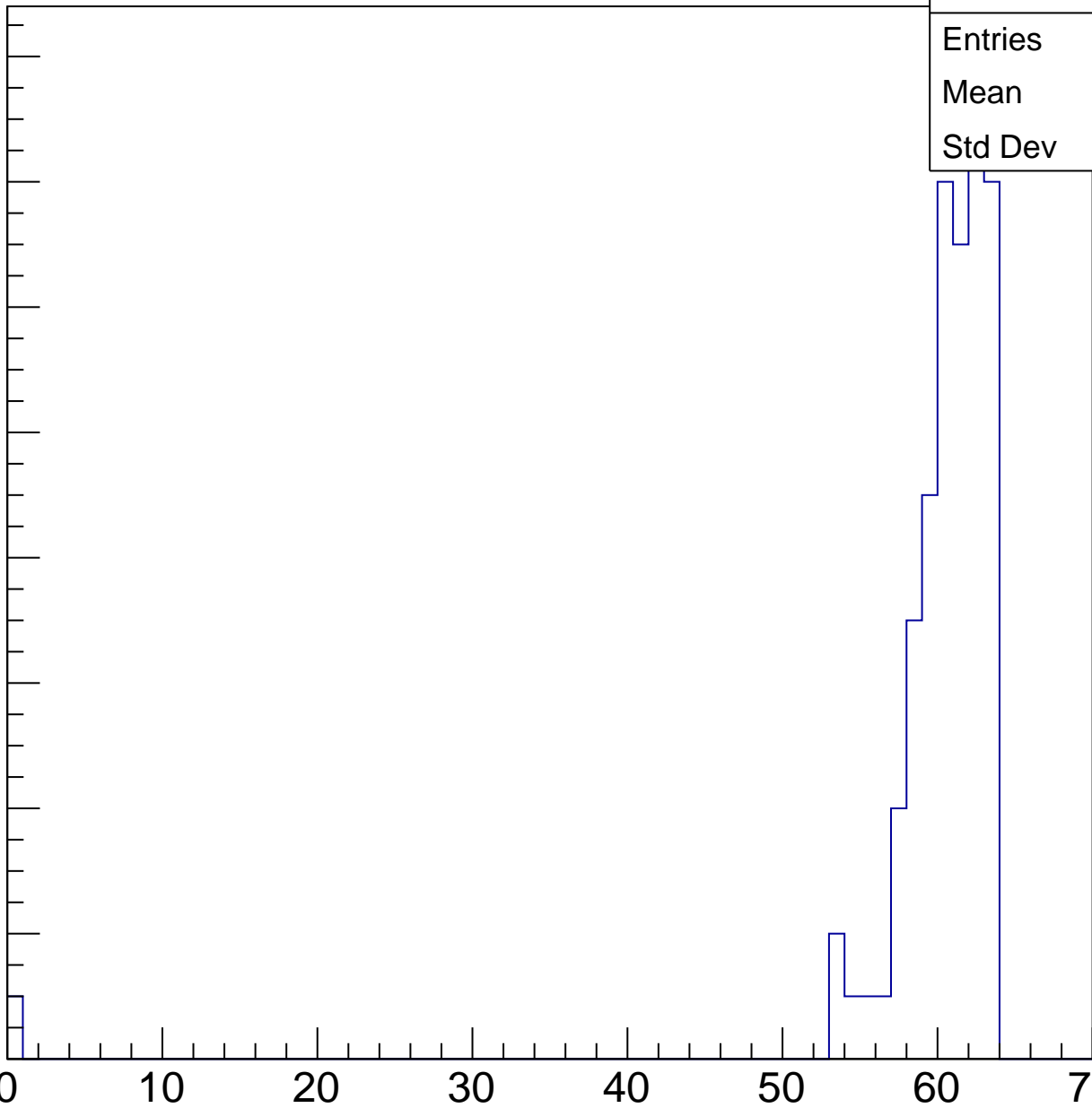
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.55
Std Dev	6.972

ampl



B1L001S, U22-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

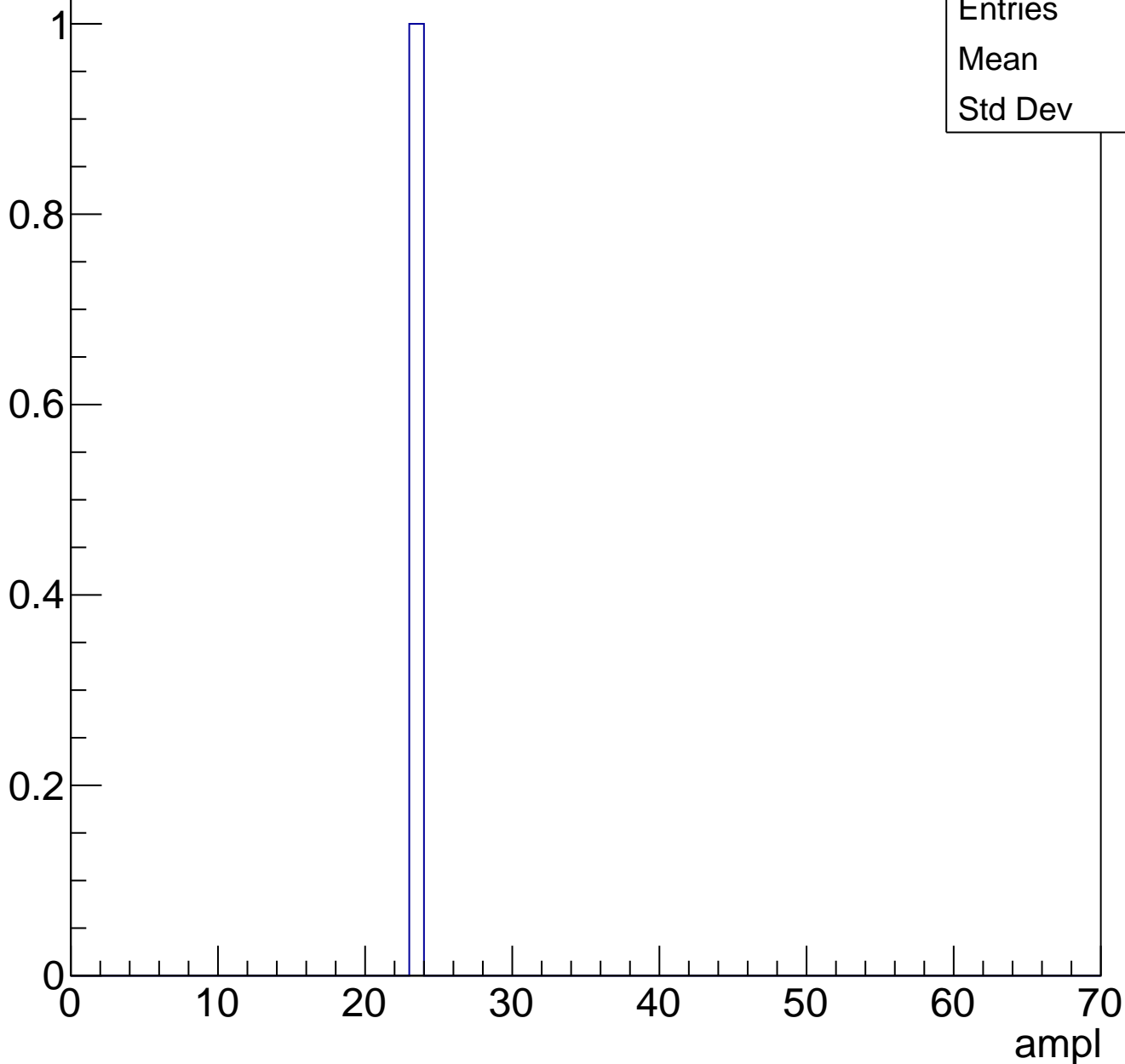
Entry



B1L001S, U22-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U22-ch57, adc0

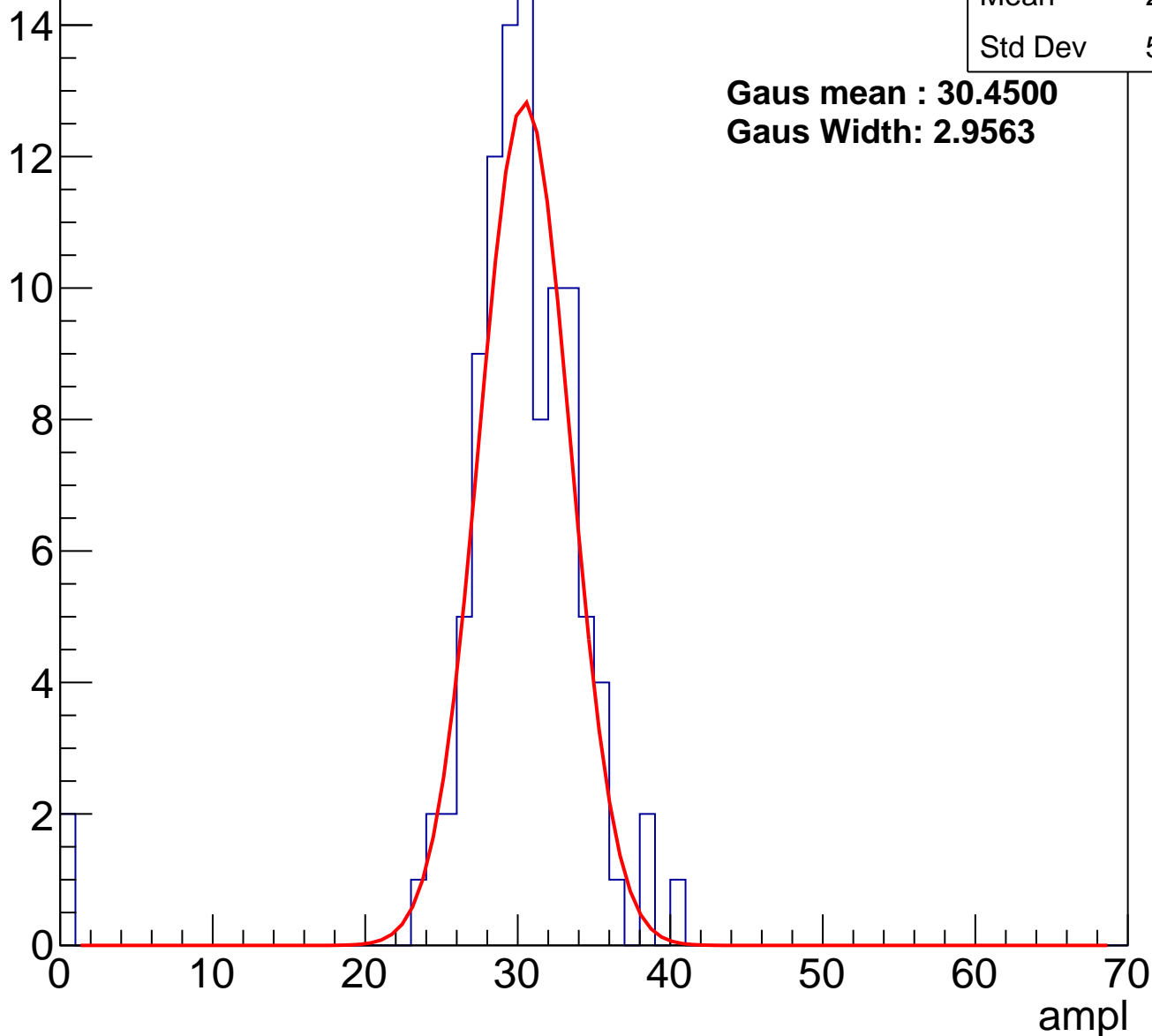
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	29.57
Std Dev	5.174

Gaus mean : 30.4500

Gaus Width: 2.9563

Entry



B1L001S, U22-ch57, adc1

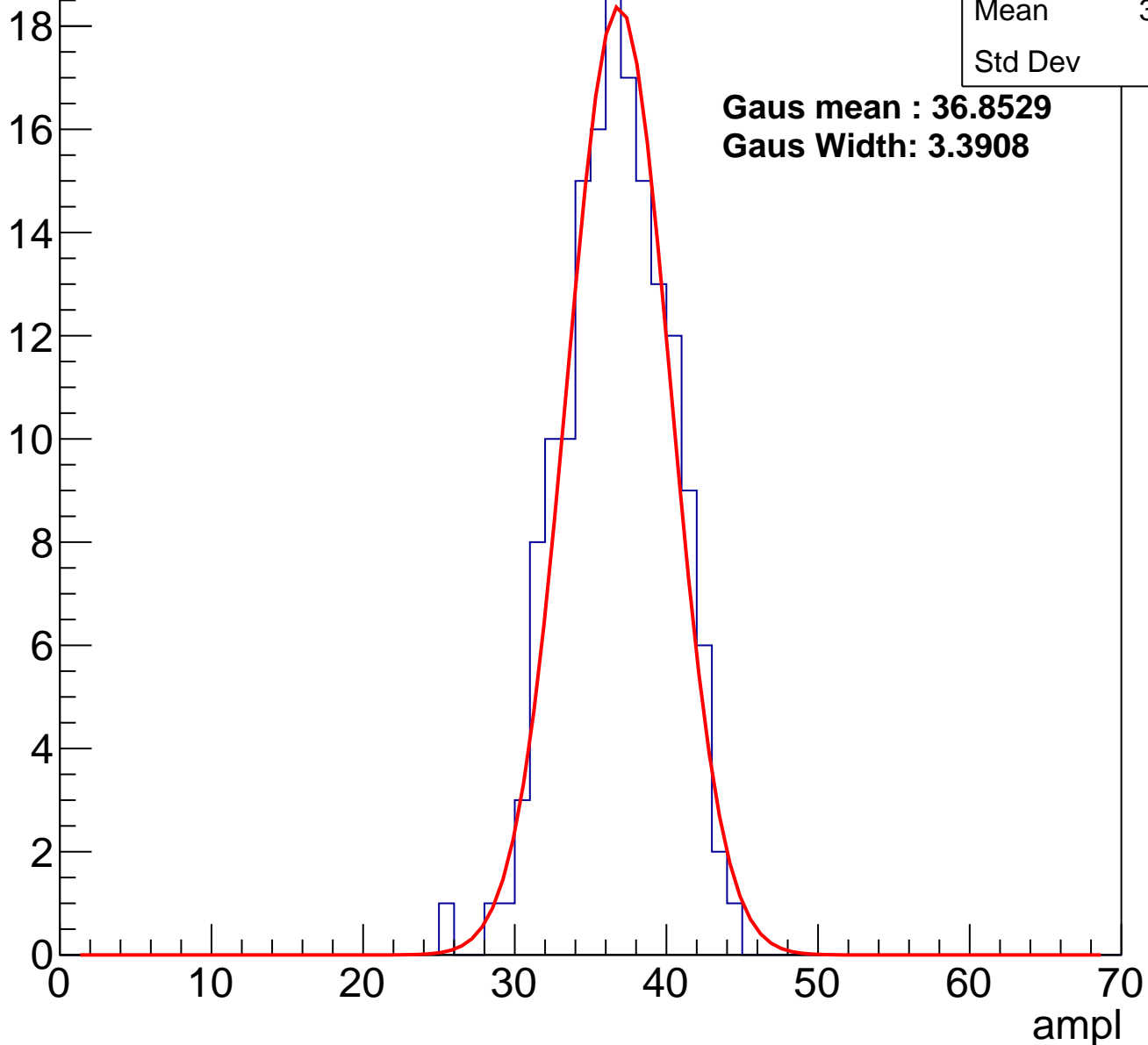
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	36.23
Std Dev	3.41

Gaus mean : 36.8529

Gaus Width: 3.3908

Entry



B1L001S, U22-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	43.75
Std Dev	3.172

Gaus mean : 44.1259

Gaus Width: 3.4461

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

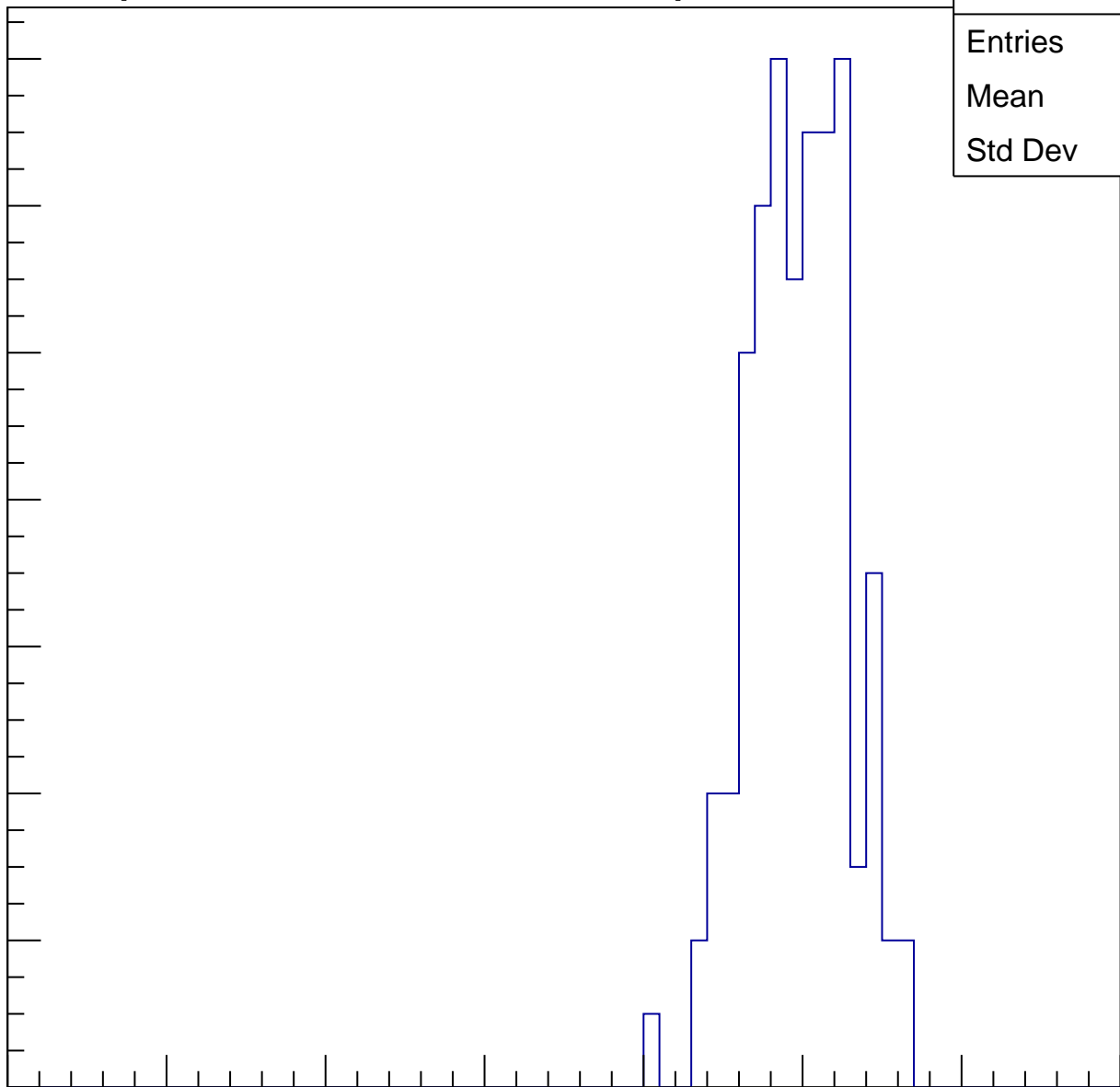
Entries	112
Mean	49.26
Std Dev	3.055

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

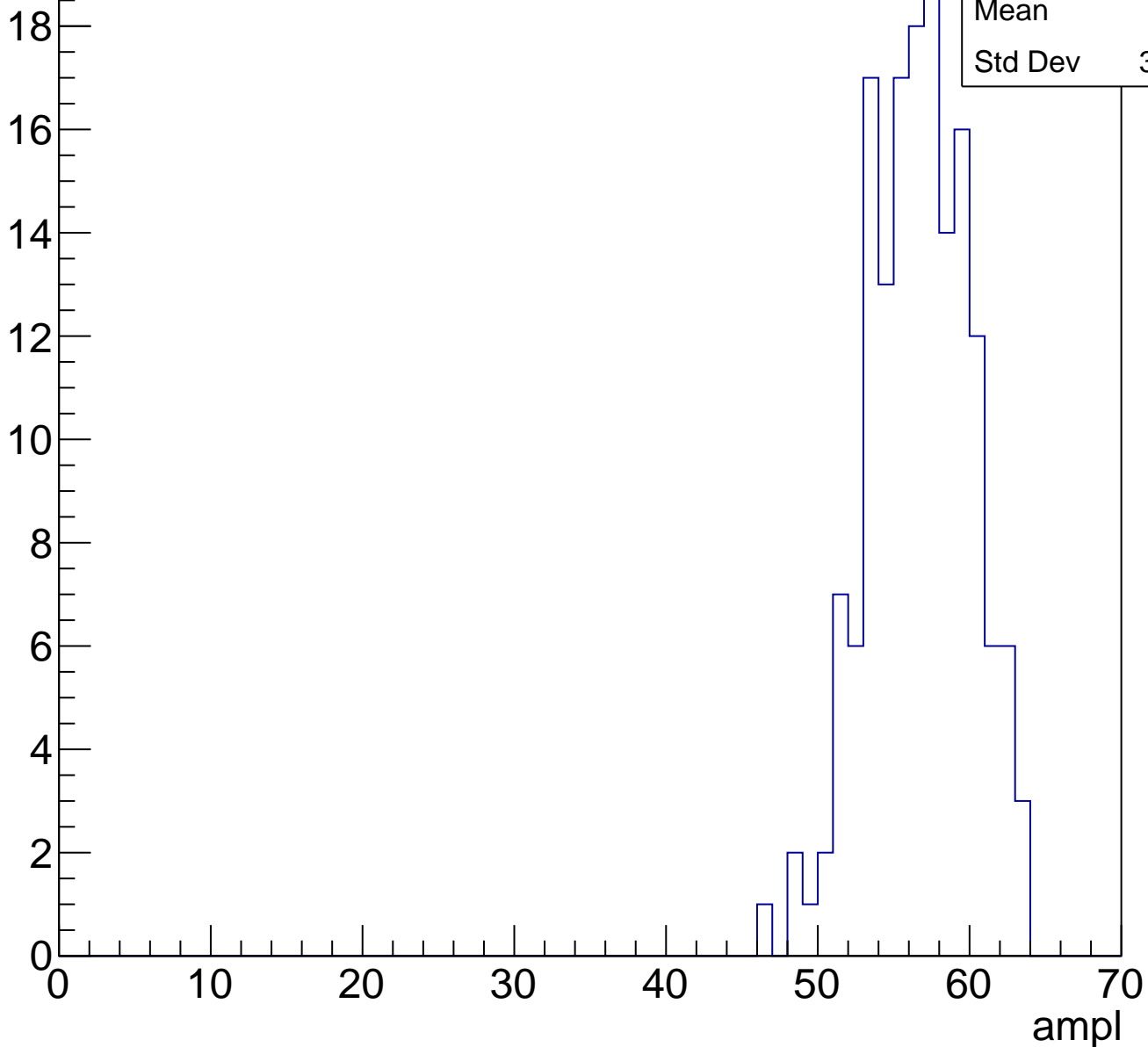
ampl



B1L001S, U22-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch57, adc5

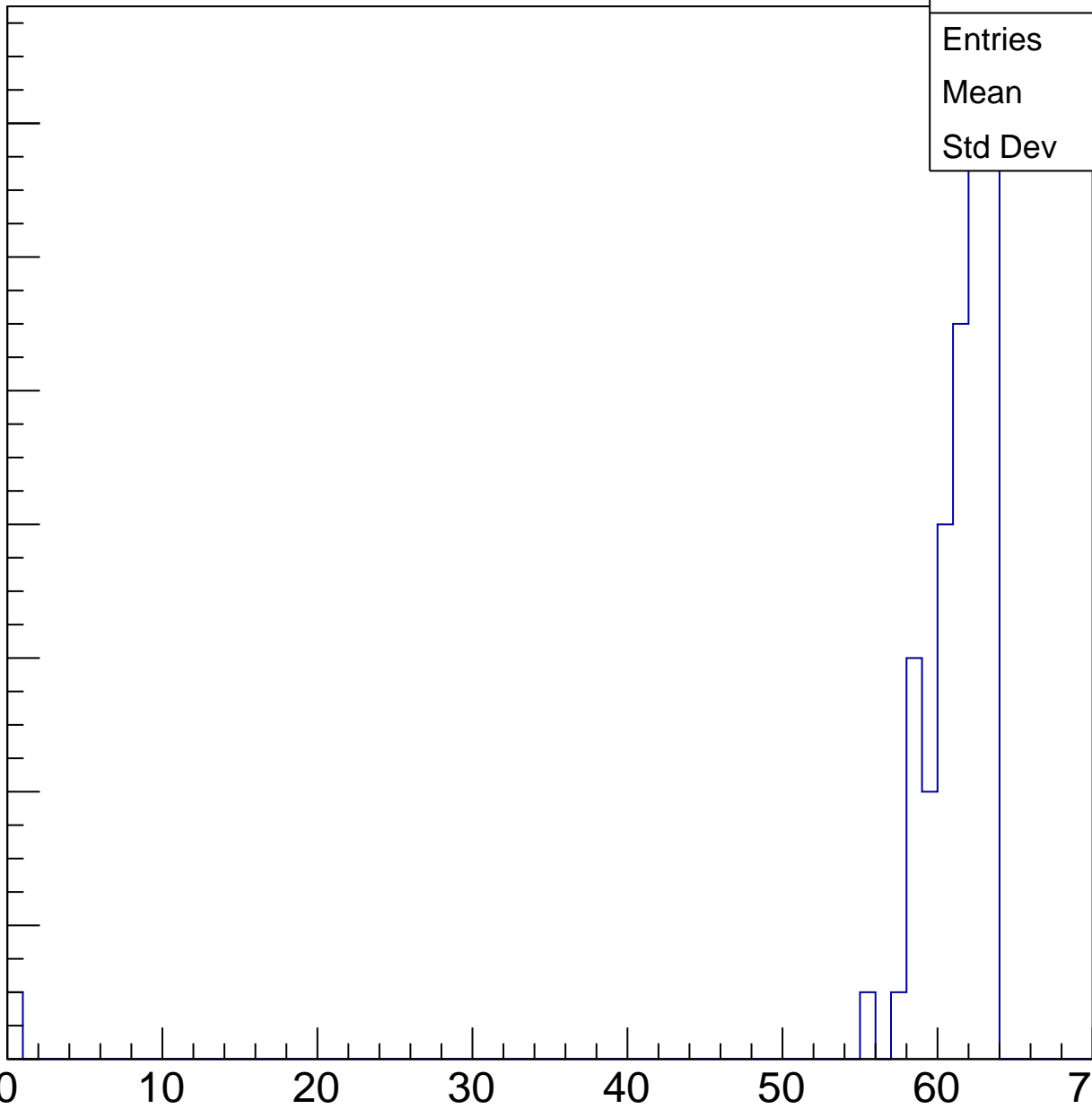
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	60
Std Dev	7.959

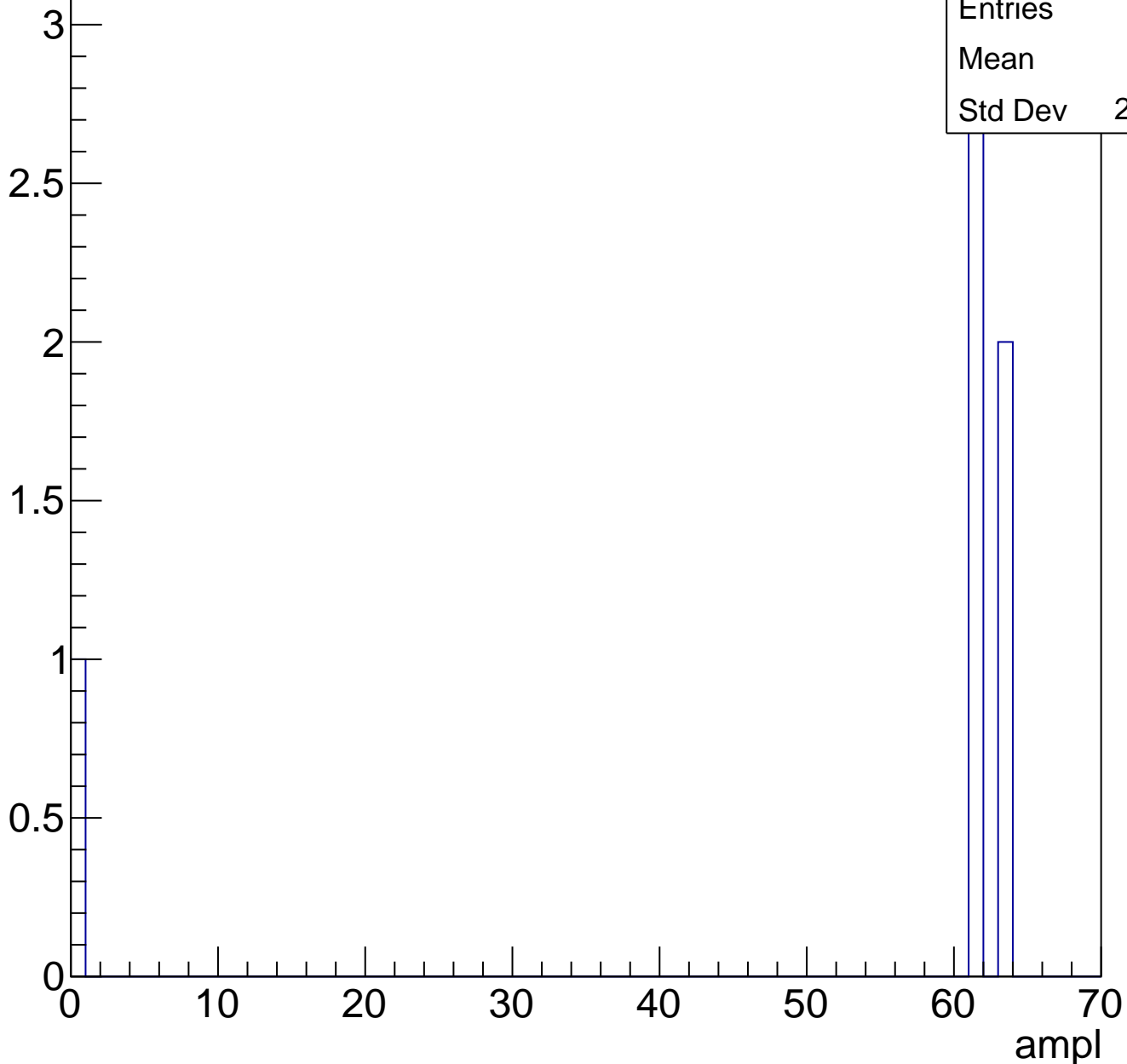
ampl



B1L001S, U22-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

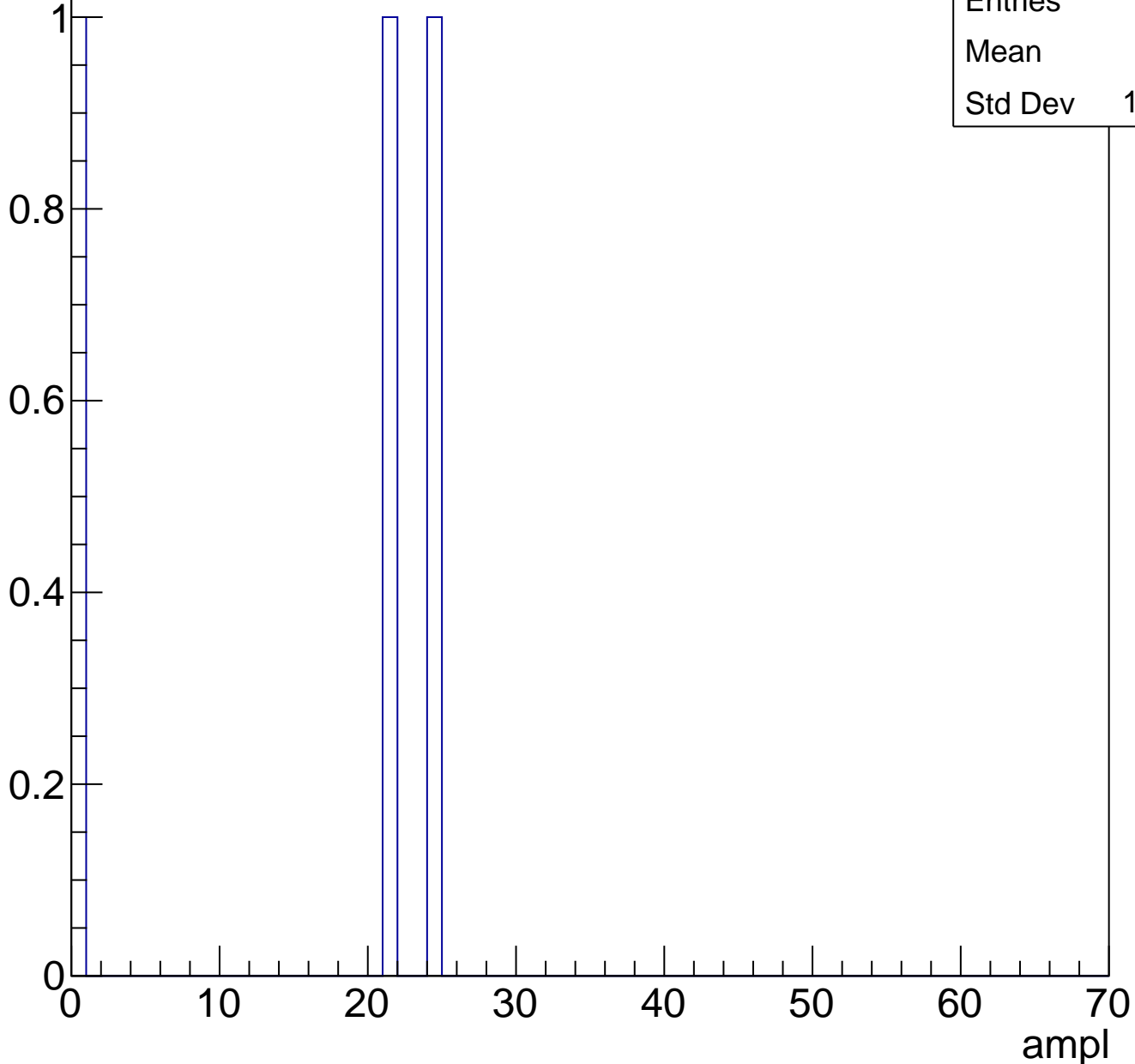


Entries	6
Mean	51.5
Std Dev	23.05

B1L001S, U22-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



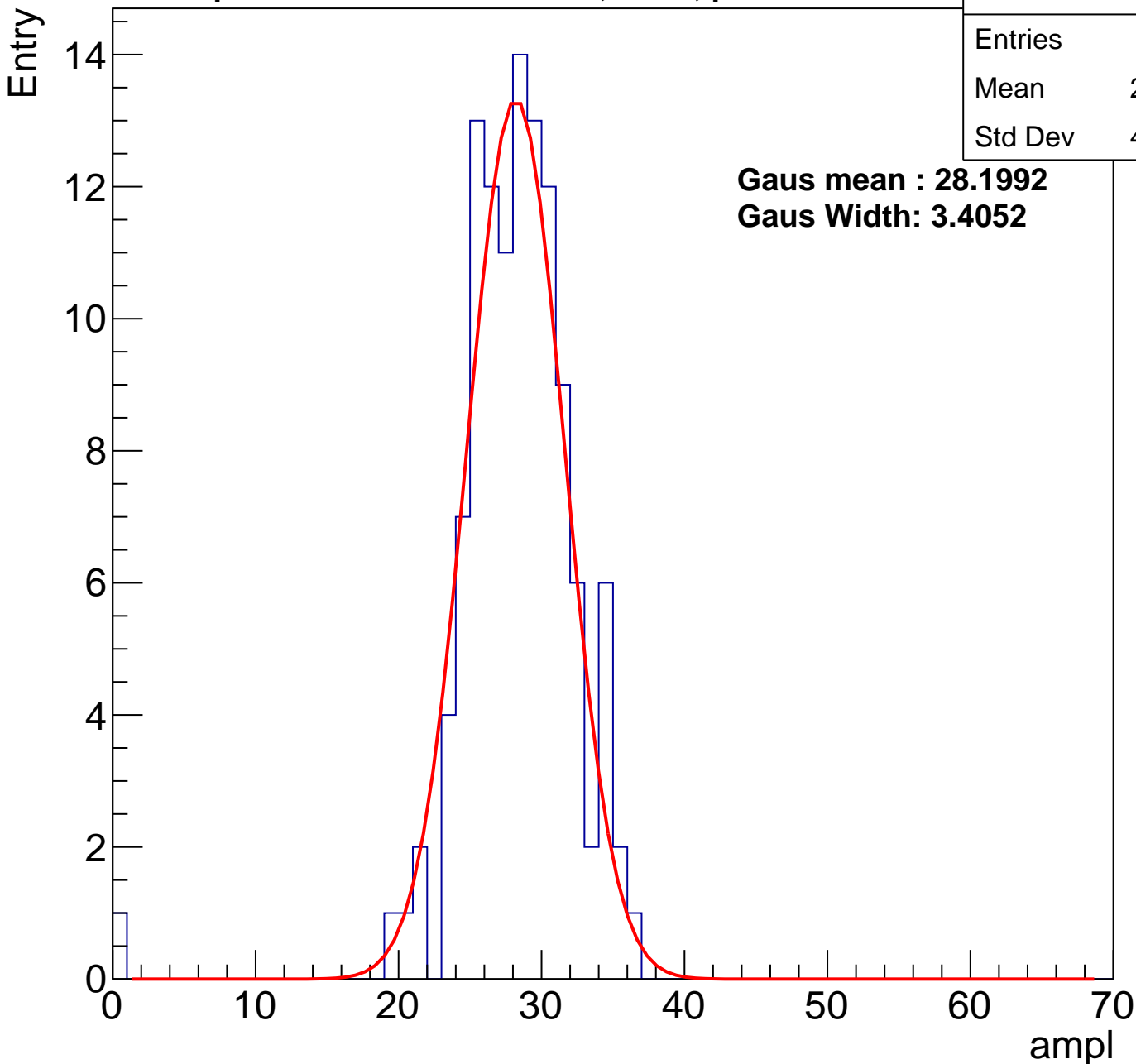
B1L001S, U22-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	27.79
Std Dev	4.212

Gaus mean : 28.1992

Gaus Width: 3.4052



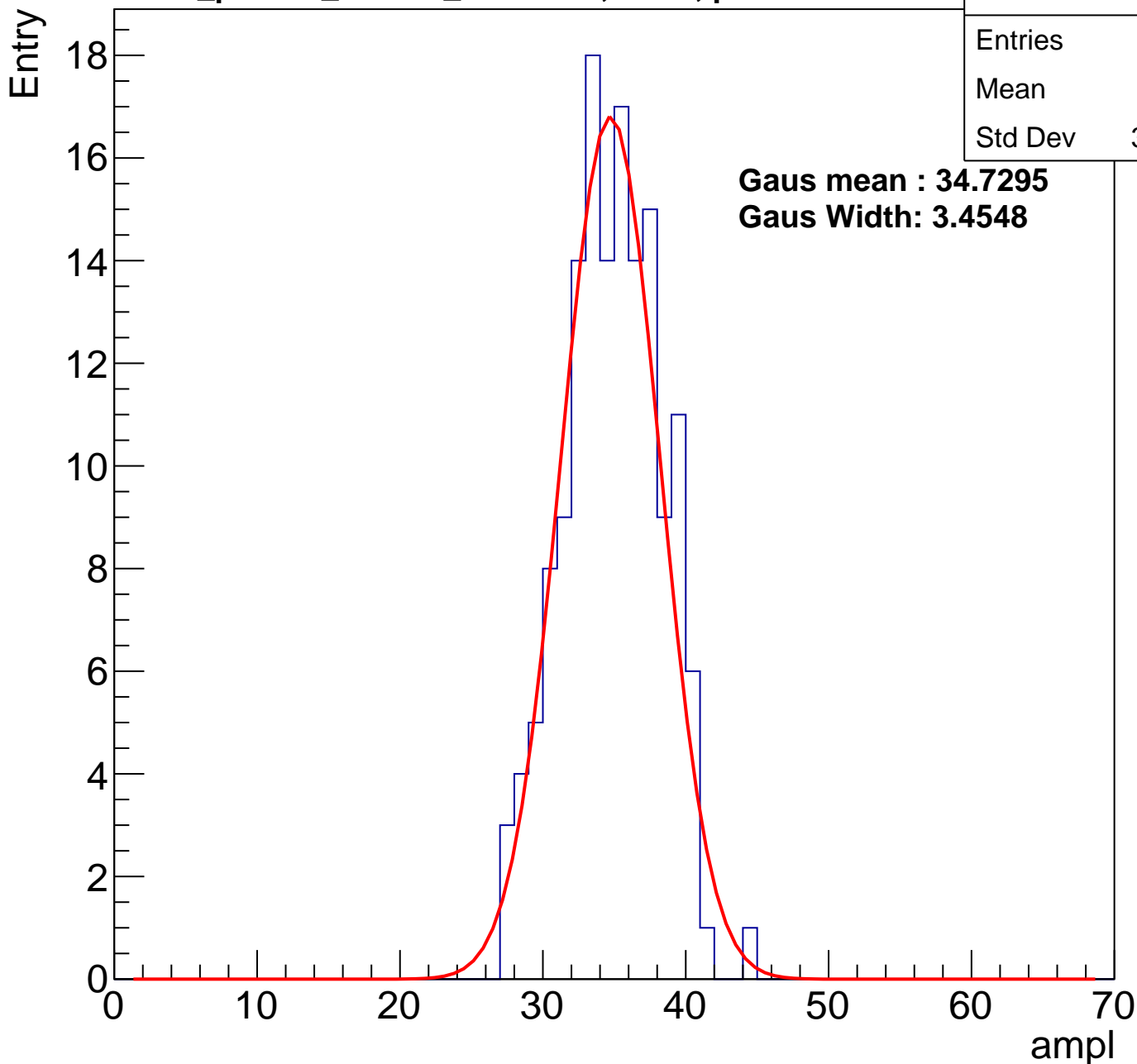
B1L001S, U22-ch58, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	34.4
Std Dev	3.345

Gaus mean : 34.7295

Gaus Width: 3.4548



B1L001S, U22-ch58, adc2

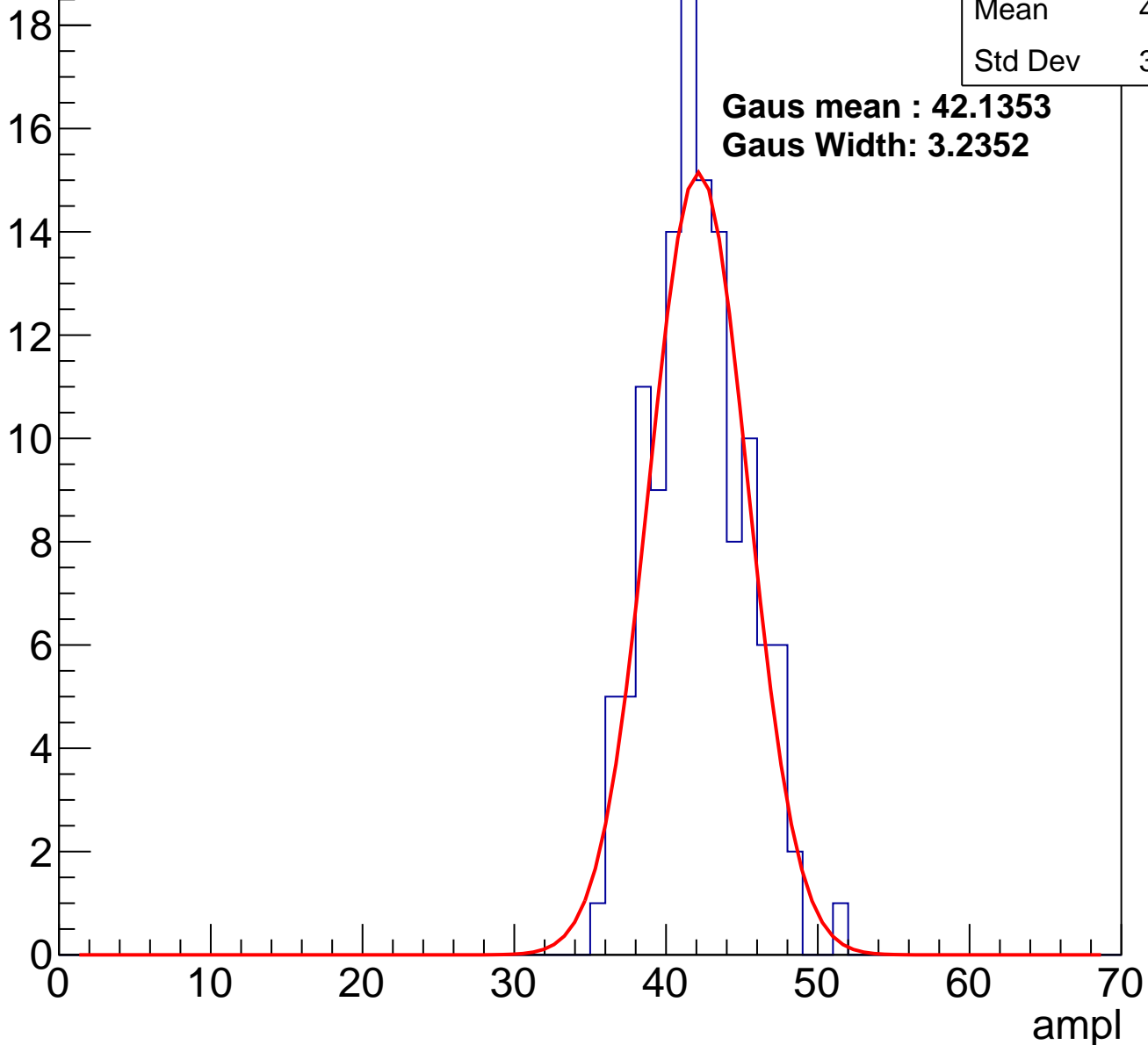
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	41.64
Std Dev	3.092

Gaus mean : 42.1353

Gaus Width: 3.2352

Entry



B1L001S, U22-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

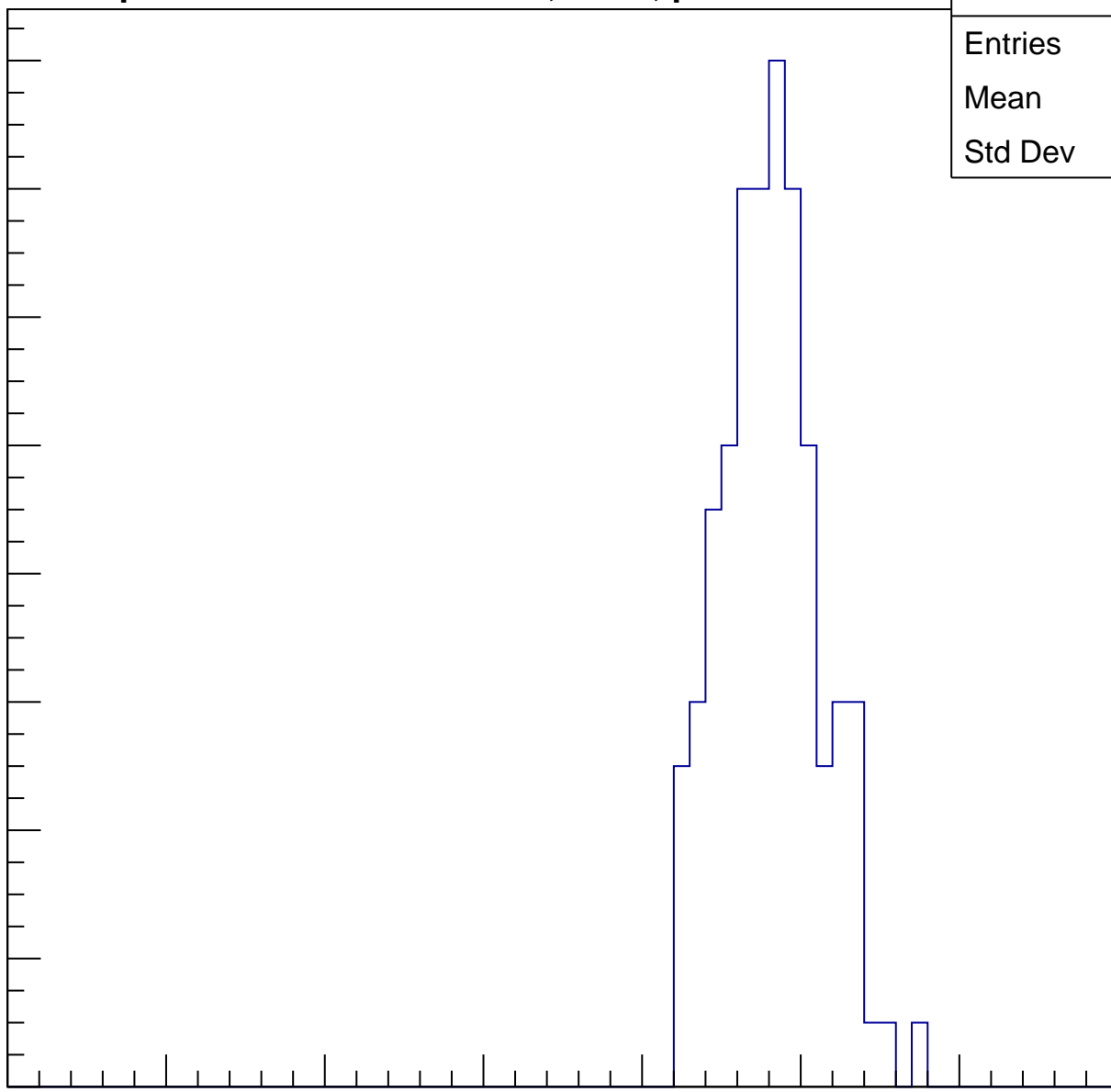
Entries	118
Mean	47.64
Std Dev	3.094

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

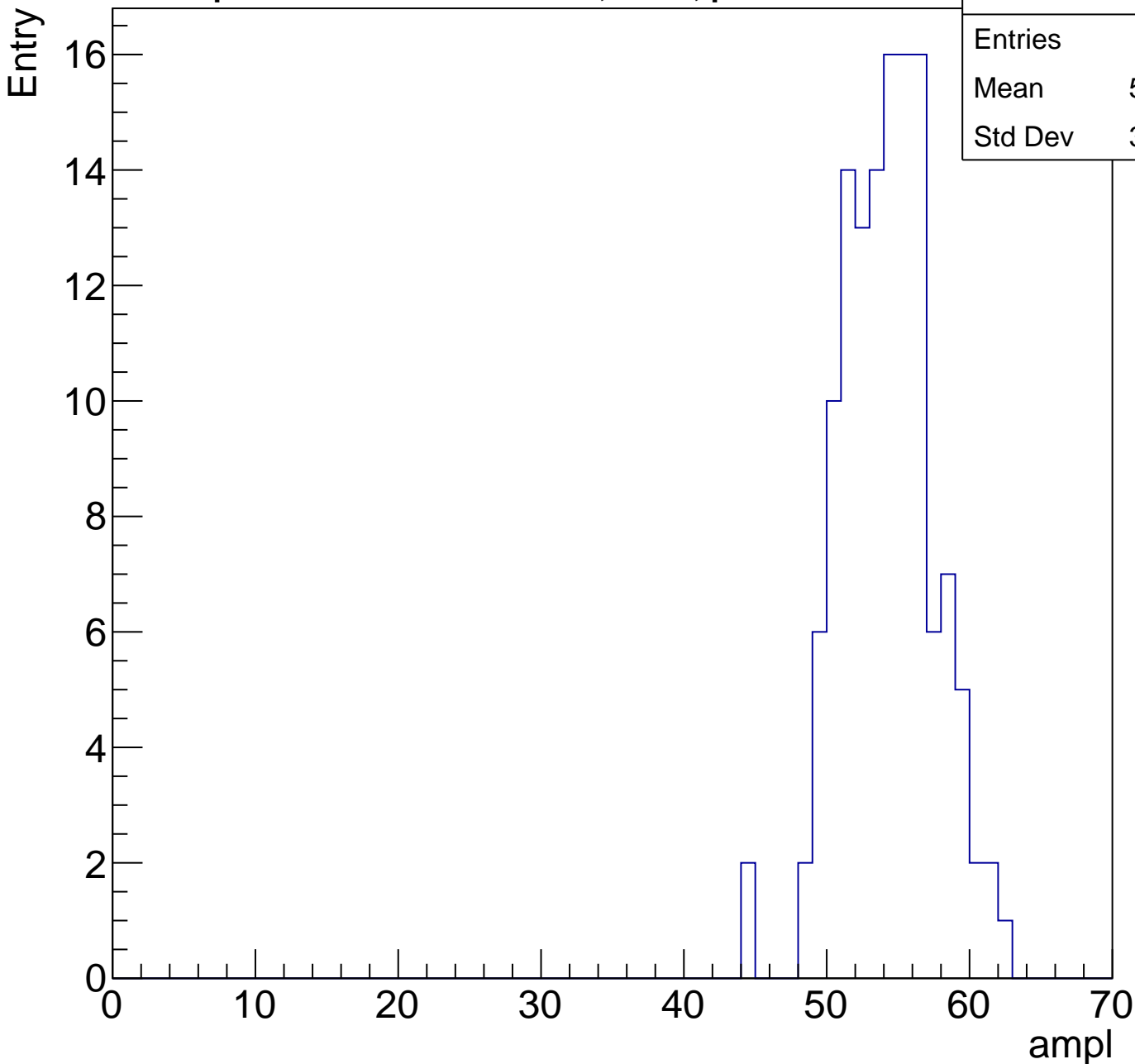
ampl



B1L001S, U22-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	53.77
Std Dev	3.224



B1L001S, U22-ch58, adc5

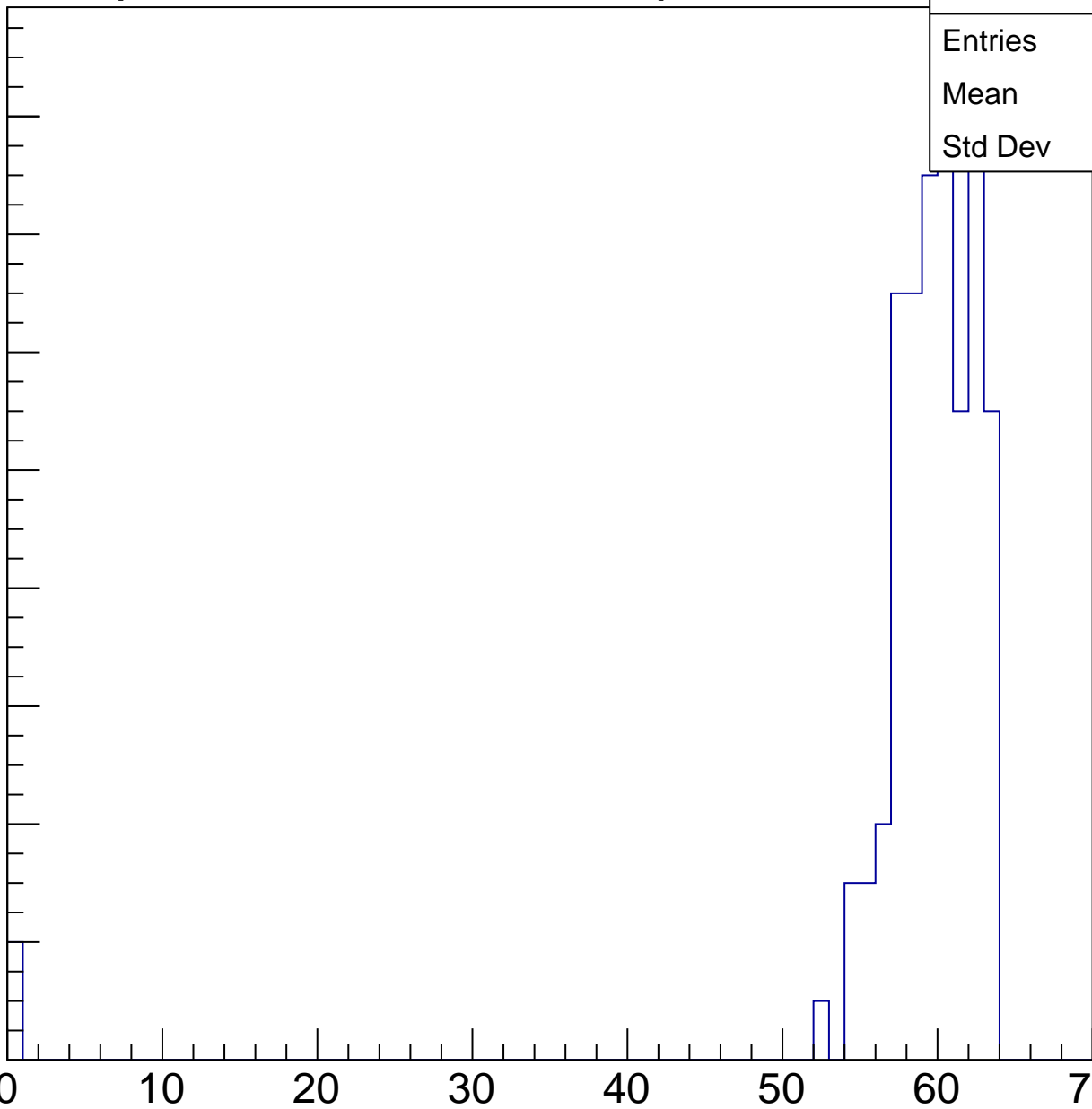
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.36
Std Dev	8.339

ampl



B1L001S, U22-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

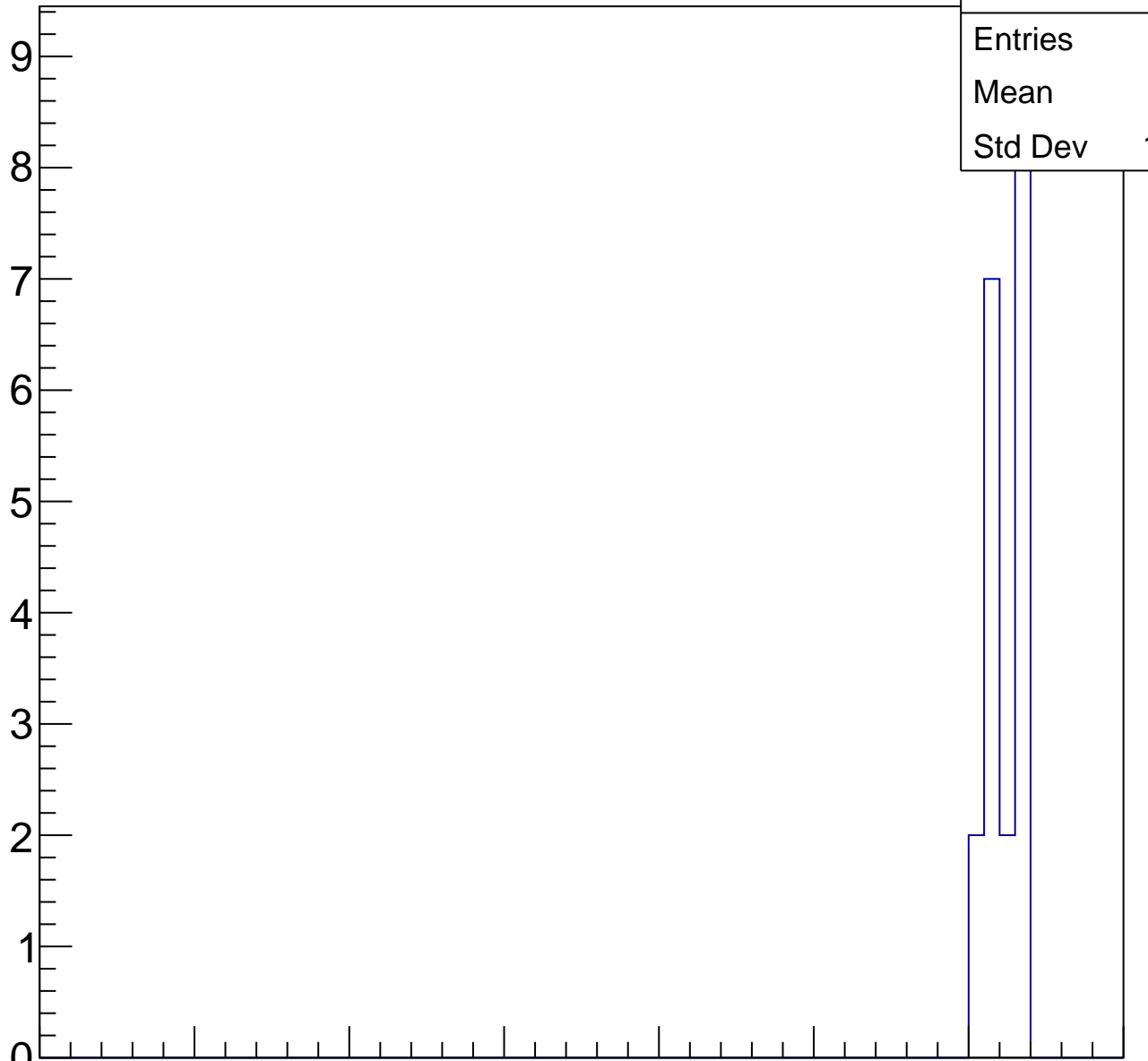
Entry

9
8
7
6
5
4
3
2
1
0

Entries	20
Mean	61.9
Std Dev	1.091

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	28.96
Std Dev	3.284

Gaus mean : 29.3721

Gaus Width: 3.5070

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

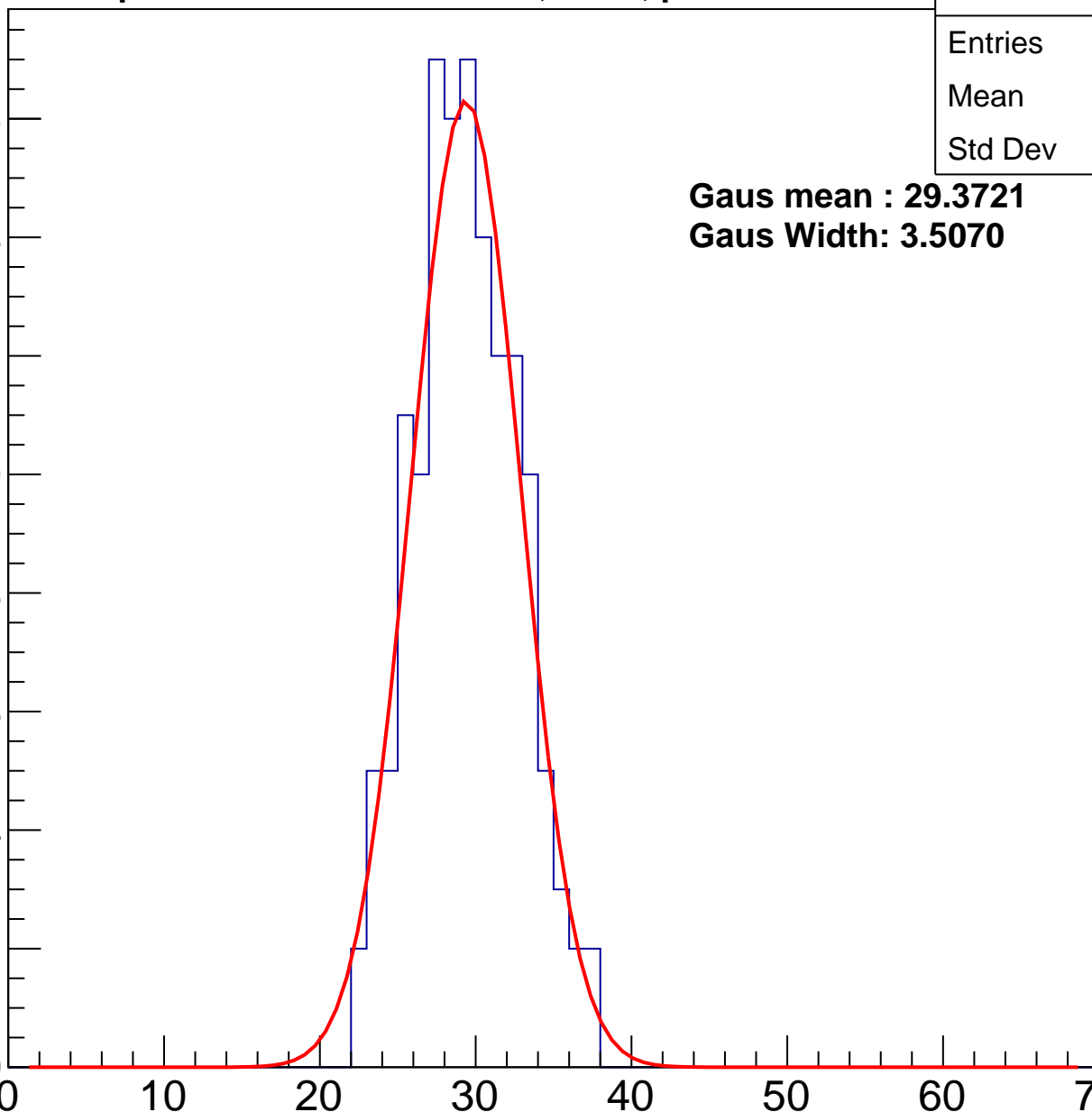
40

50

60

70

ampl



B1L001S, U22-ch59, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	36.05
Std Dev	3.723

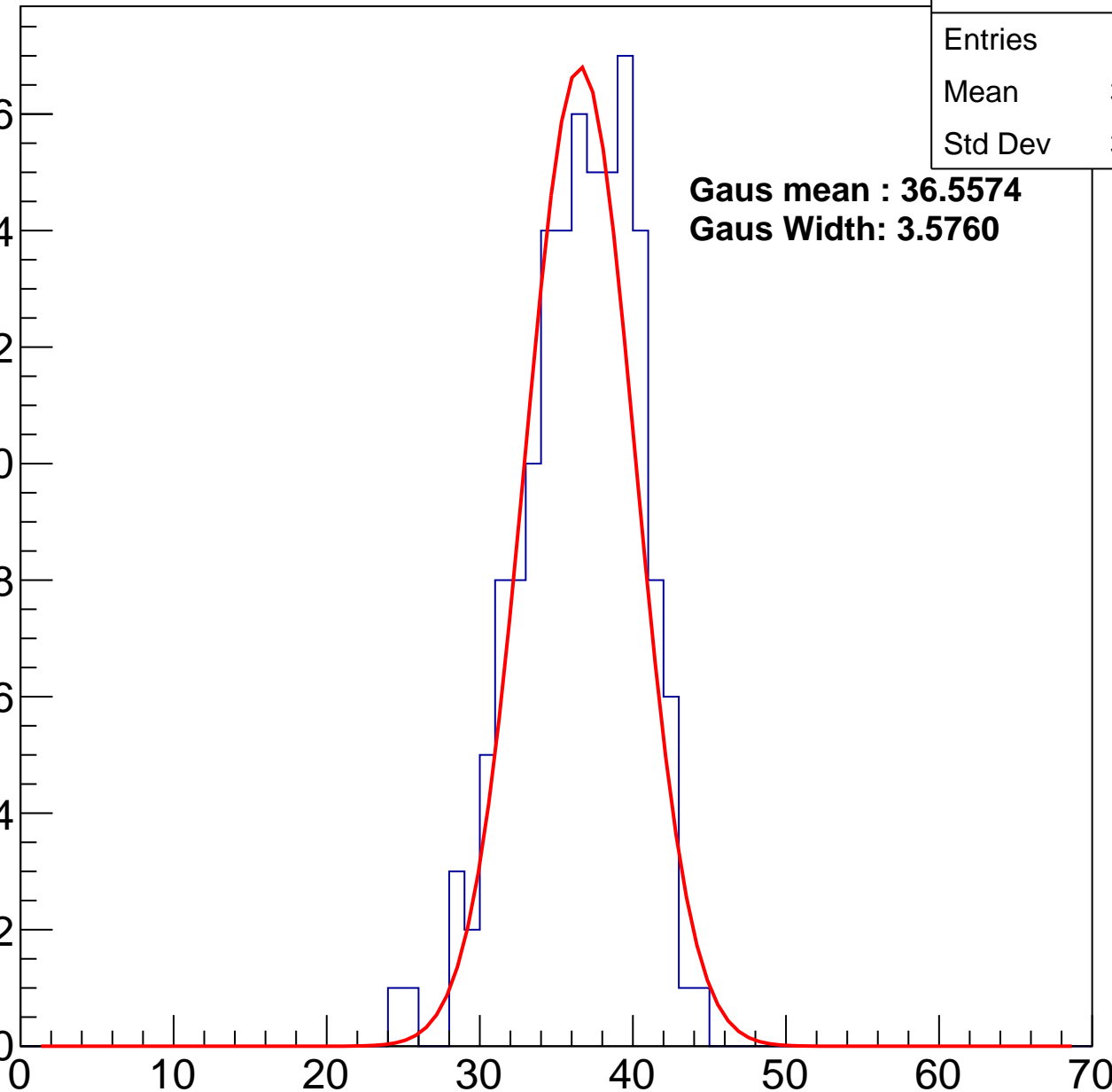
Gaus mean : 36.5574

Gaus Width: 3.5760

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch59, adc2

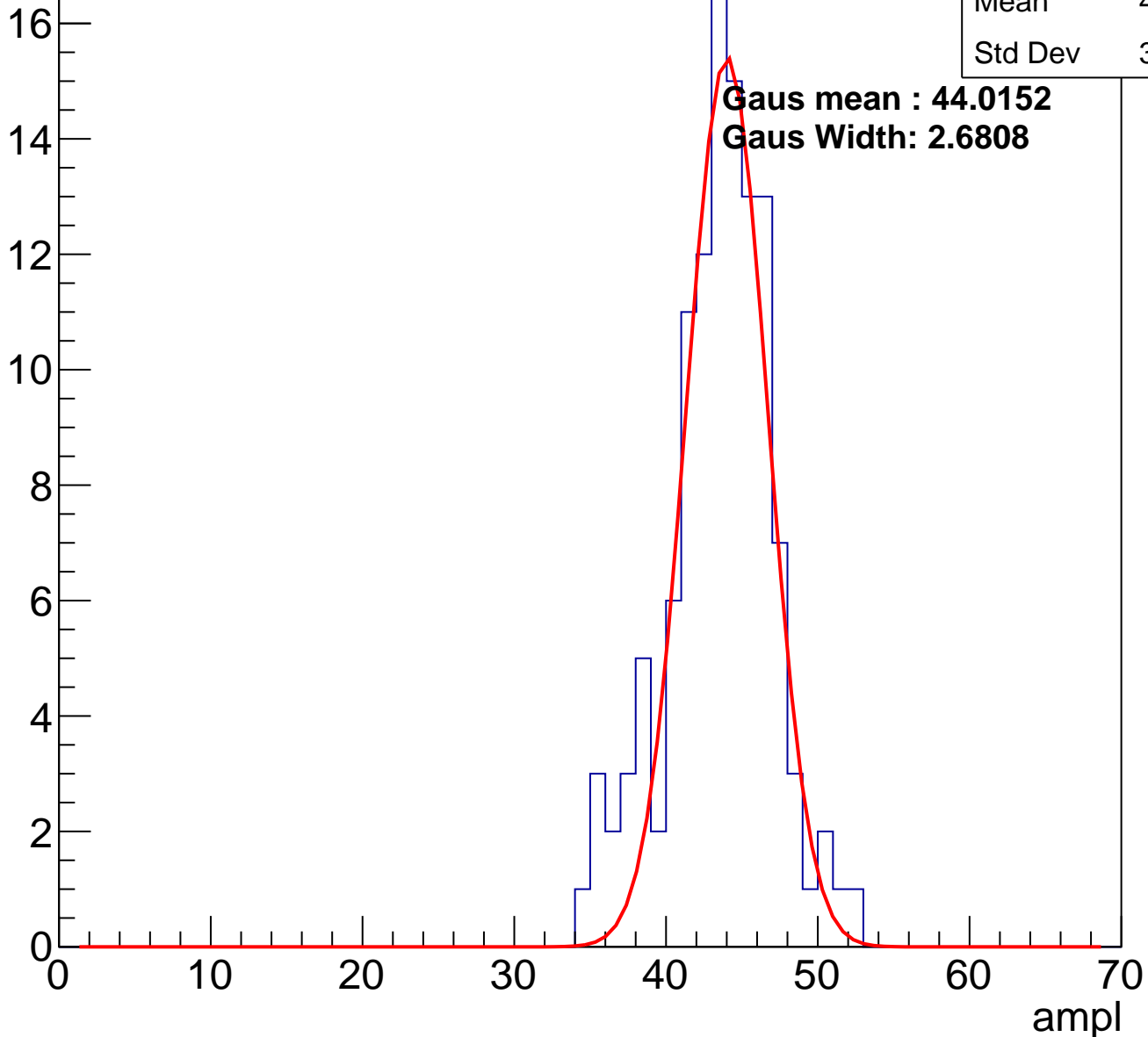
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	118
Mean	43.08
Std Dev	3.439

Gaus mean : 44.0152

Gaus Width: 2.6808



B1L001S, U22-ch59, adc3

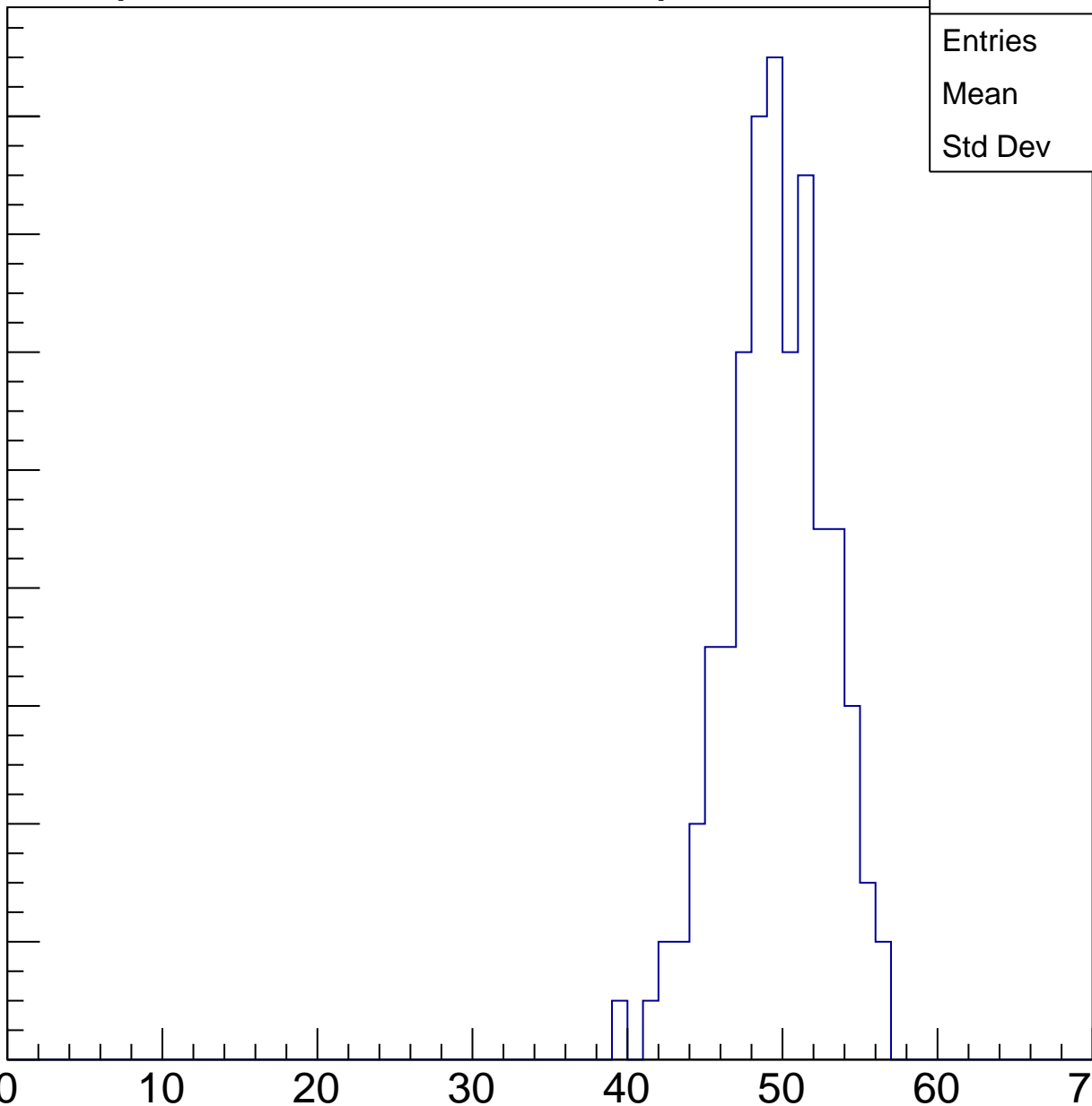
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	49.11
Std Dev	3.255

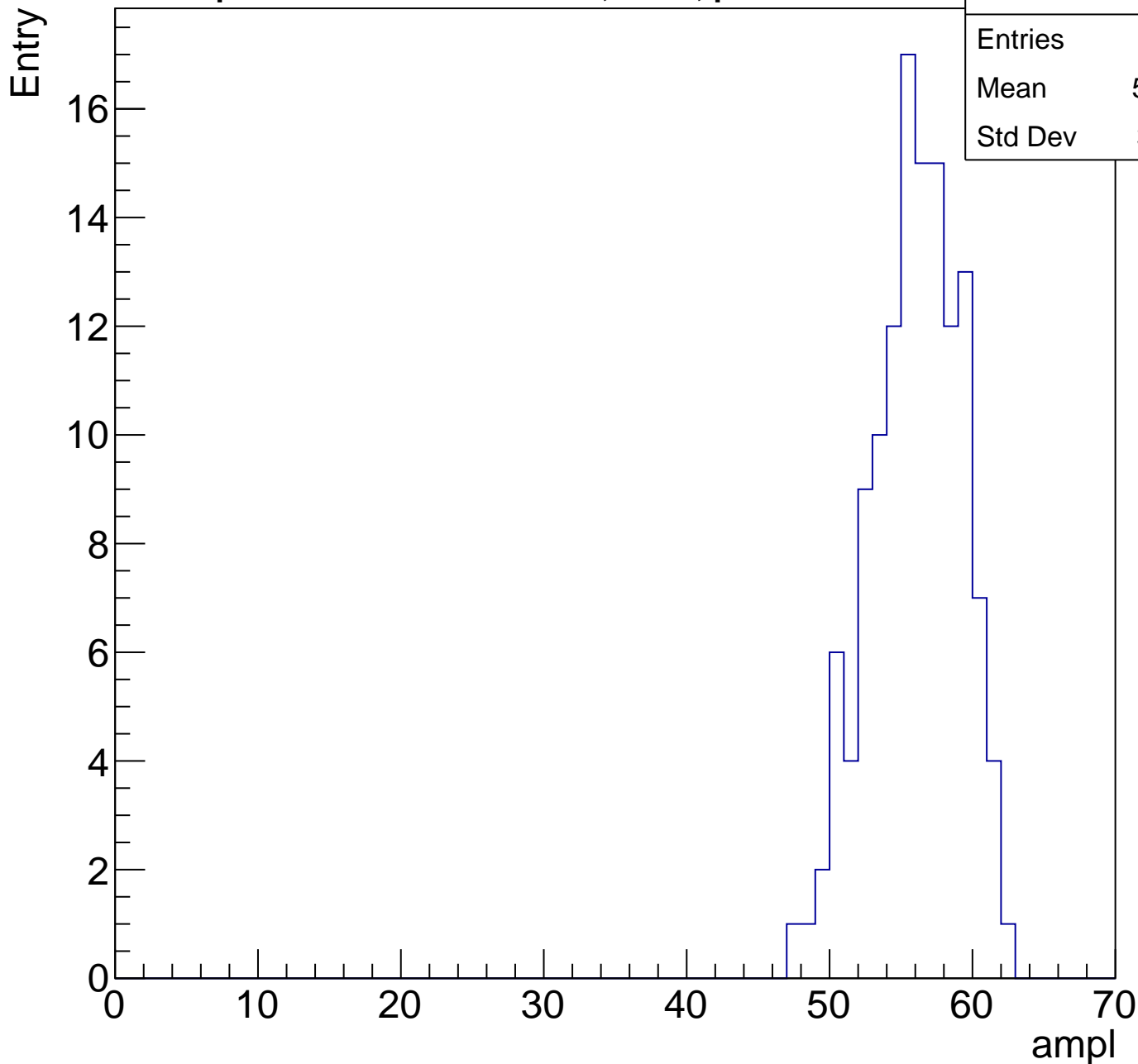
ampl



B1L001S, U22-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	55.52
Std Dev	3.121



B1L001S, U22-ch59, adc5

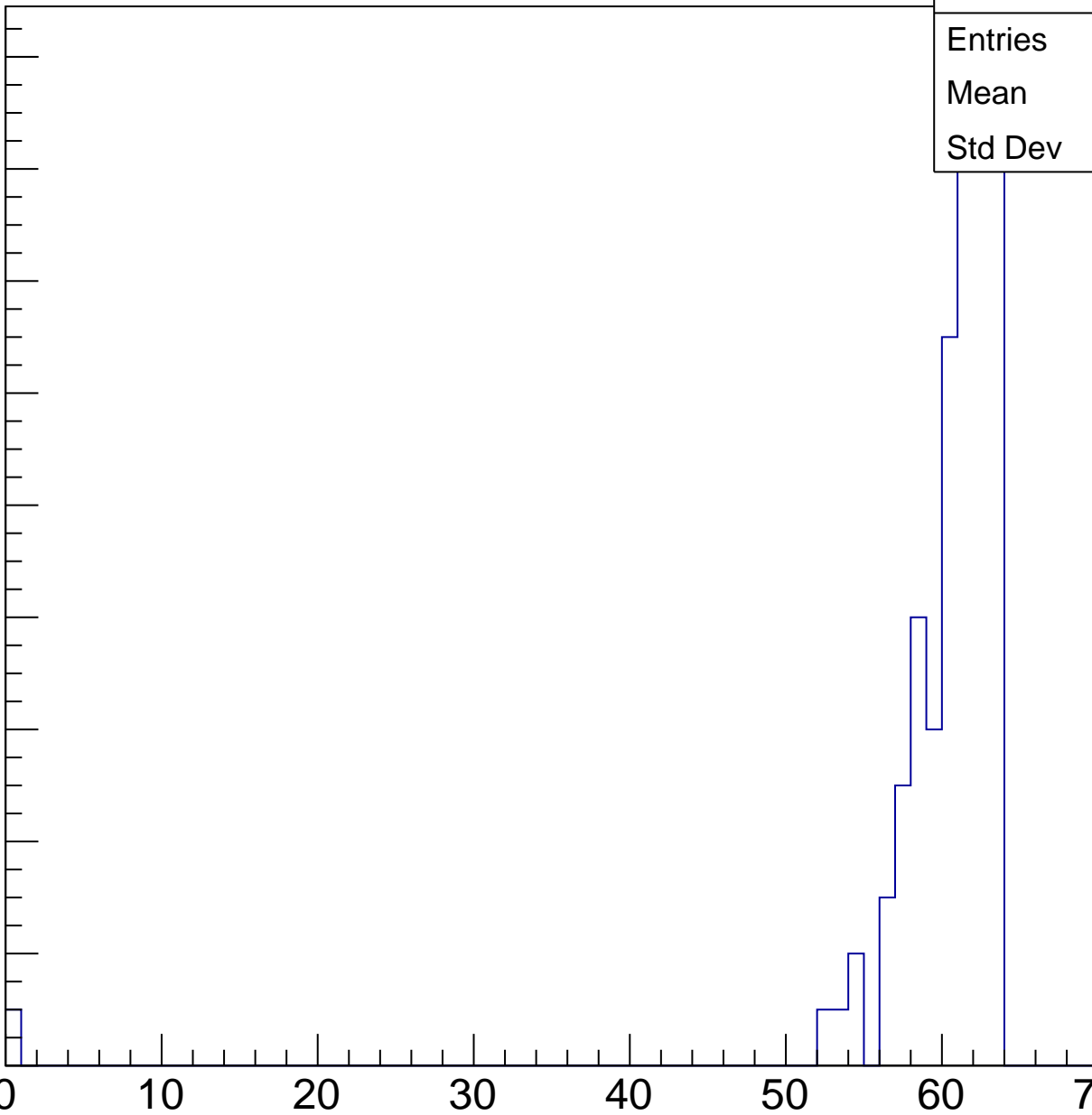
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.65
Std Dev	6.754

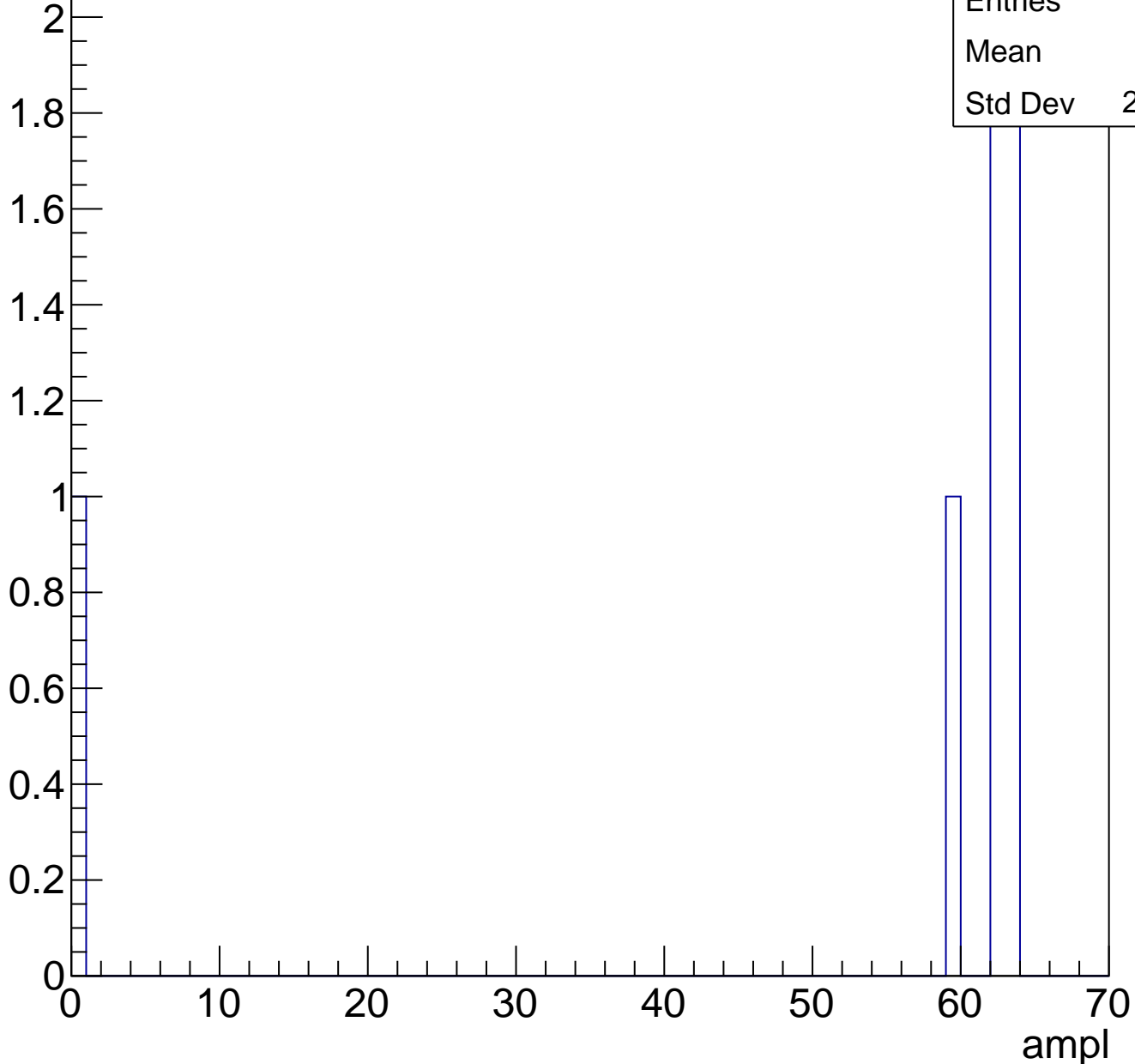
ampl



B1L001S, U22-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch60, adc0

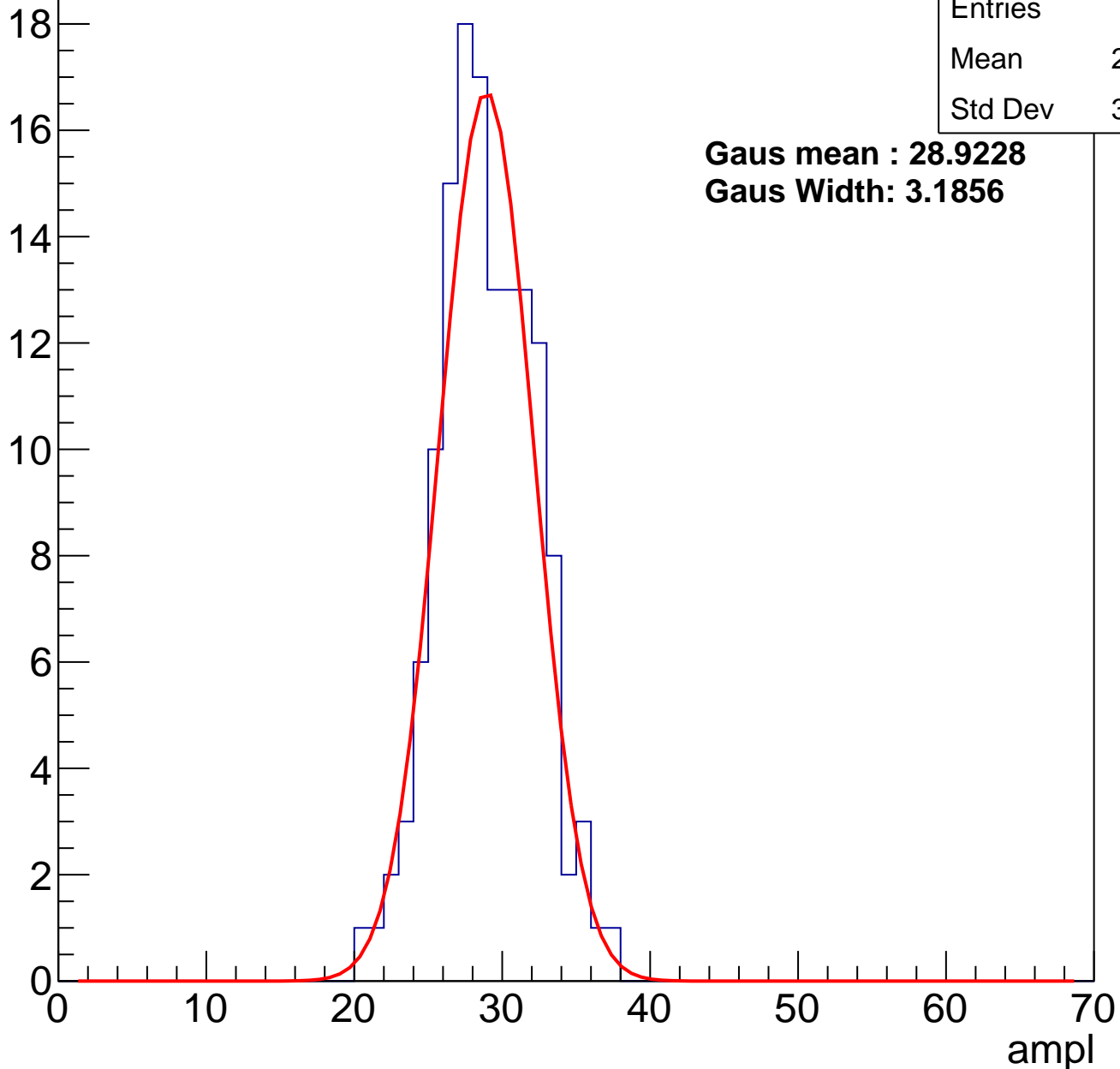
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	28.52
Std Dev	3.202

Gaus mean : 28.9228

Gaus Width: 3.1856

Entry



B1L001S, U22-ch60, adc1

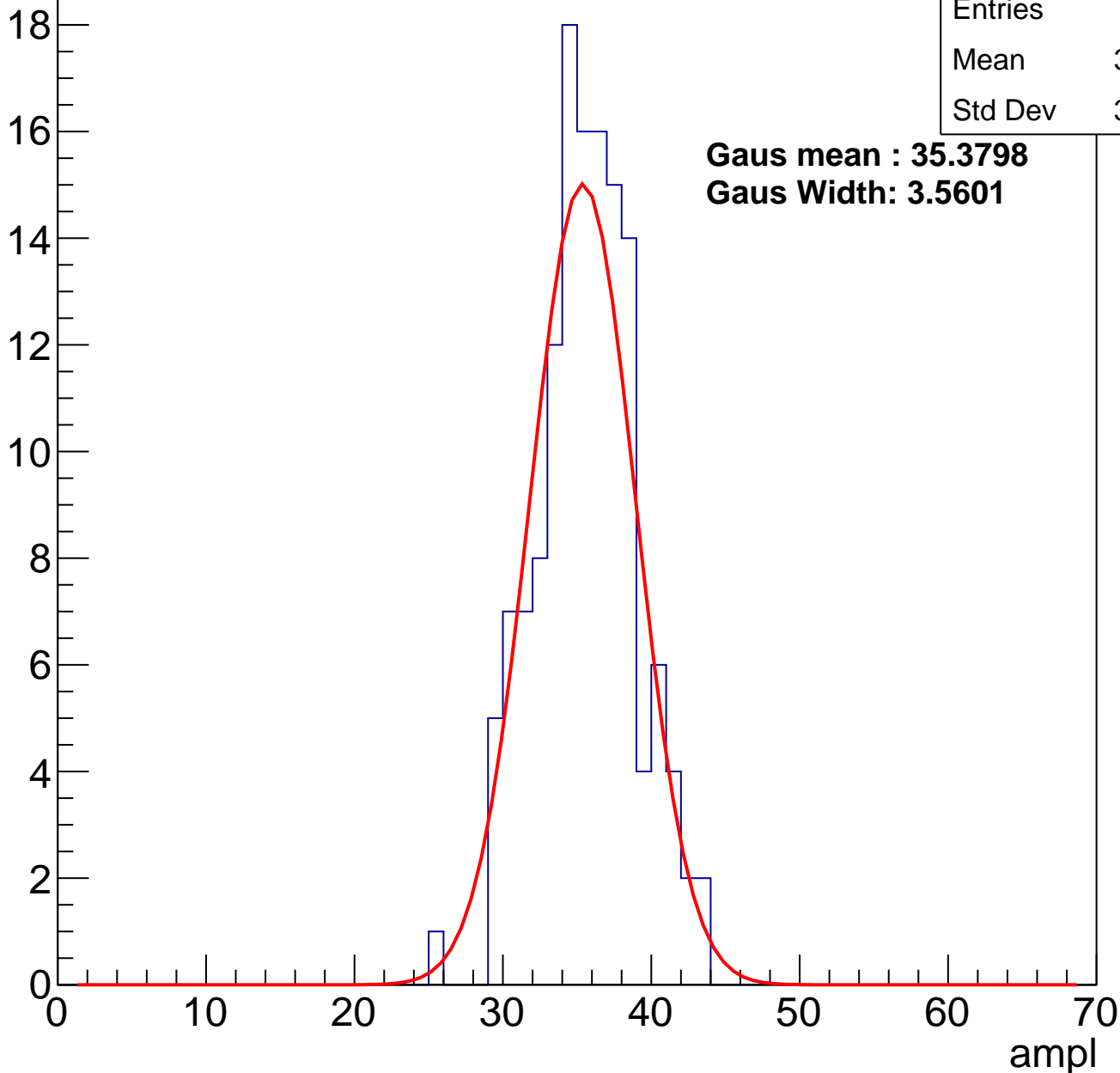
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.14
Std Dev	3.296

Gaus mean : 35.3798

Gaus Width: 3.5601

Entry



B1L001S, U22-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	42.23
Std Dev	3.16

Gaus mean : 42.7102

Gaus Width: 3.2999

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

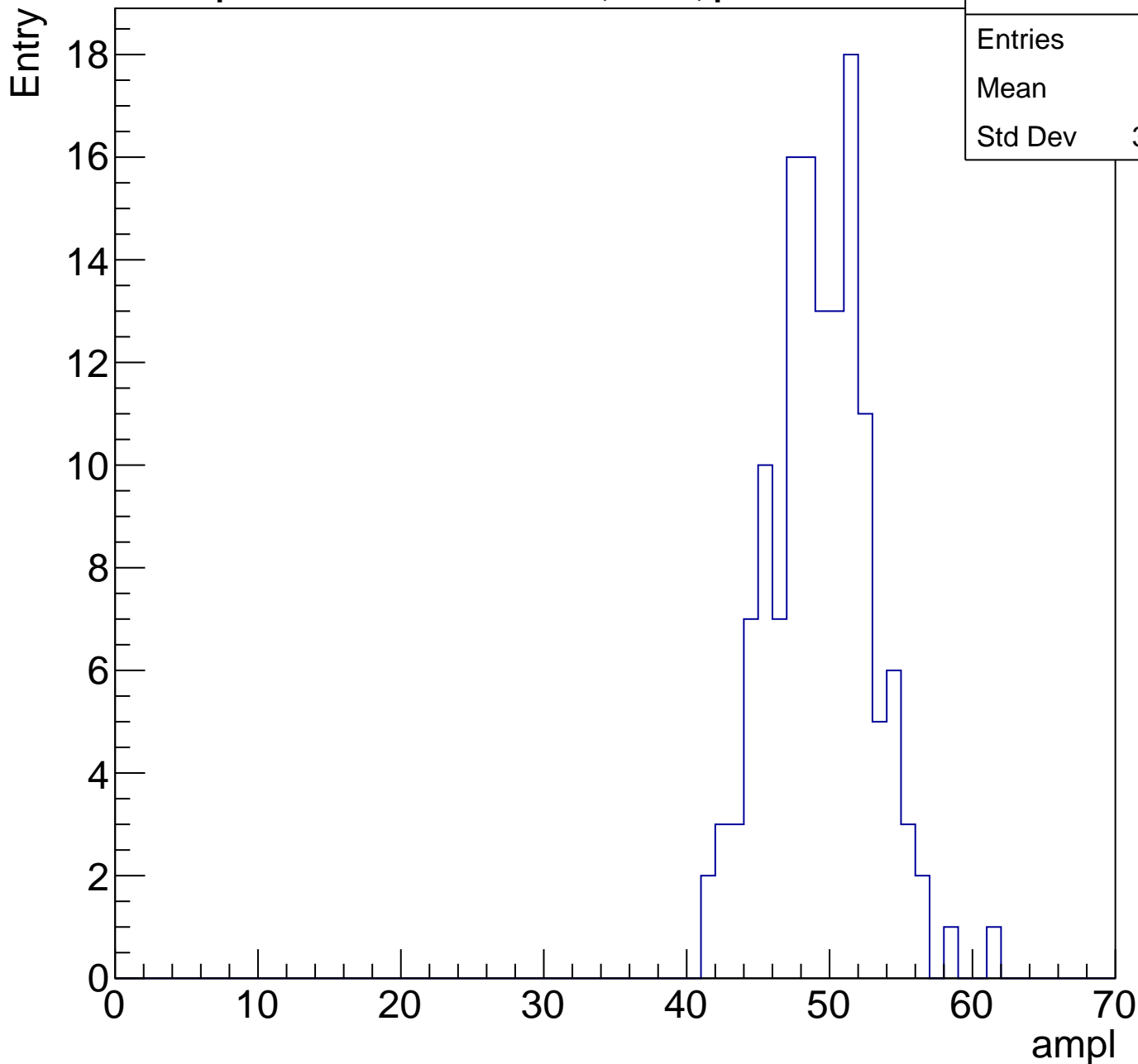
60

70

B1L001S, U22-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

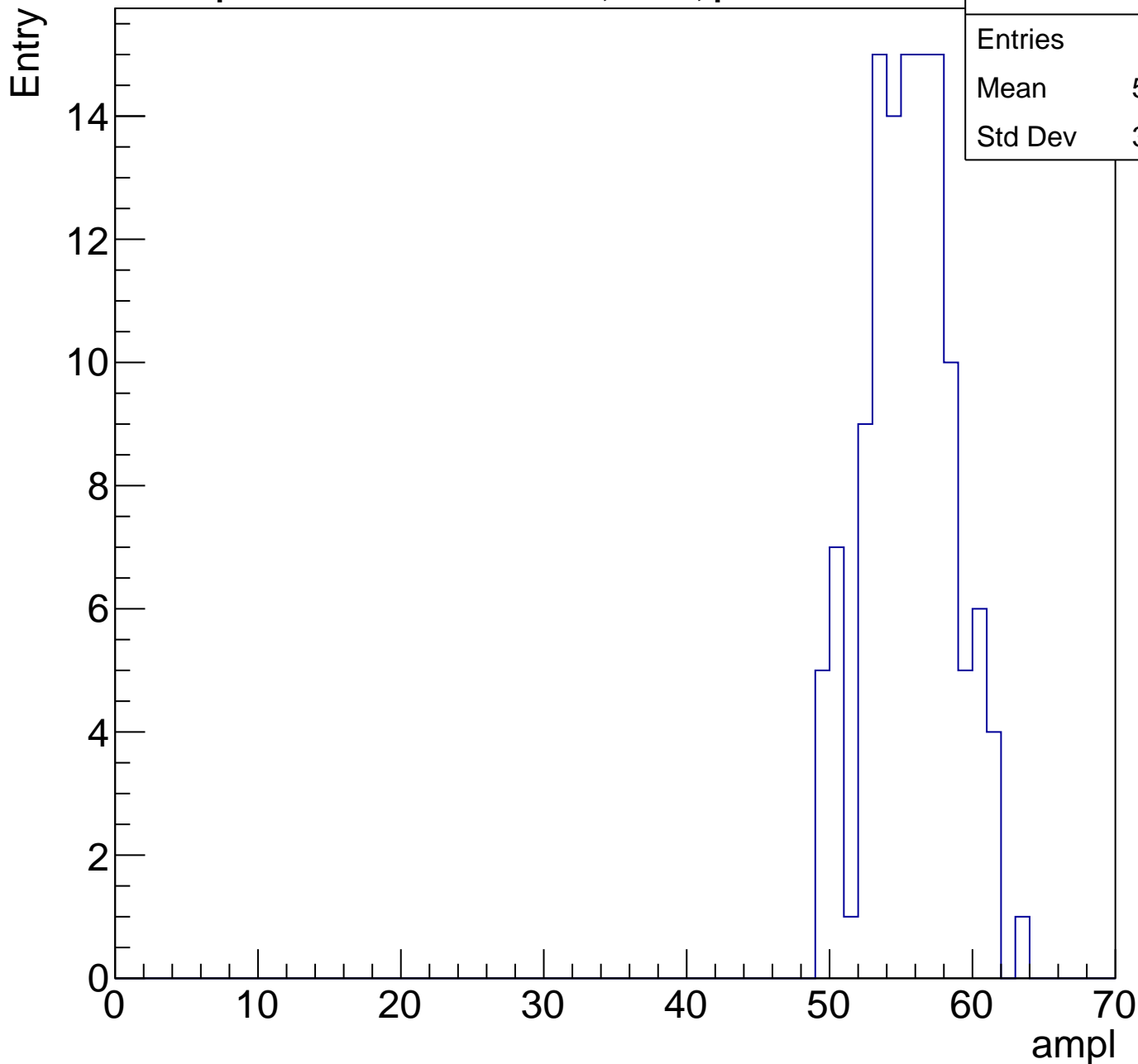
Entries	137
Mean	48.9
Std Dev	3.527



B1L001S, U22-ch60, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	55.14
Std Dev	3.039



B1L001S, U22-ch60, adc5

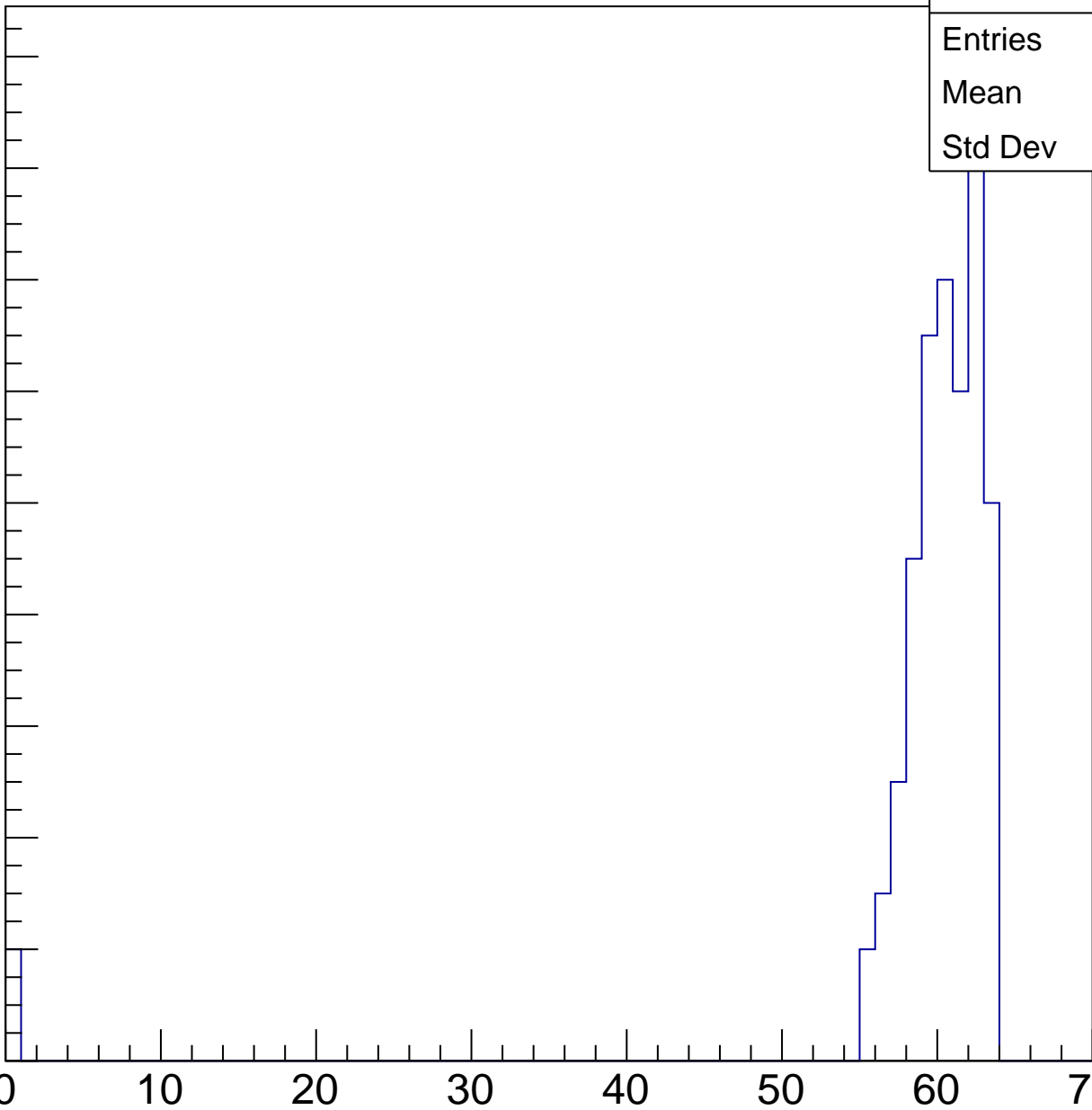
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	88
Mean	58.75
Std Dev	9.188

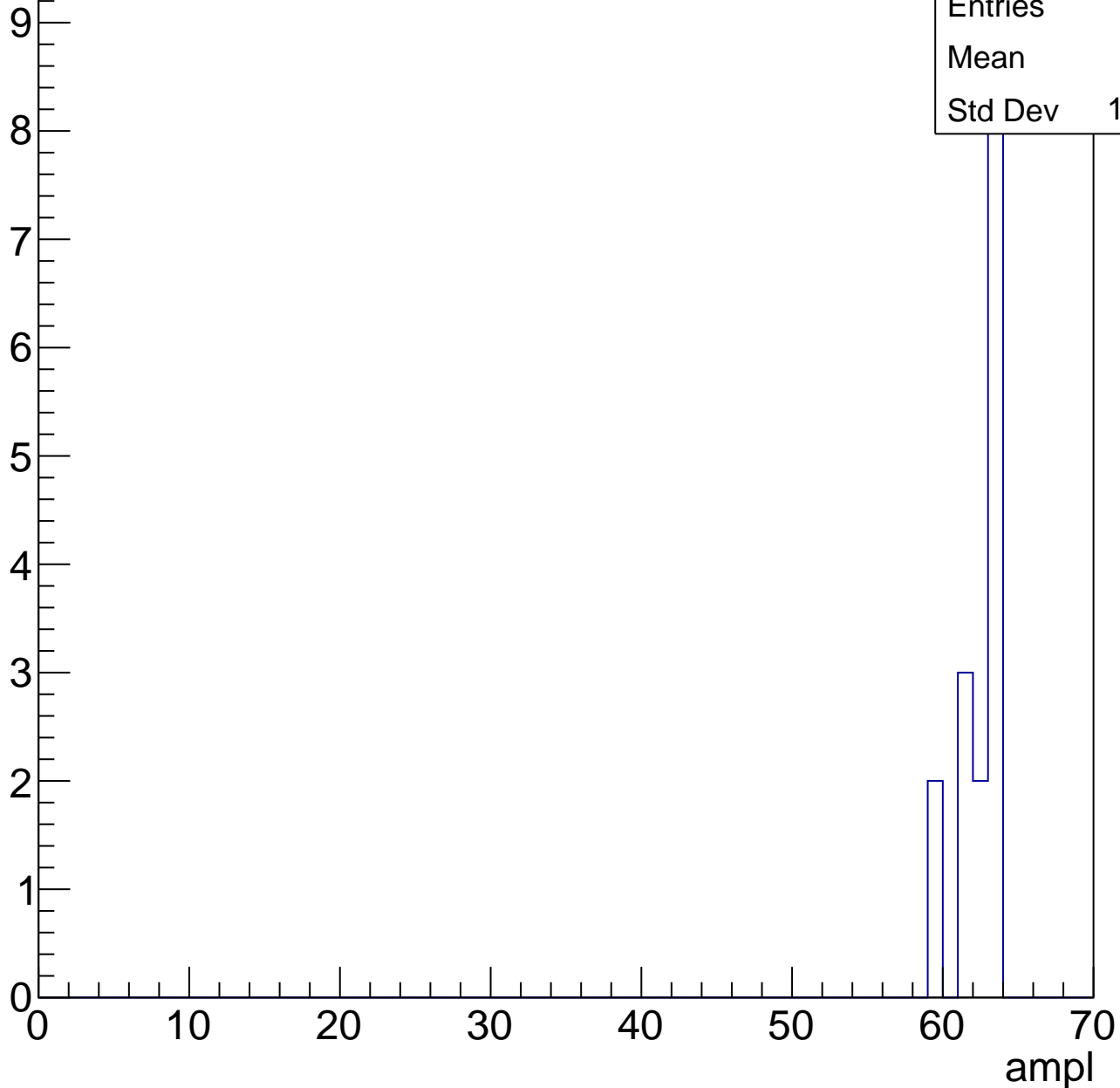
ampl



B1L001S, U22-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch61, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.31
Std Dev	3.333

Gaus mean : 29.8637

Gaus Width: 3.4930

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

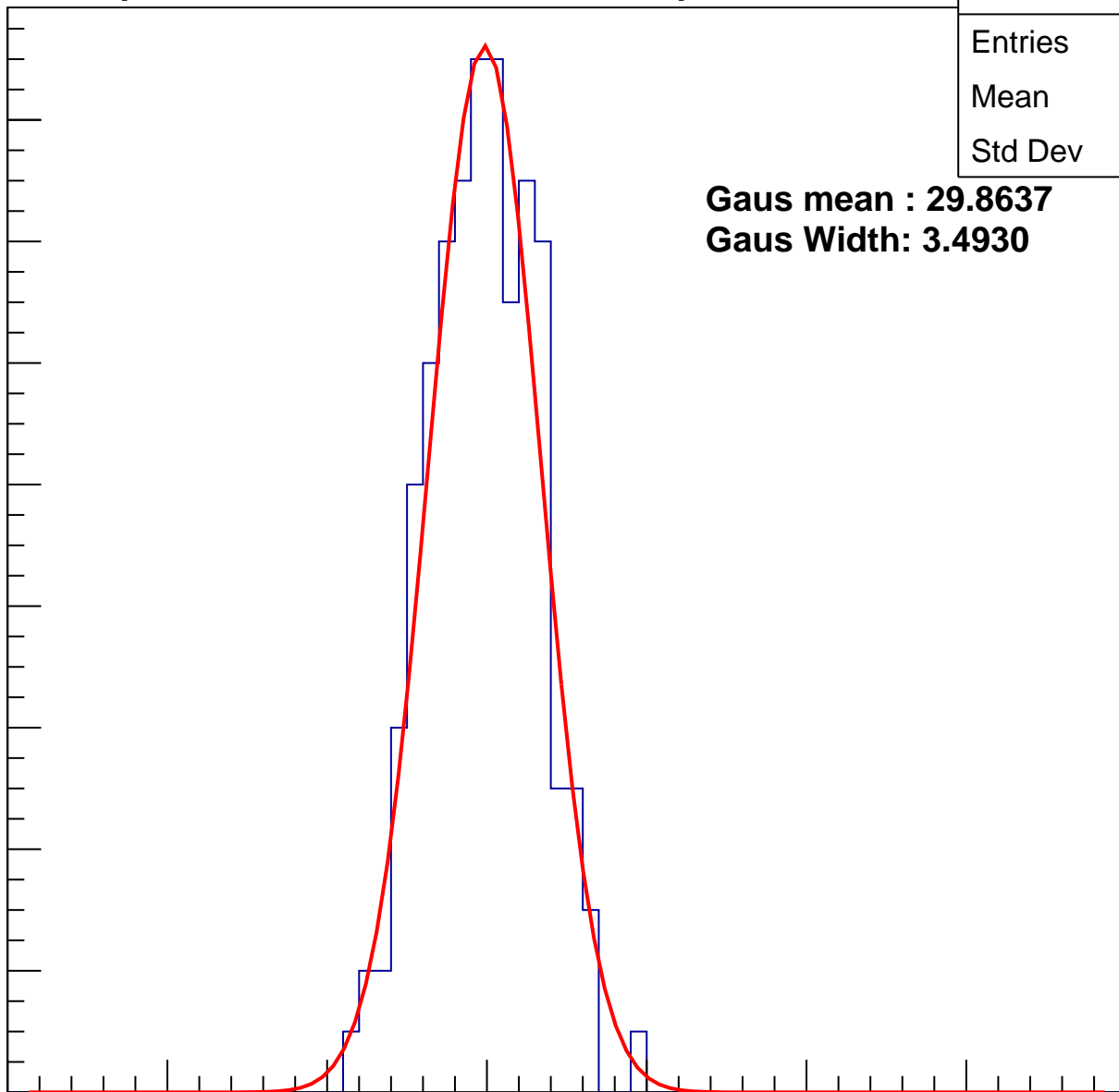
40

50

60

70

ampl



B1L001S, U22-ch61, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.59
Std Dev	3.298

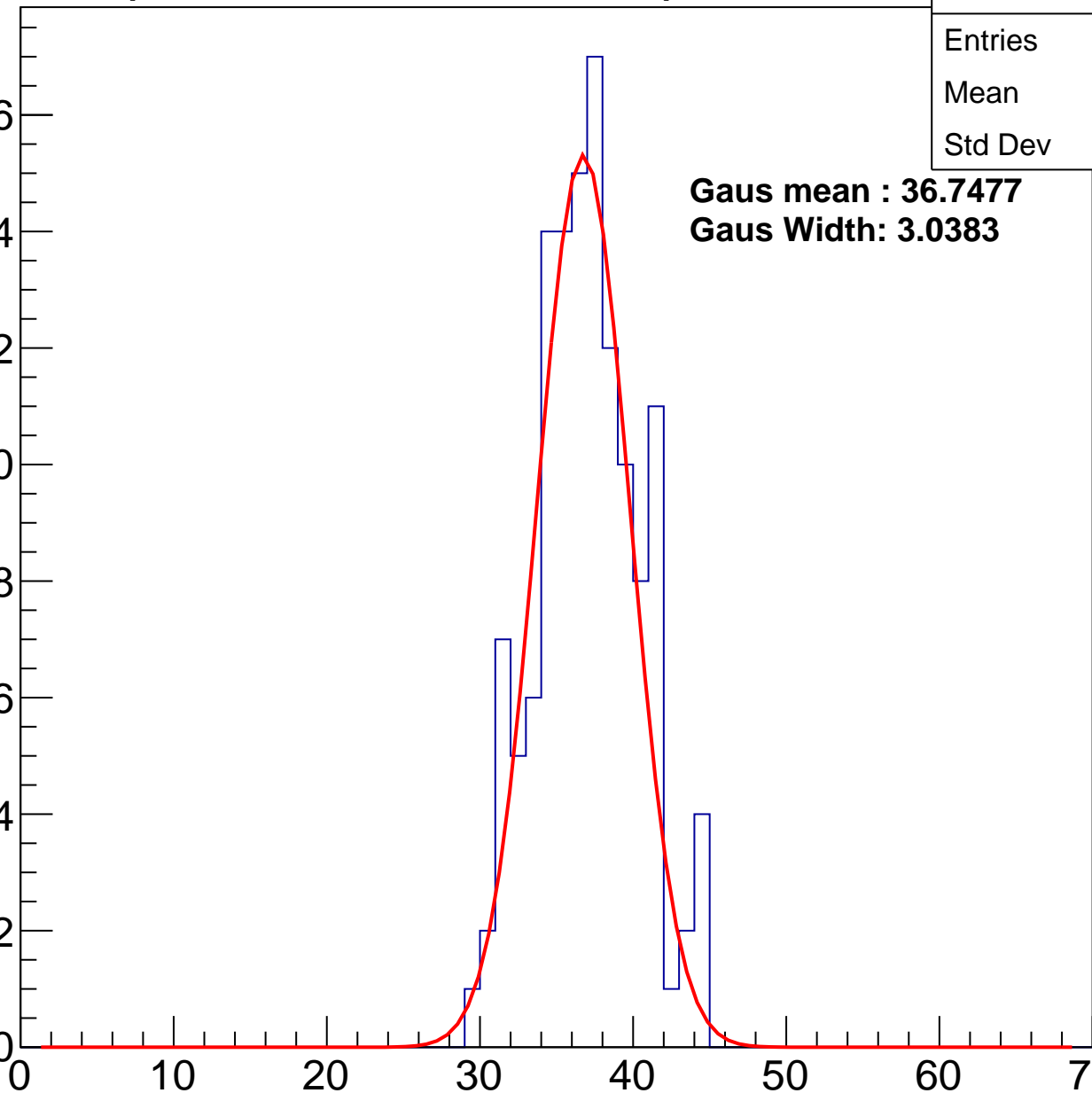
Gaus mean : 36.7477

Gaus Width: 3.0383

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch61, adc2

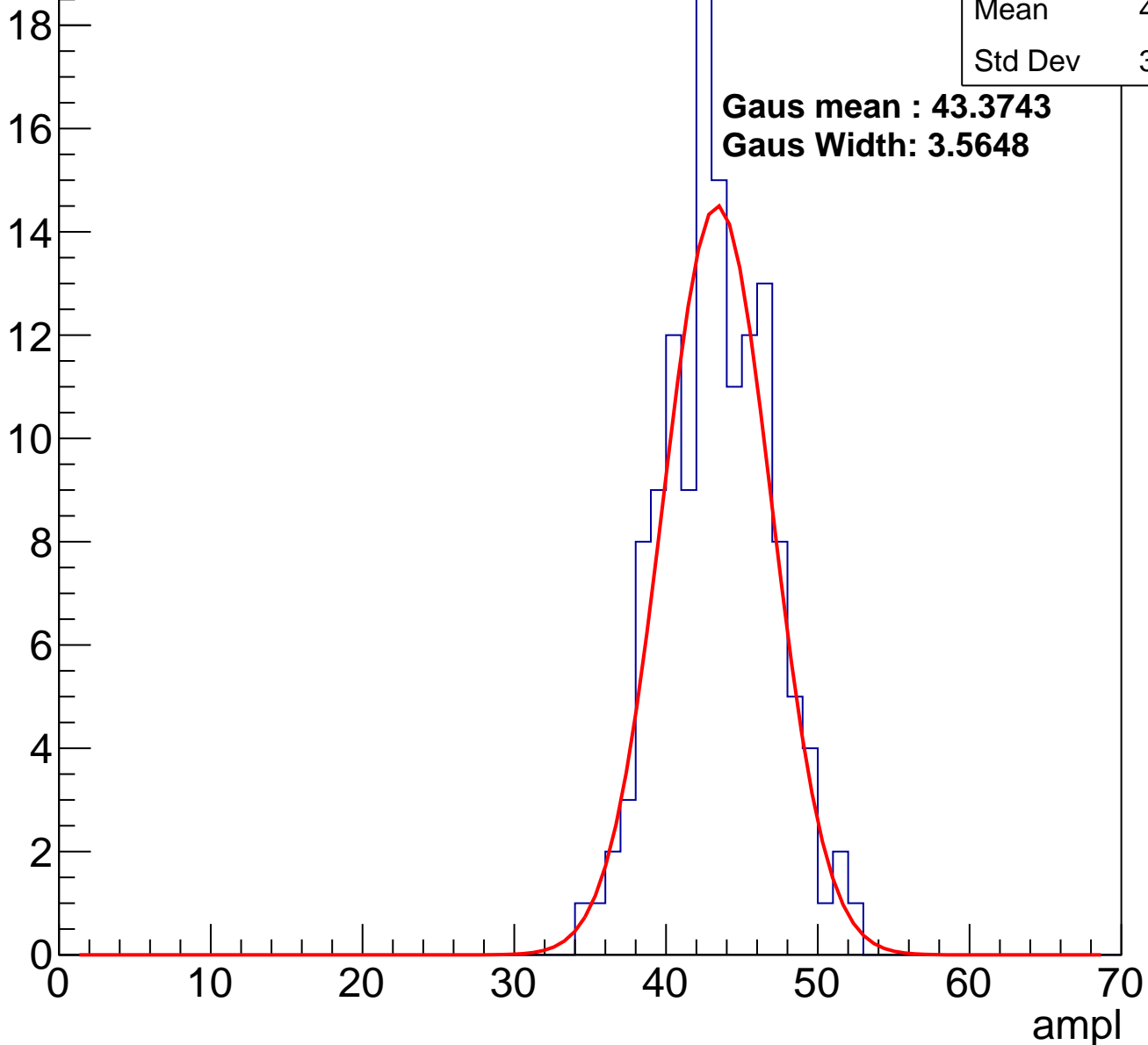
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	42.92
Std Dev	3.523

Gaus mean : 43.3743

Gaus Width: 3.5648

Entry



B1L001S, U22-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

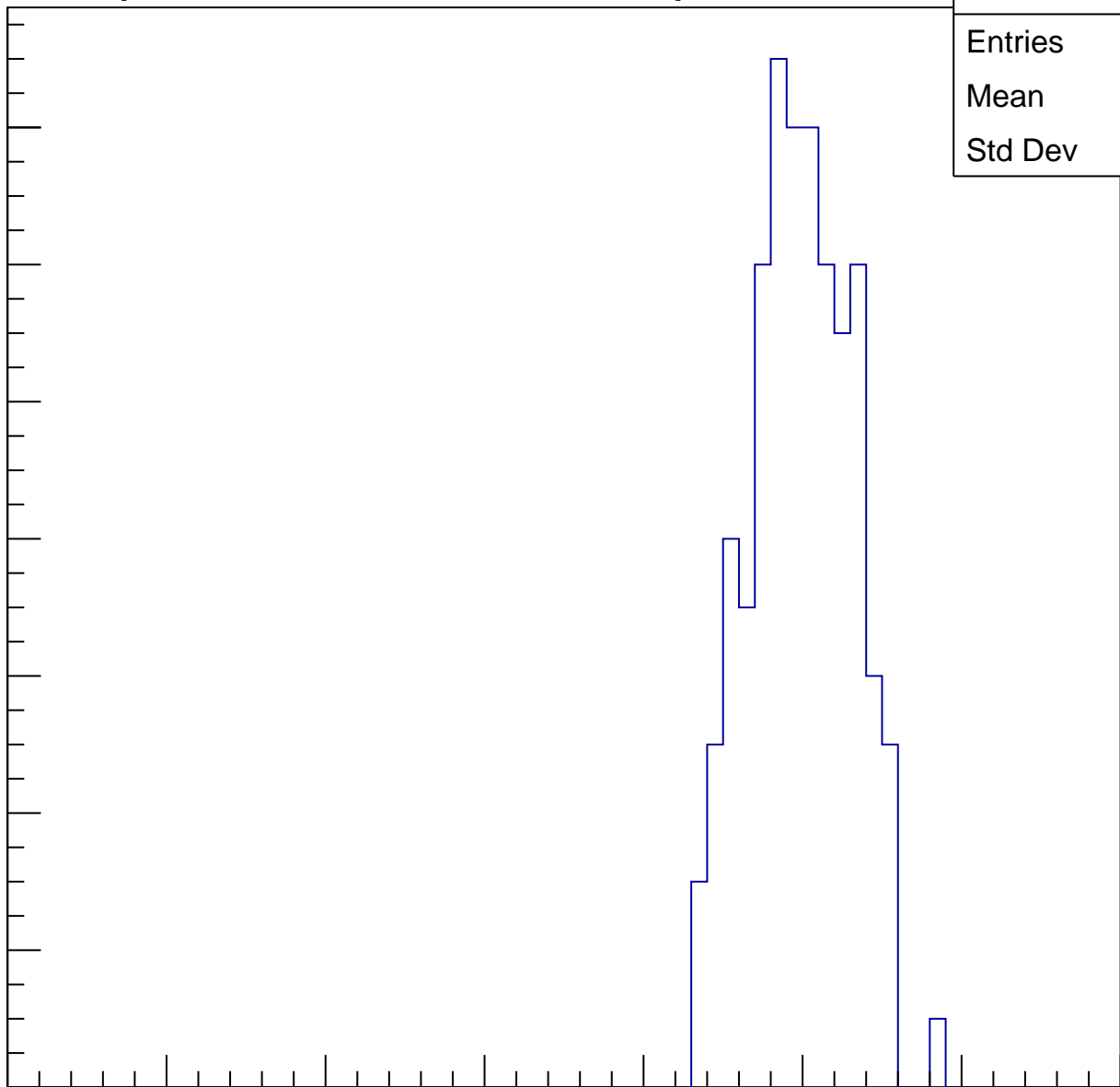
Entries	125
Mean	49.42
Std Dev	3.135

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

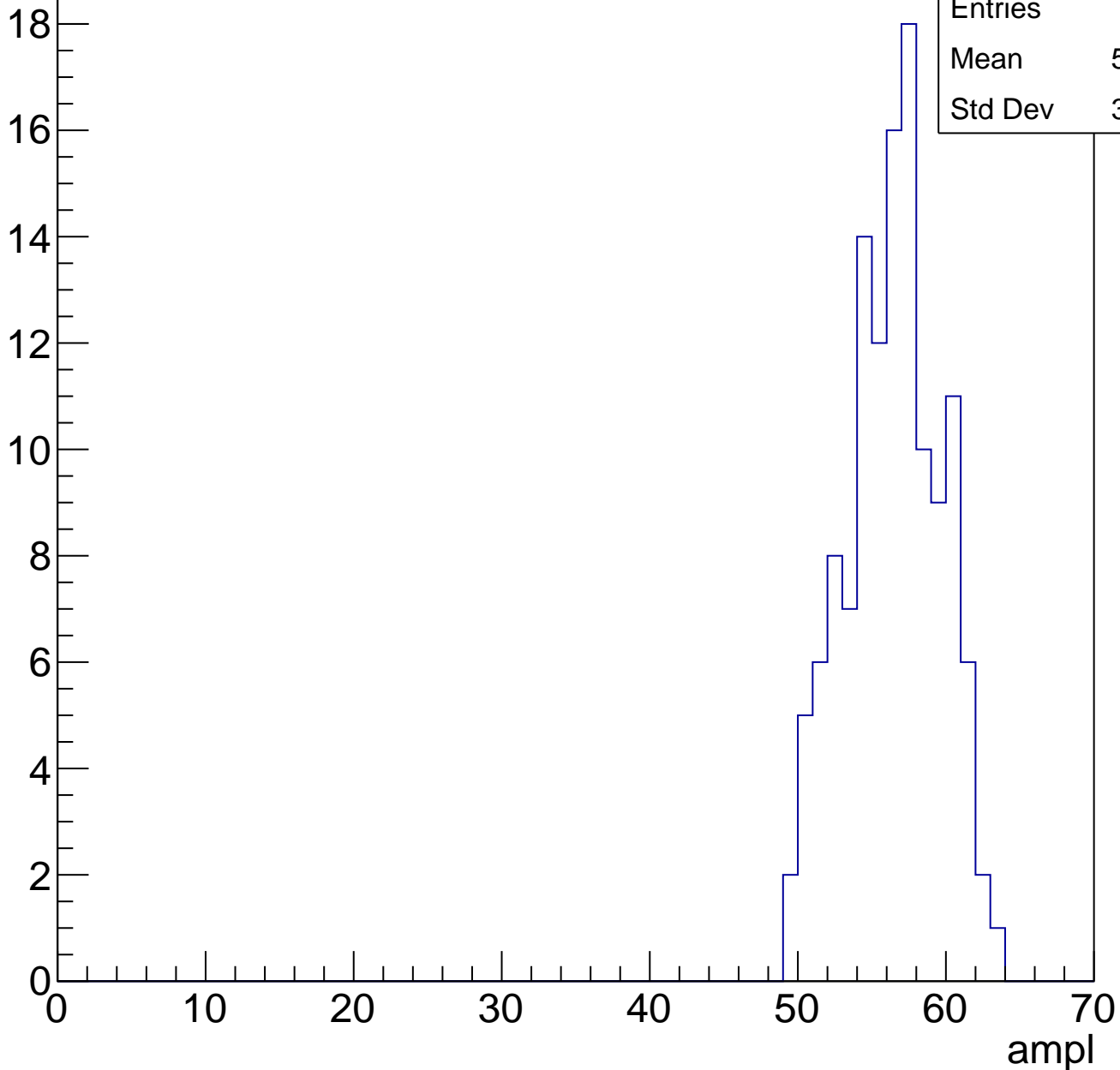


B1L001S, U22-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	55.93
Std Dev	3.165

Entry



B1L001S, U22-ch61, adc5

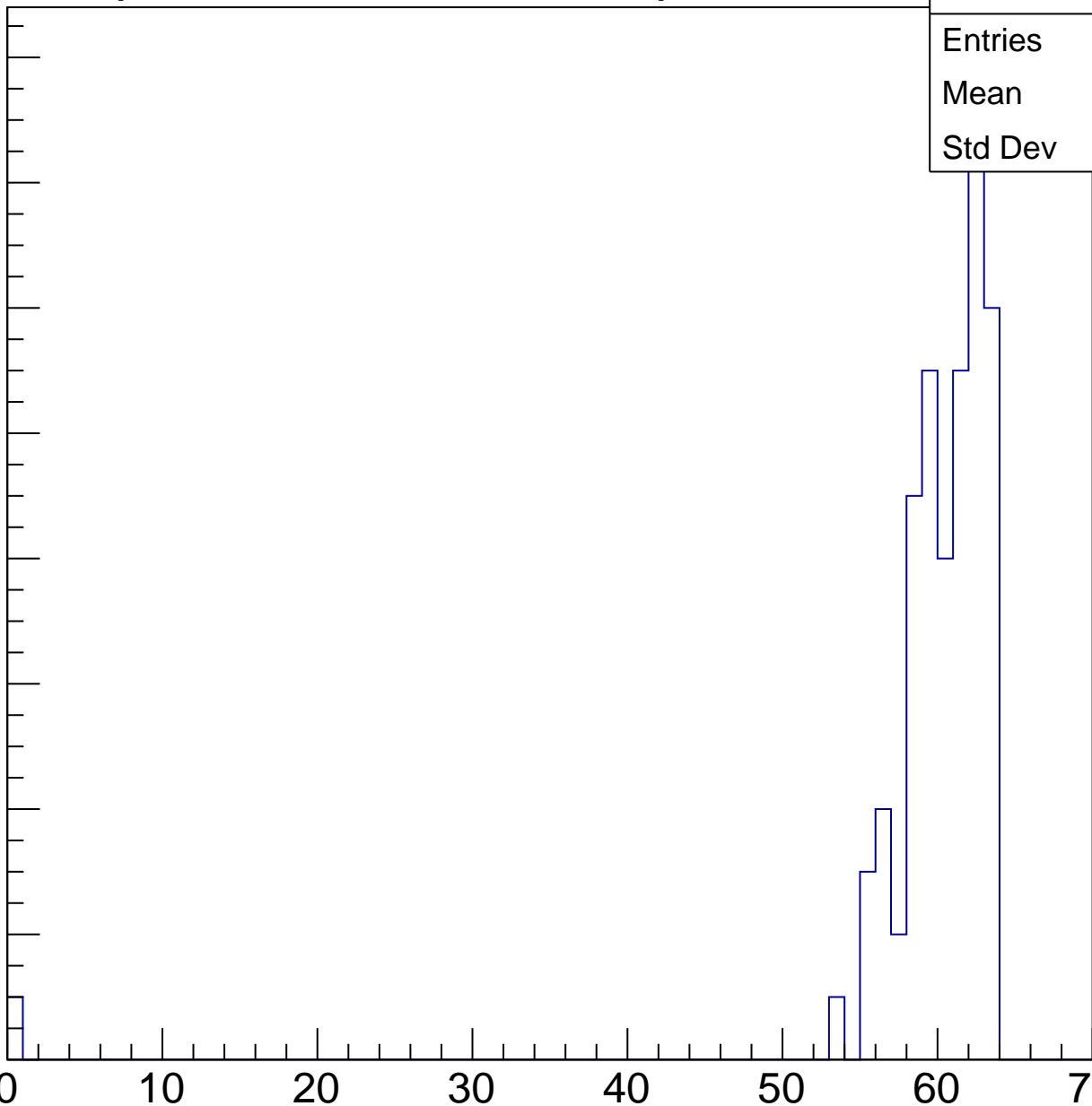
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.31
Std Dev	7.16

ampl

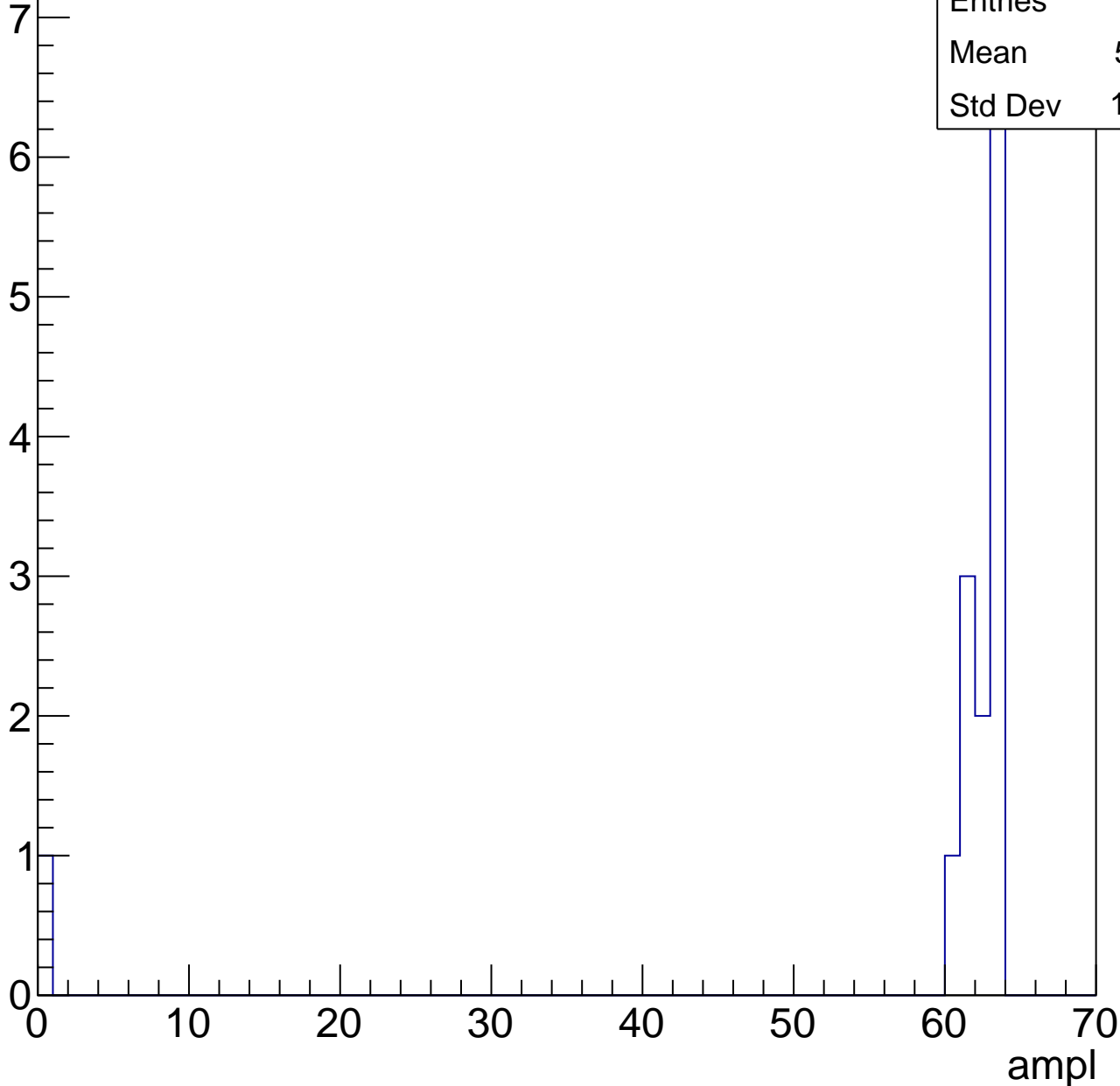


B1L001S, U22-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.71
Std Dev	16.04



B1L001S, U22-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U22-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

127

Mean

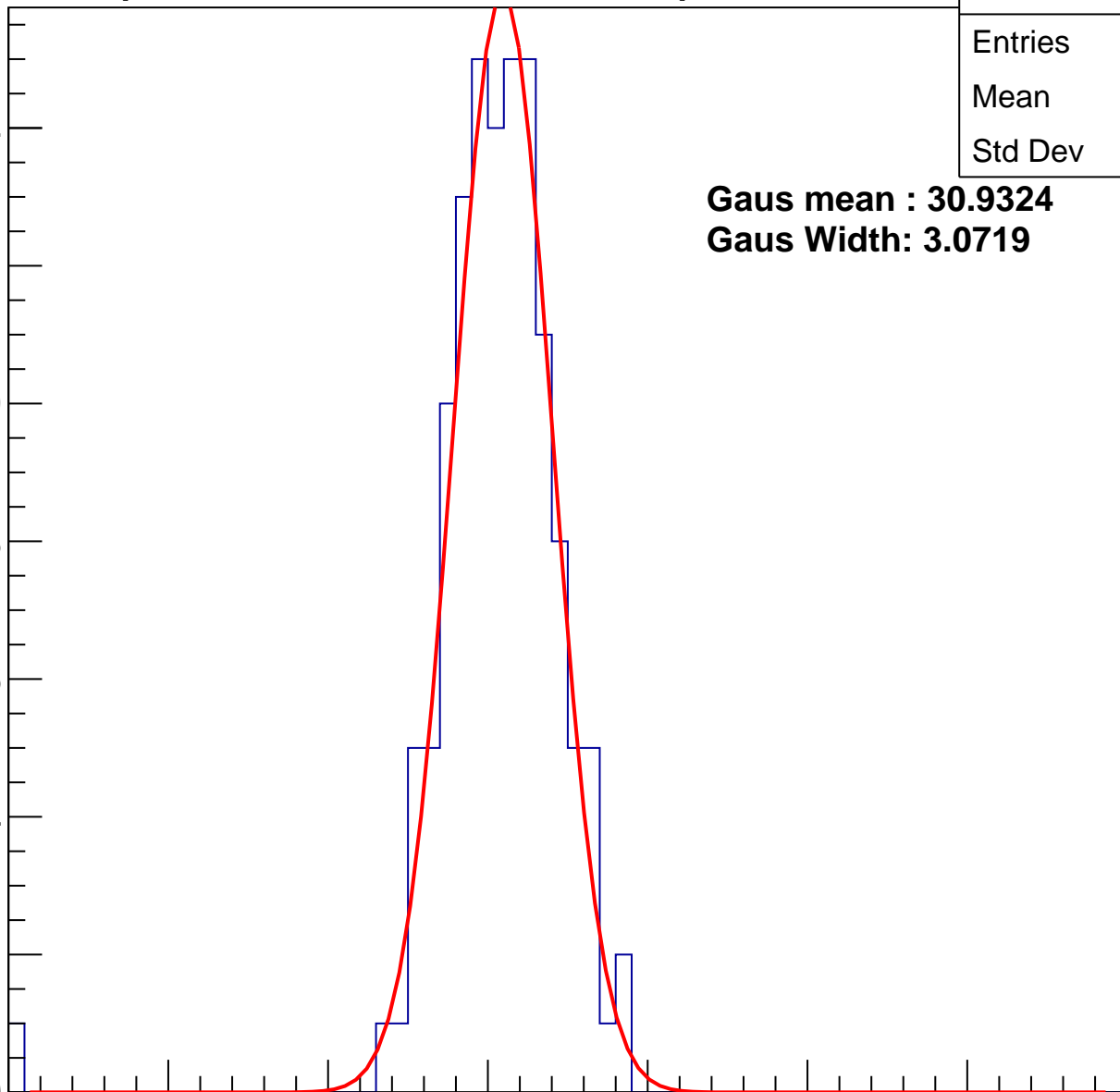
30.23

Std Dev

4.09

Gaus mean : 30.9324

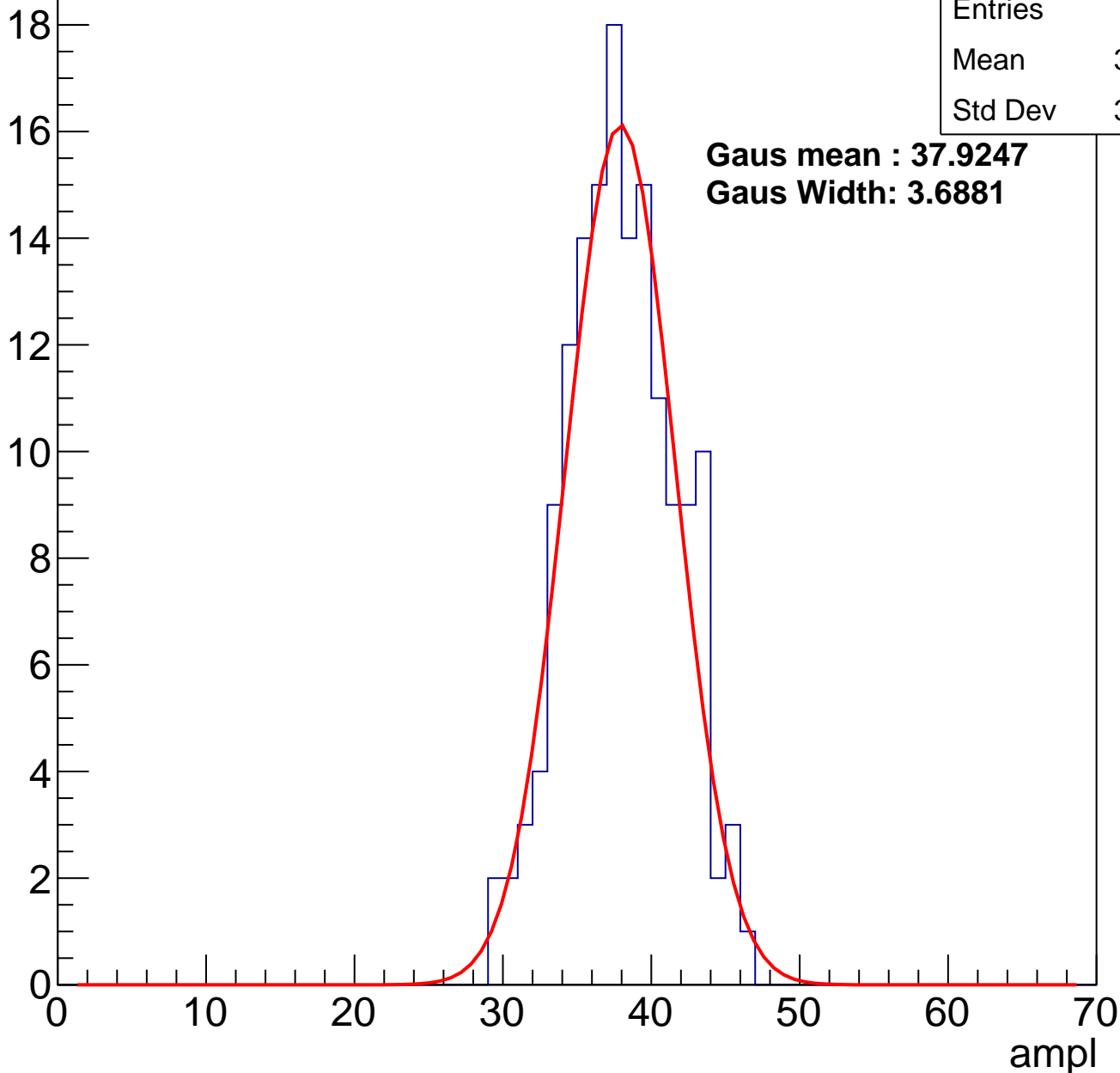
Gaus Width: 3.0719



B1L001S, U22-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	44.09
Std Dev	3.25

Gaus mean : 44.6261

Gaus Width: 3.4033

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

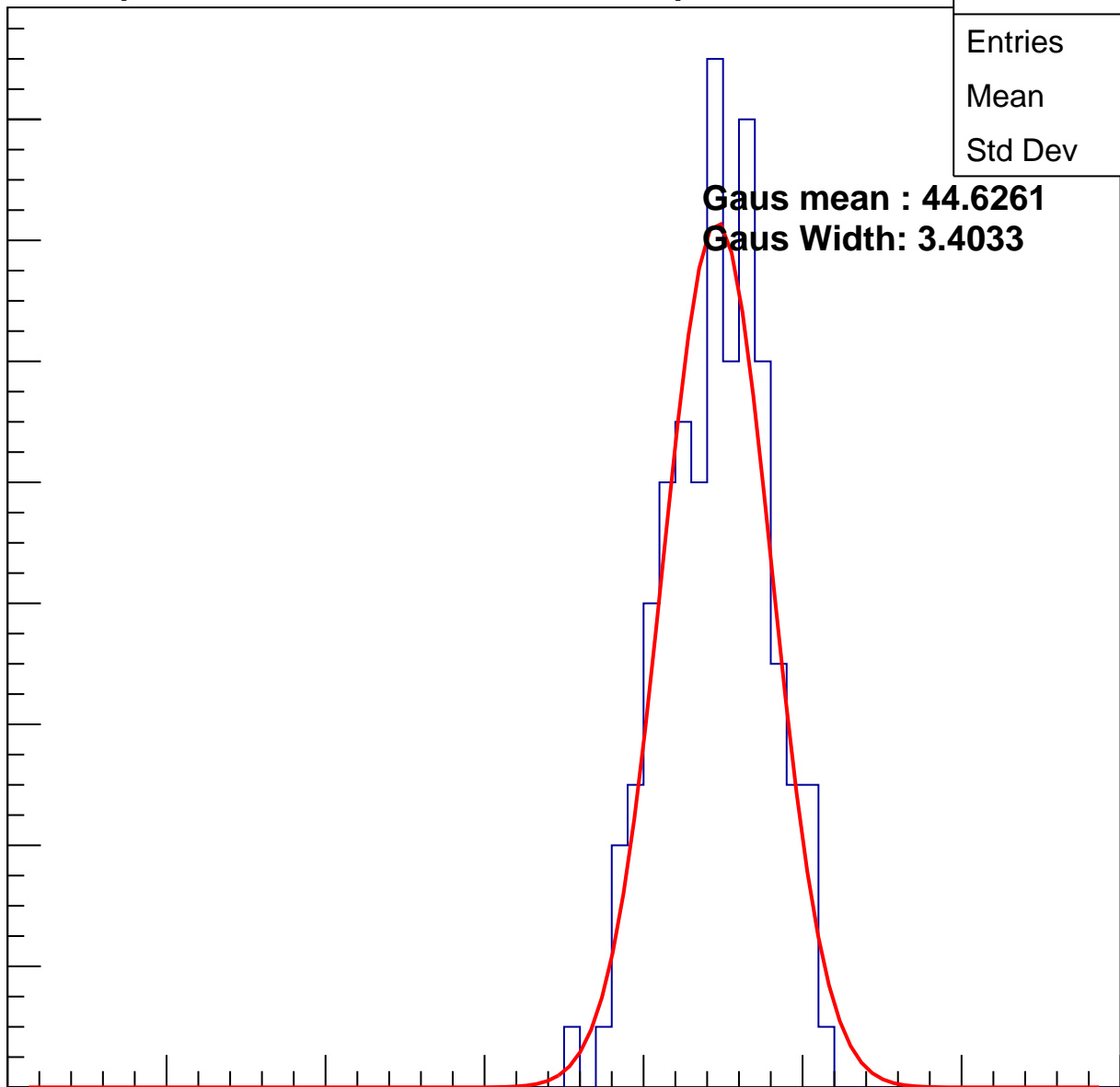
40

50

60

70

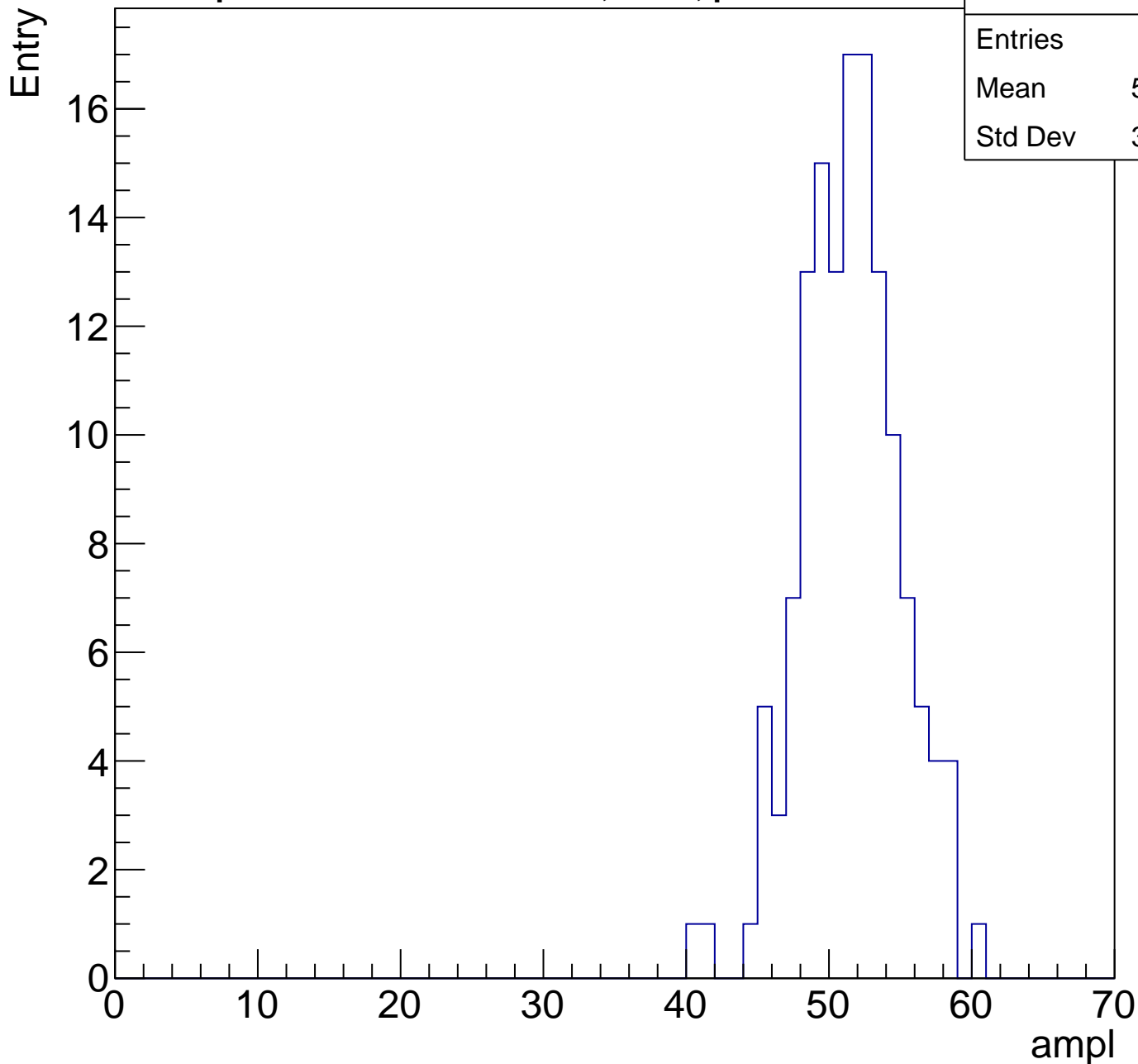
ampl



B1L001S, U22-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	51.03
Std Dev	3.464



B1L001S, U22-ch62, adc4

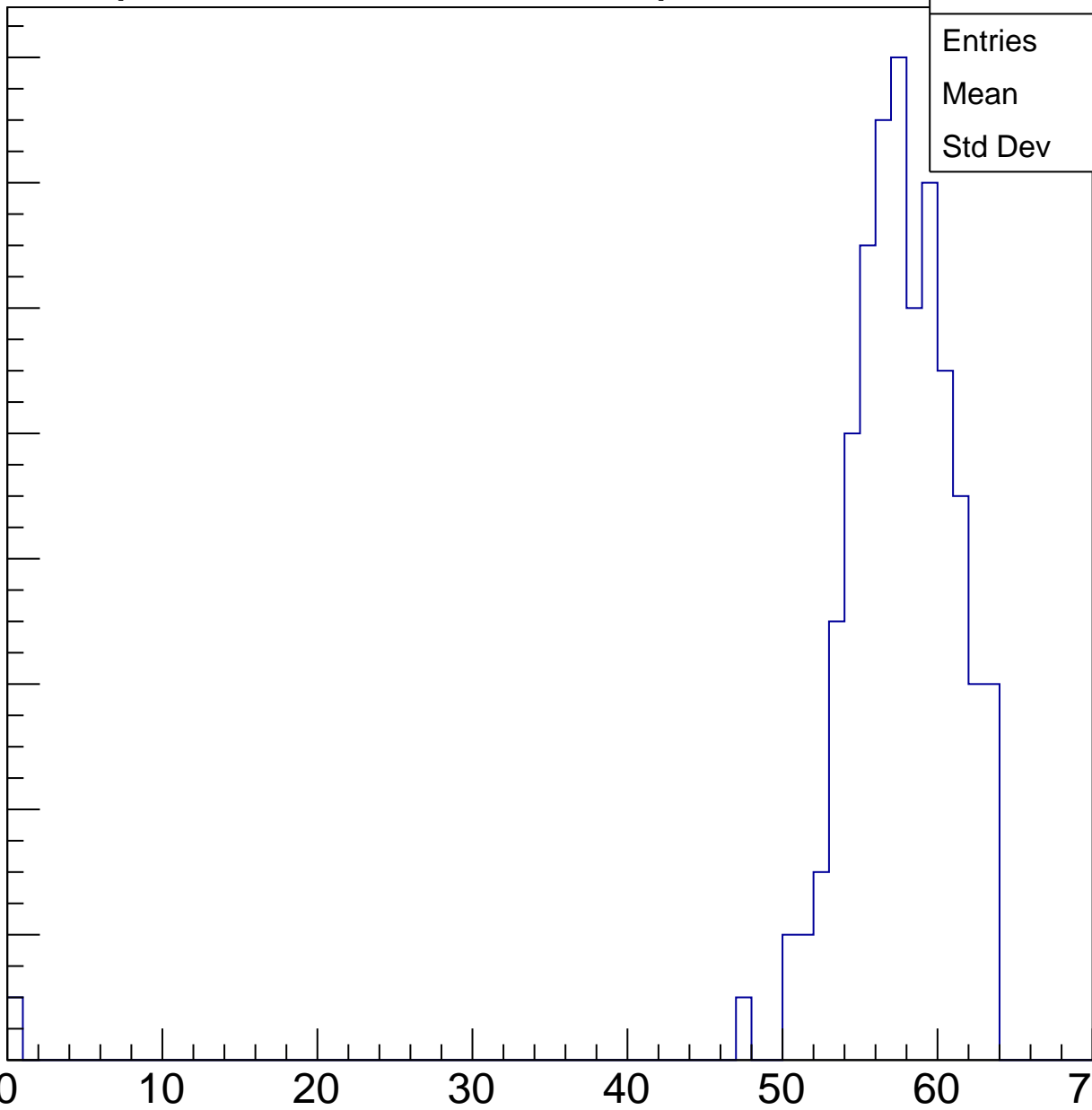
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	56.75
Std Dev	5.95

ampl



B1L001S, U22-ch62, adc5

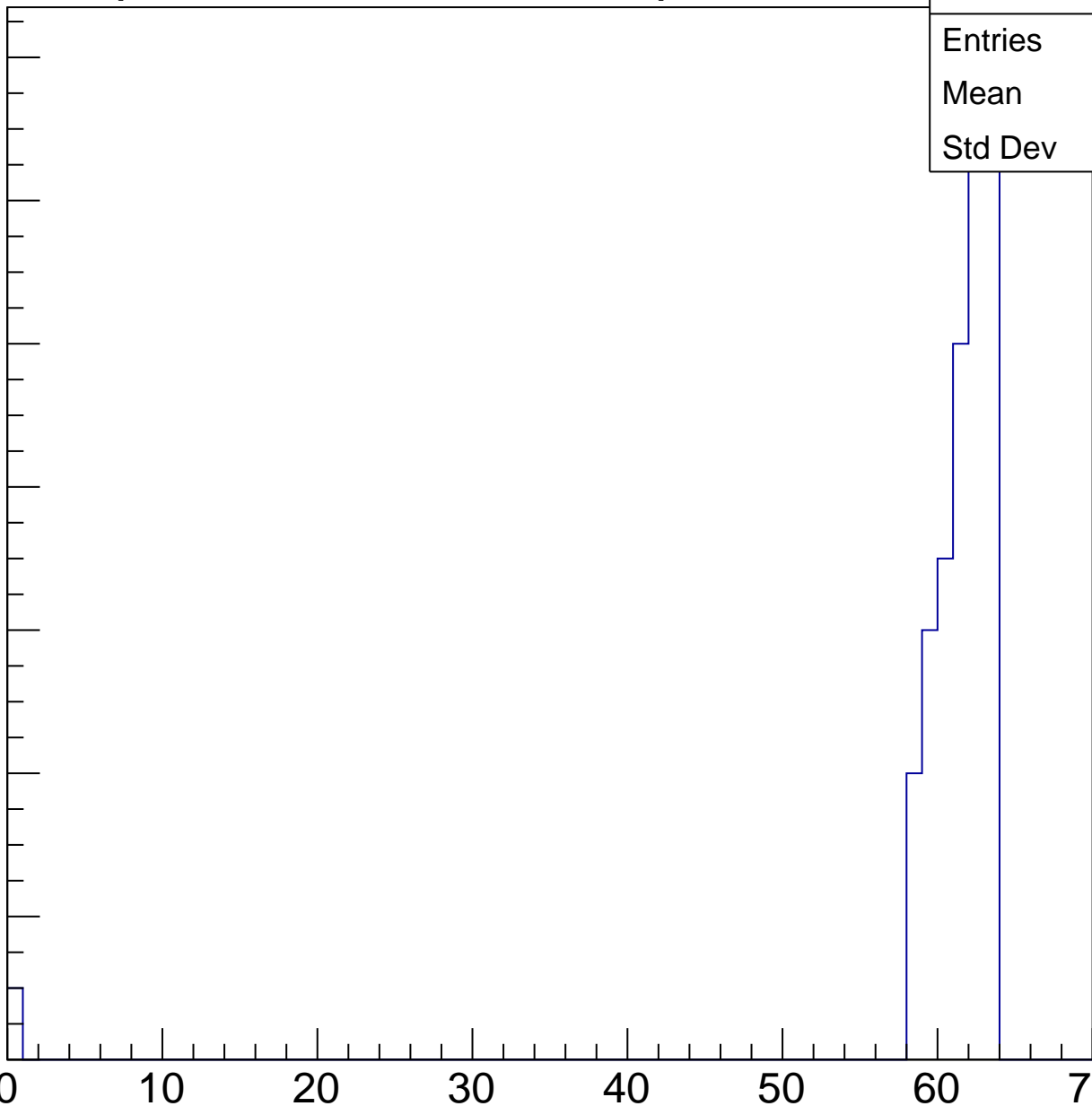
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	55
Mean	60.05
Std Dev	8.317

ampl



B1L001S, U22-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch63, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	30.54
Std Dev	2.884

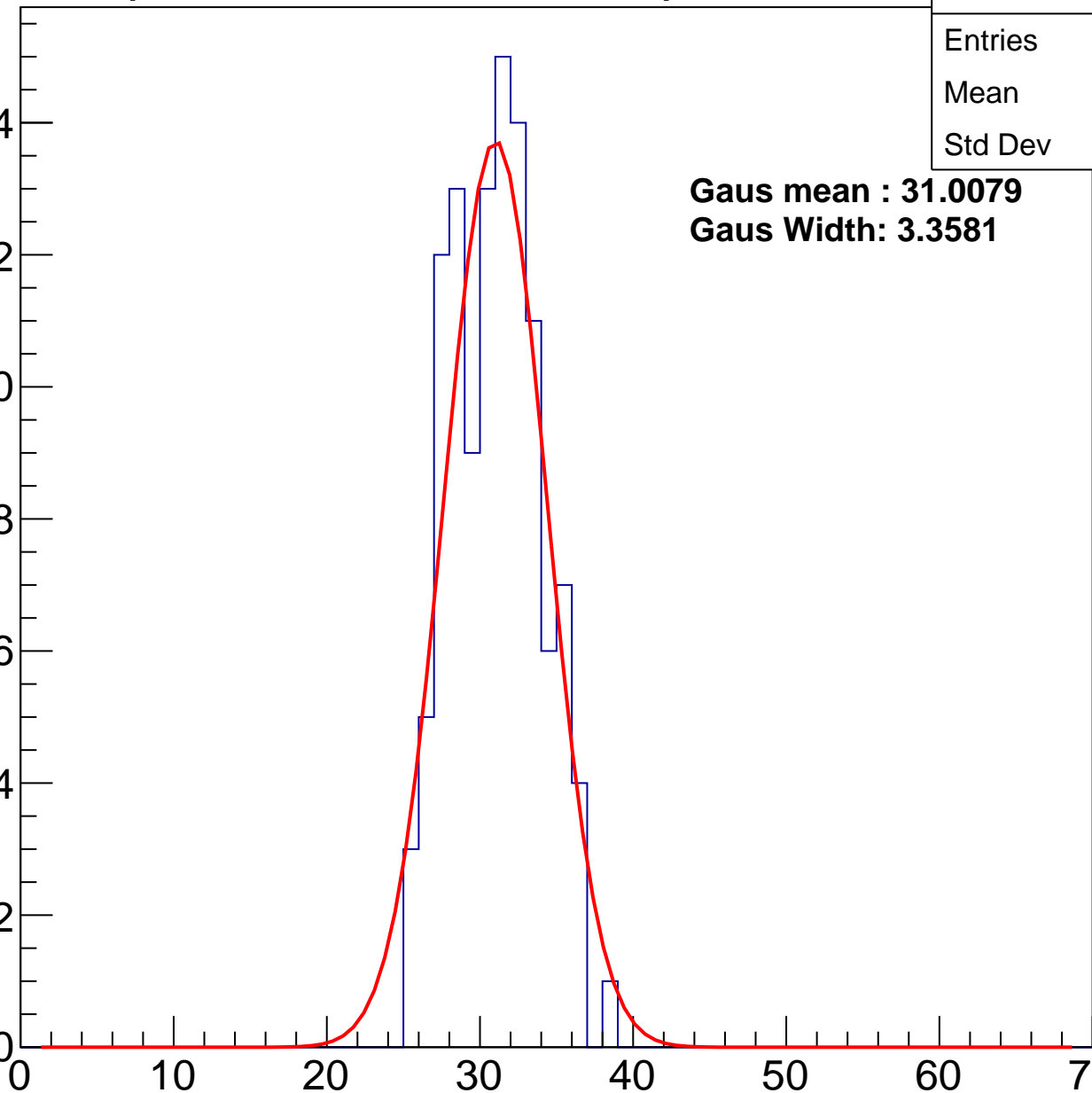
Gaus mean : 31.0079

Gaus Width: 3.3581

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch63, adc1

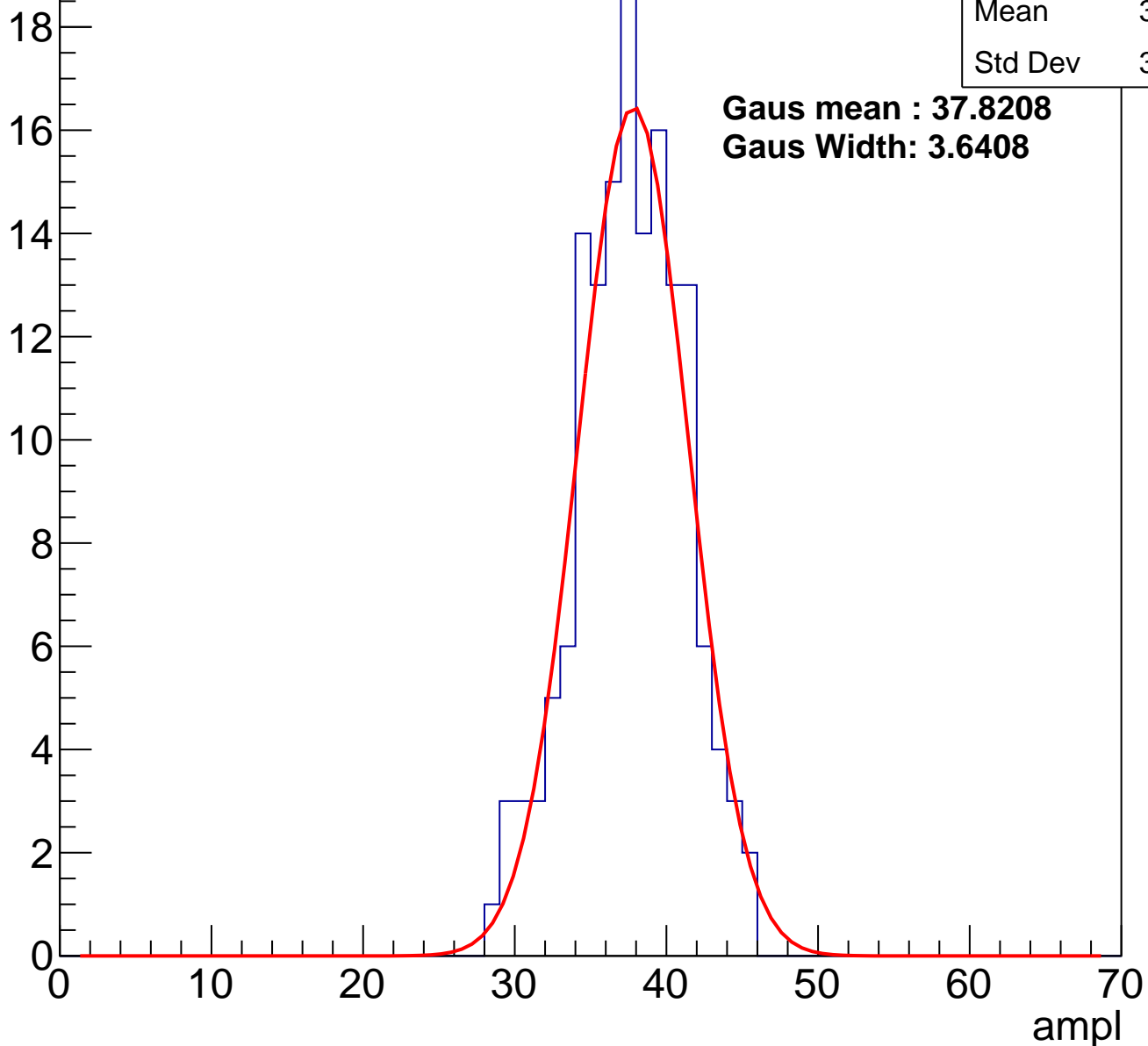
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	37.16
Std Dev	3.533

Gaus mean : 37.8208

Gaus Width: 3.6408

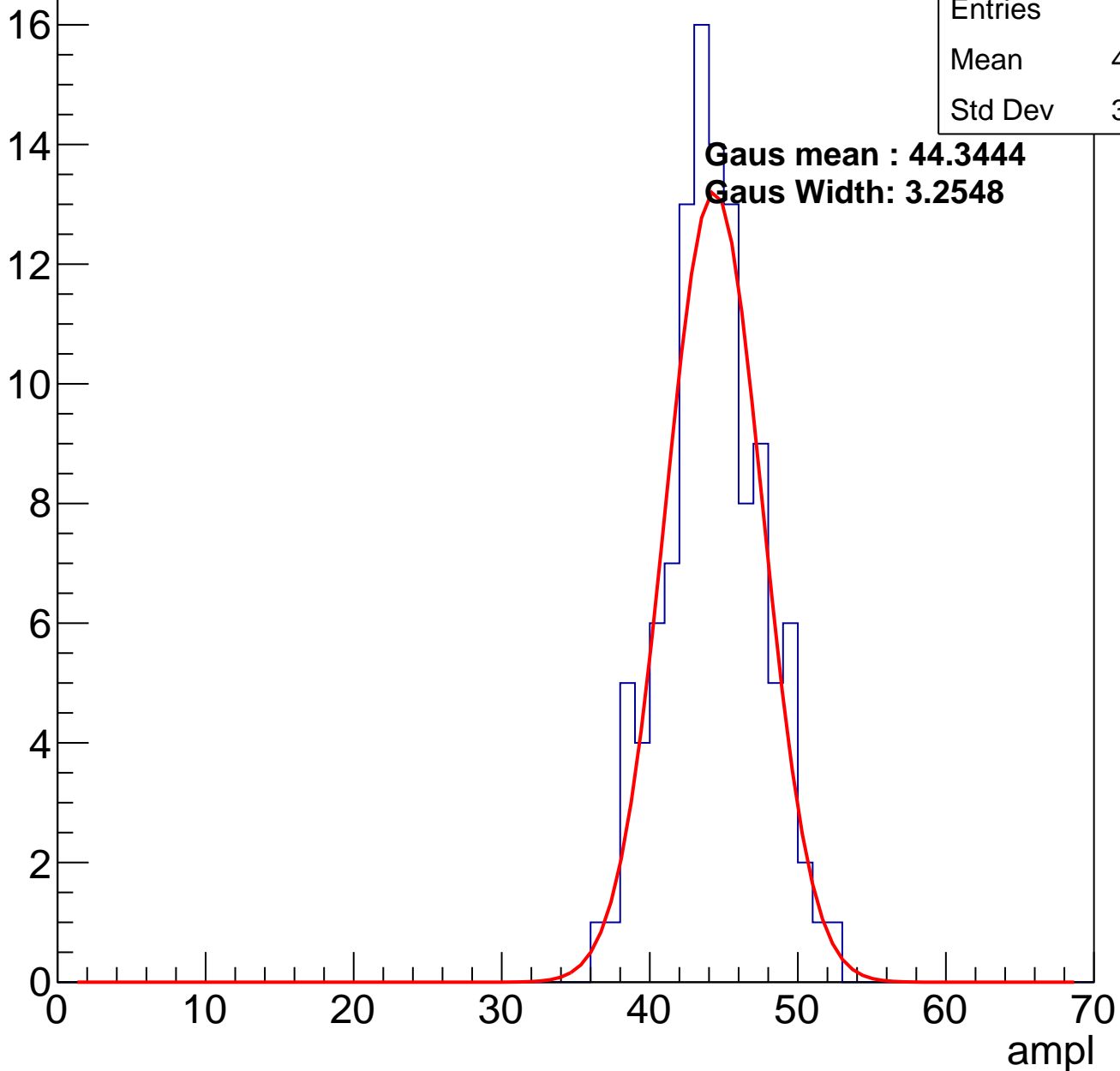
Entry



B1L001S, U22-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

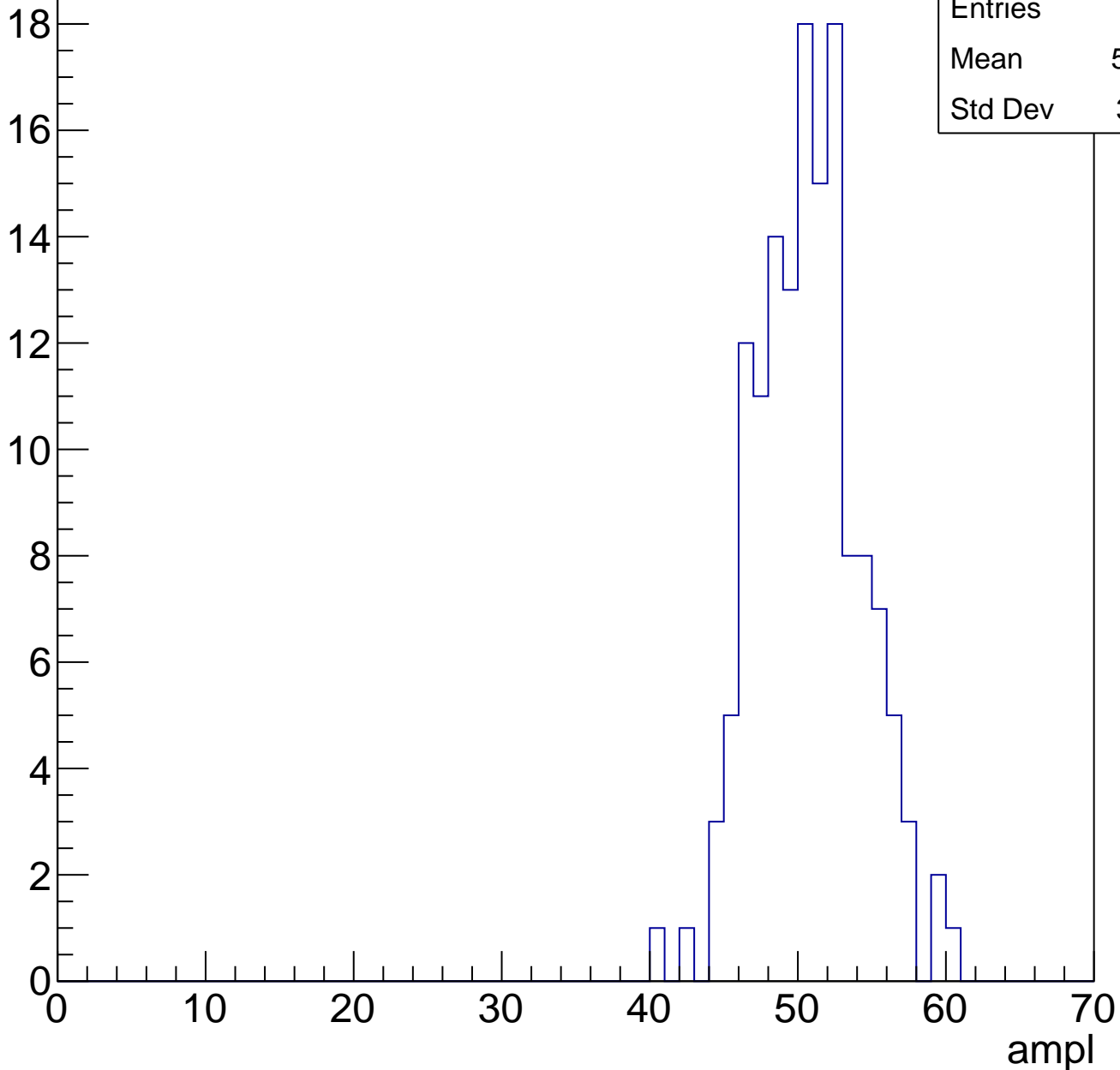
Entry



B1L001S, U22-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch63, adc4

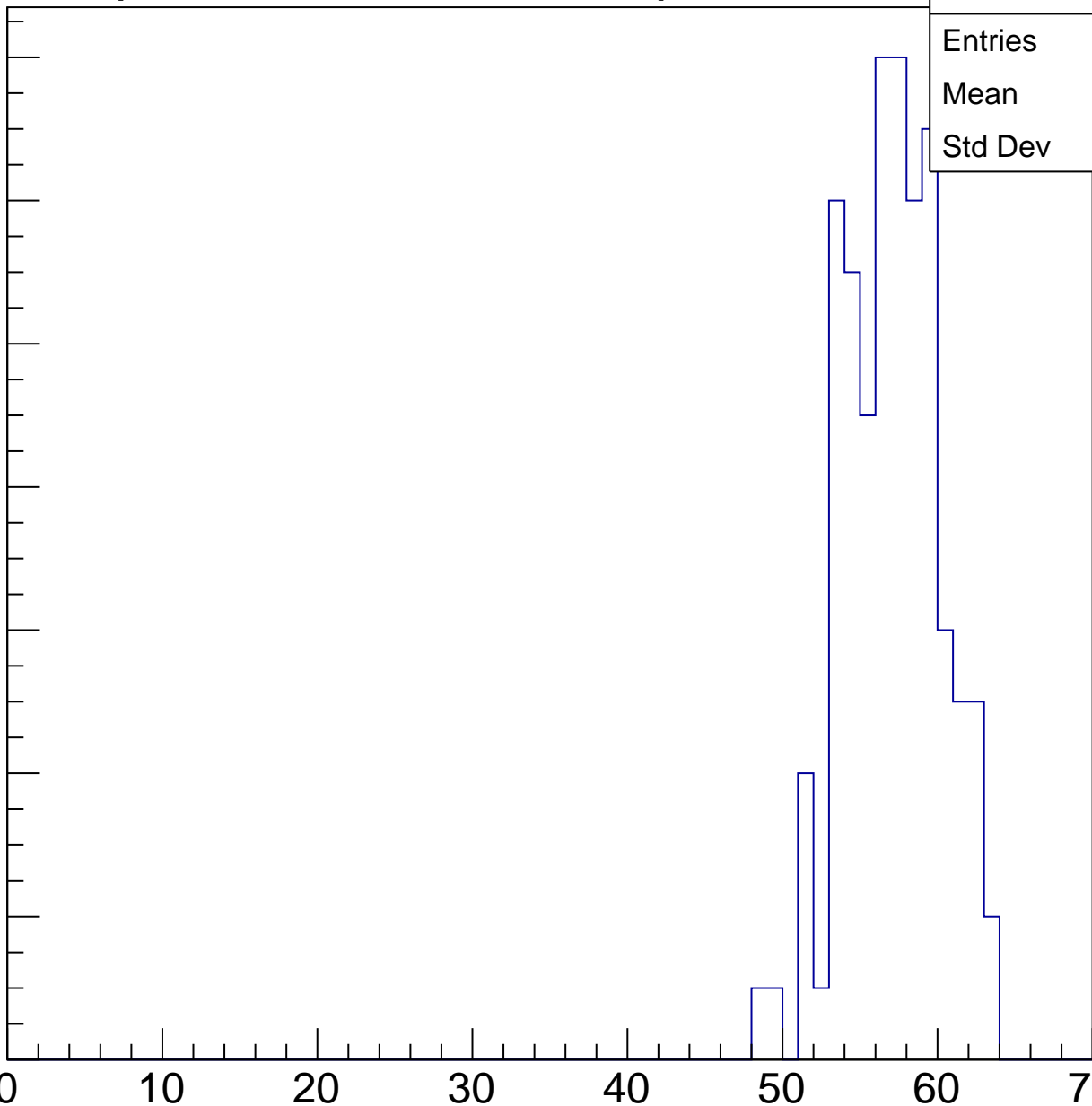
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	56.58
Std Dev	3.061

ampl

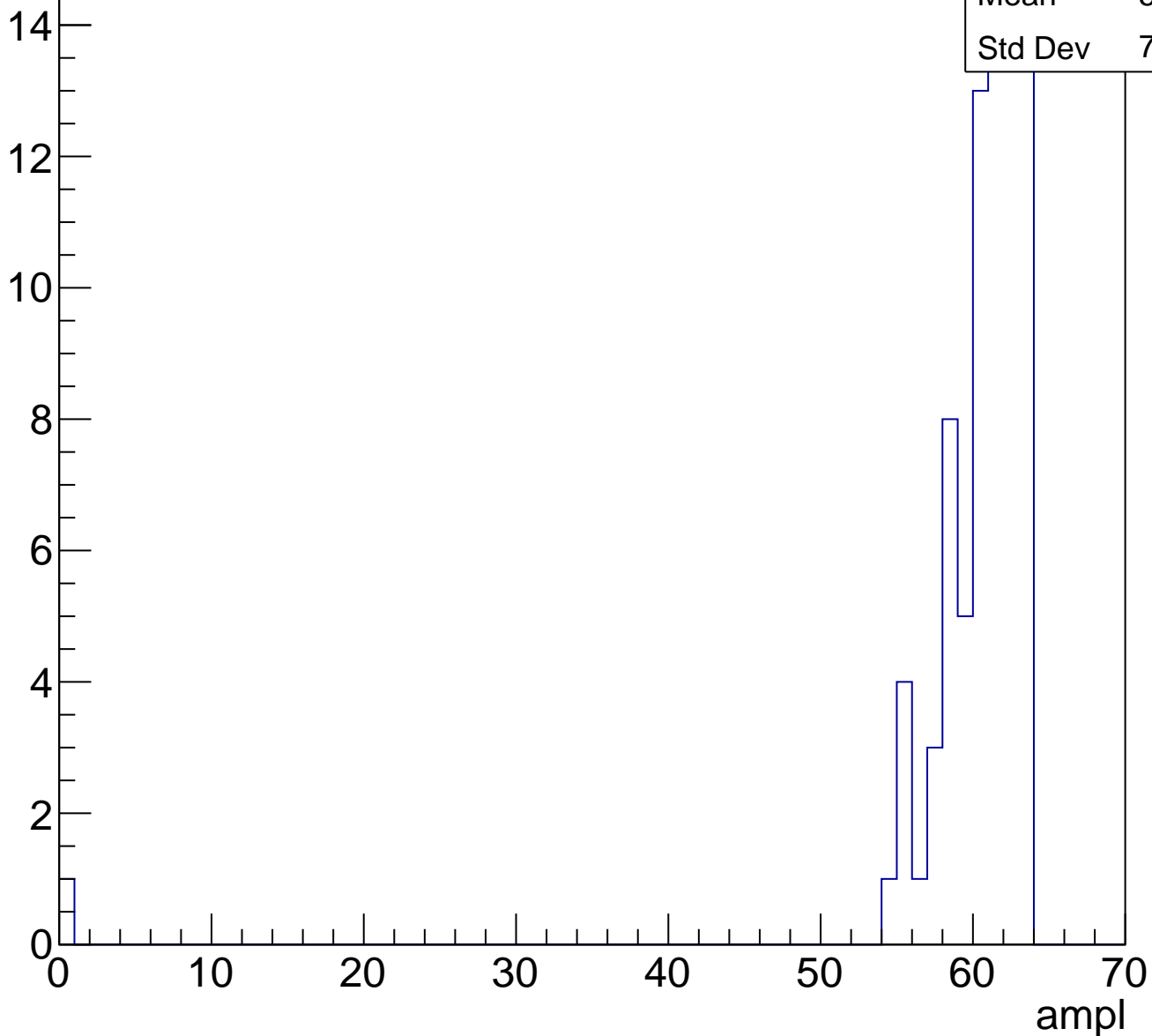


B1L001S, U22-ch63, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	80
Mean	59.59
Std Dev	7.074

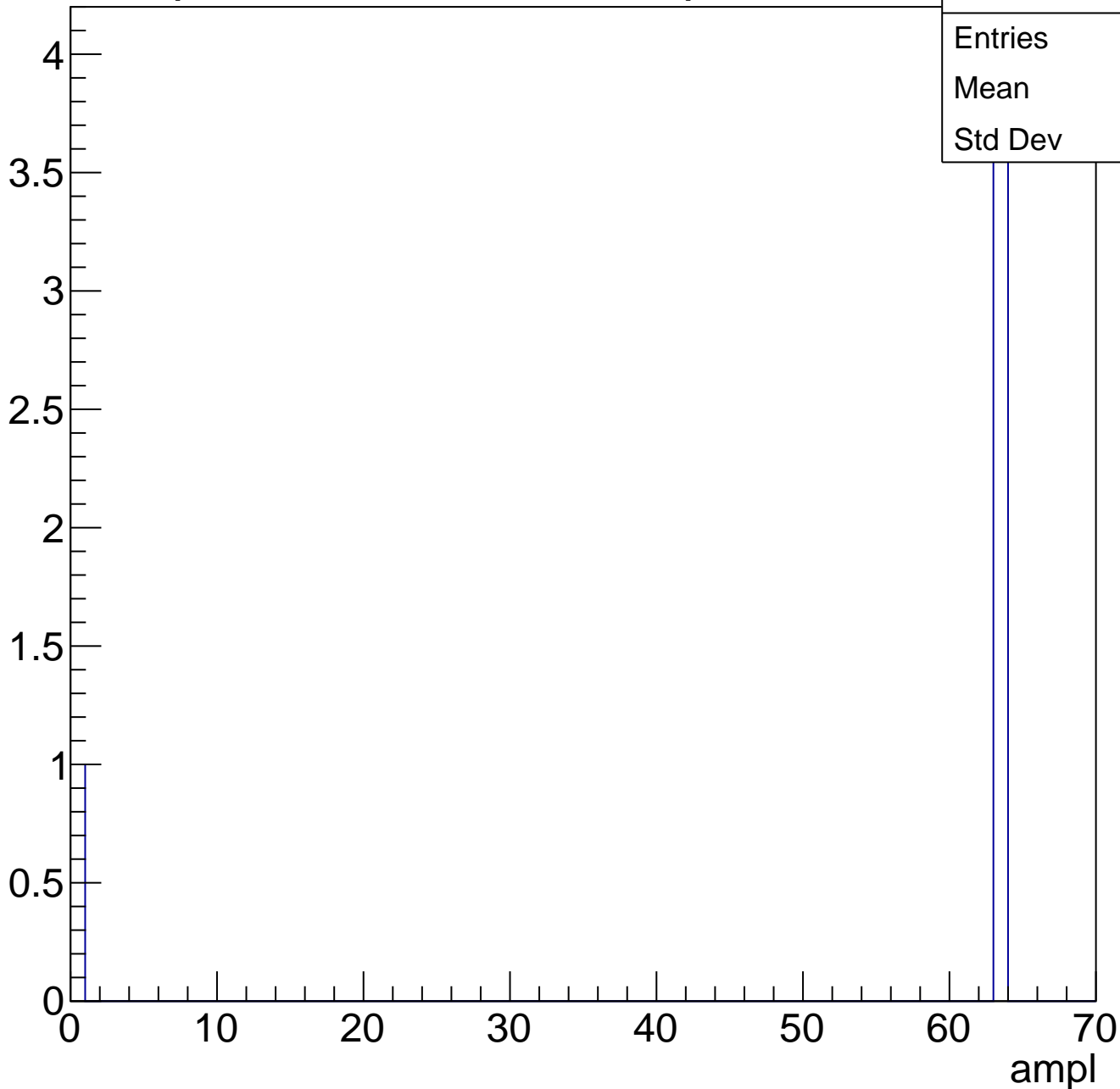
Entry



B1L001S, U22-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch63, adc7

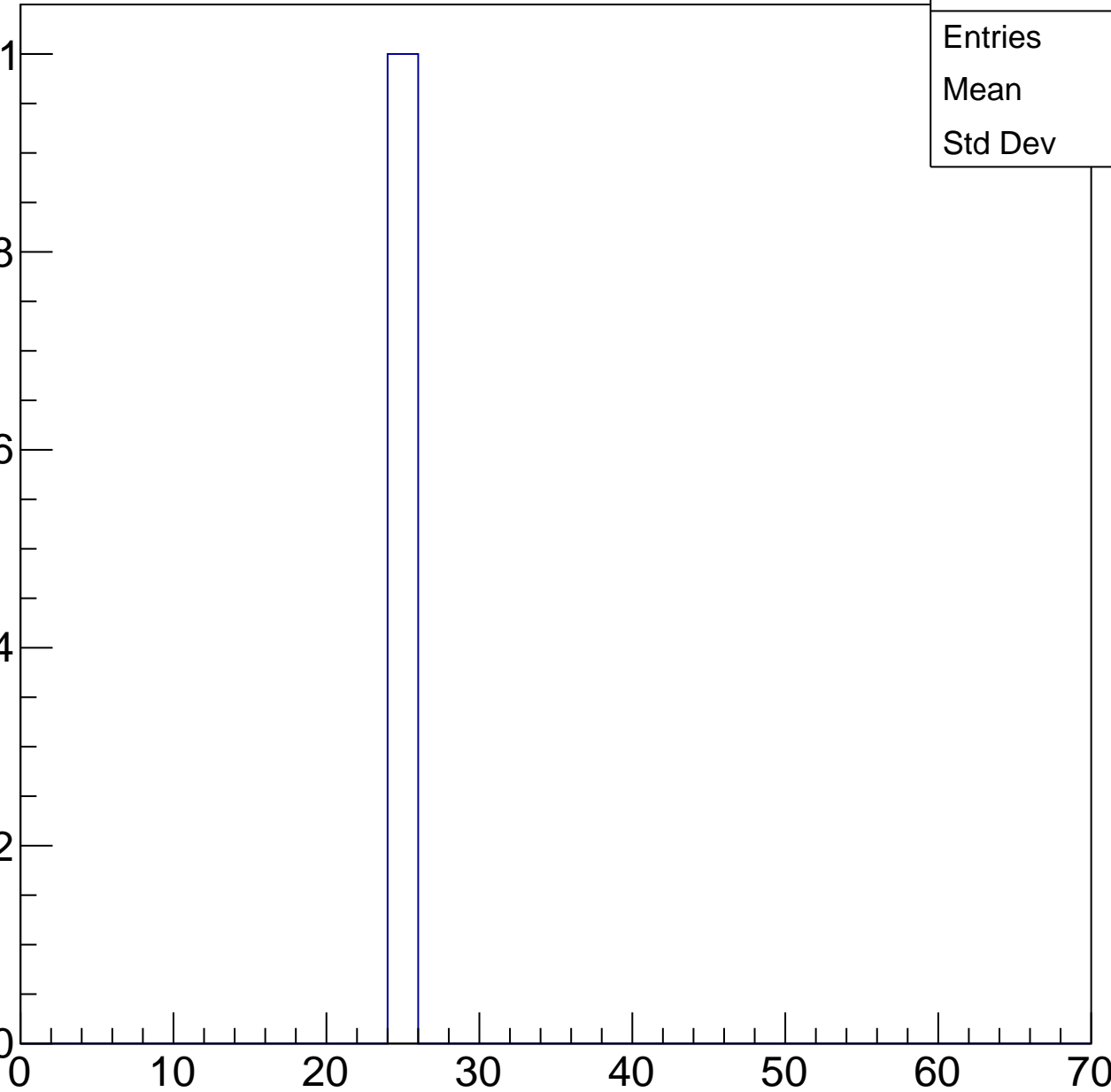
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	24.5
Std Dev	0.5

ampl



B1L001S, U22-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	29.69
Std Dev	2.866

Gaus mean : 30.2092

Gaus Width: 2.9635

Entry

16

14

12

10

8

6

4

2

0

0

10

20

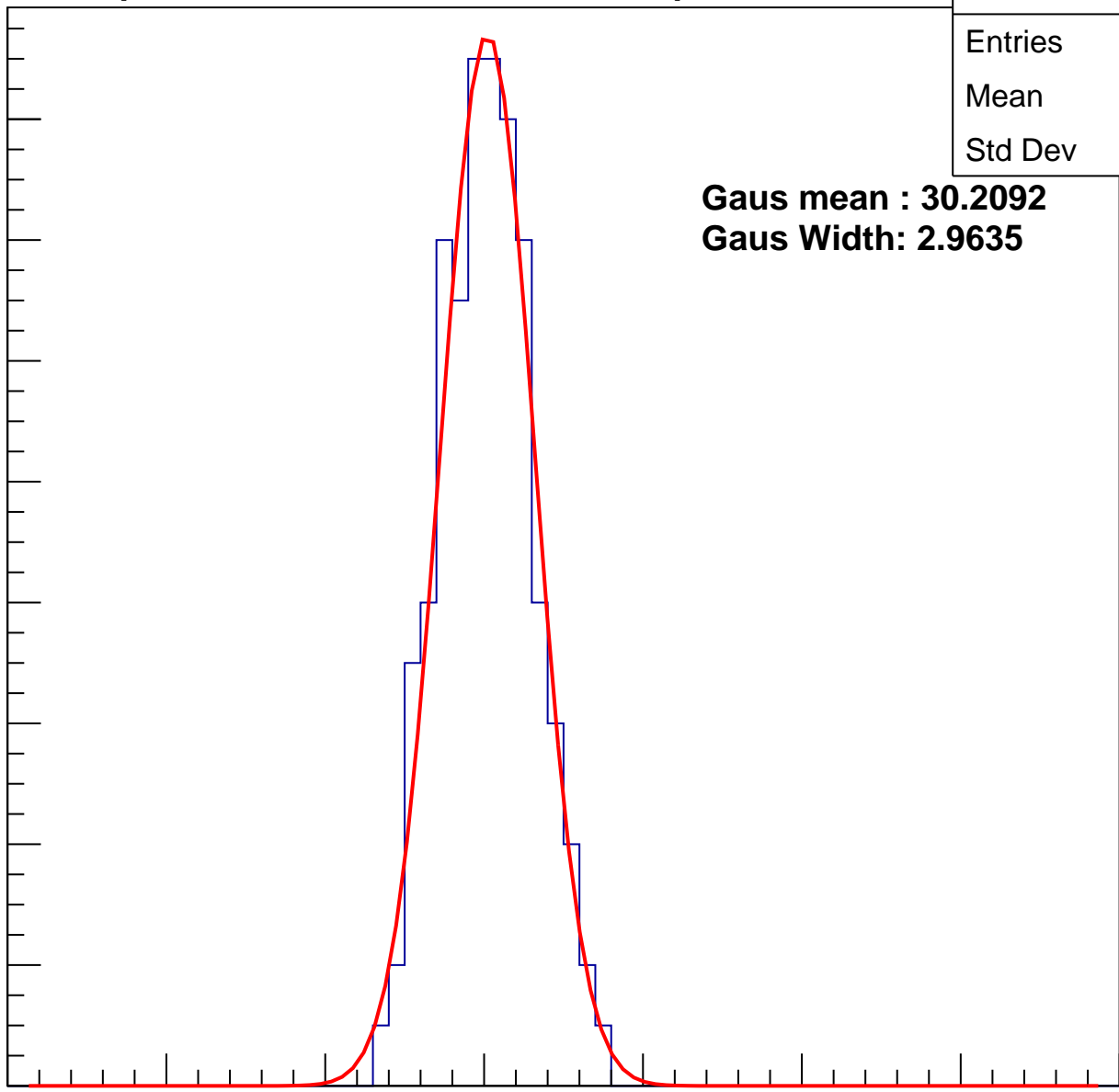
30

40

50

60

ampl



B1L001S, U22-ch64, adc1

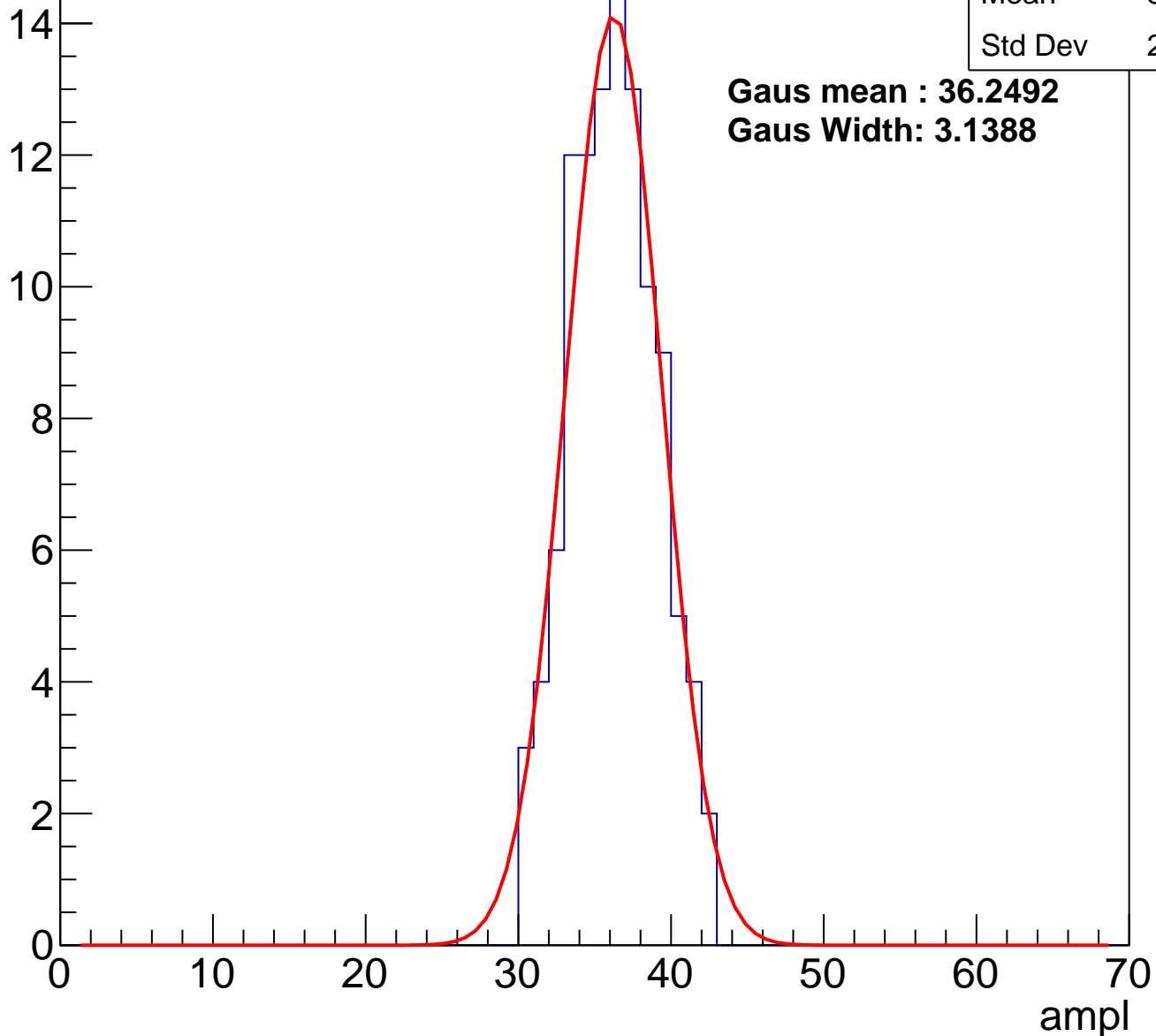
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	35.79
Std Dev	2.812

Gaus mean : 36.2492

Gaus Width: 3.1388

Entry



B1L001S, U22-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

146

Mean

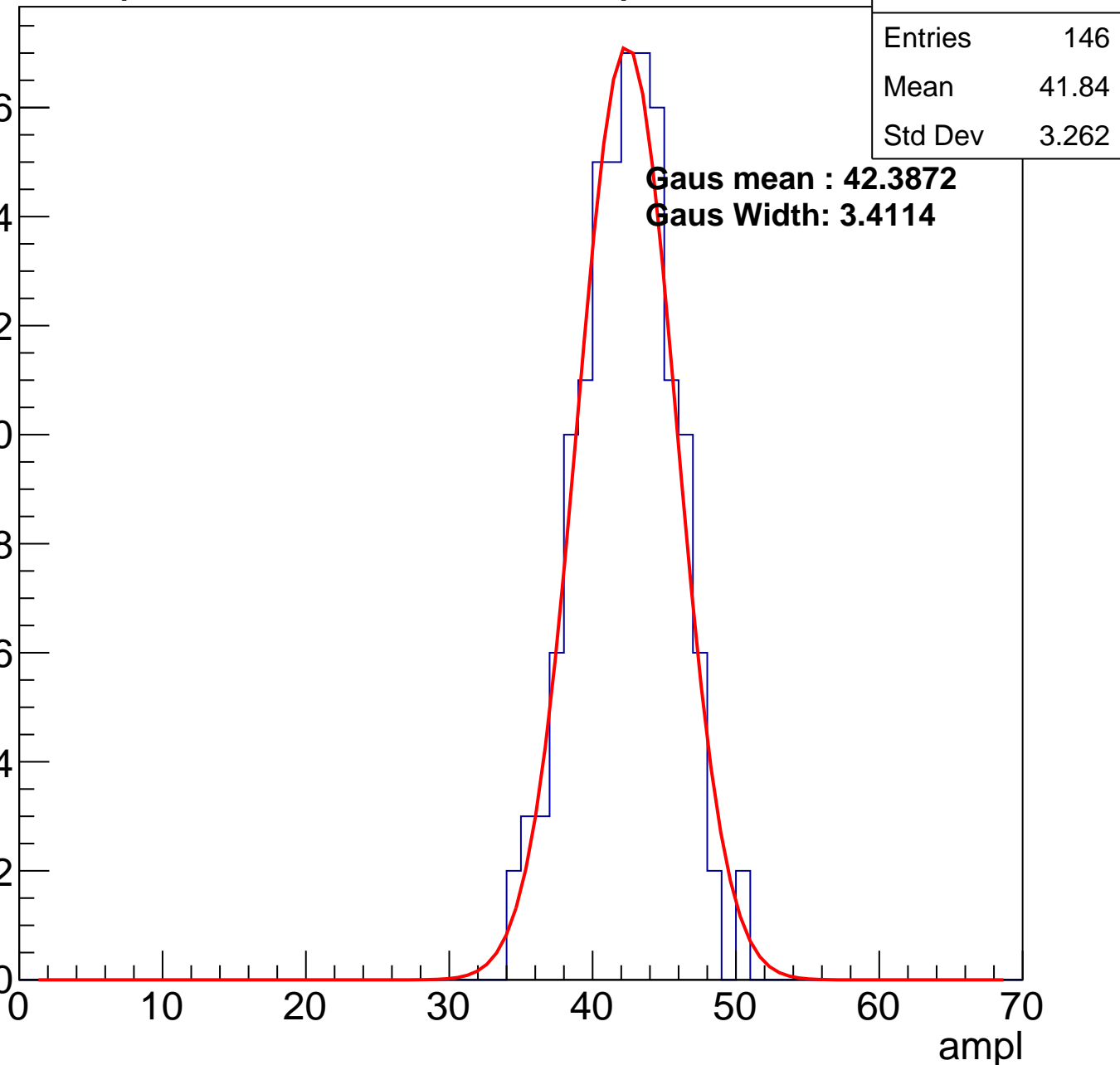
41.84

Std Dev

3.262

Gaus mean : 42.3872

Gaus Width: 3.4114

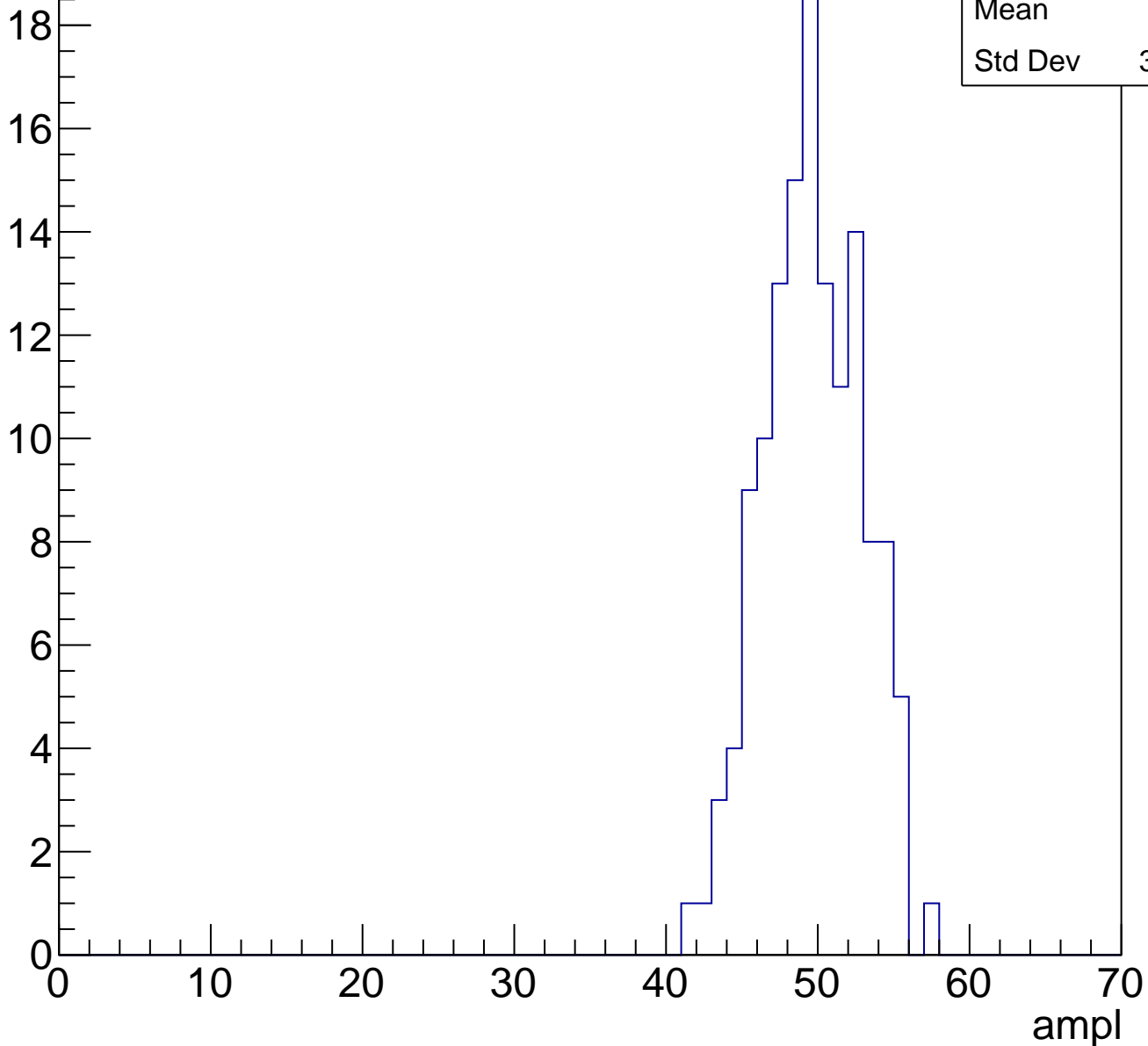


B1L001S, U22-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	49.2
Std Dev	3.187

Entry



B1L001S, U22-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	55.02
Std Dev	3.159

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

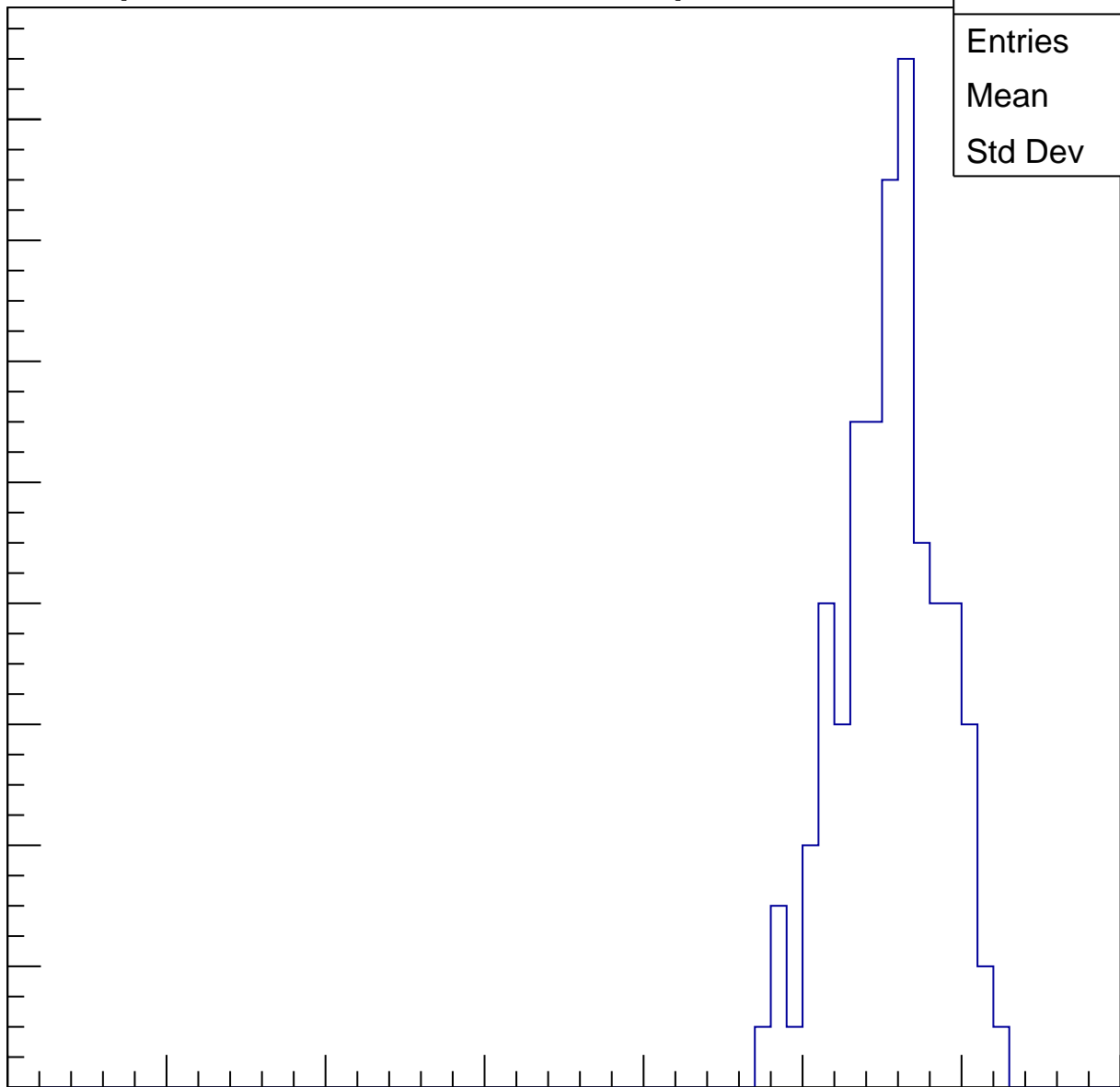
40

50

60

70

ampl



B1L001S, U22-ch64, adc5

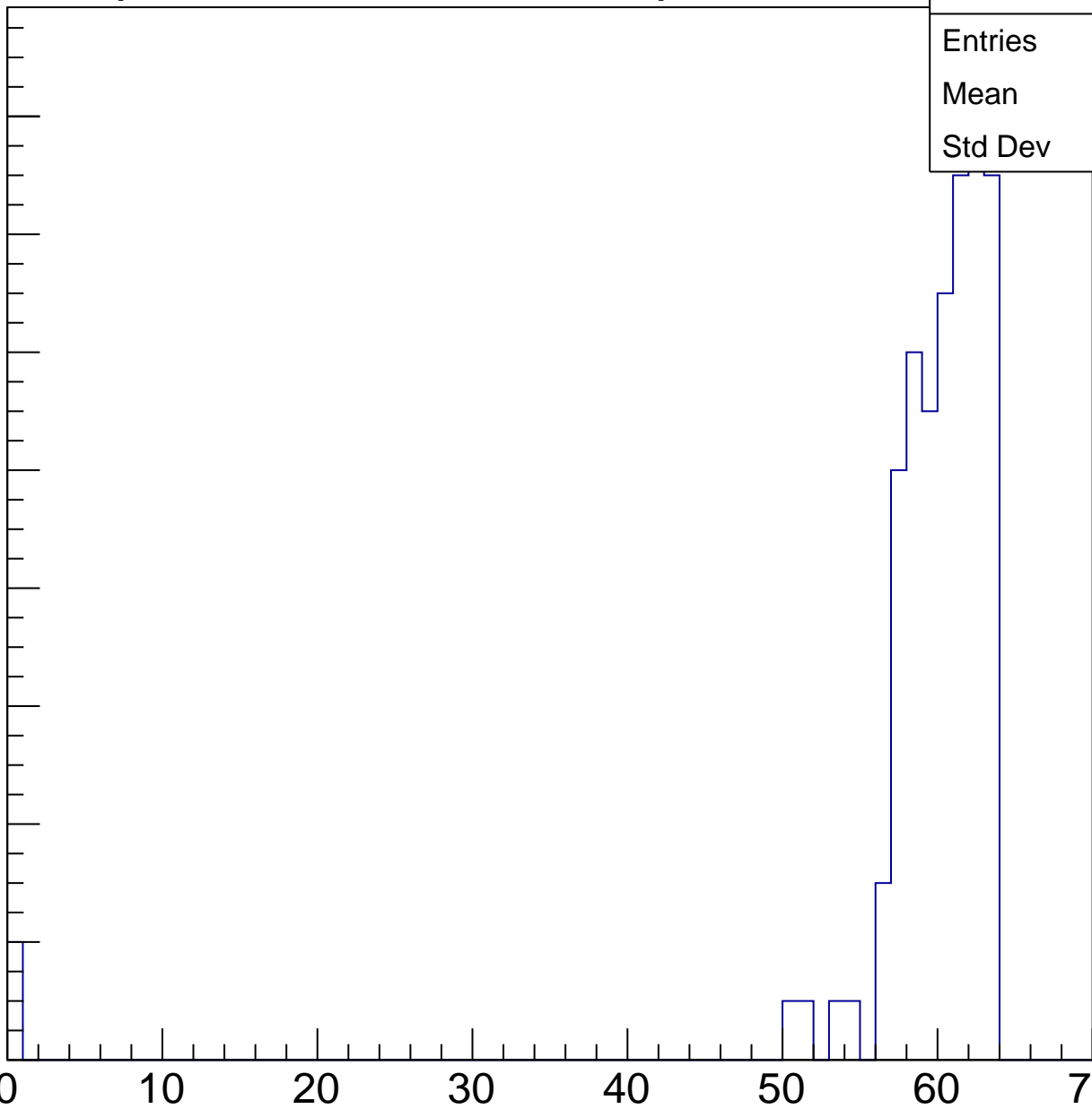
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.68
Std Dev	8.69

ampl

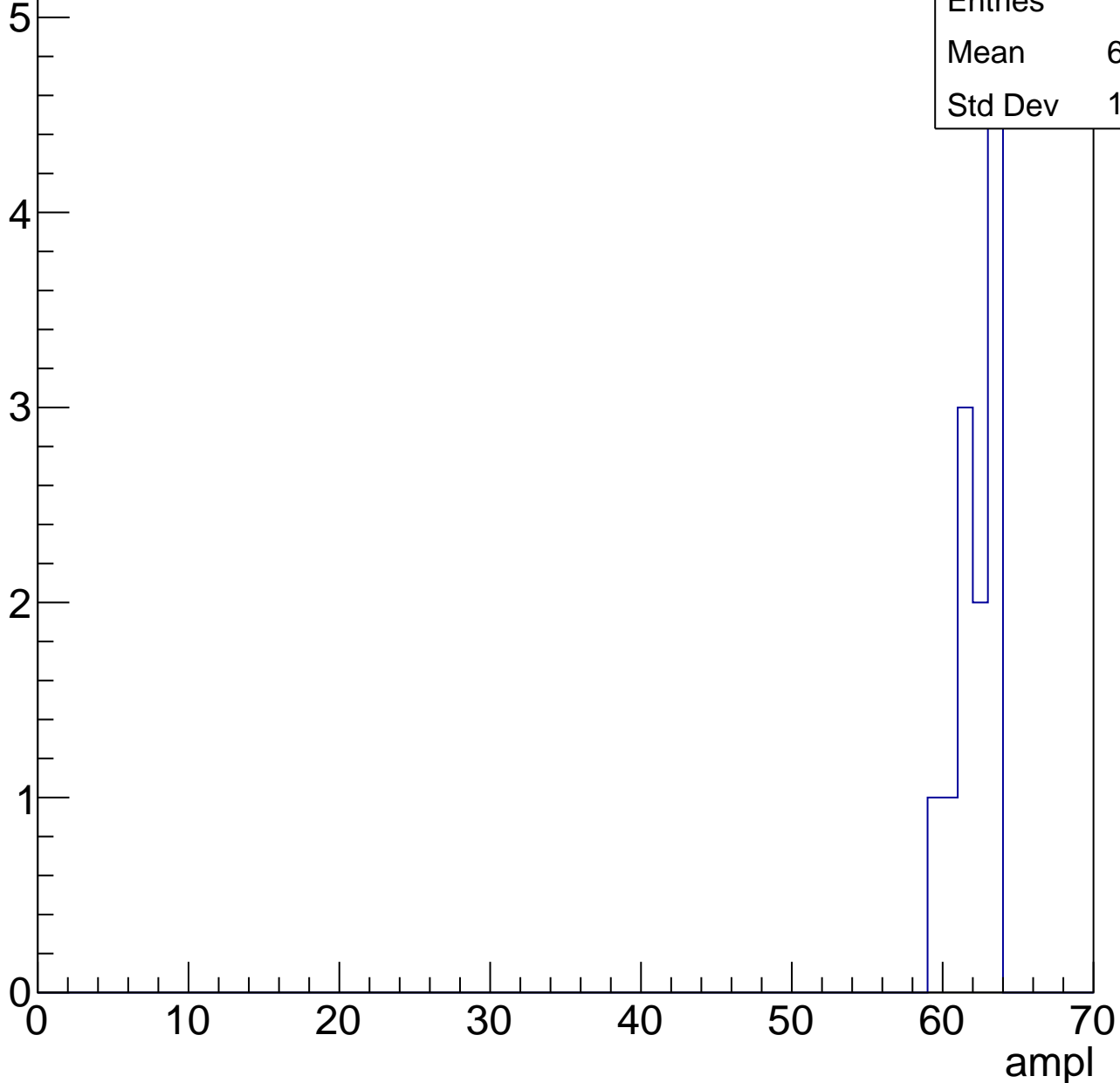


B1L001S, U22-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.299



B1L001S, U22-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch65, adc0

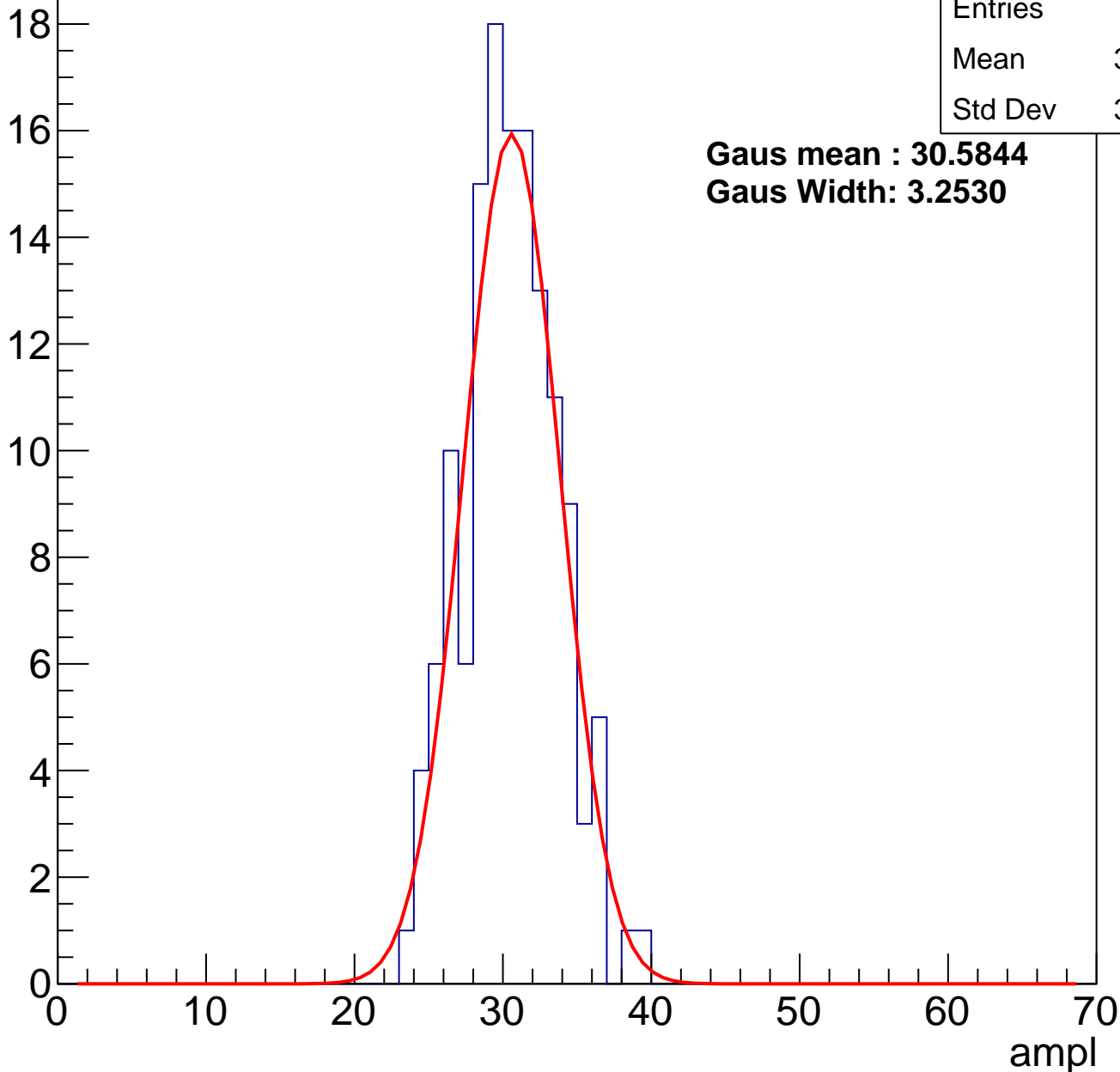
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.04
Std Dev	3.157

Gaus mean : 30.5844

Gaus Width: 3.2530

Entry



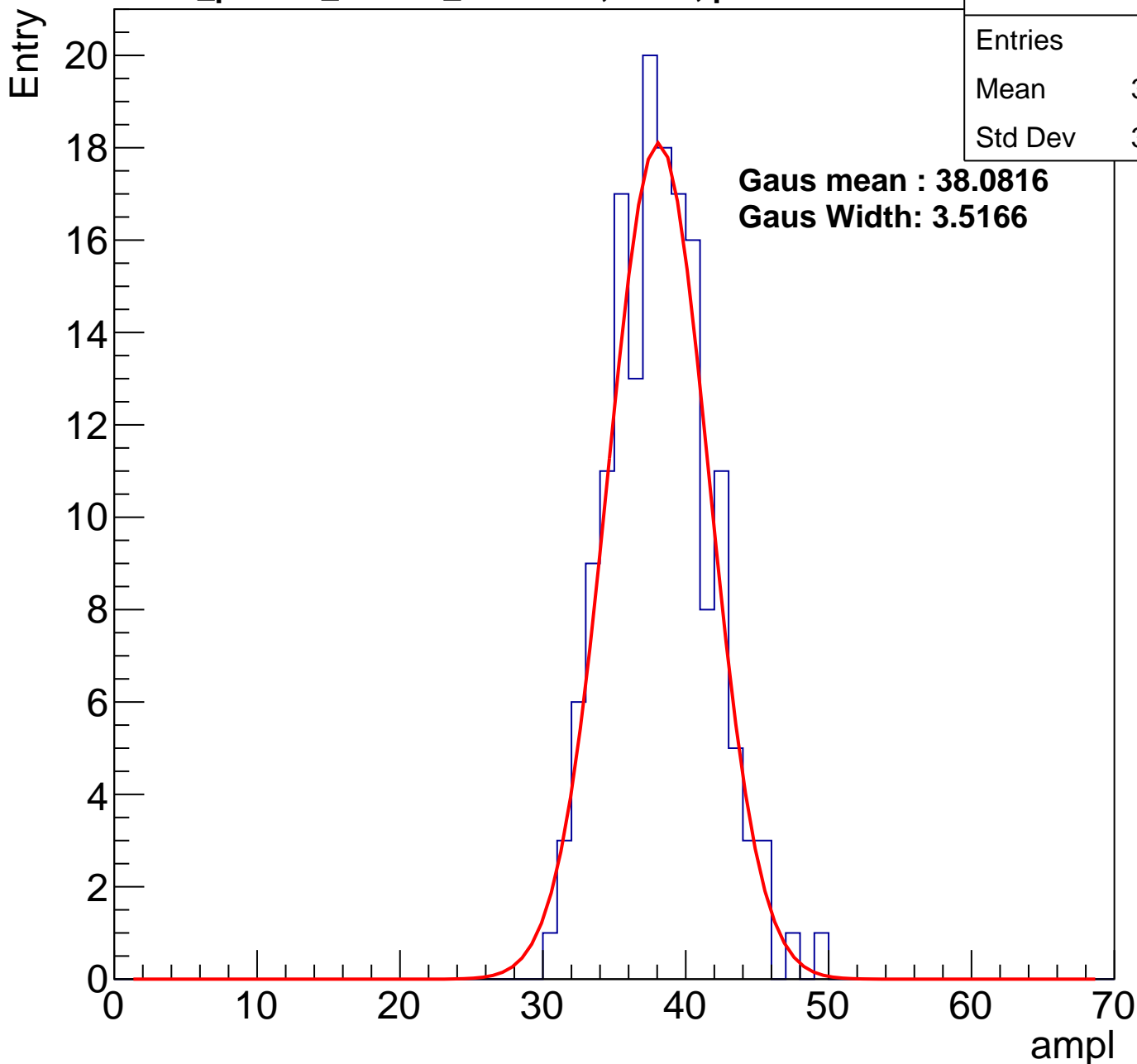
B1L001S, U22-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	37.69
Std Dev	3.458

Gaus mean : 38.0816

Gaus Width: 3.5166



B1L001S, U22-ch65, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	43.85
Std Dev	2.666

Gaus mean : 44.4759

Gaus Width: 2.4849

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

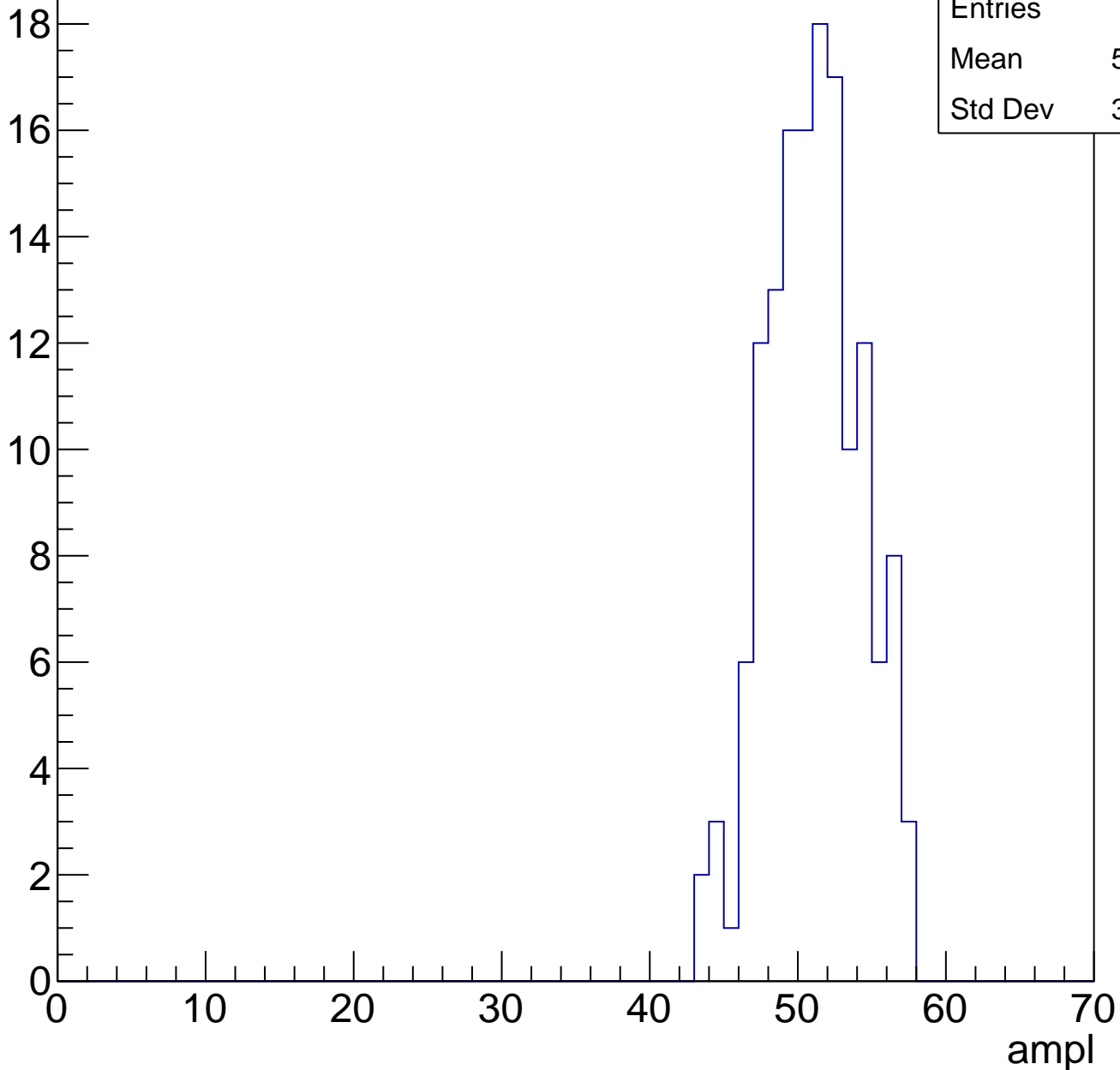
70

B1L001S, U22-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	50.63
Std Dev	3.128

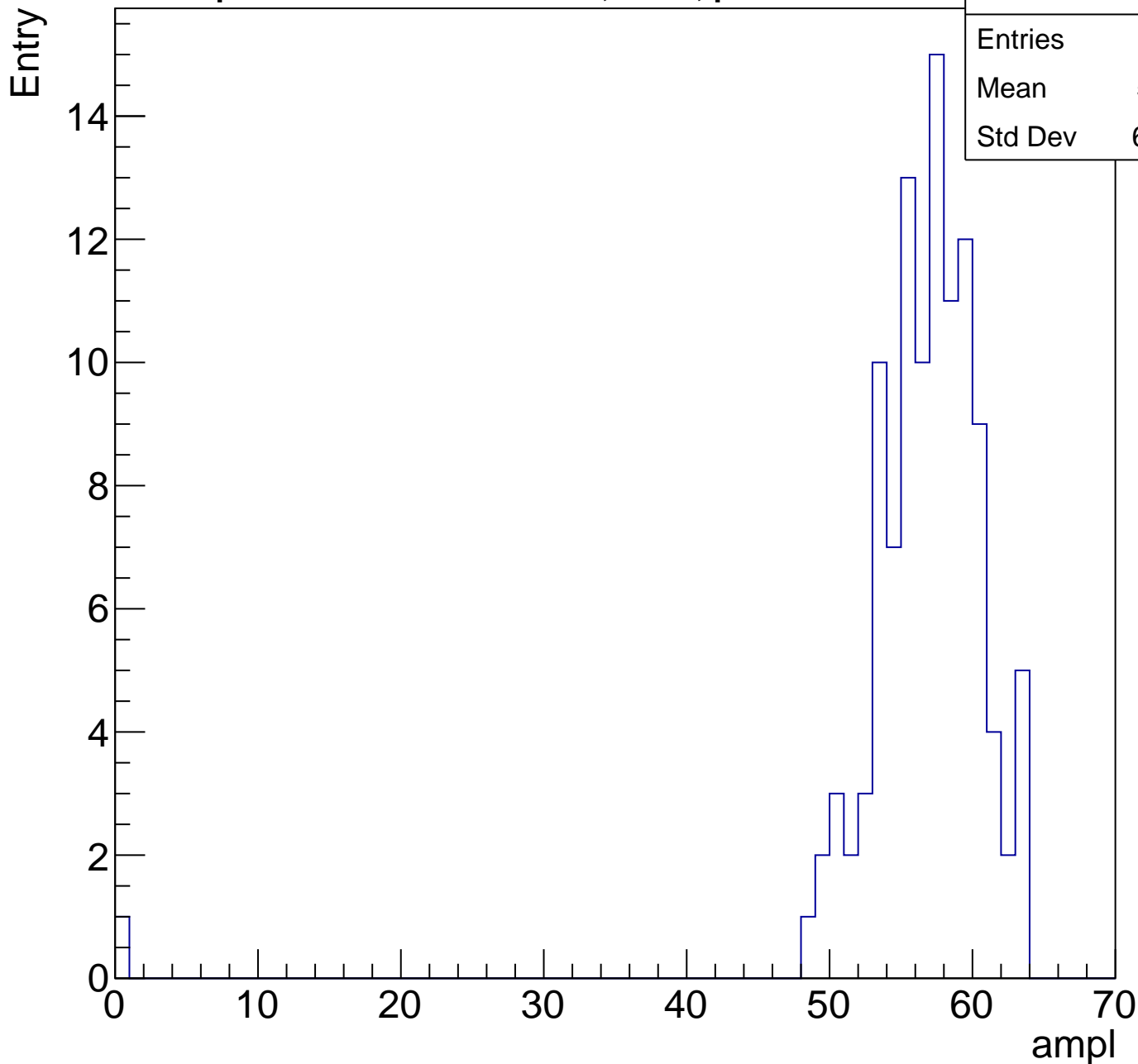
Entry



B1L001S, U22-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56.01
Std Dev	6.298



B1L001S, U22-ch65, adc5

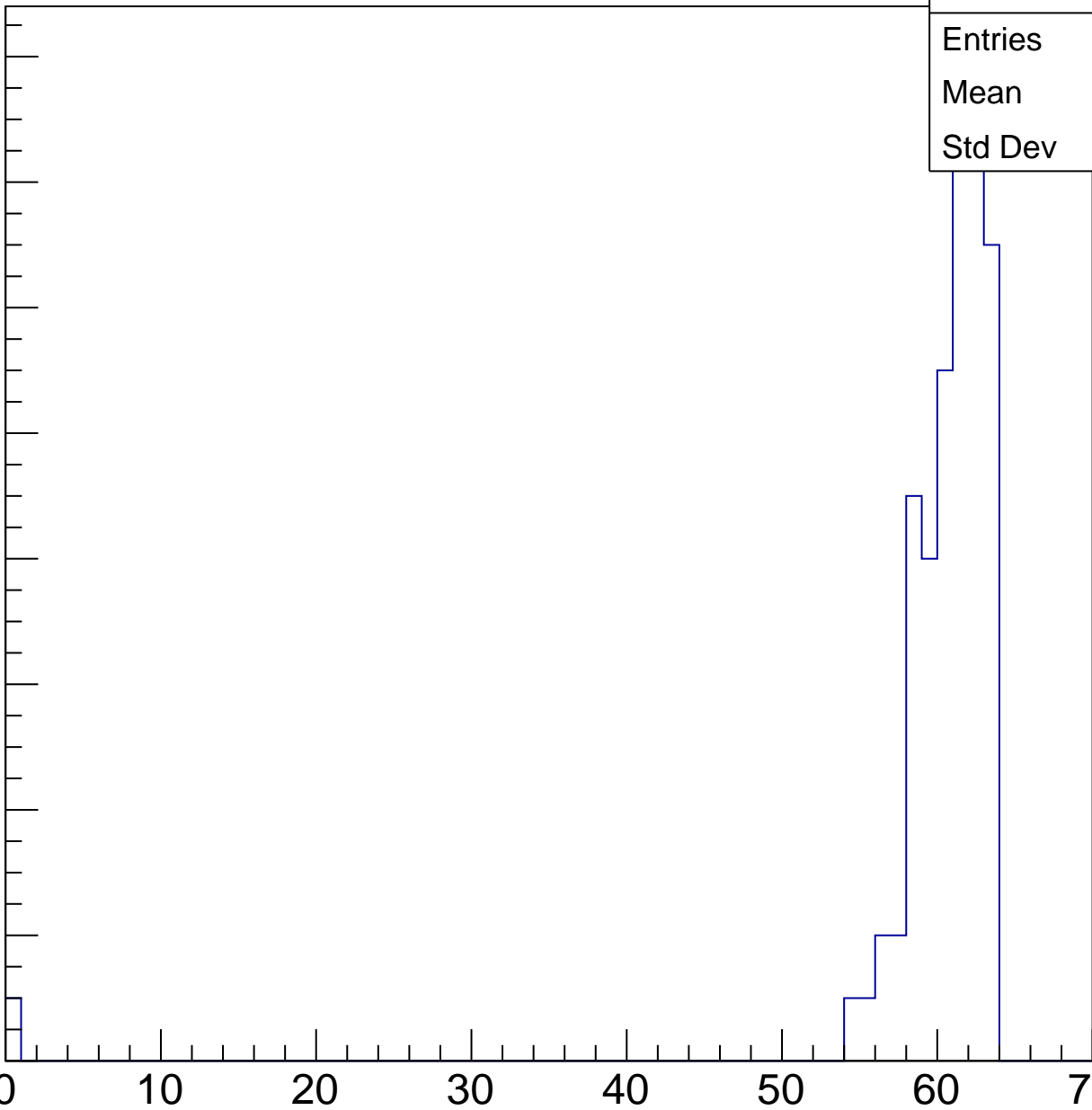
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.7
Std Dev	7.022

ampl



B1L001S, U22-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

ampl

B1L001S, U22-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U22-ch66, adc0

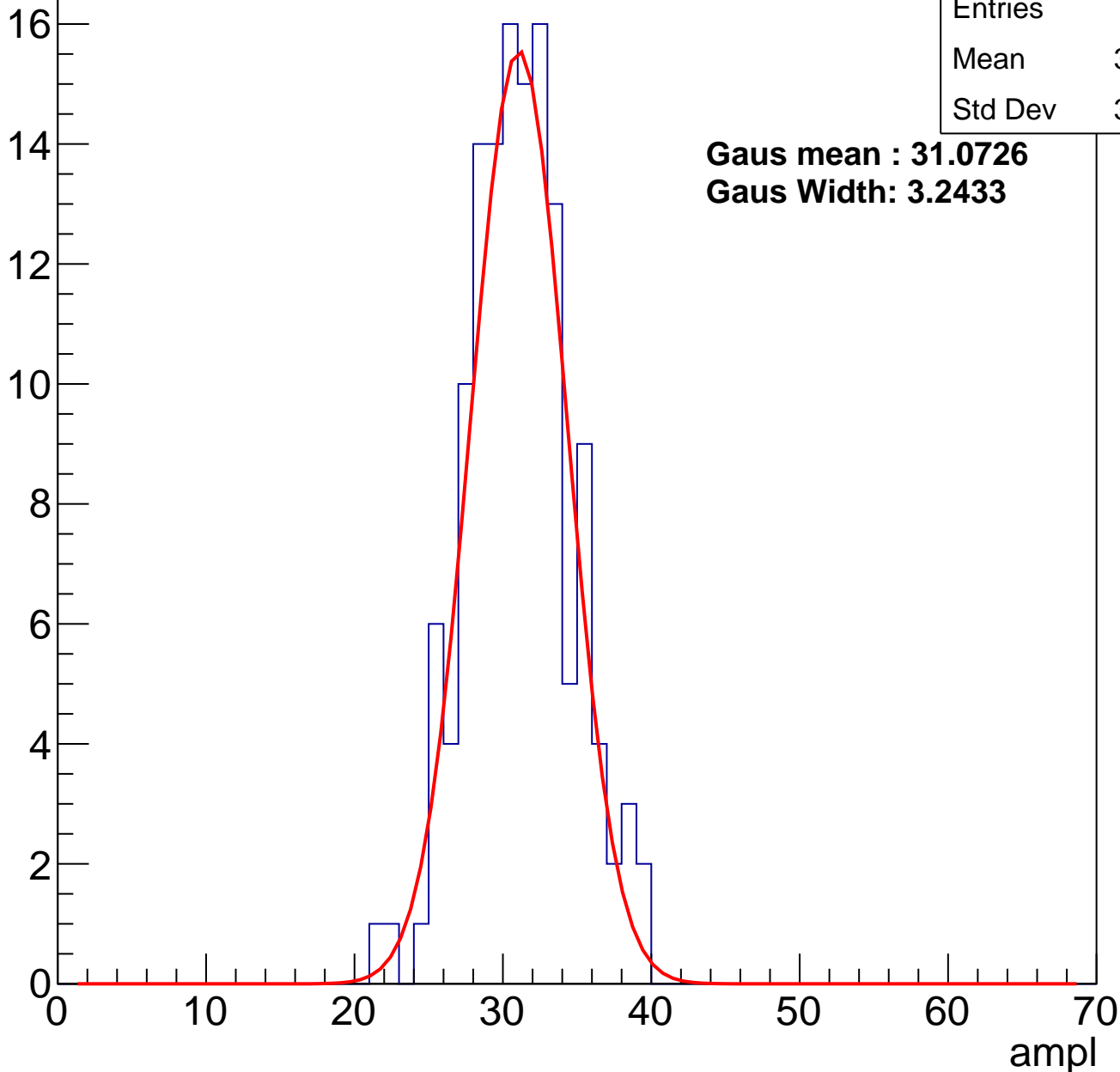
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.66
Std Dev	3.422

Gaus mean : 31.0726

Gaus Width: 3.2433

Entry



B1L001S, U22-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

159

Mean

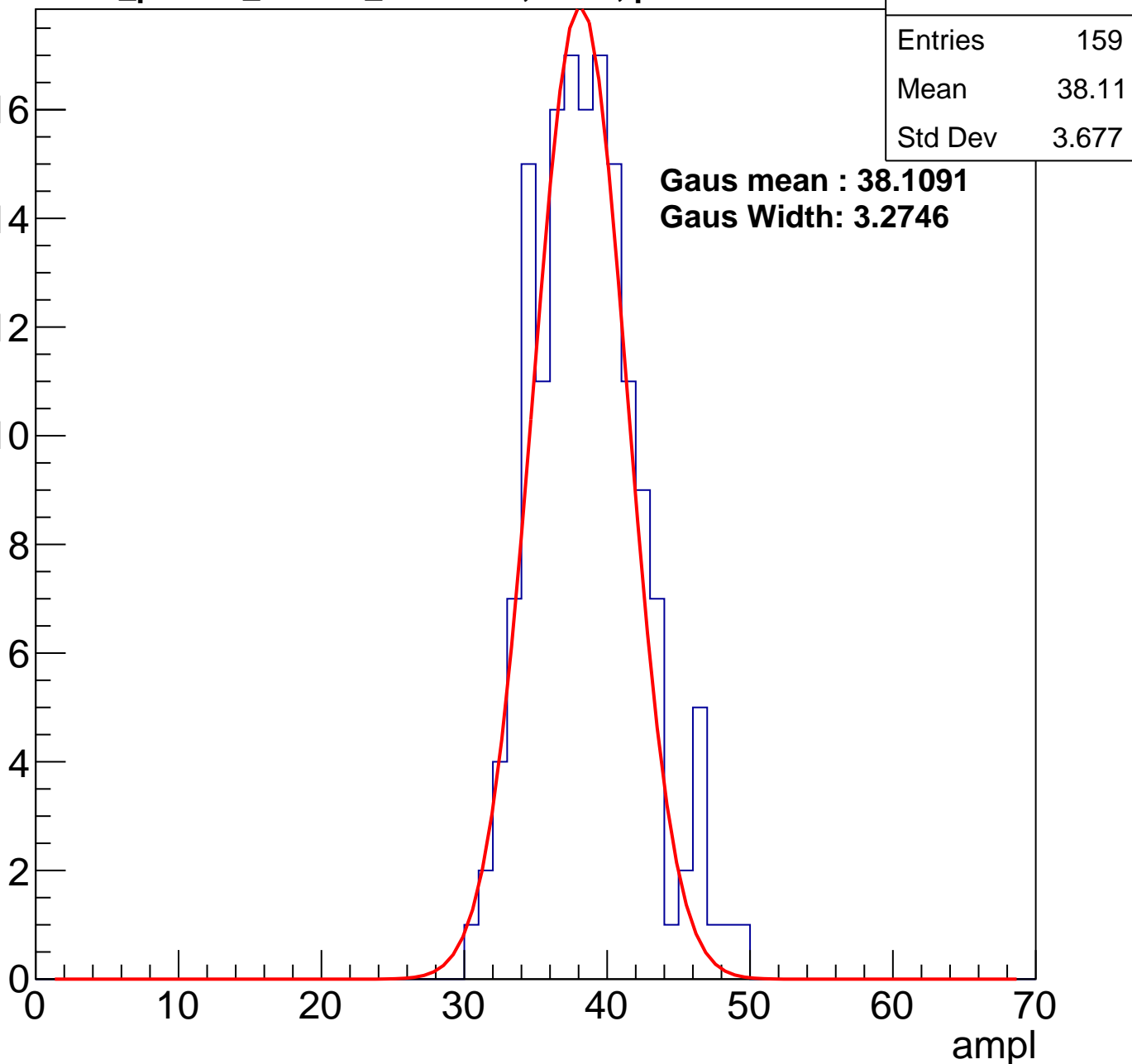
38.11

Std Dev

3.677

Gaus mean : 38.1091

Gaus Width: 3.2746



B1L001S, U22-ch66, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

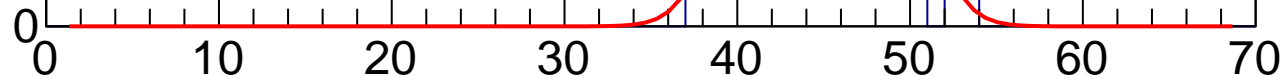
60

ampl

Entries	84
Mean	44.31
Std Dev	3.09

Gaus mean : 44.9113

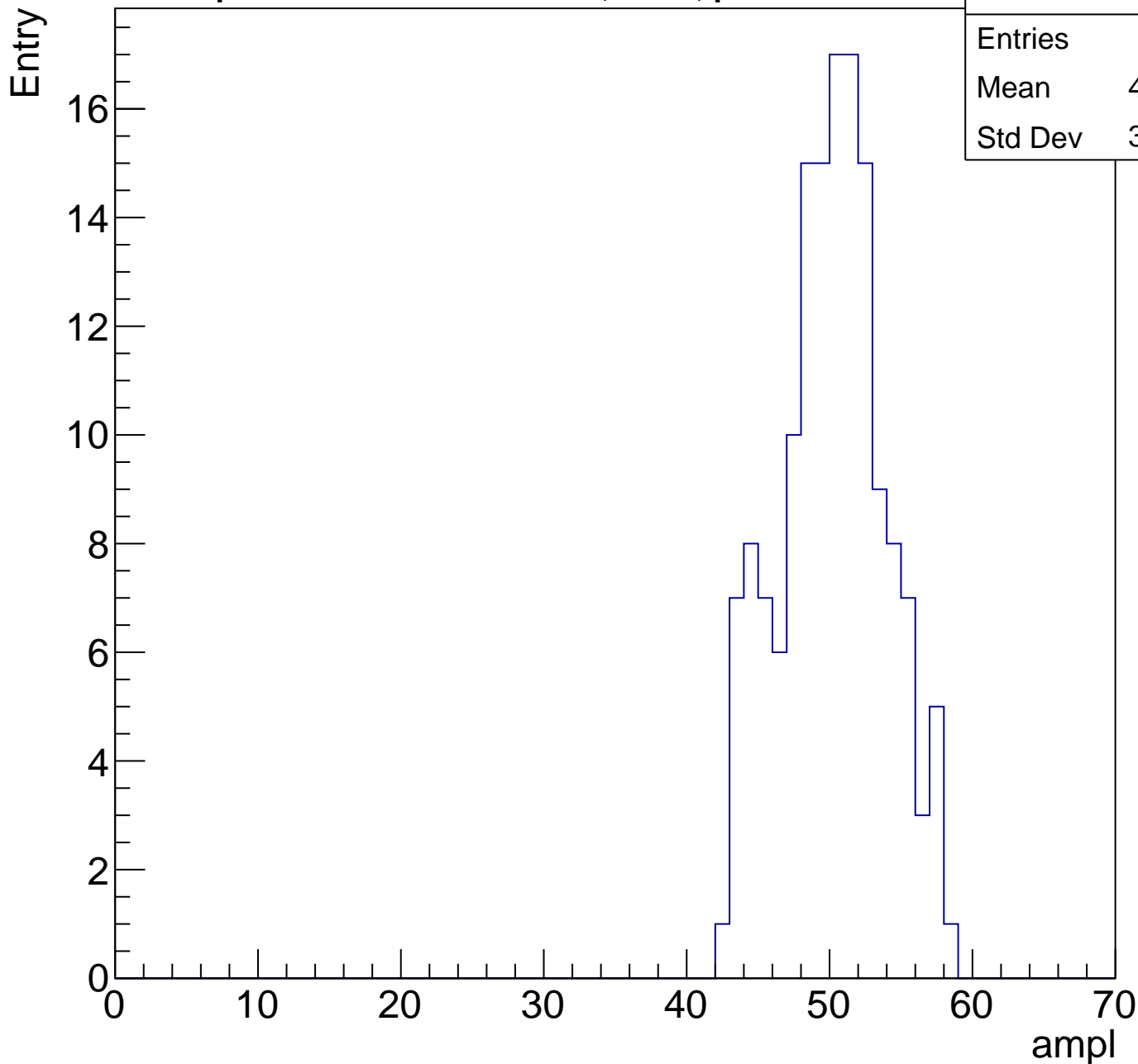
Gaus Width: 3.0540



B1L001S, U22-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	49.75
Std Dev	3.639



B1L001S, U22-ch66, adc4

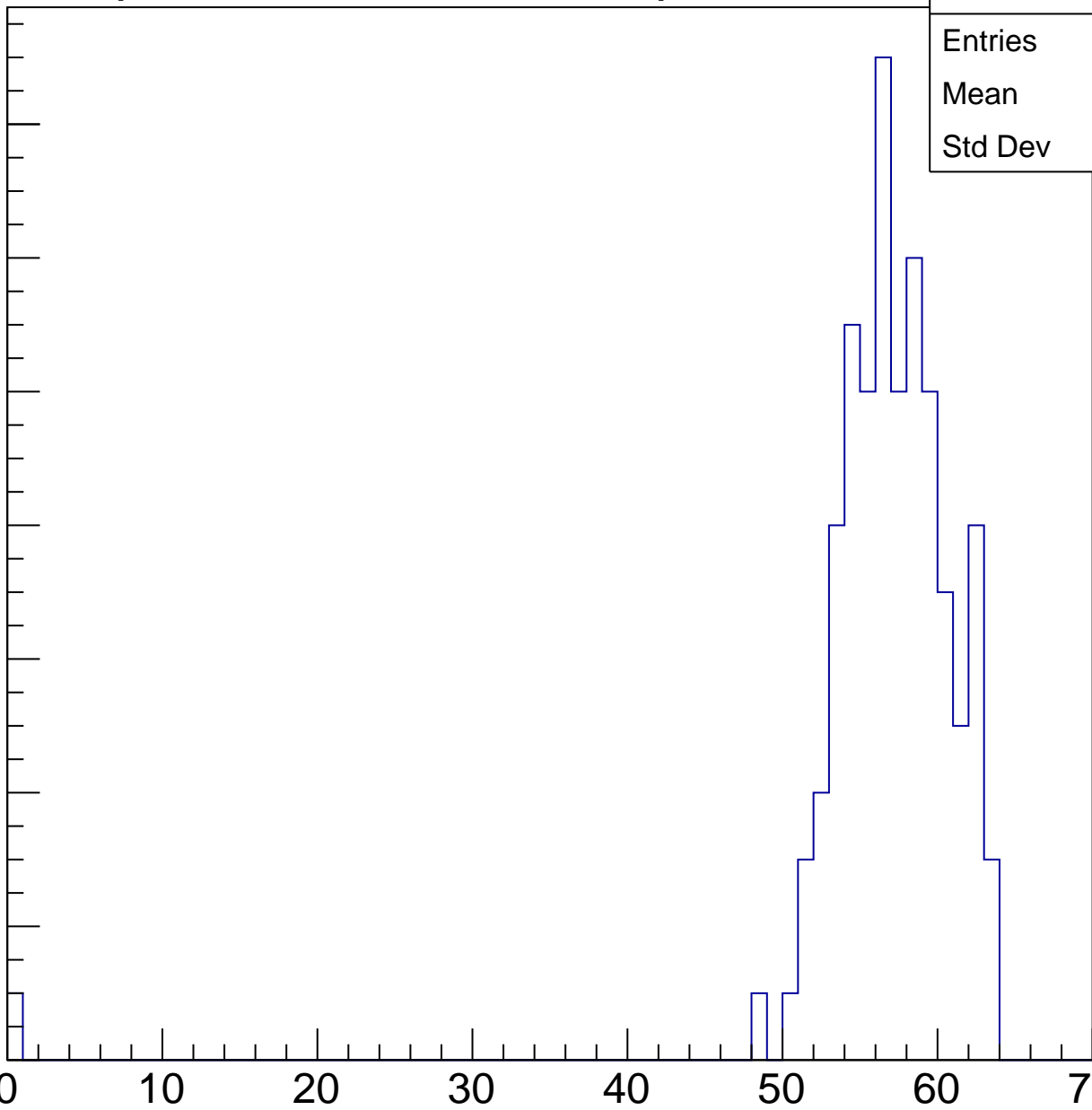
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	56.27
Std Dev	6.291

ampl



B1L001S, U22-ch66, adc5

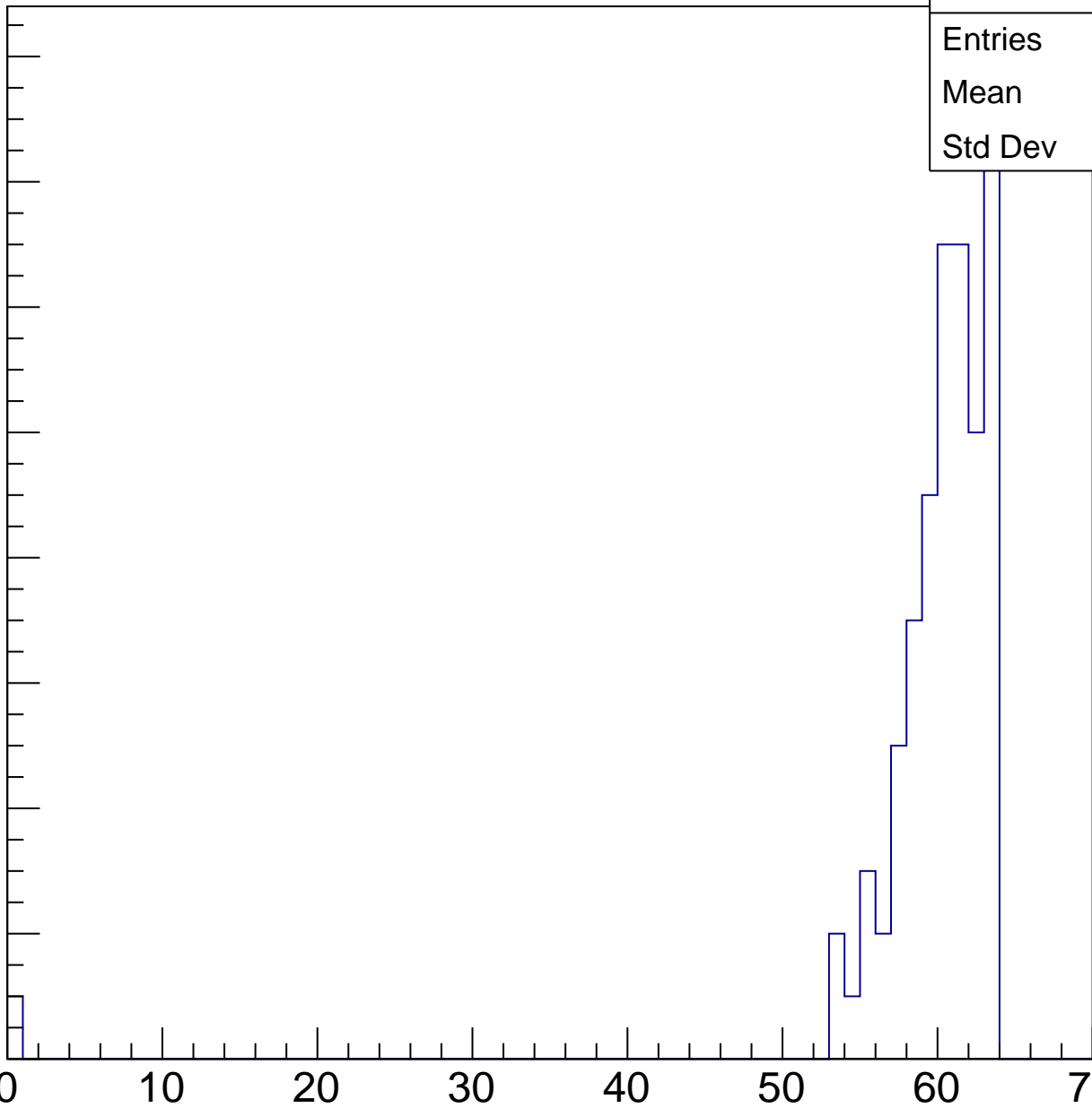
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.27
Std Dev	7.05

ampl



B1L001S, U22-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

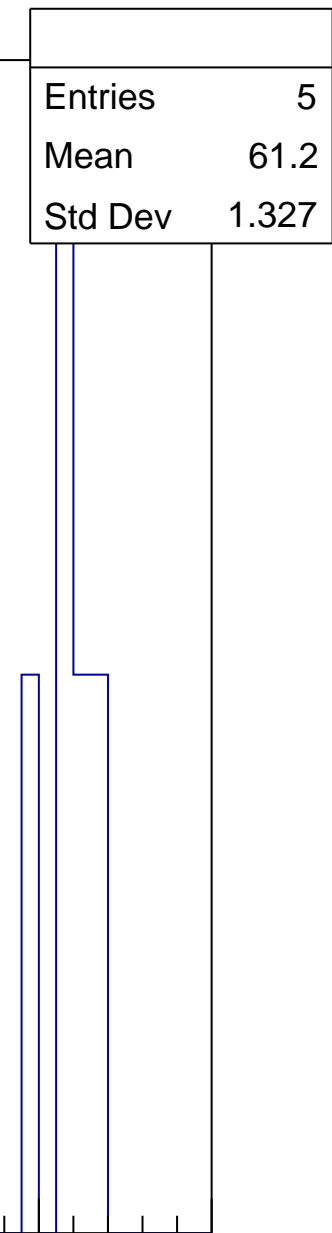
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.327

0 10 20 30 40 50 60 70

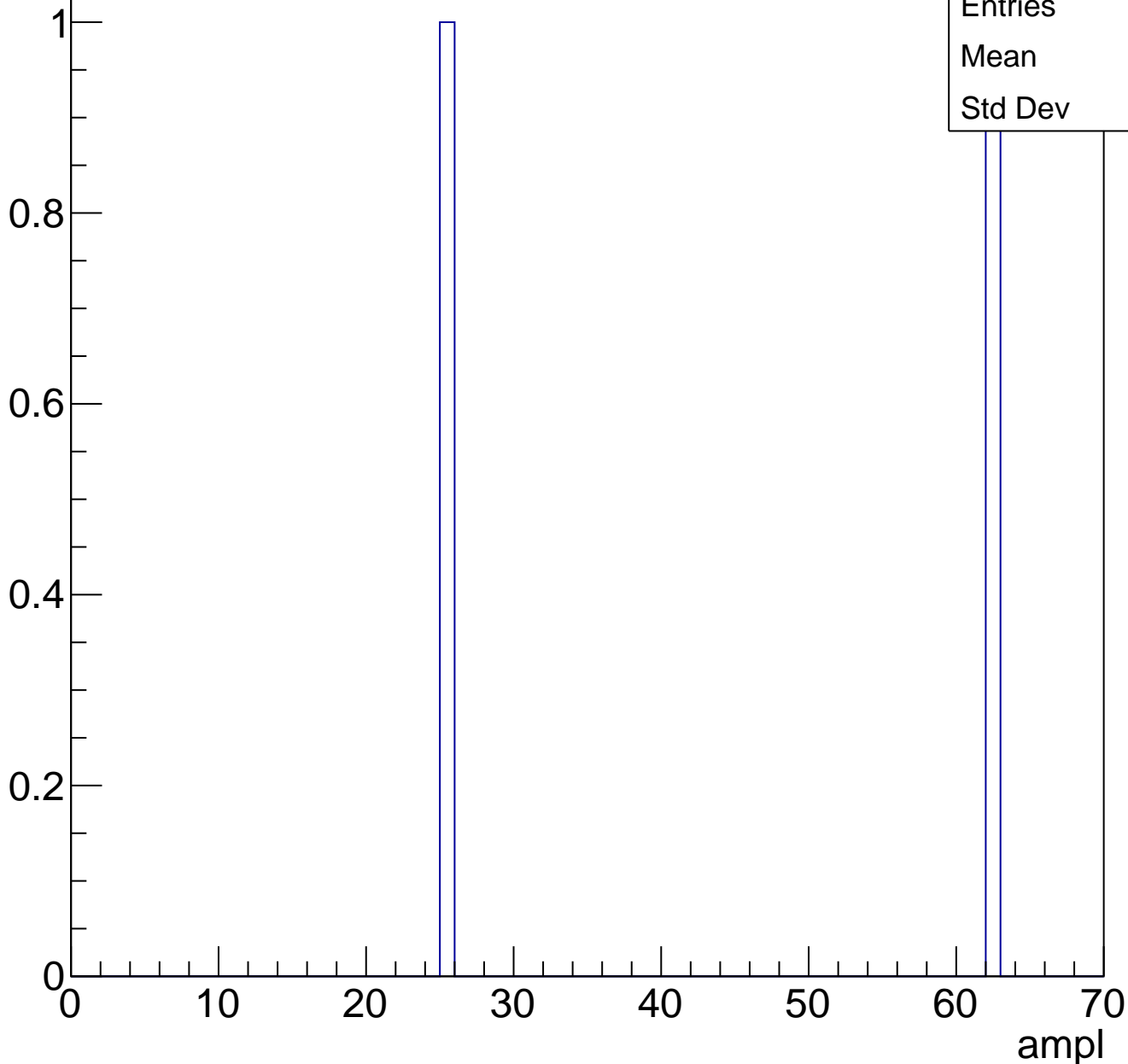
ampl



B1L001S, U22-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch67, adc0

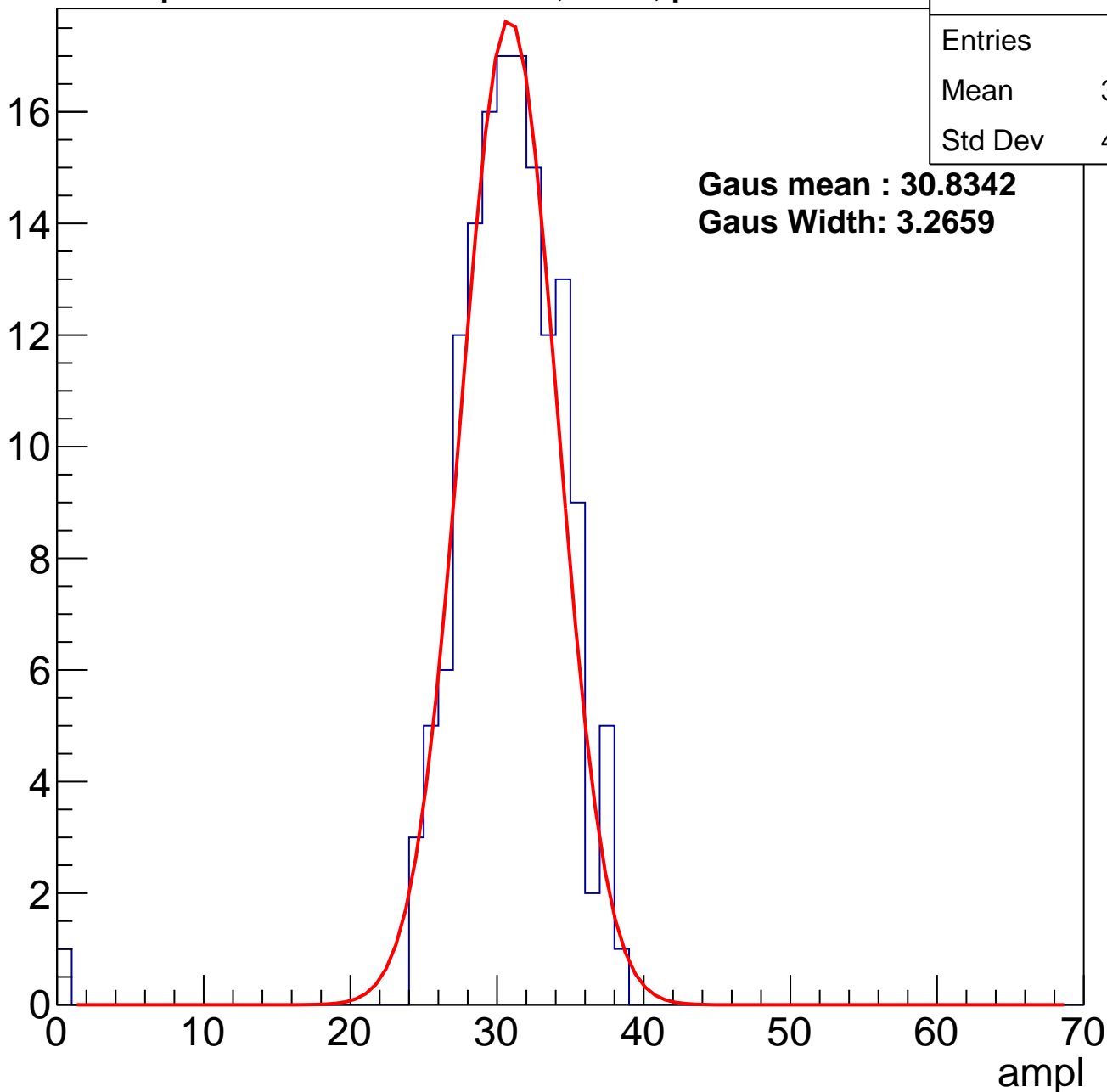
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	148
Mean	30.39
Std Dev	4.015

Gaus mean : 30.8342

Gaus Width: 3.2659



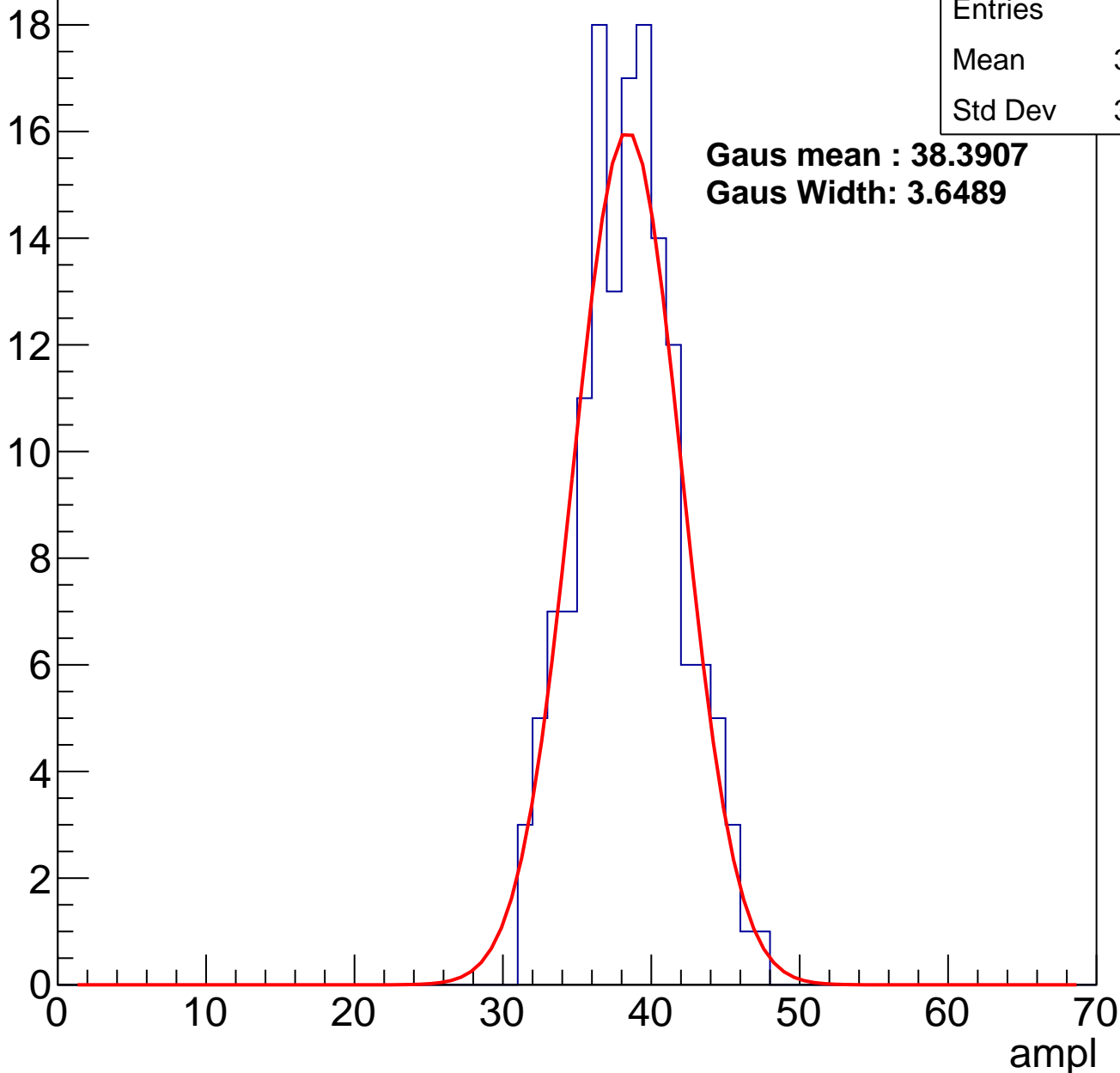
B1L001S, U22-ch67, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	38.05
Std Dev	3.398

Gaus mean : 38.3907
Gaus Width: 3.6489

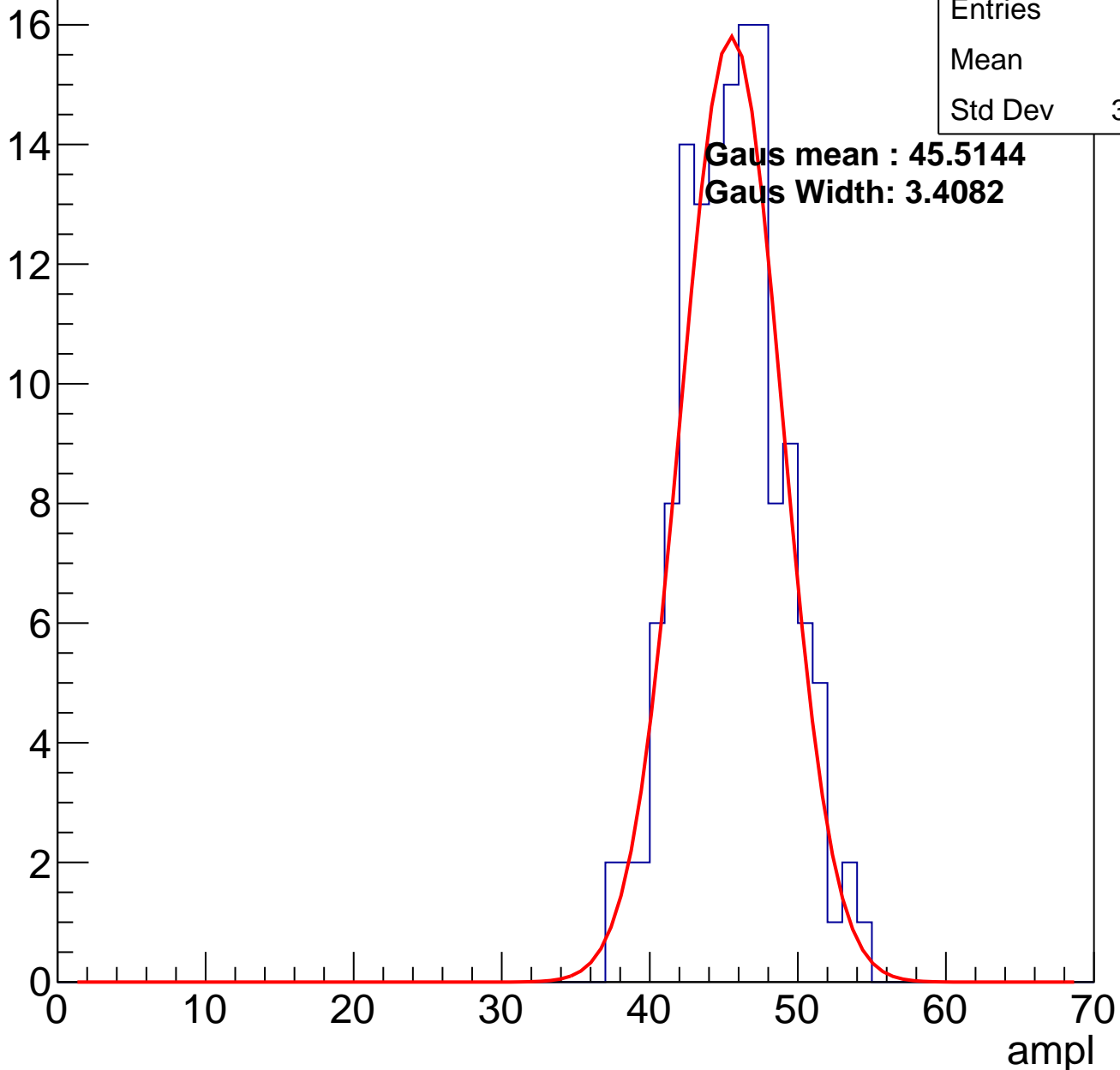
Entry



B1L001S, U22-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

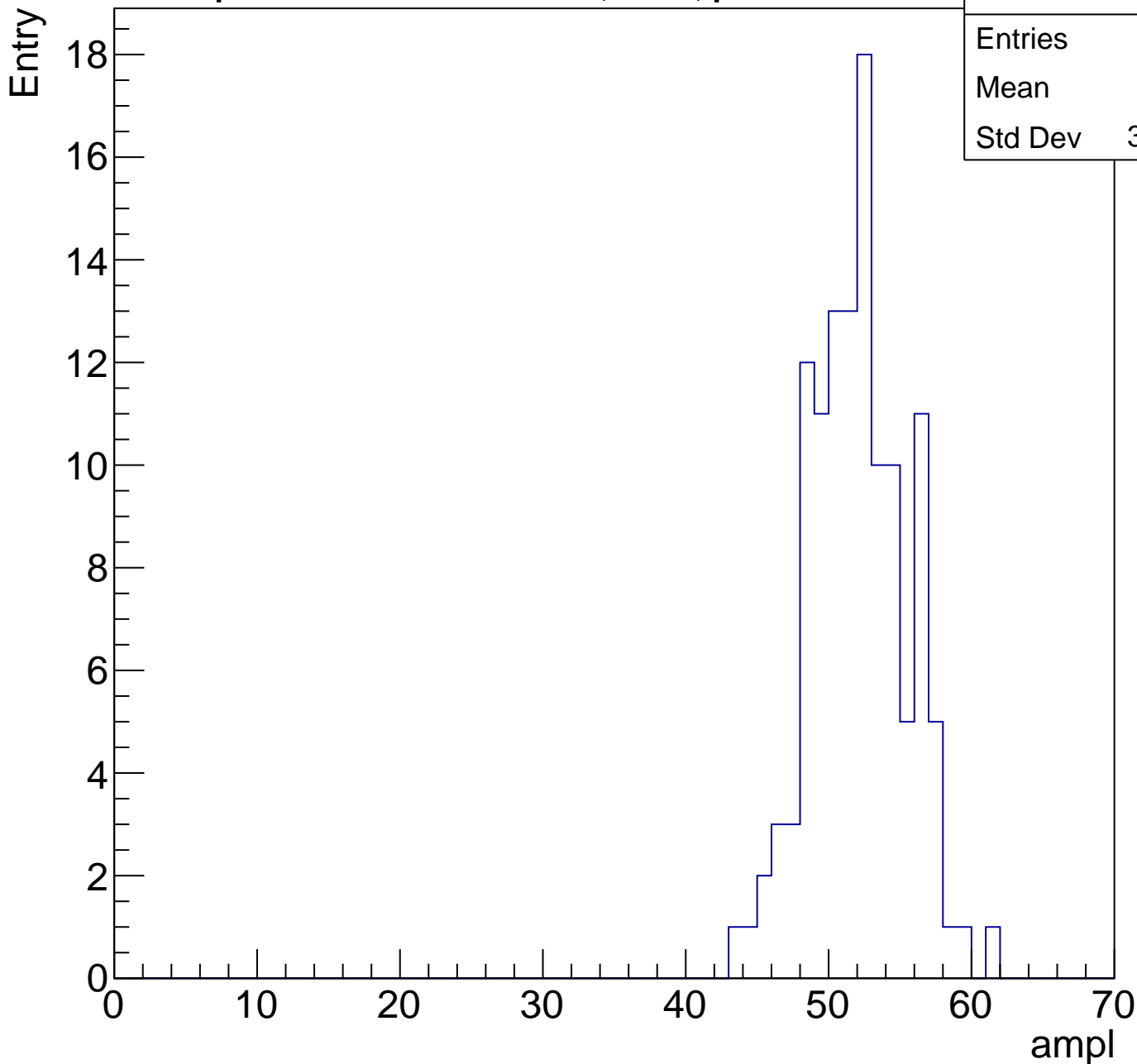
Entry



B1L001S, U22-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	51.6
Std Dev	3.309



B1L001S, U22-ch67, adc4

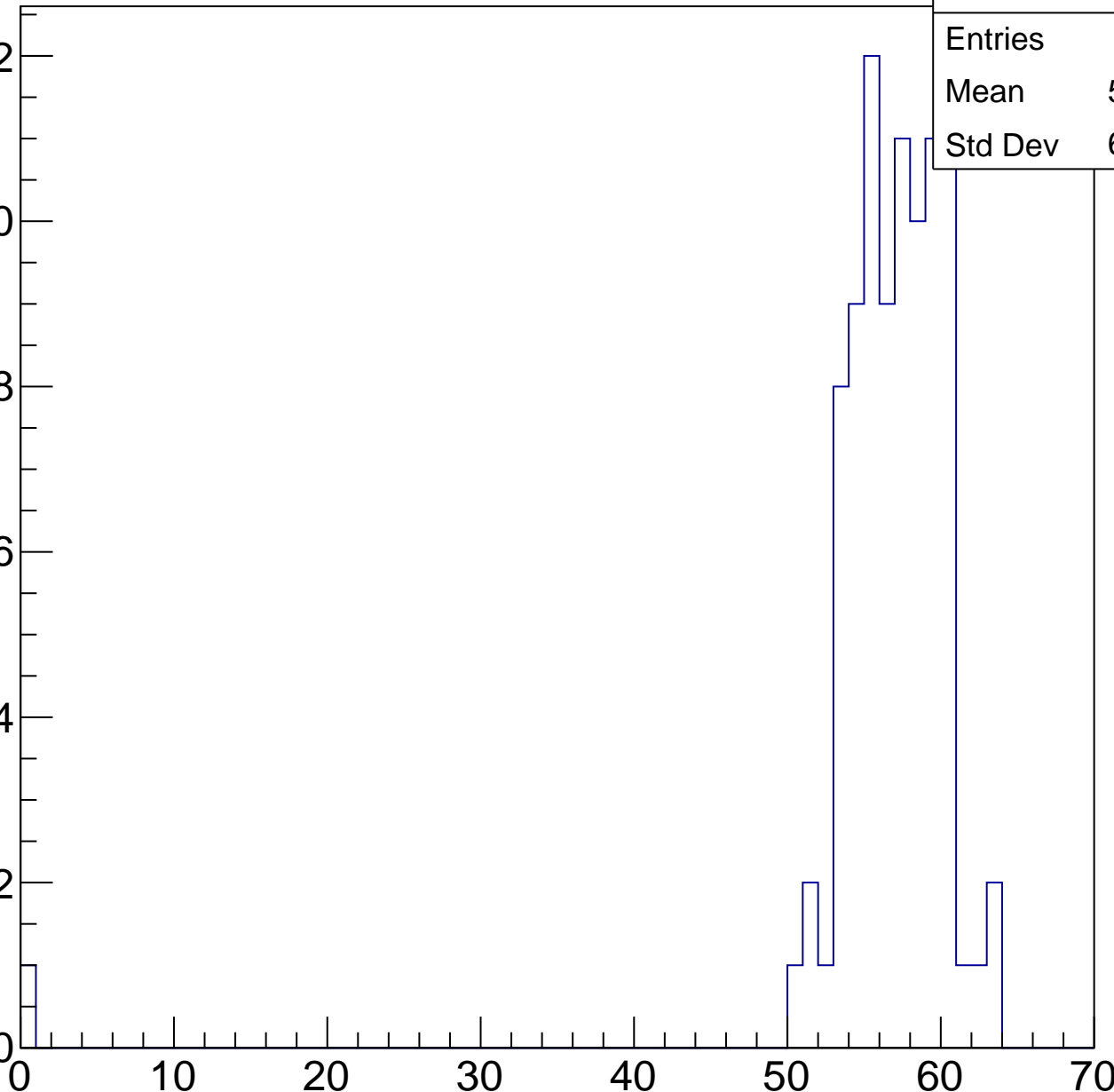
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	91
Mean	56.08
Std Dev	6.512

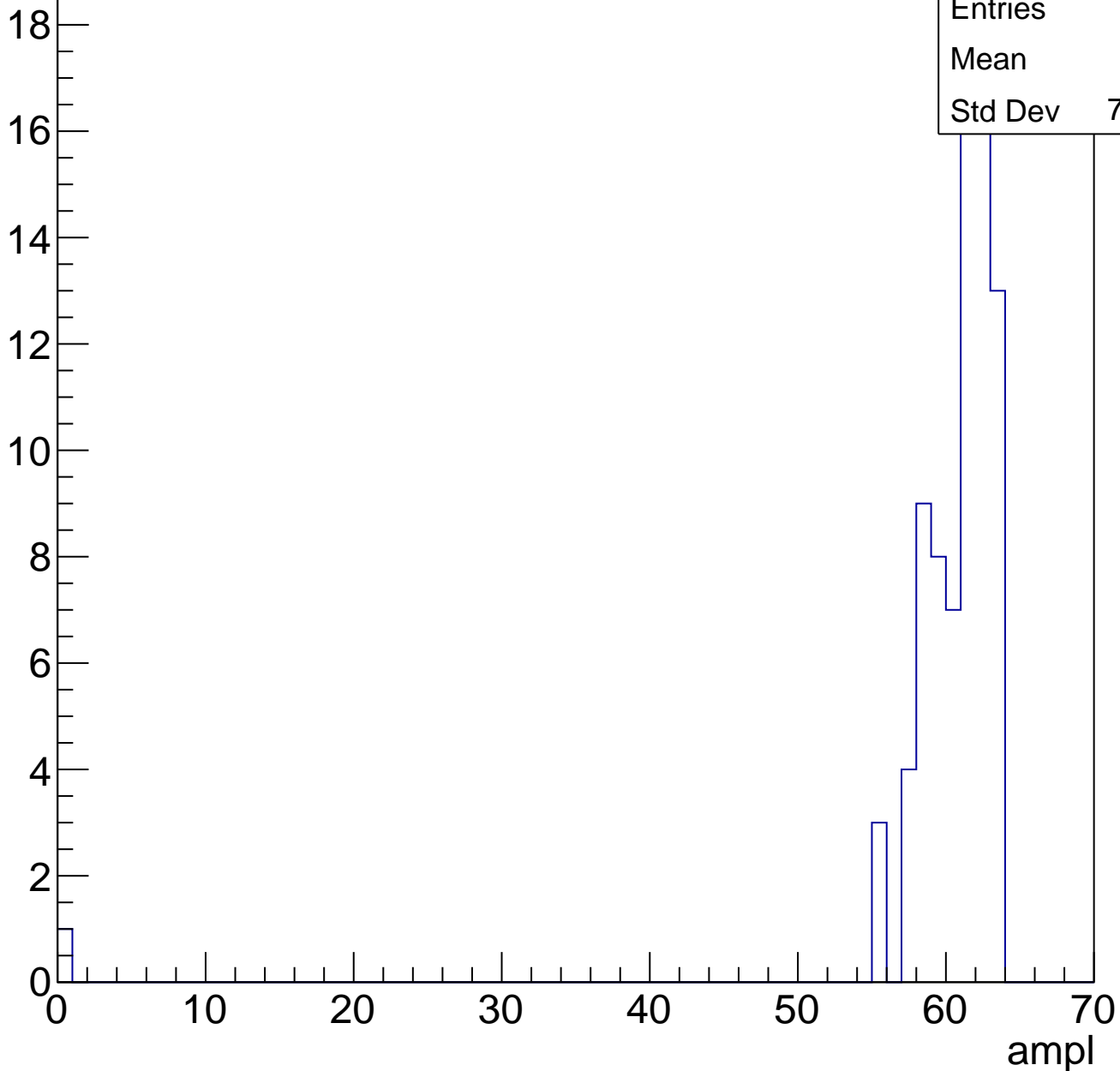
ampl



B1L001S, U22-ch67, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

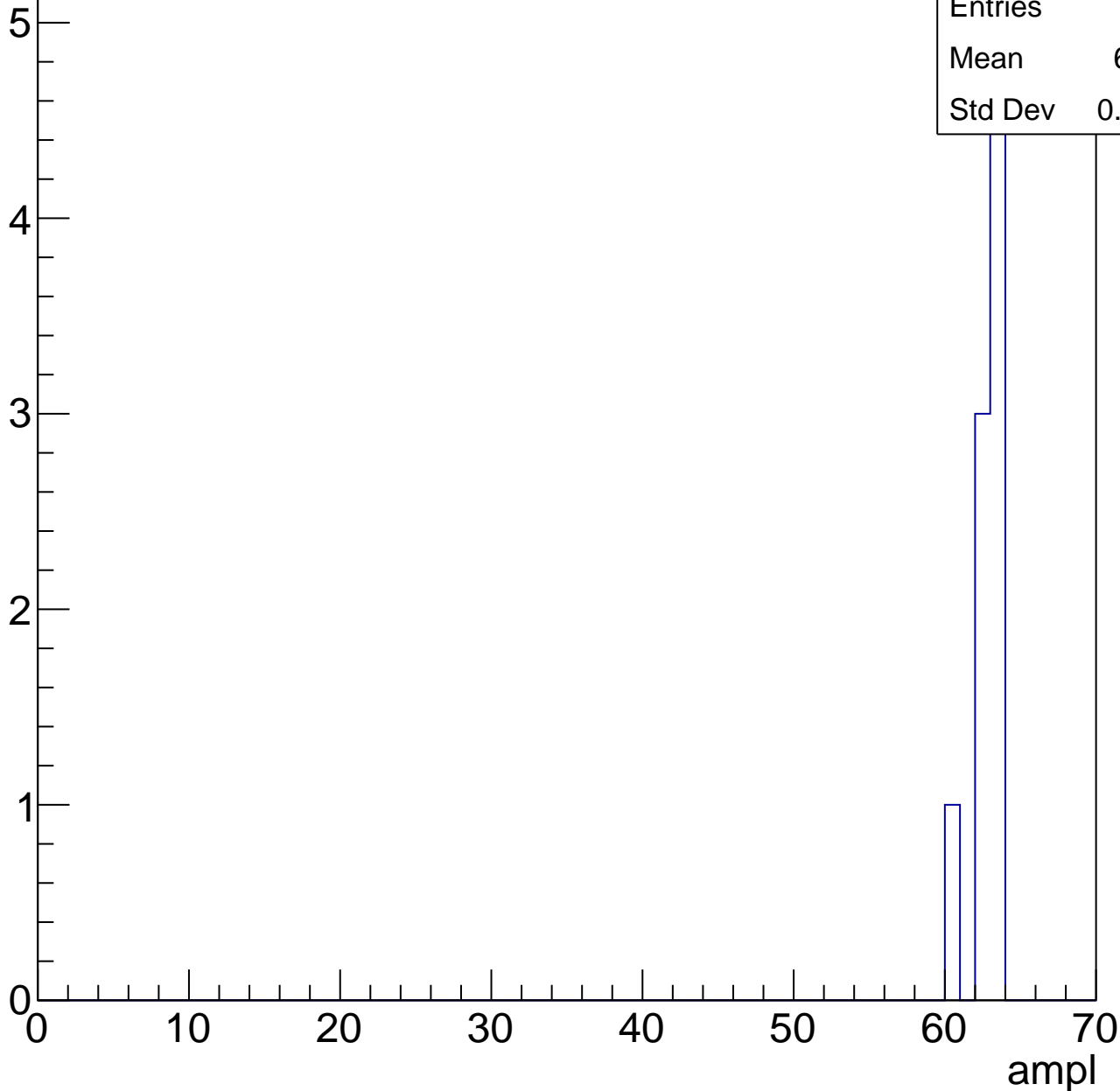


B1L001S, U22-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	62.33
Std Dev	0.9428



B1L001S, U22-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U22-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.57
Std Dev	3.501

Gaus mean : 31.2141

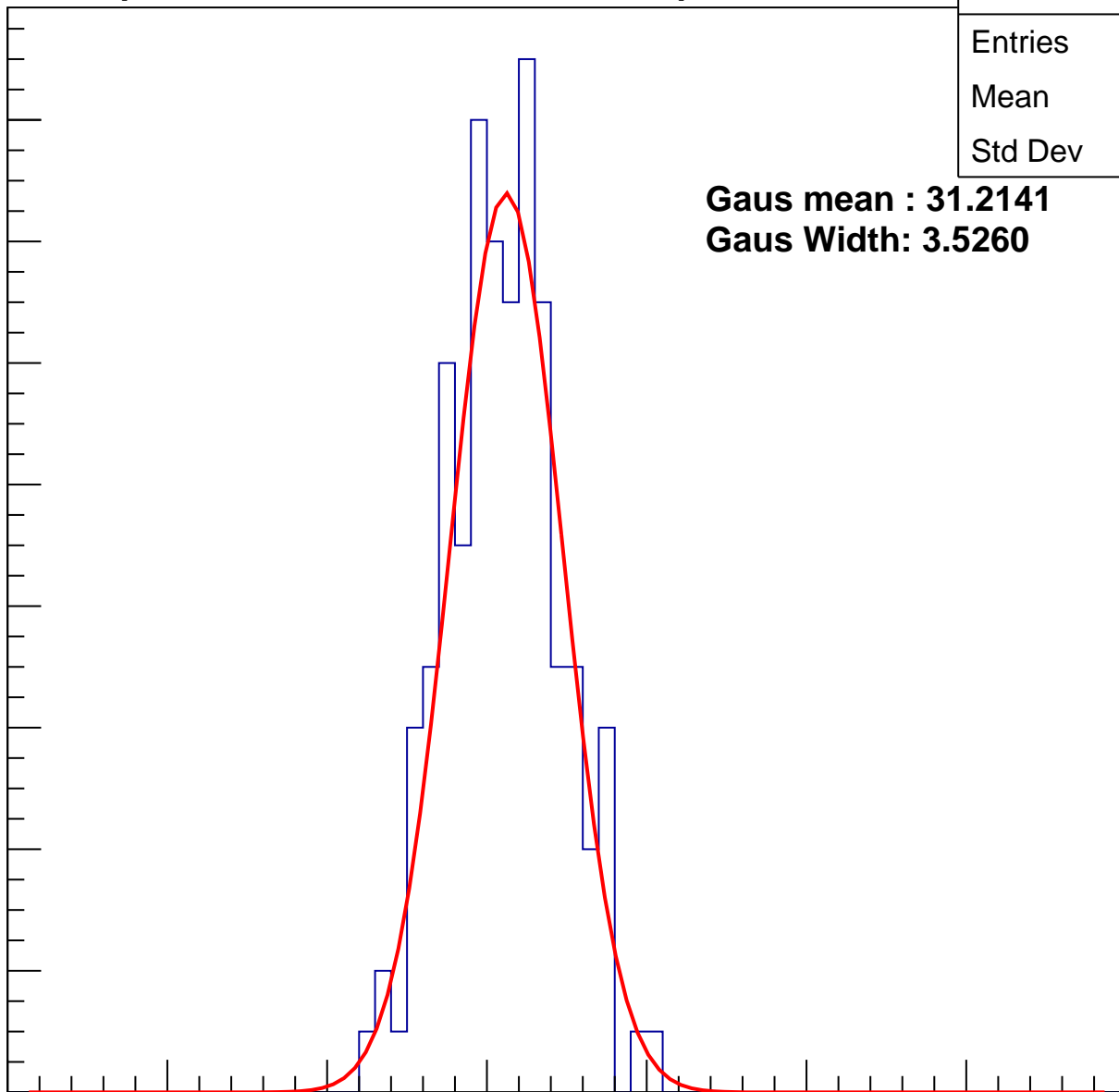
Gaus Width: 3.5260

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

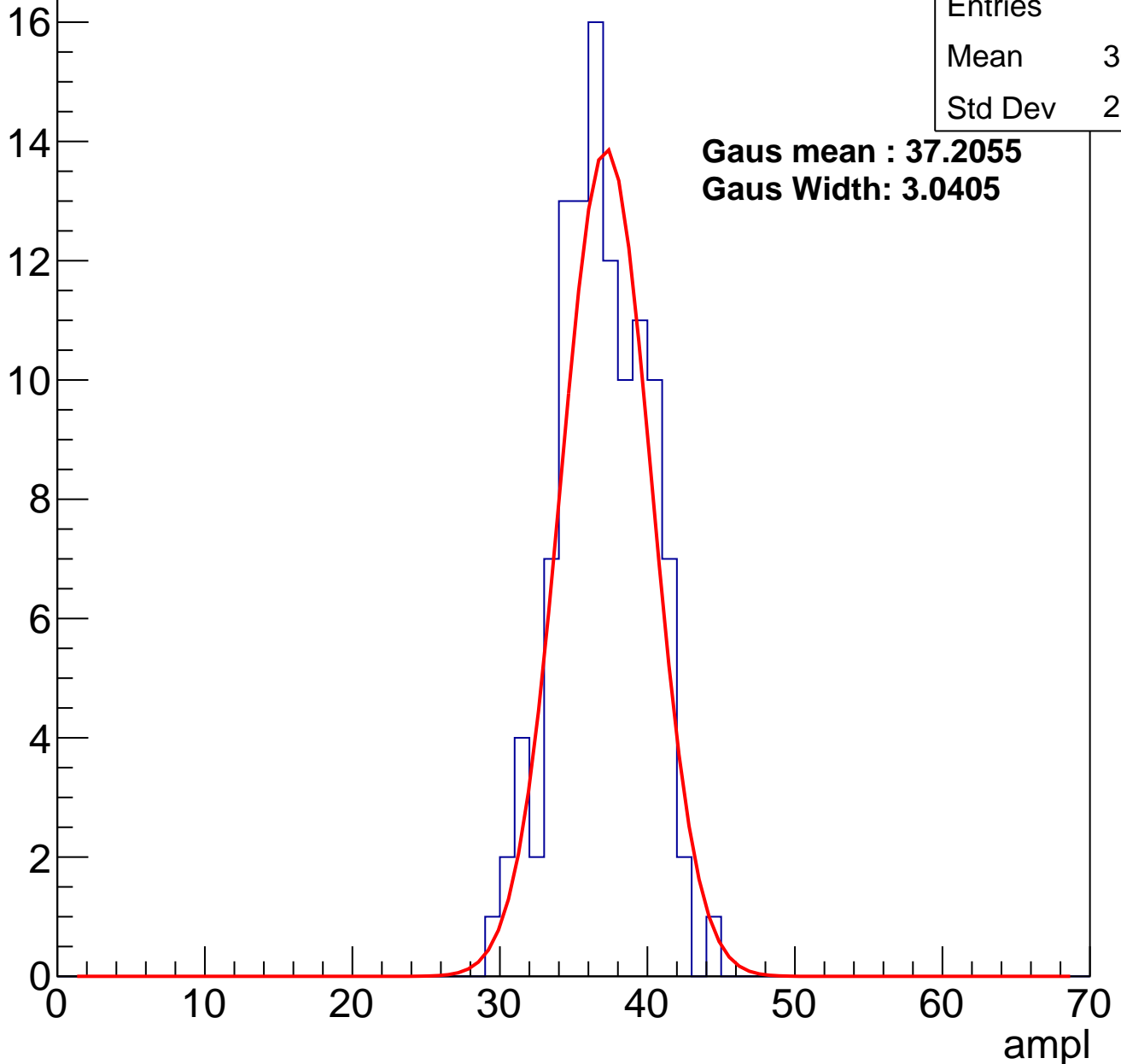
ampl



B1L001S, U22-ch68, adc1

calib_packv5_042523_0143.root, FC#2, port C2

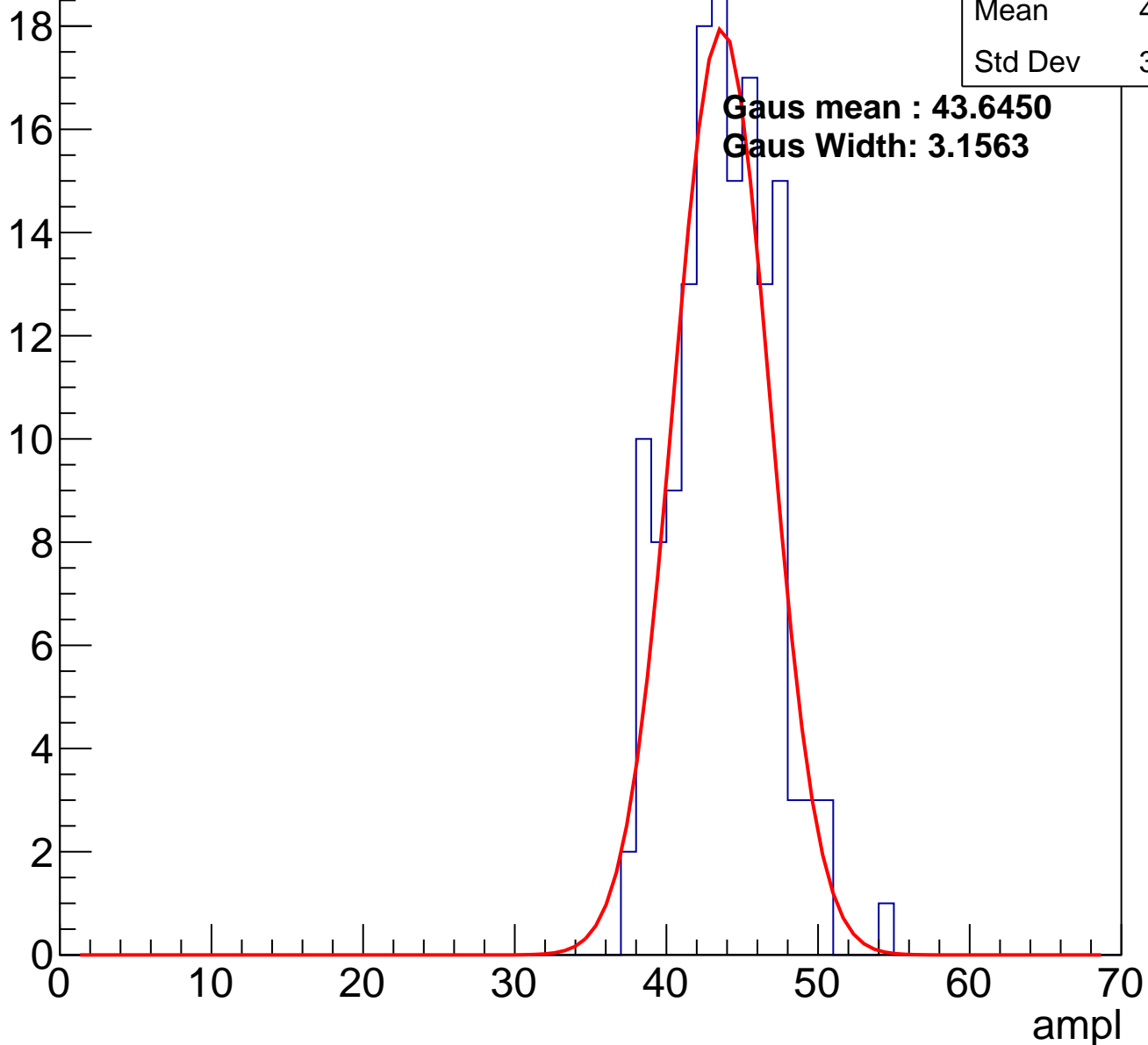
Entry



B1L001S, U22-ch68, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.6
Std Dev	3.148

Entry

16

14

12

10

8

6

4

2

0

0

10

20

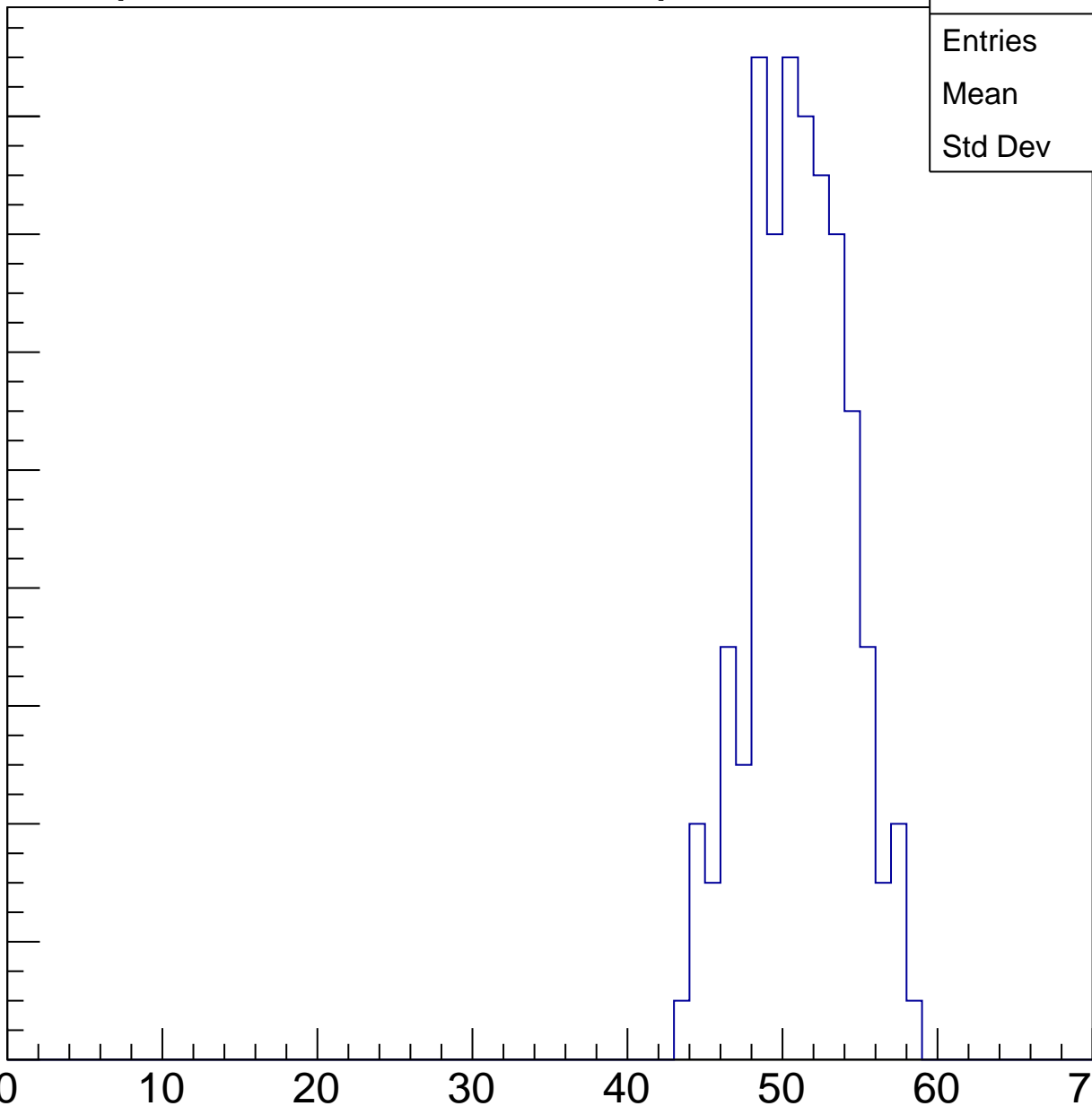
30

40

50

60

ampl



B1L001S, U22-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

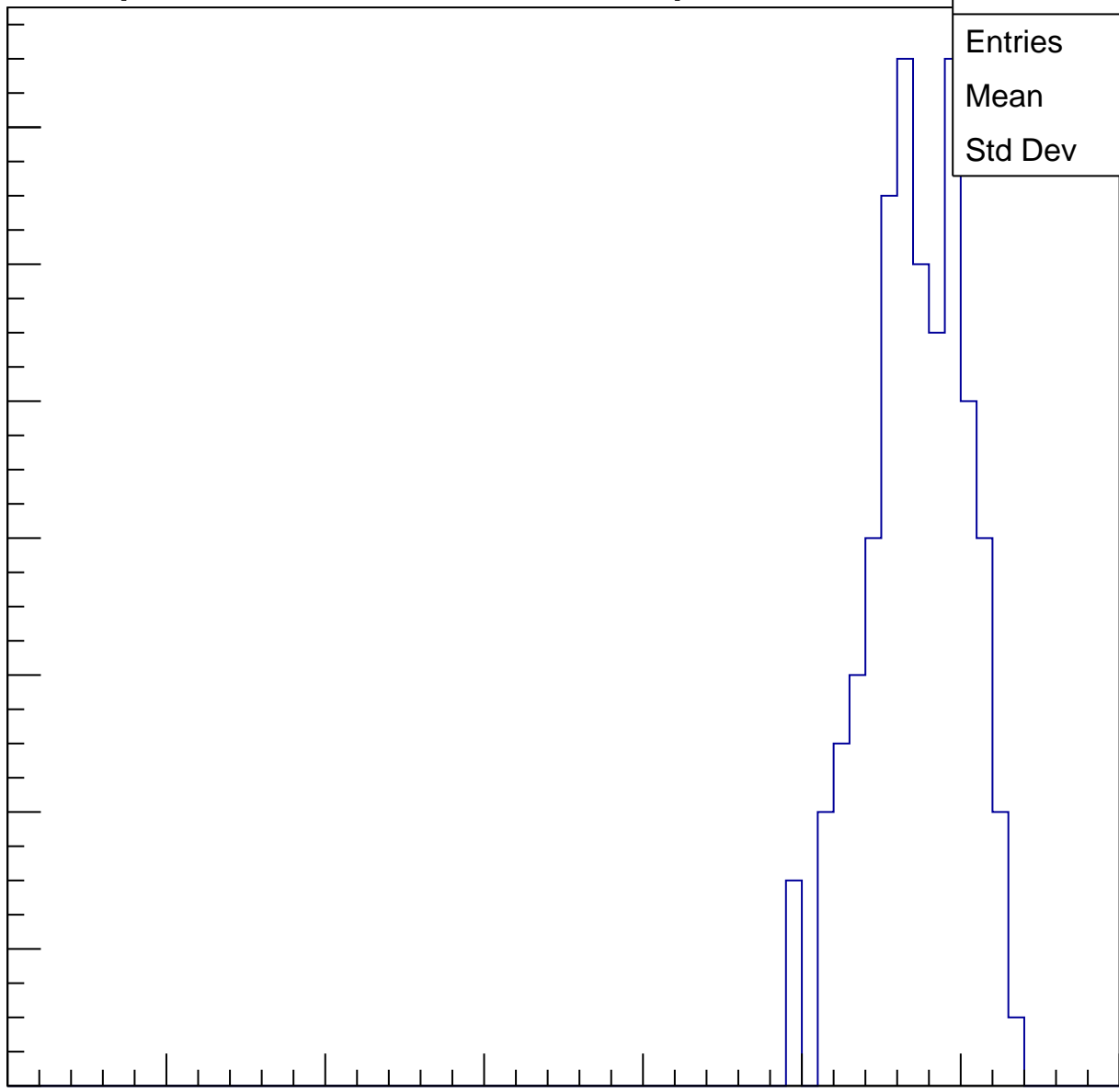
Entries	115
Mean	56.71
Std Dev	3.109

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch68, adc5

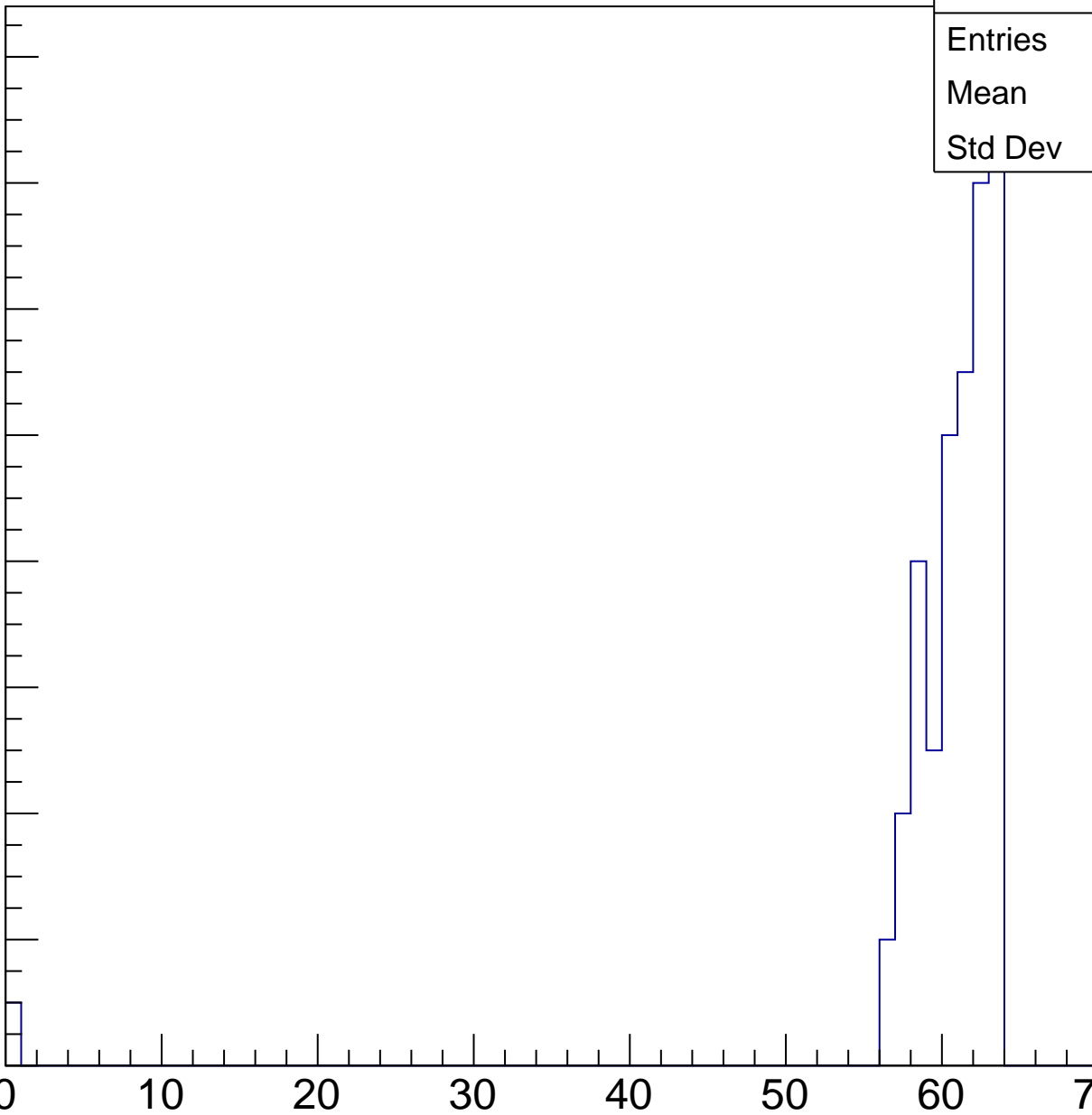
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.8
Std Dev	7.424

ampl



B1L001S, U22-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch68, adc7

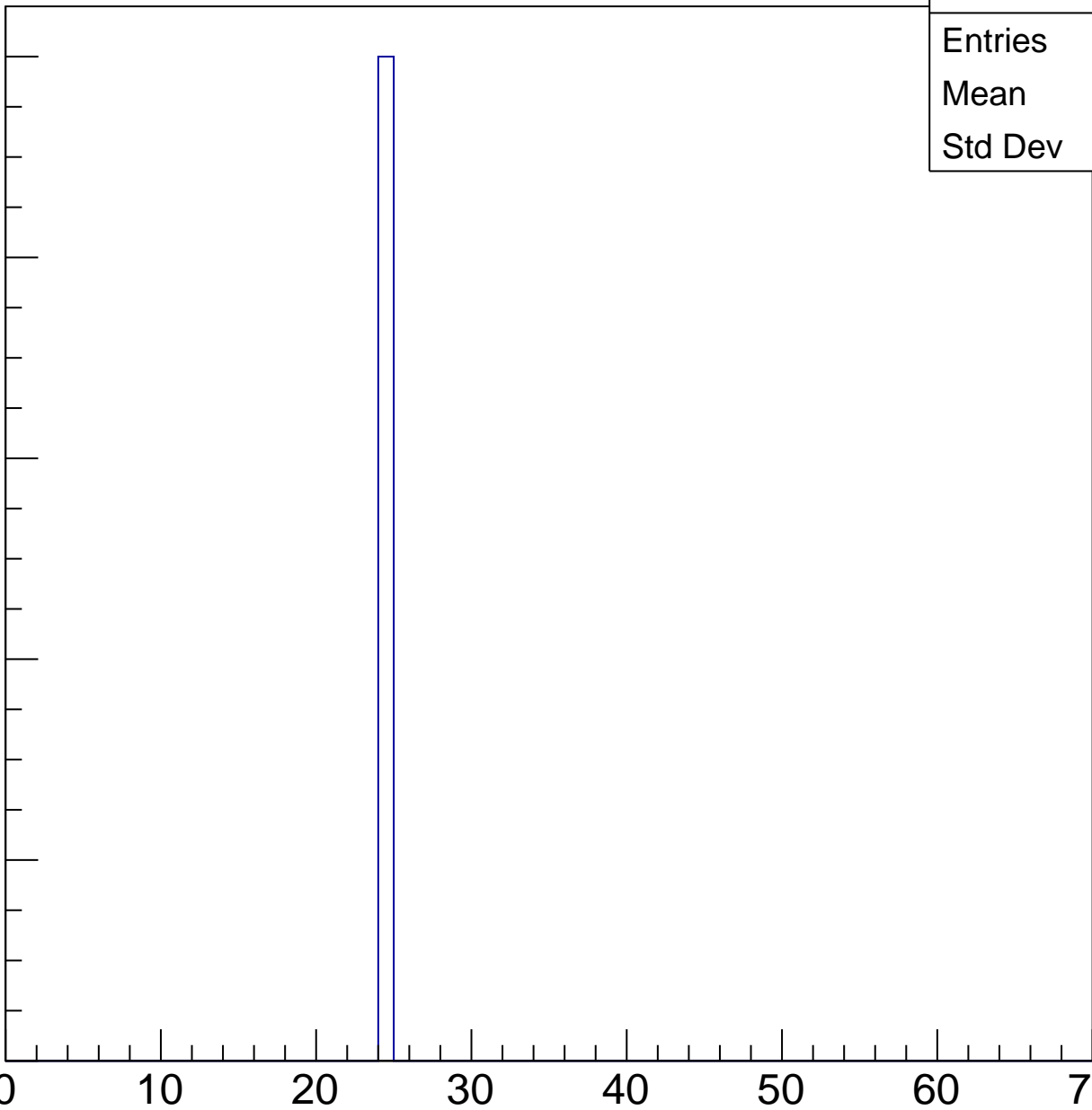
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U22-ch69, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.05
Std Dev	3.141

Gaus mean : 29.5196

Gaus Width: 3.4298

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

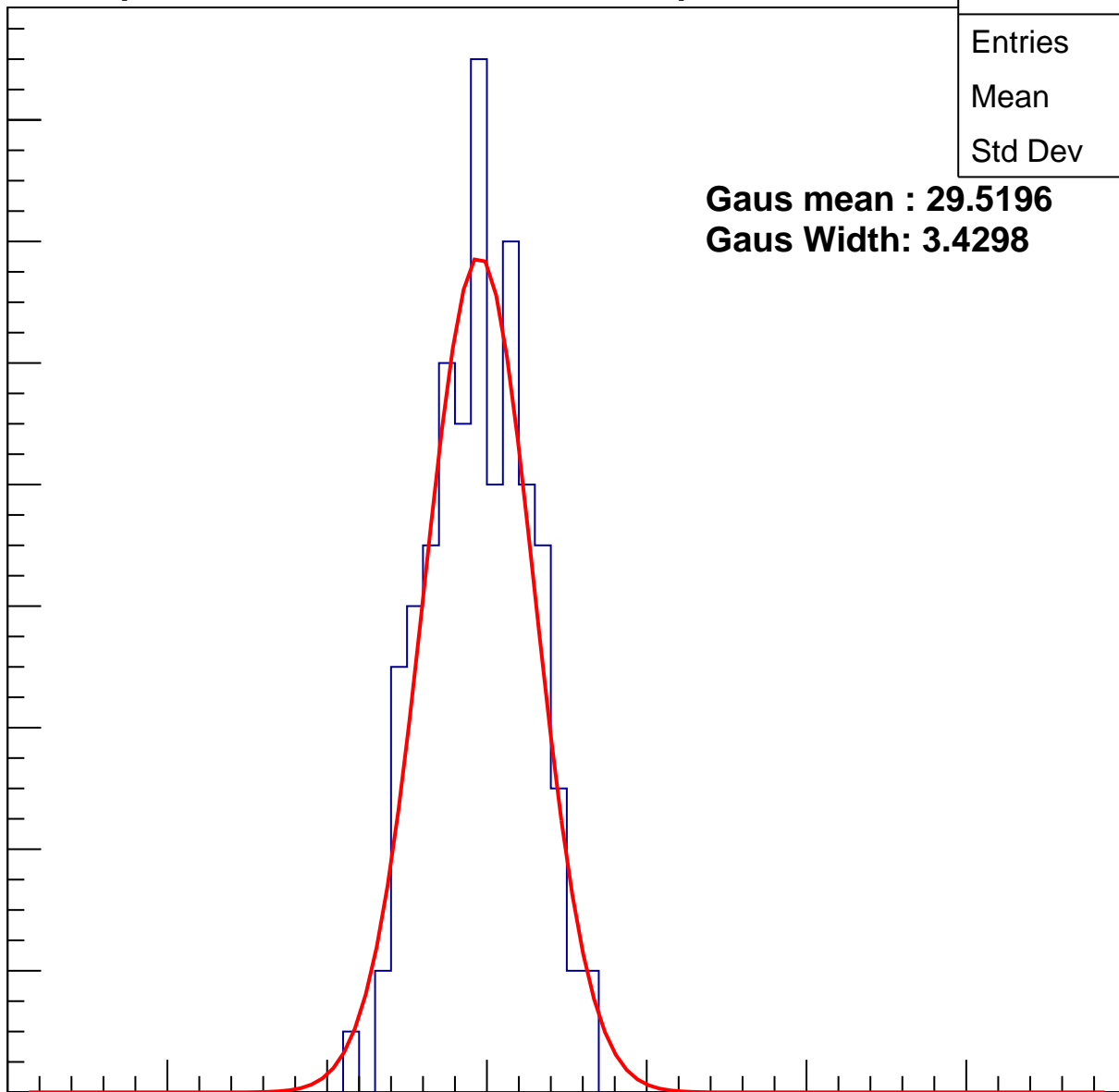
40

50

60

70

ampl



B1L001S, U22-ch69, adc1

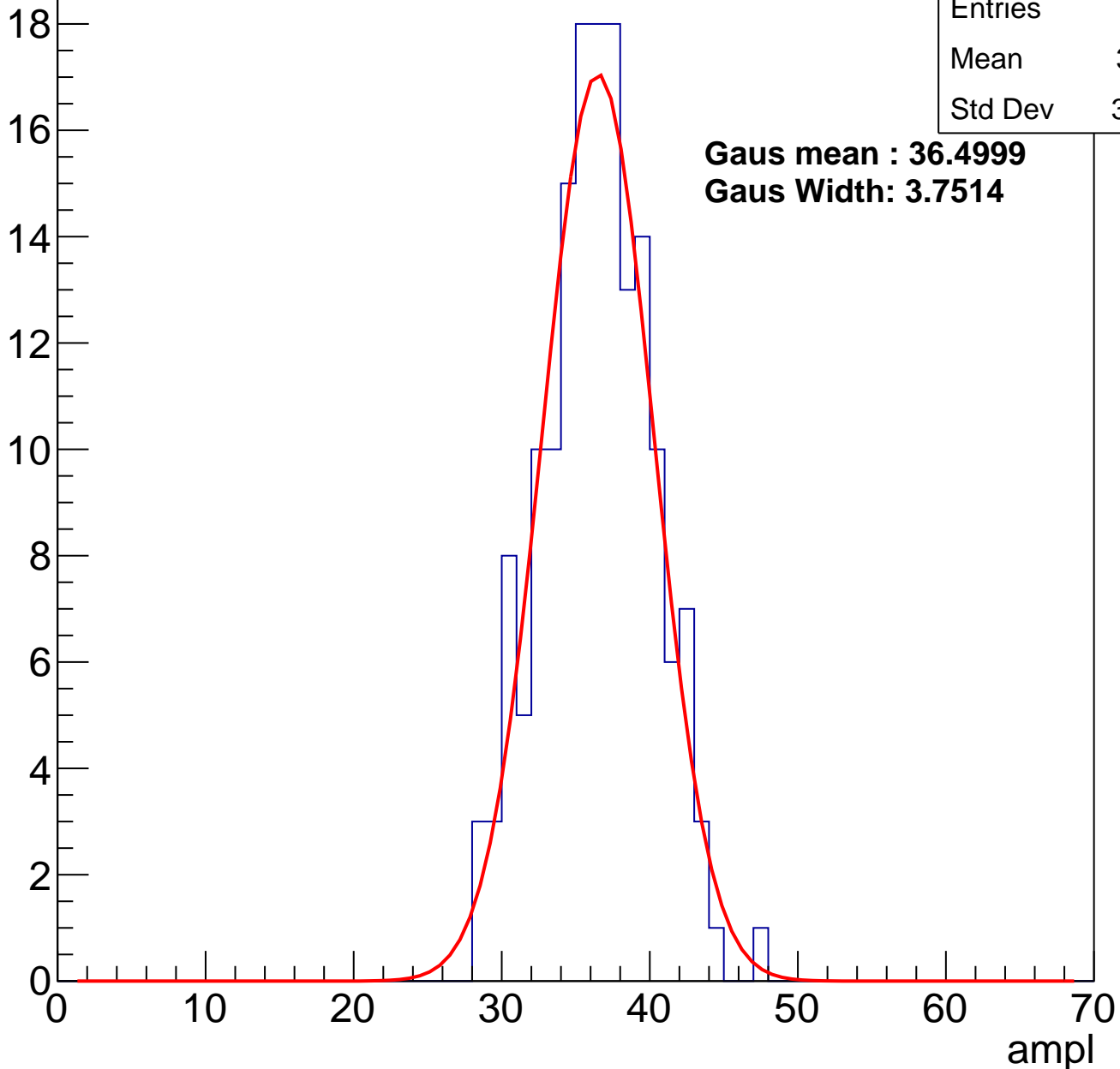
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	36.01
Std Dev	3.649

Gaus mean : 36.4999

Gaus Width: 3.7514

Entry



B1L001S, U22-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	43.13
Std Dev	3.047

Gaus mean : 43.5206

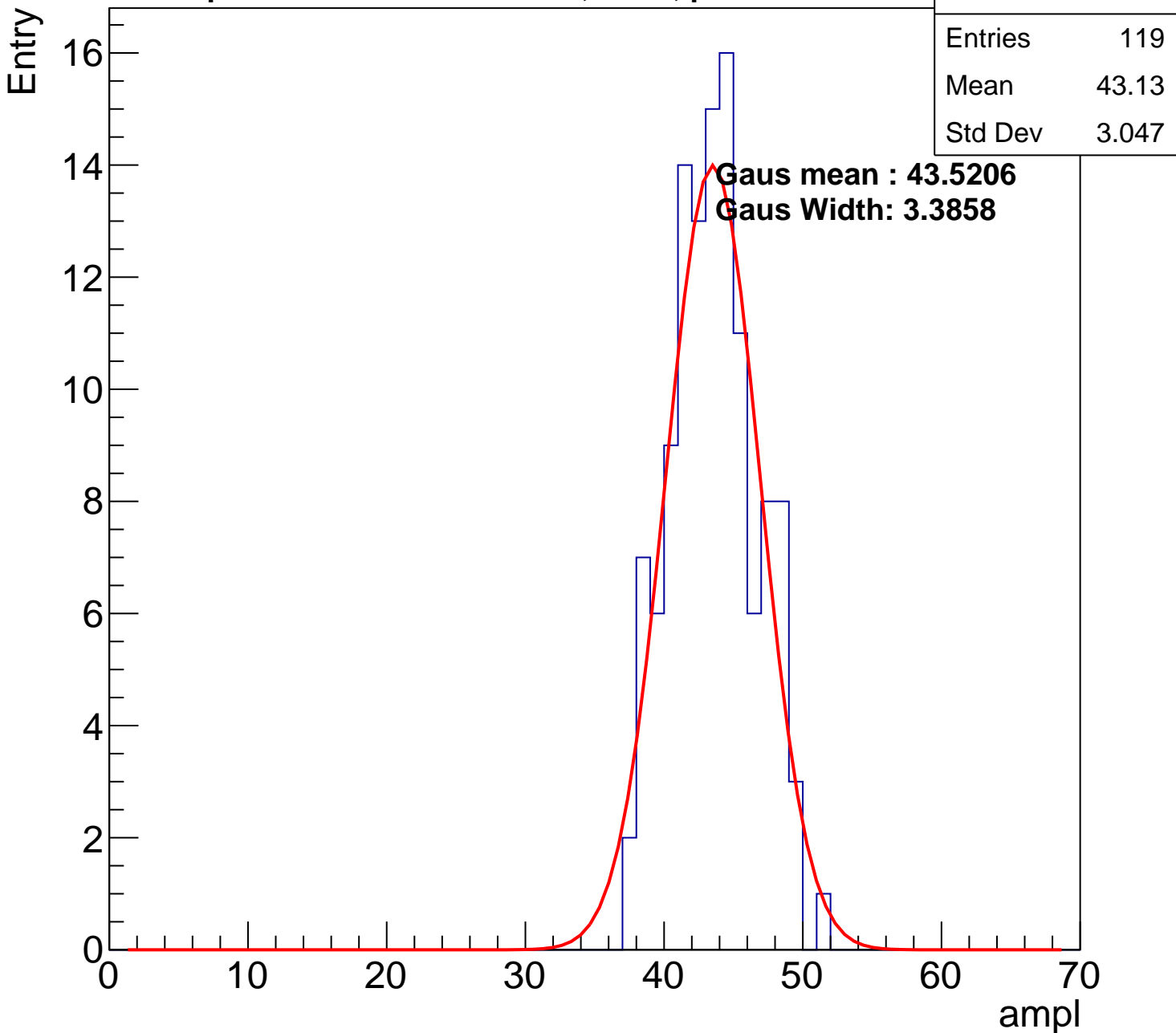
Gaus Width: 3.3858

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

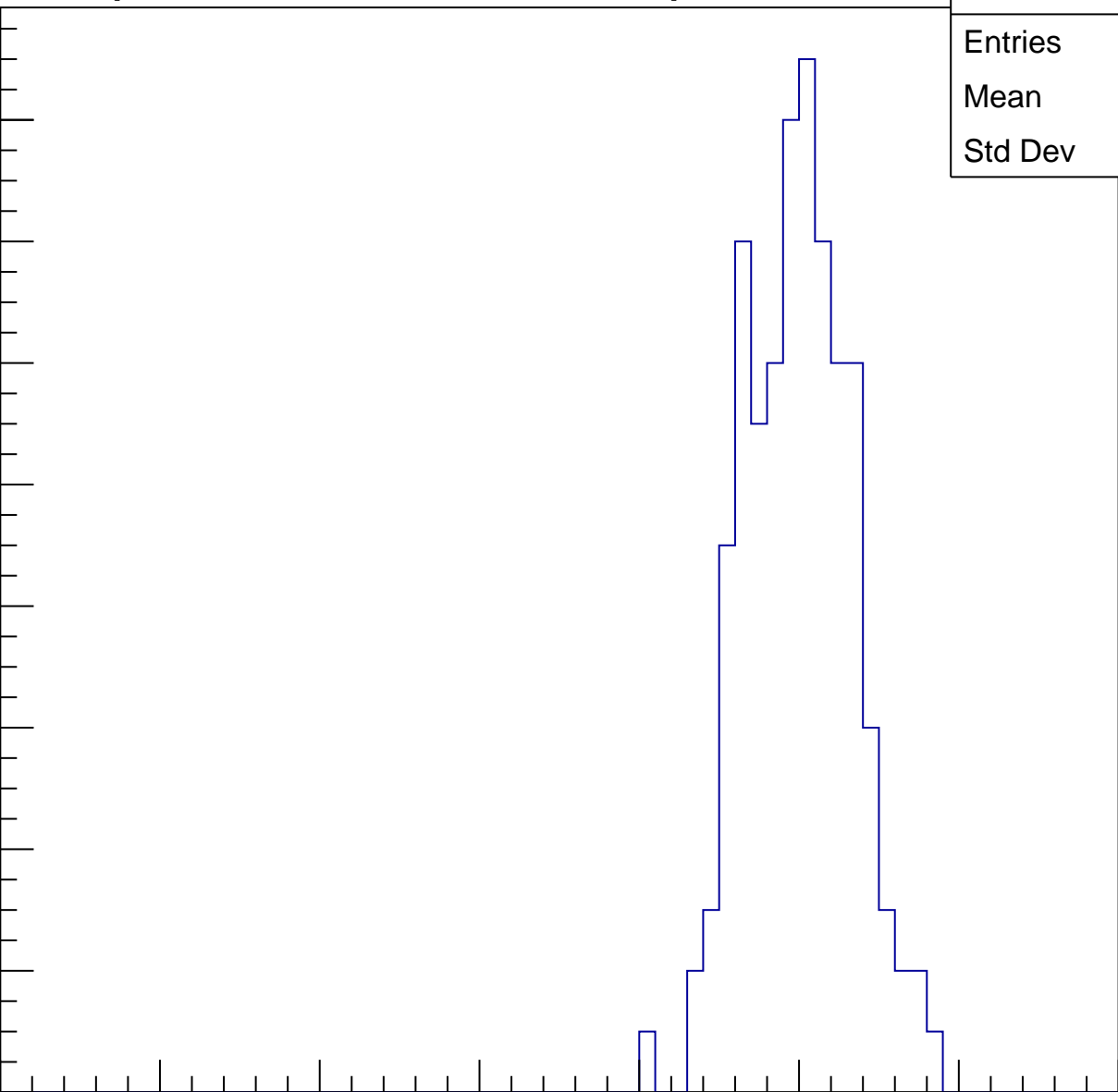
Entries	137
Mean	49.5
Std Dev	3.263

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch69, adc4

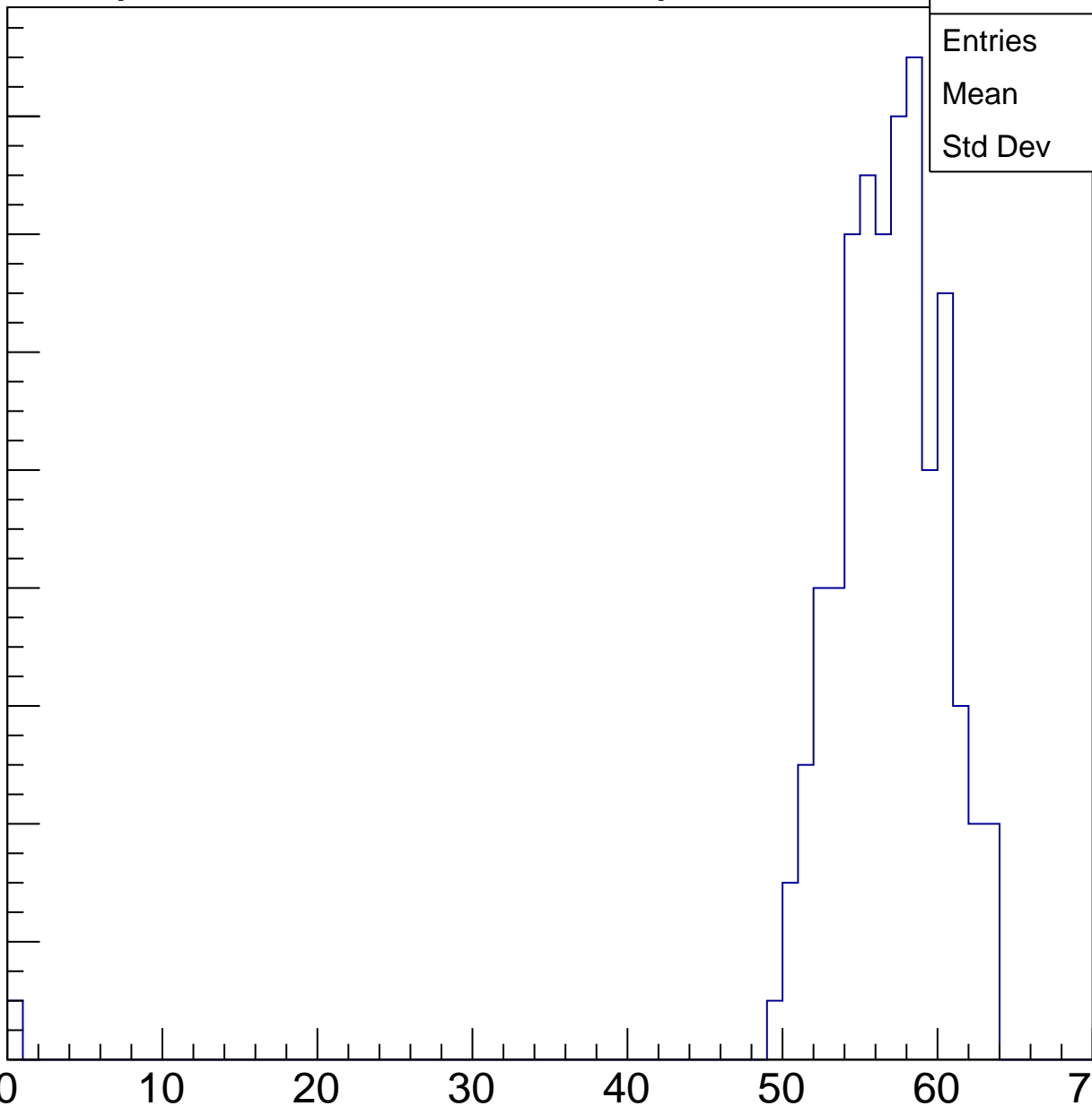
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	56.06
Std Dev	5.728

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch69, adc5

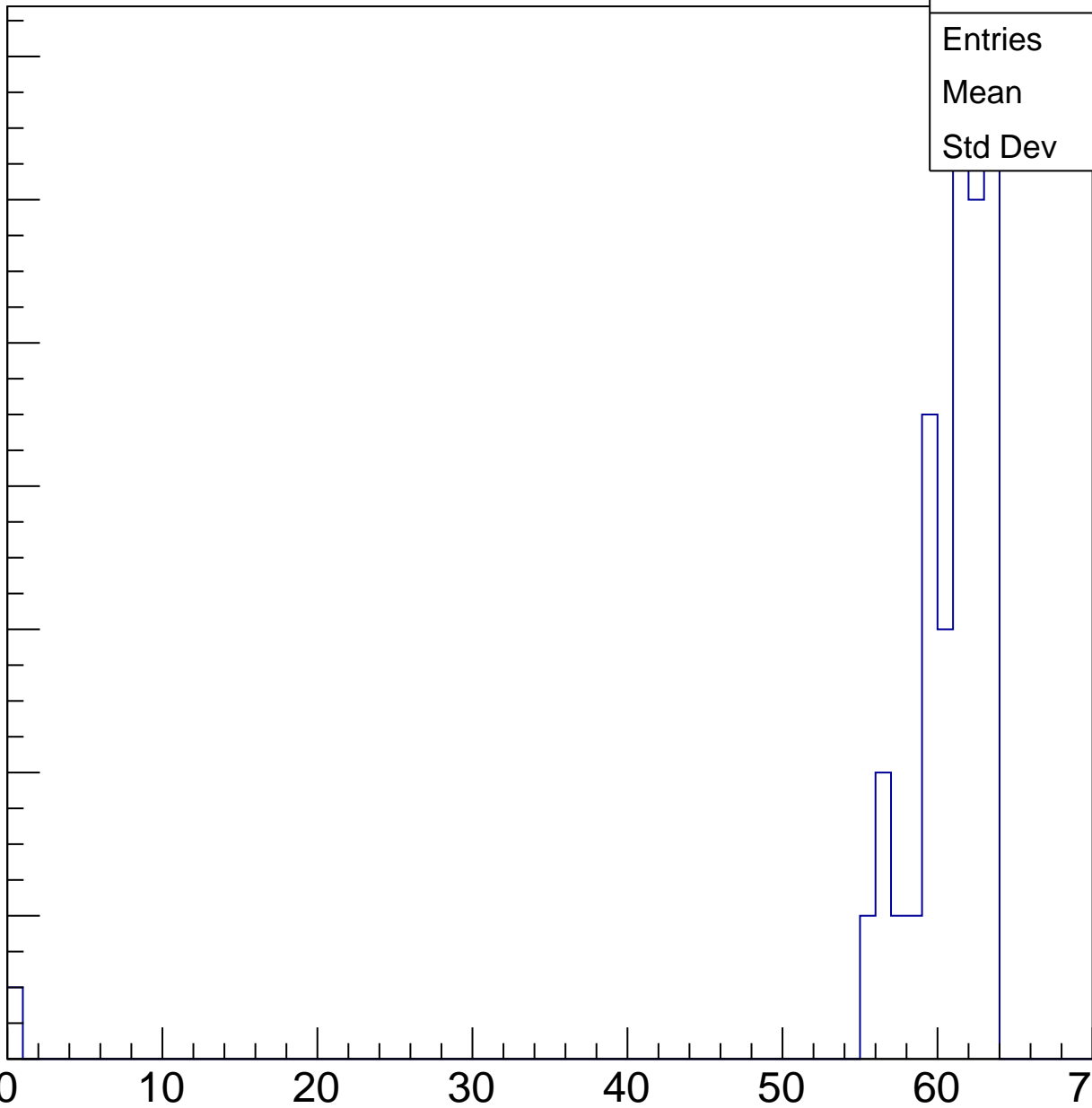
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.6
Std Dev	7.771

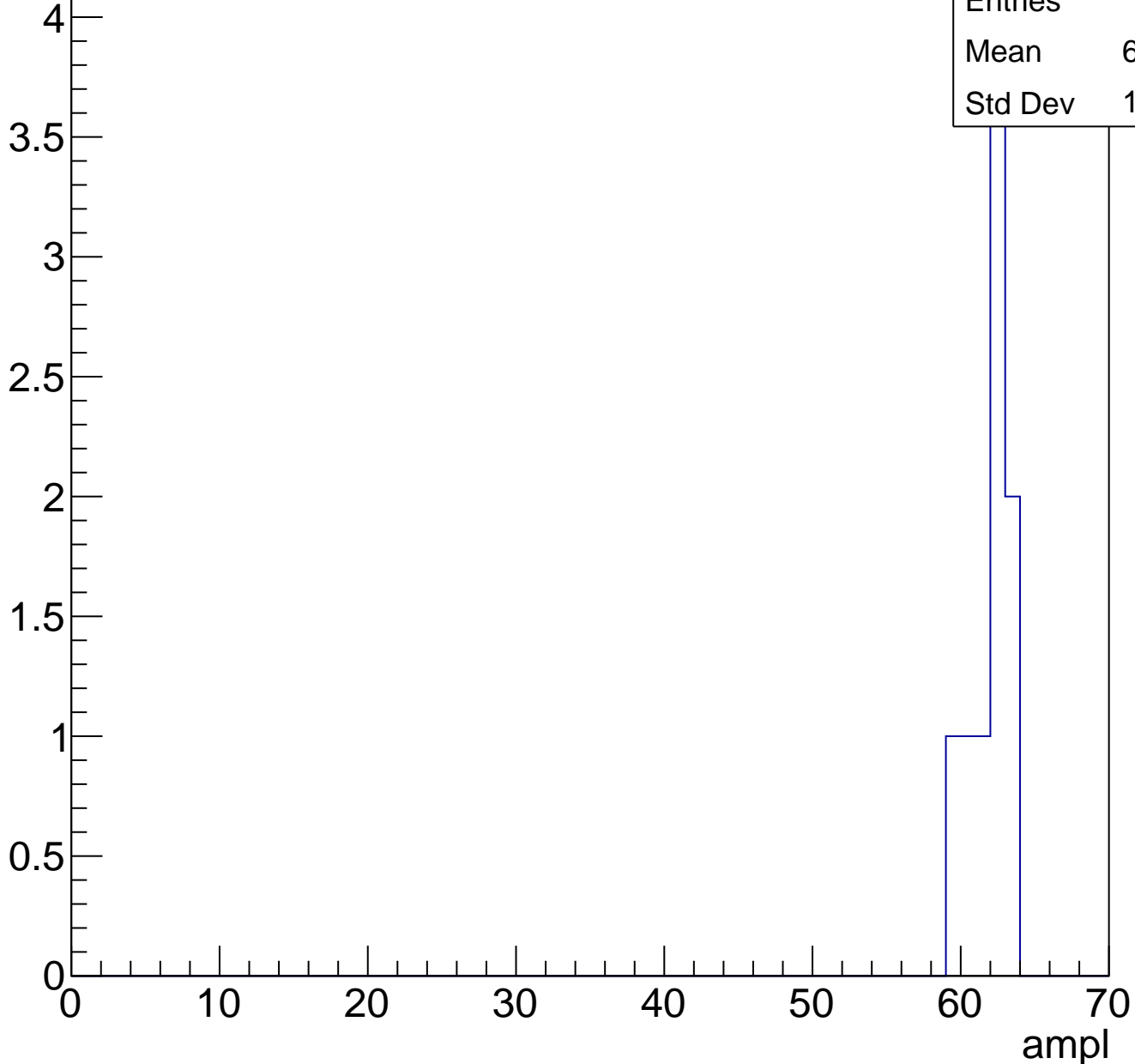
ampl



B1L001S, U22-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch70, adc0

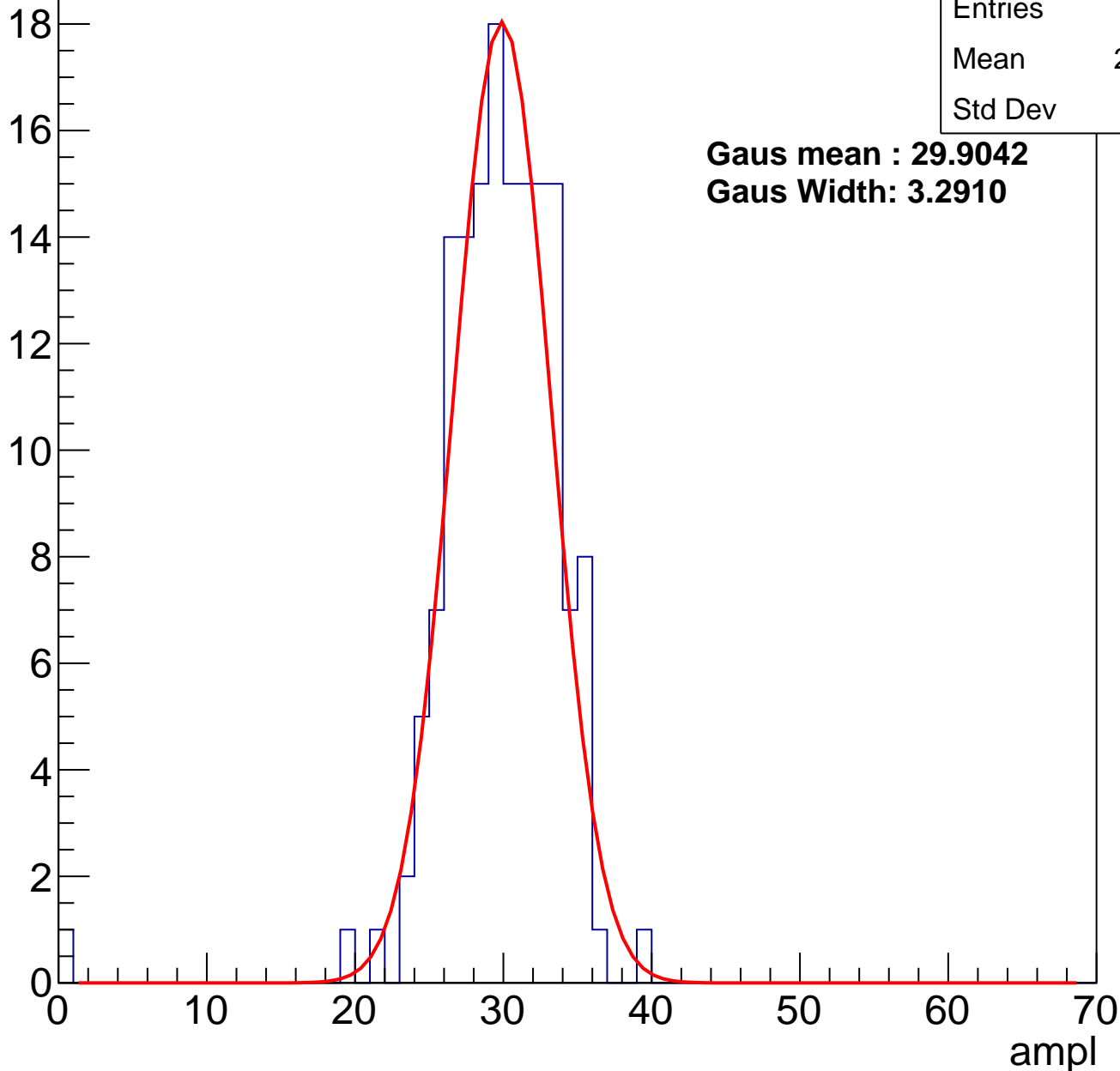
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	29.34
Std Dev	4.06

Gaus mean : 29.9042

Gaus Width: 3.2910

Entry



B1L001S, U22-ch70, adc1

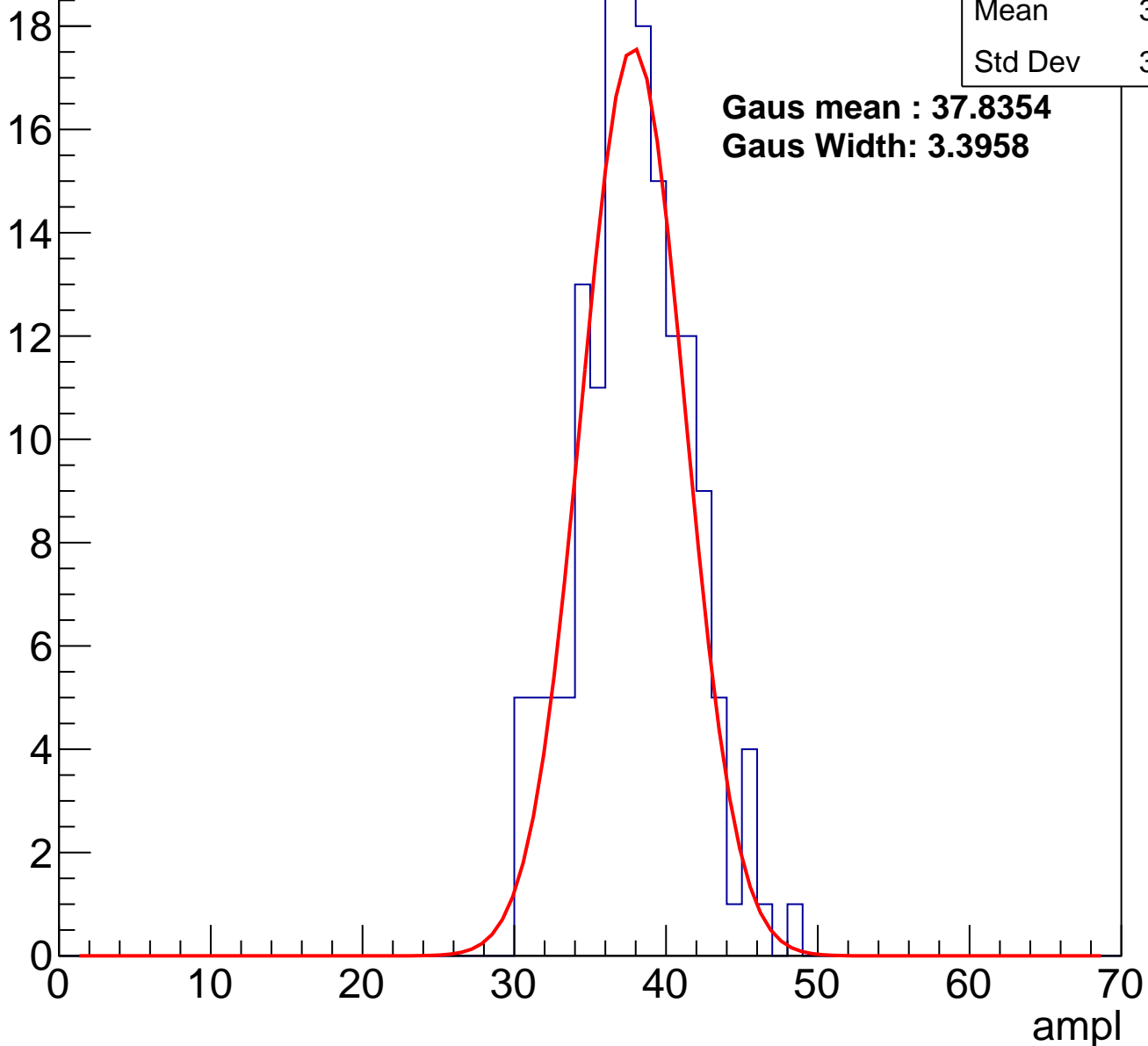
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	37.48
Std Dev	3.592

Gaus mean : 37.8354

Gaus Width: 3.3958

Entry



B1L001S, U22-ch70, adc2

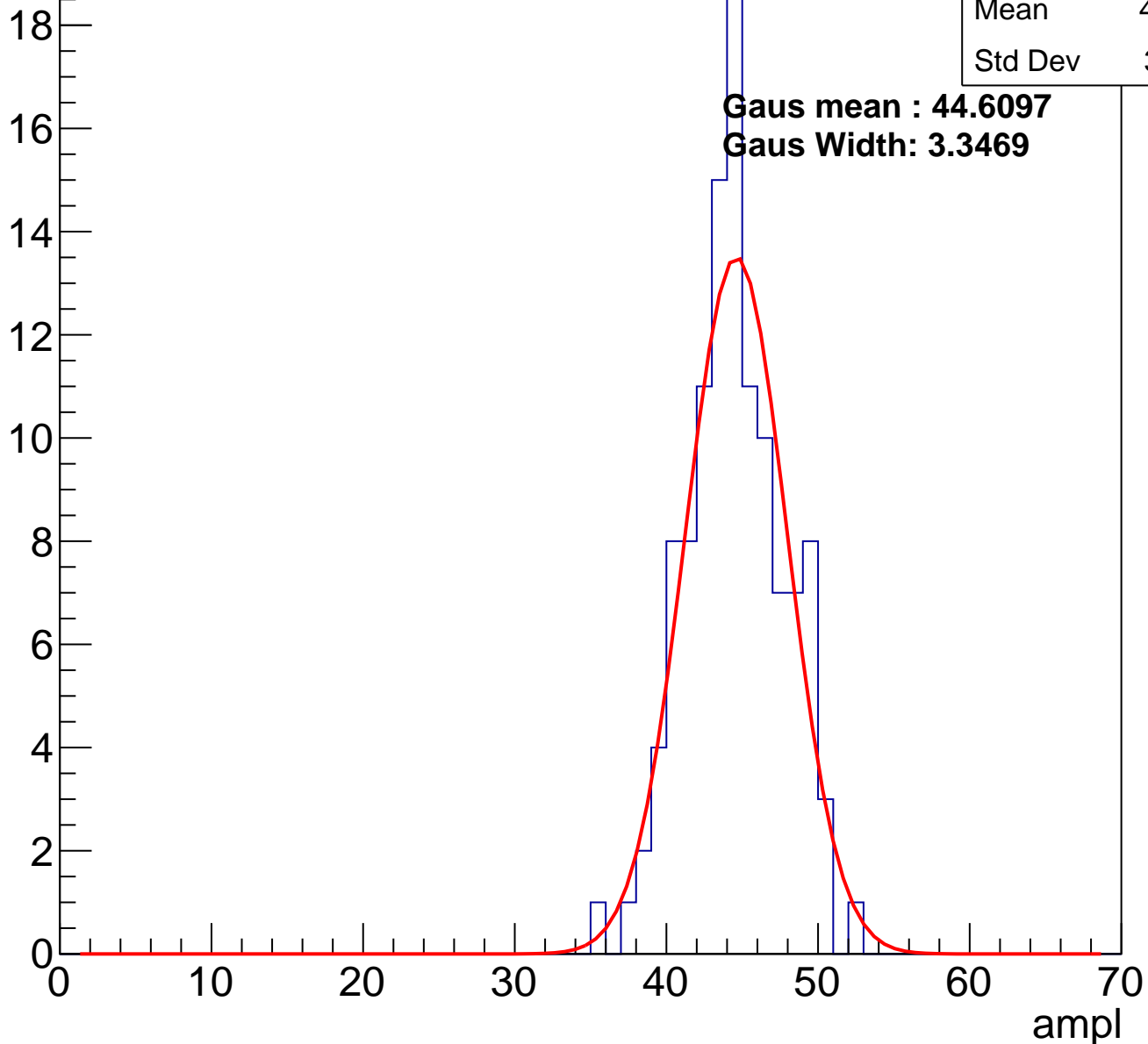
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	44.04
Std Dev	3.161

Gaus mean : 44.6097

Gaus Width: 3.3469

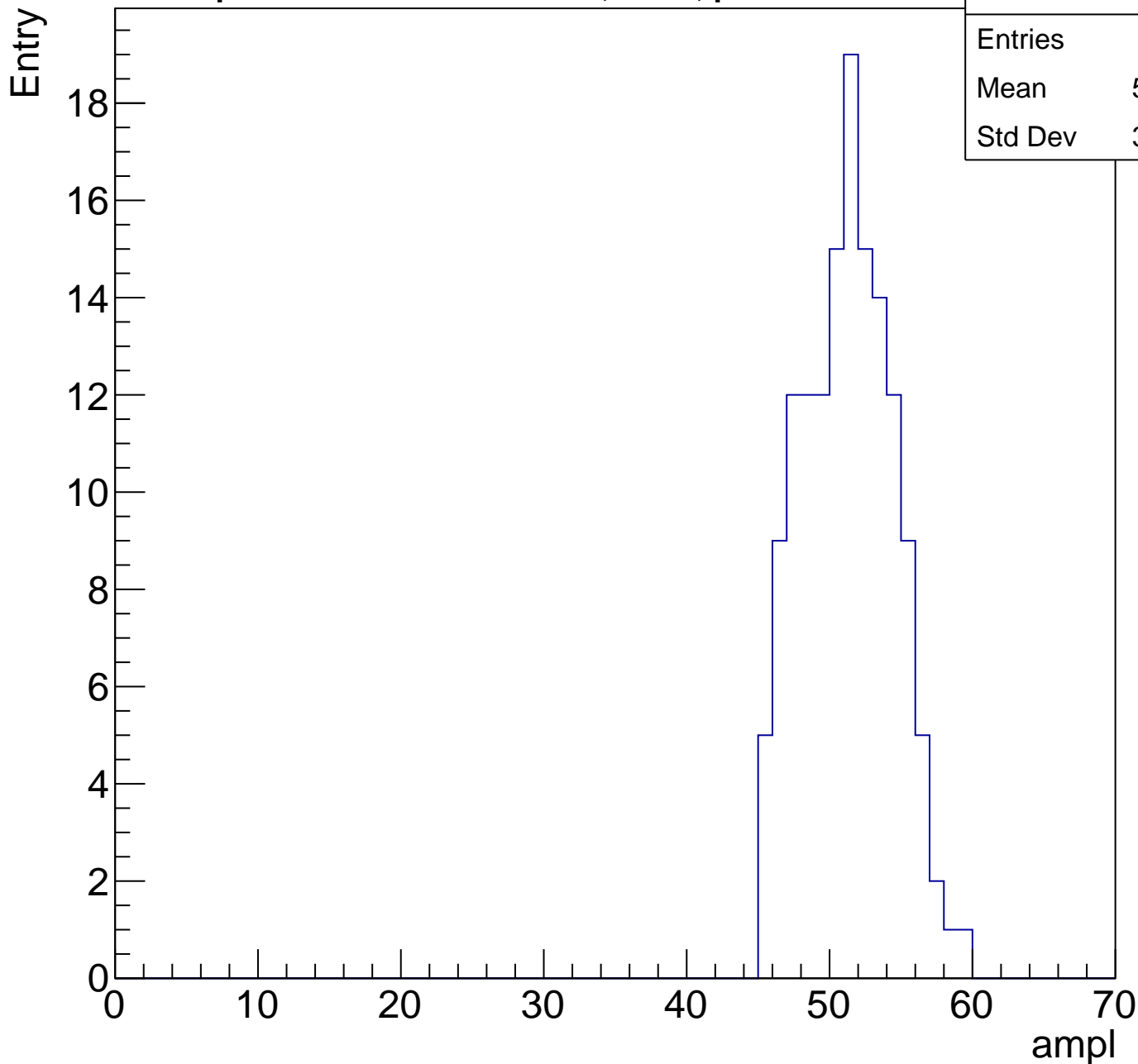
Entry



B1L001S, U22-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	50.78
Std Dev	3.123



B1L001S, U22-ch70, adc4

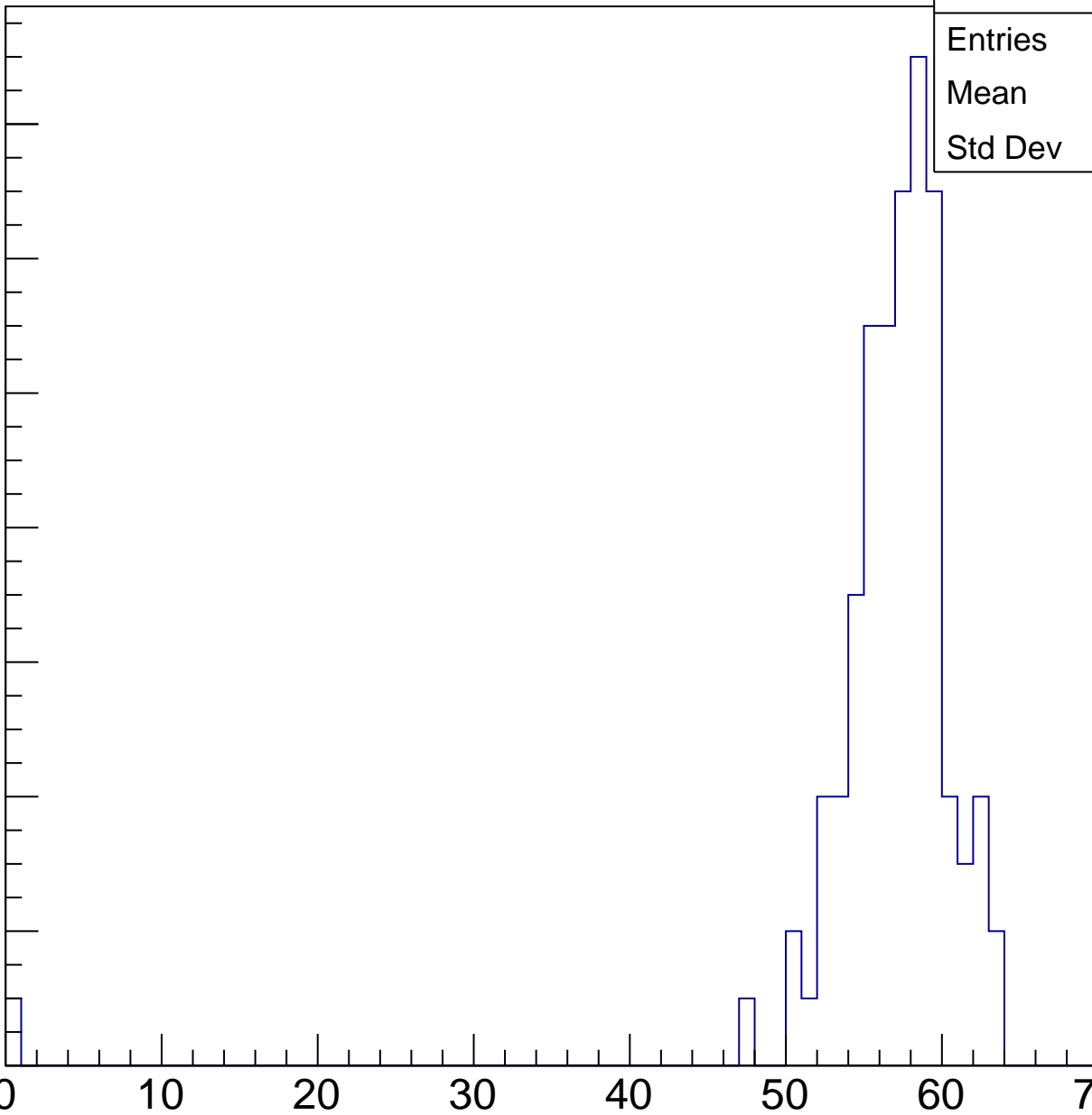
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	56.17
Std Dev	6.472

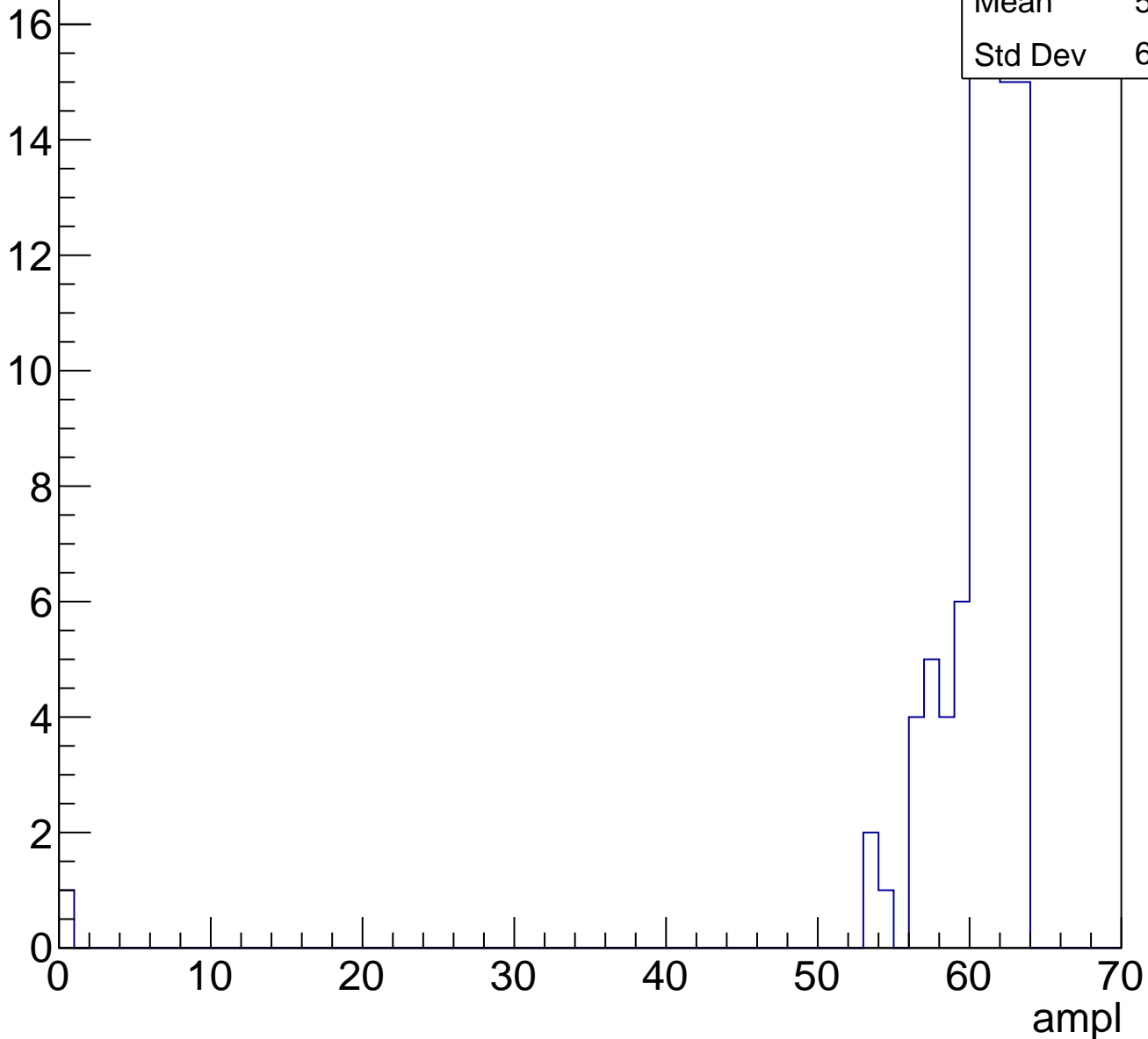
ampl



B1L001S, U22-ch70, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	86
Mean	59.62
Std Dev	6.875

B1L001S, U22-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	10
Std Dev	10

B1L001S, U22-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.76
Std Dev	3.373

Gaus mean : 29.2184

Gaus Width: 3.2354

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

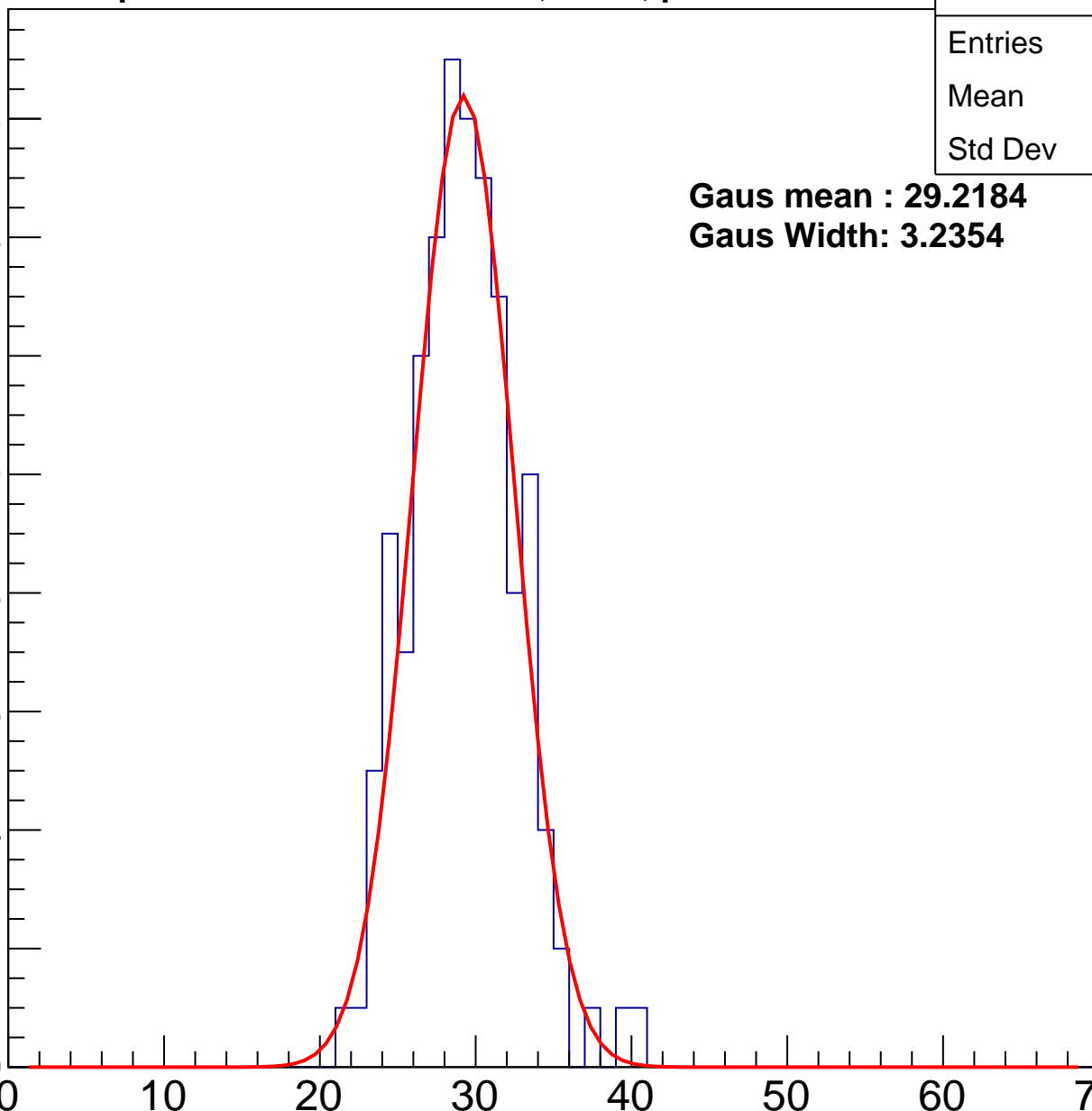
40

50

60

70

ampl



B1L001S, U22-ch71, adc1

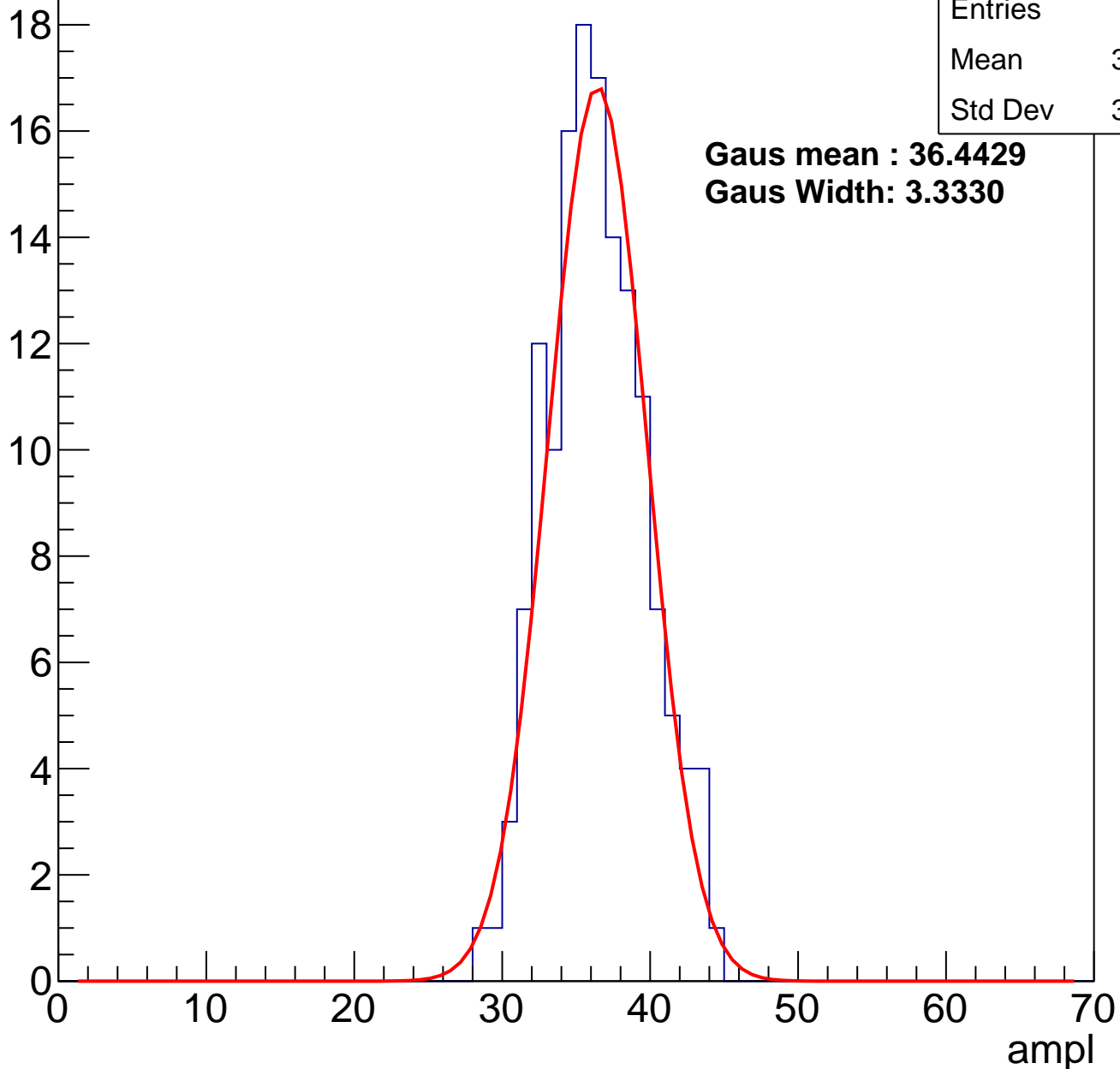
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	35.93
Std Dev	3.297

Gaus mean : 36.4429

Gaus Width: 3.3330

Entry



B1L001S, U22-ch71, adc2

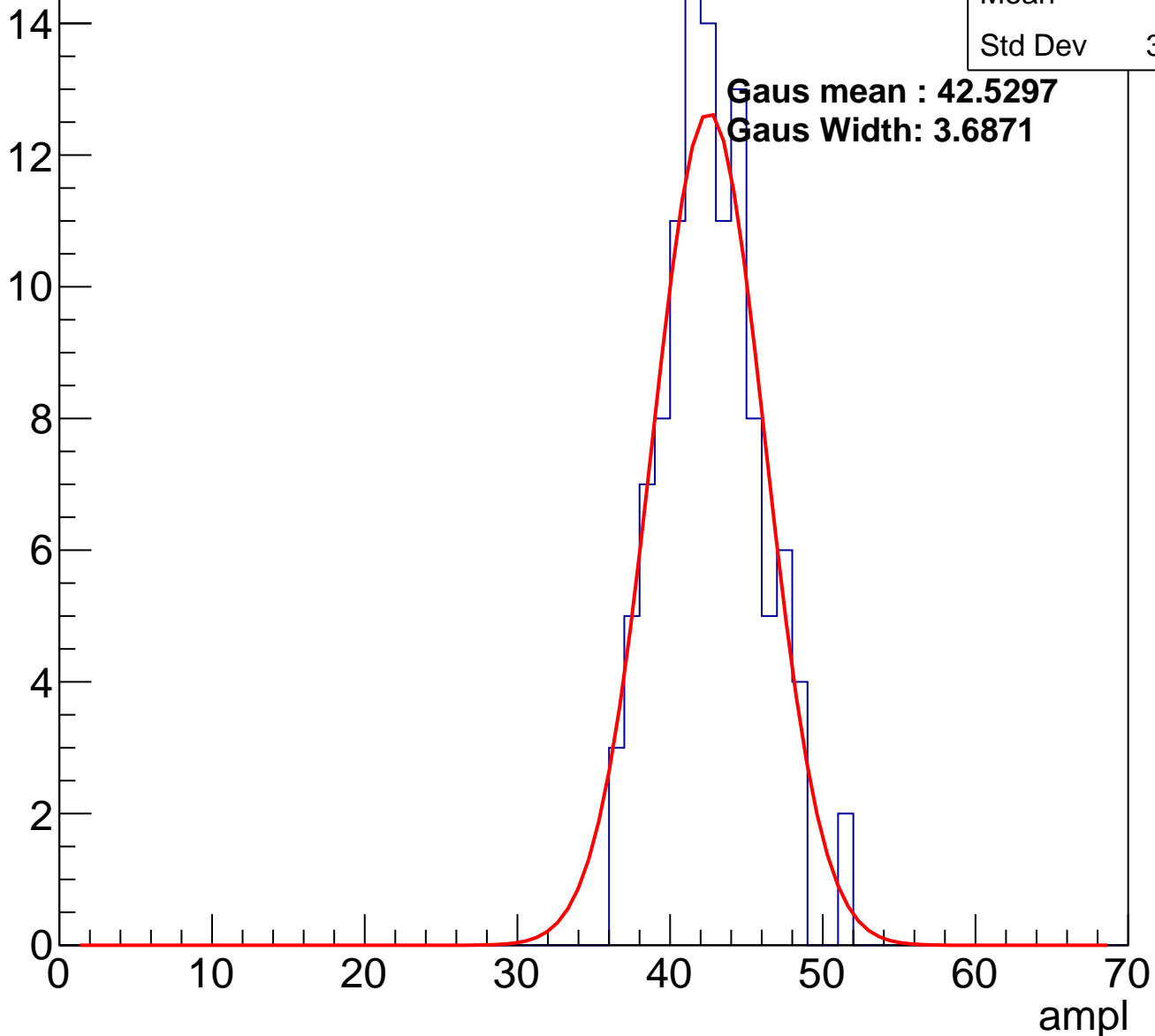
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	42.19
Std Dev	3.195

Gaus mean : 42.5297

Gaus Width: 3.6871

Entry

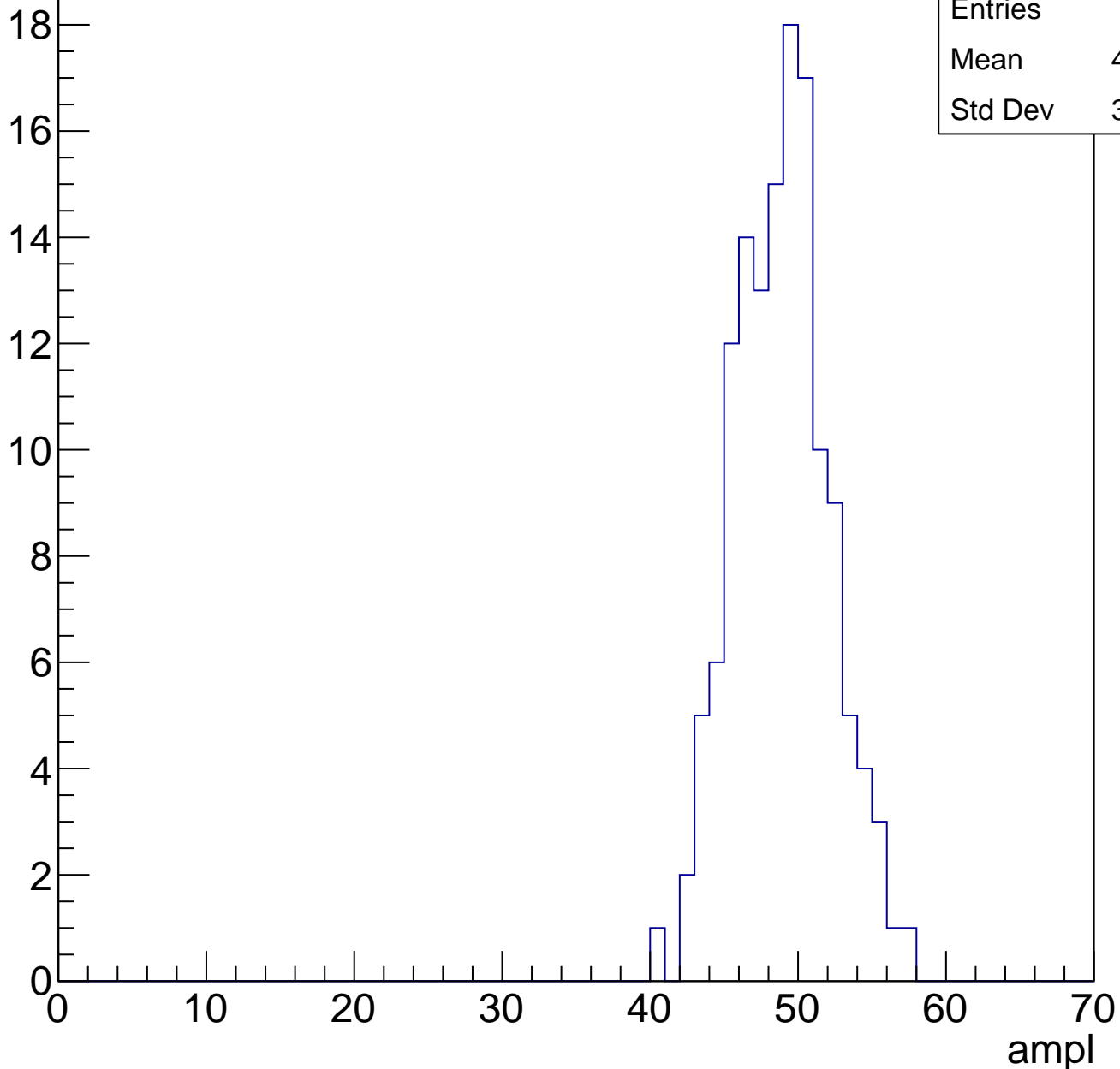


B1L001S, U22-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	48.43
Std Dev	3.196

Entry



B1L001S, U22-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	55.58
Std Dev	3.343

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

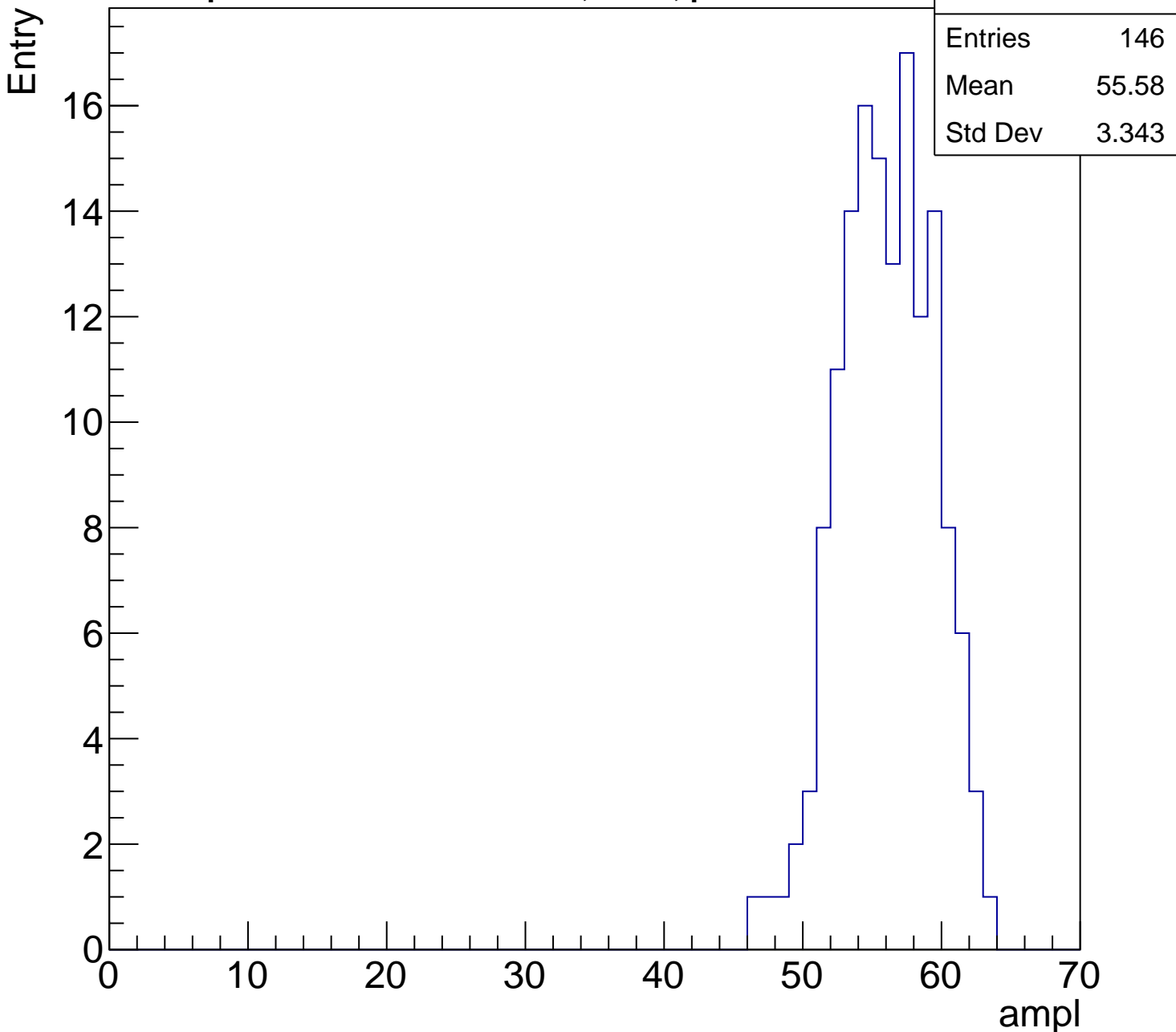
40

50

60

ampl

70



B1L001S, U22-ch71, adc5

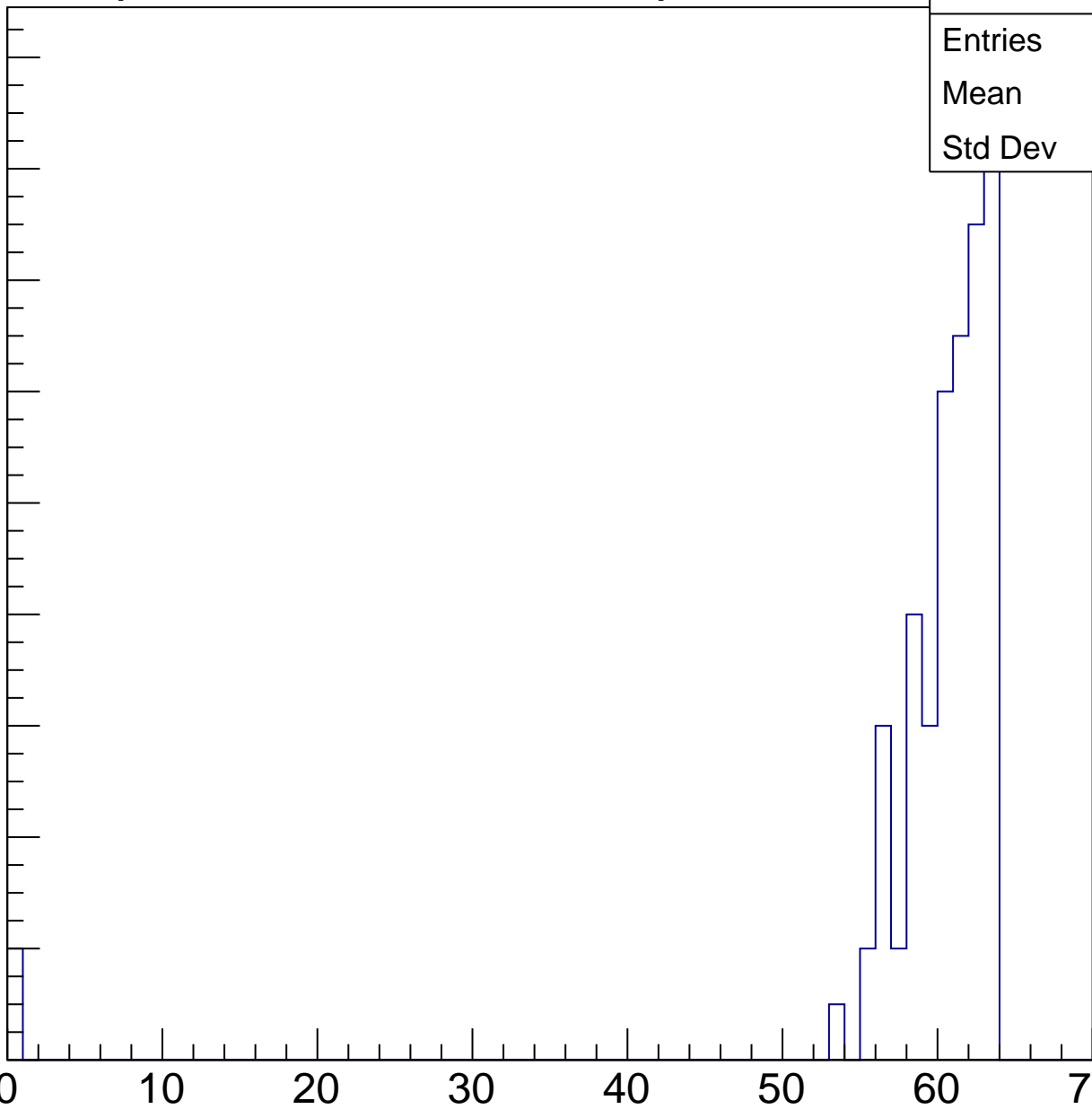
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.92
Std Dev	9.447

ampl



B1L001S, U22-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

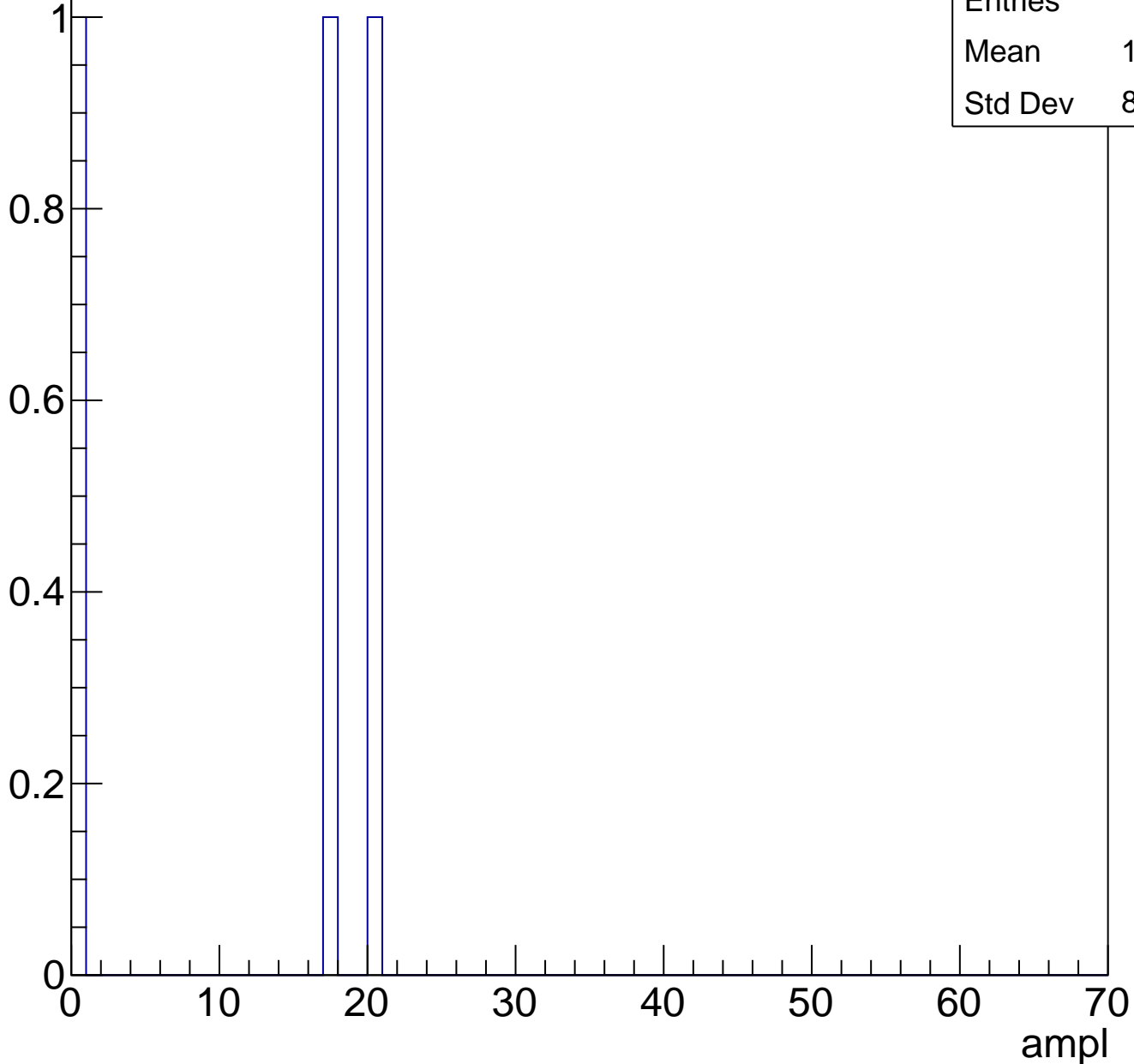
ampl

0 10 20 30 40 50 60 70

B1L001S, U22-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch72, adc0

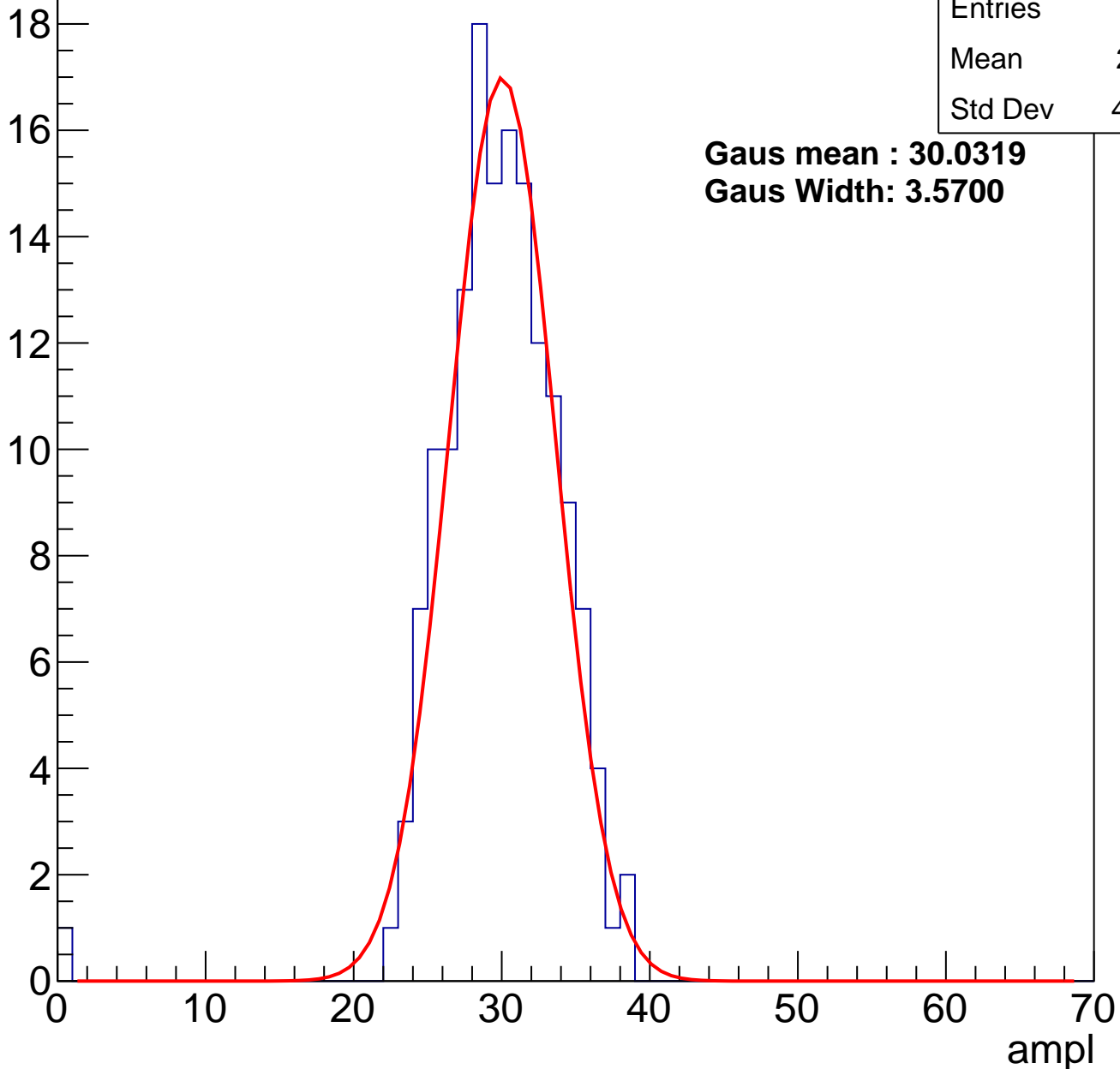
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	29.41
Std Dev	4.193

Gaus mean : 30.0319

Gaus Width: 3.5700

Entry



B1L001S, U22-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	36.28
Std Dev	3.007

Gaus mean : 36.6902

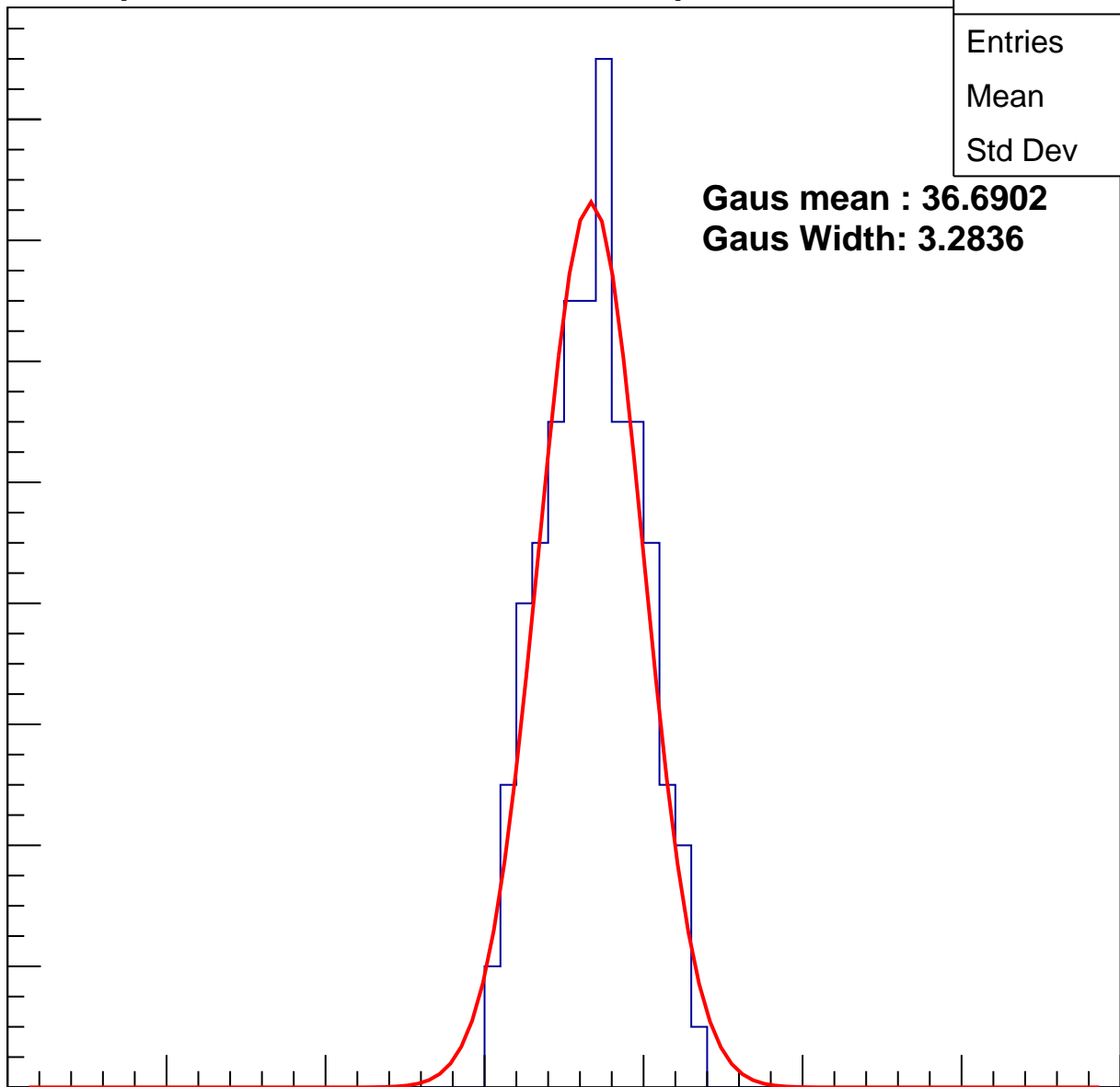
Gaus Width: 3.2836

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch72, adc2

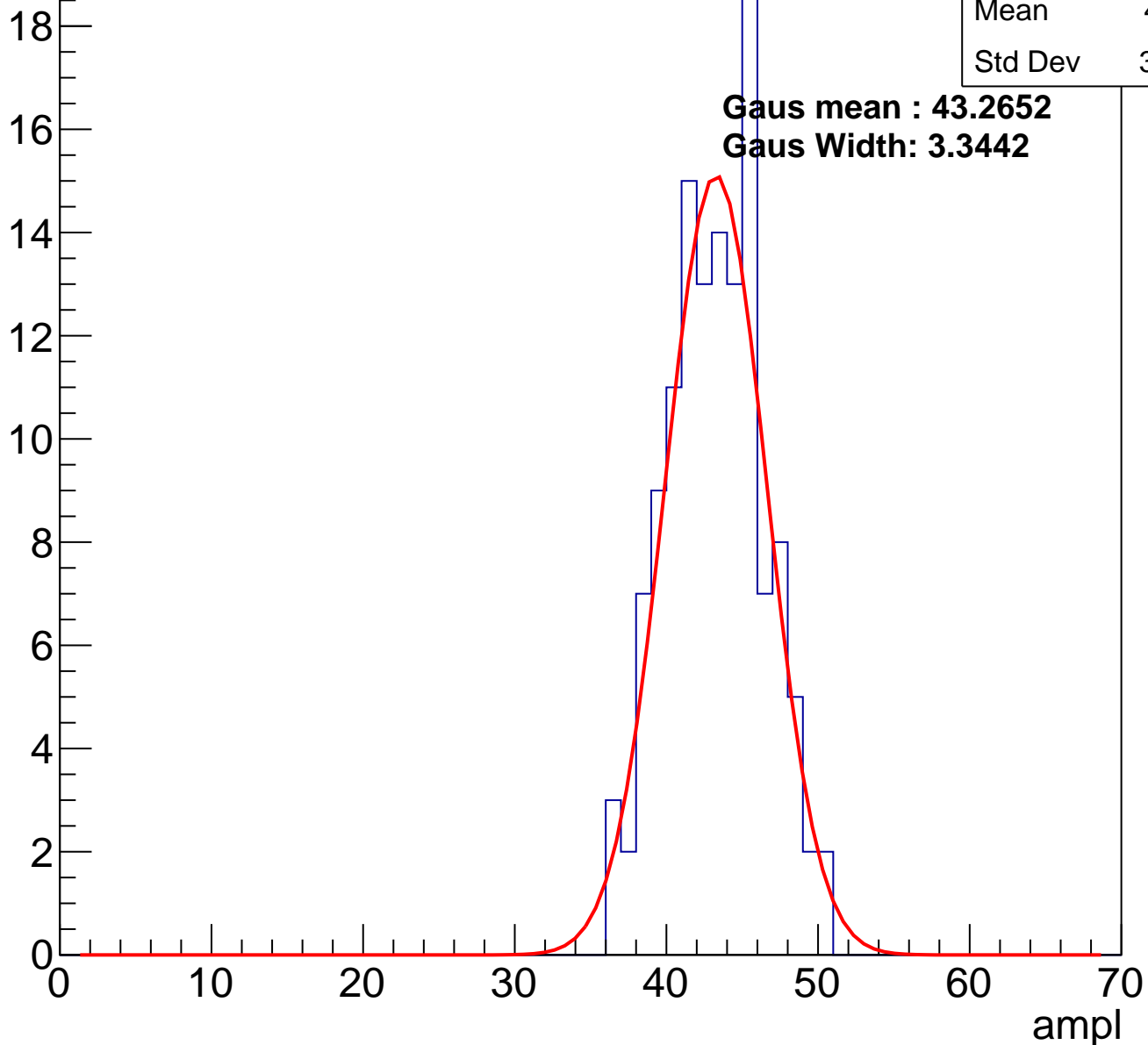
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	42.81
Std Dev	3.138

Entry

Gaus mean : 43.2652

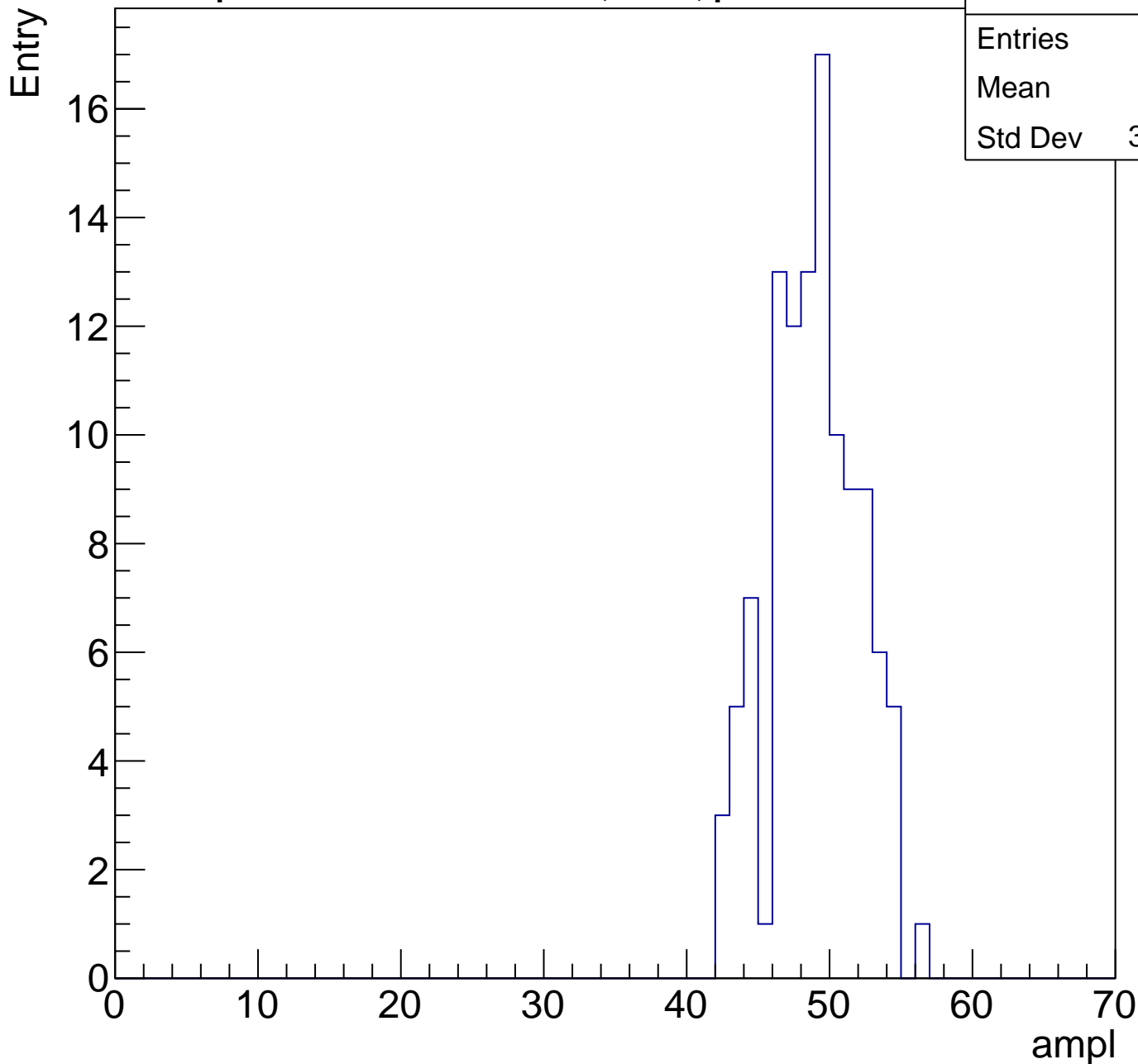
Gaus Width: 3.3442



B1L001S, U22-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	48.5
Std Dev	3.099



B1L001S, U22-ch72, adc4

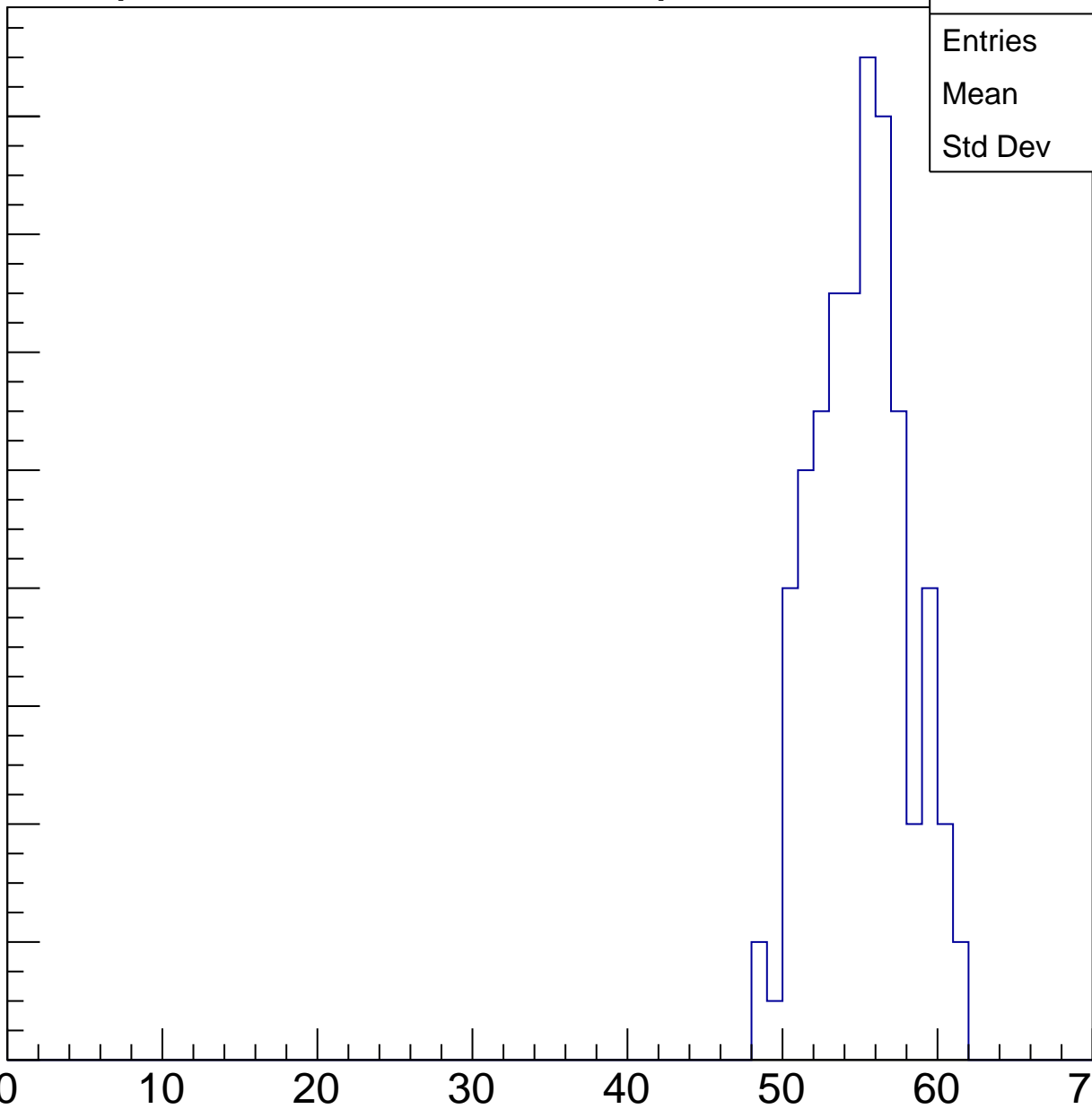
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	54.52
Std Dev	2.924

ampl



B1L001S, U22-ch72, adc5

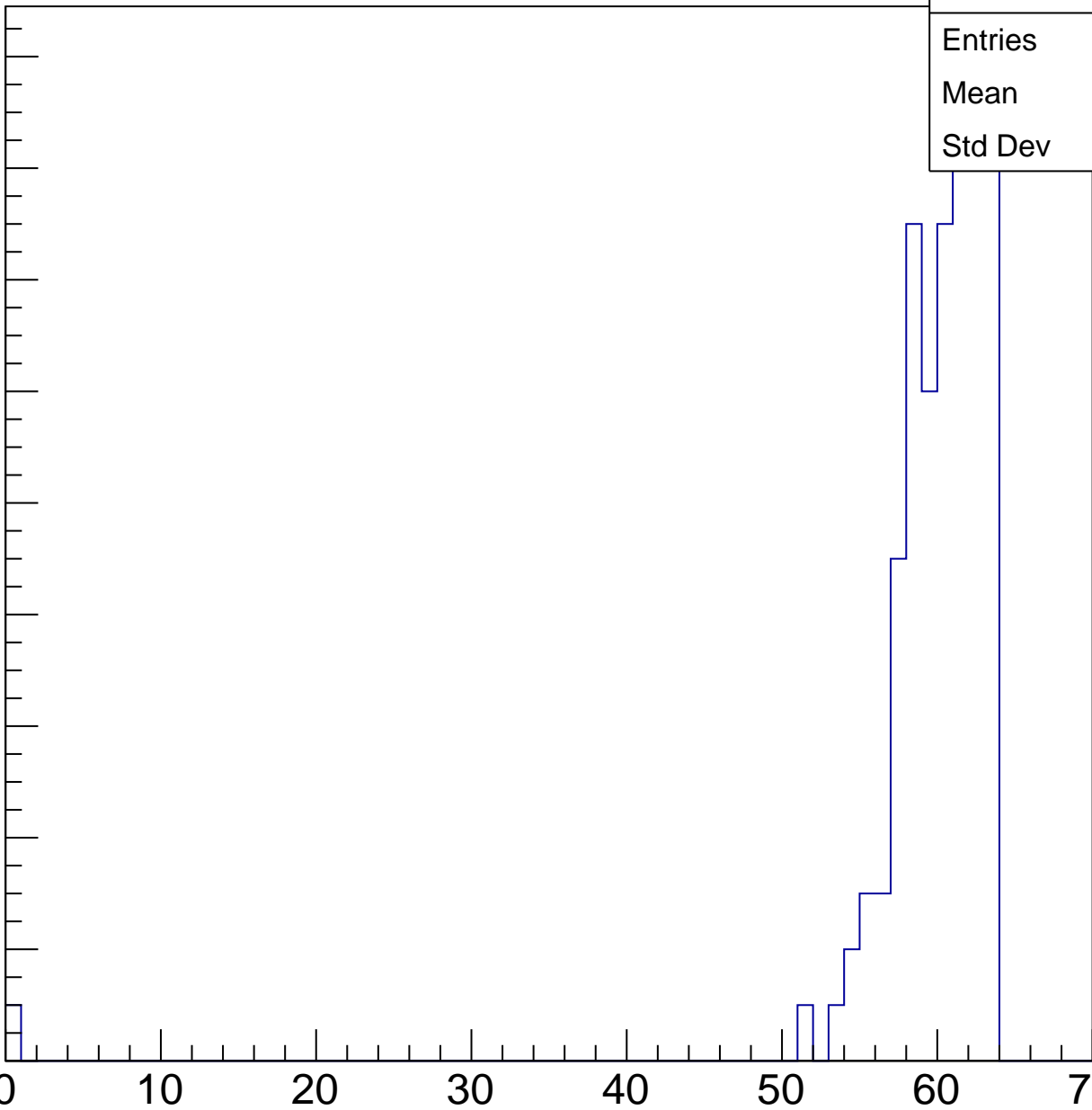
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	115
Mean	59.31
Std Dev	6.091

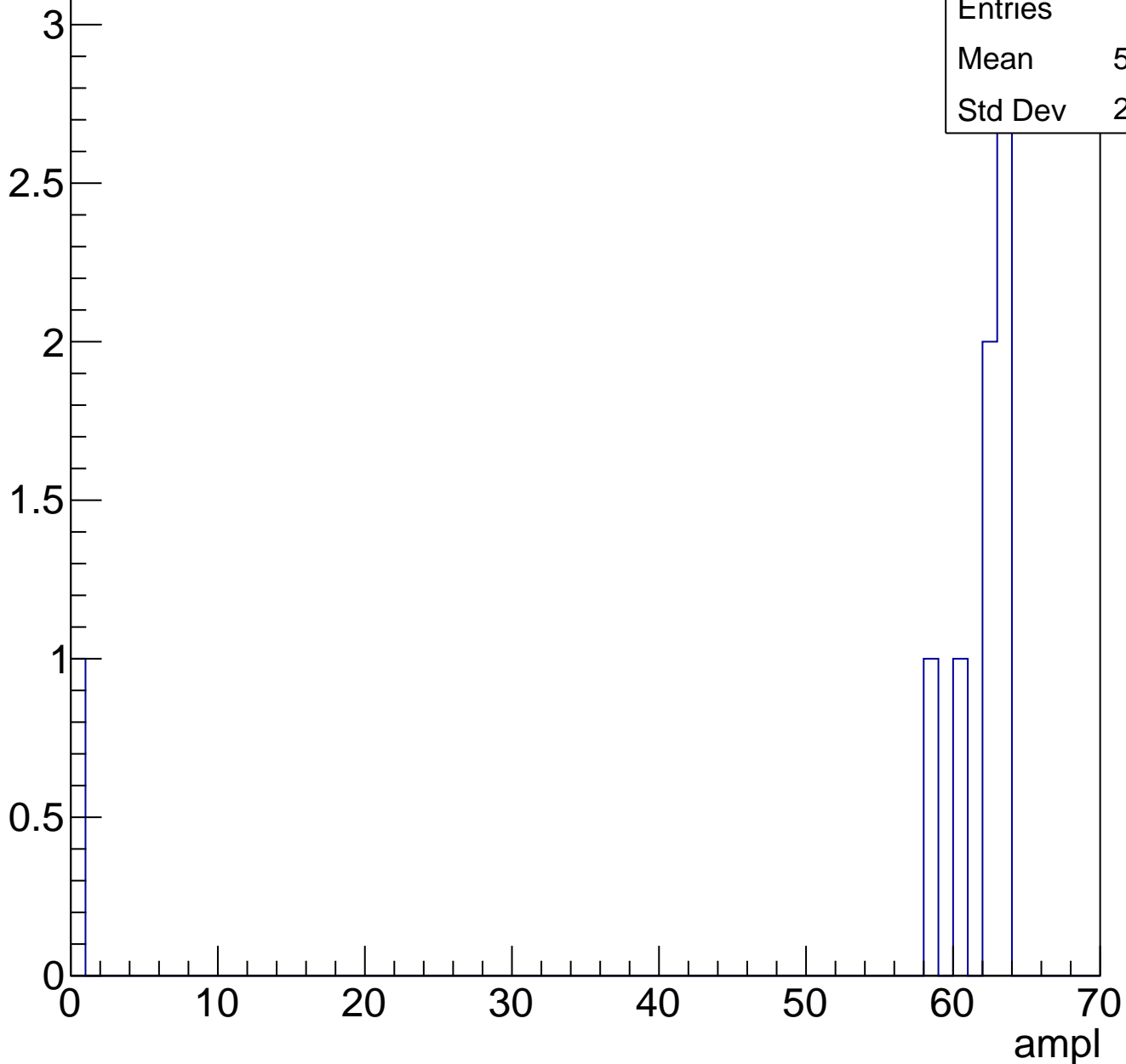
ampl



B1L001S, U22-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L001S, U22-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	28.98
Std Dev	4.27

Gaus mean : 29.9901

Gaus Width: 3.5286

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

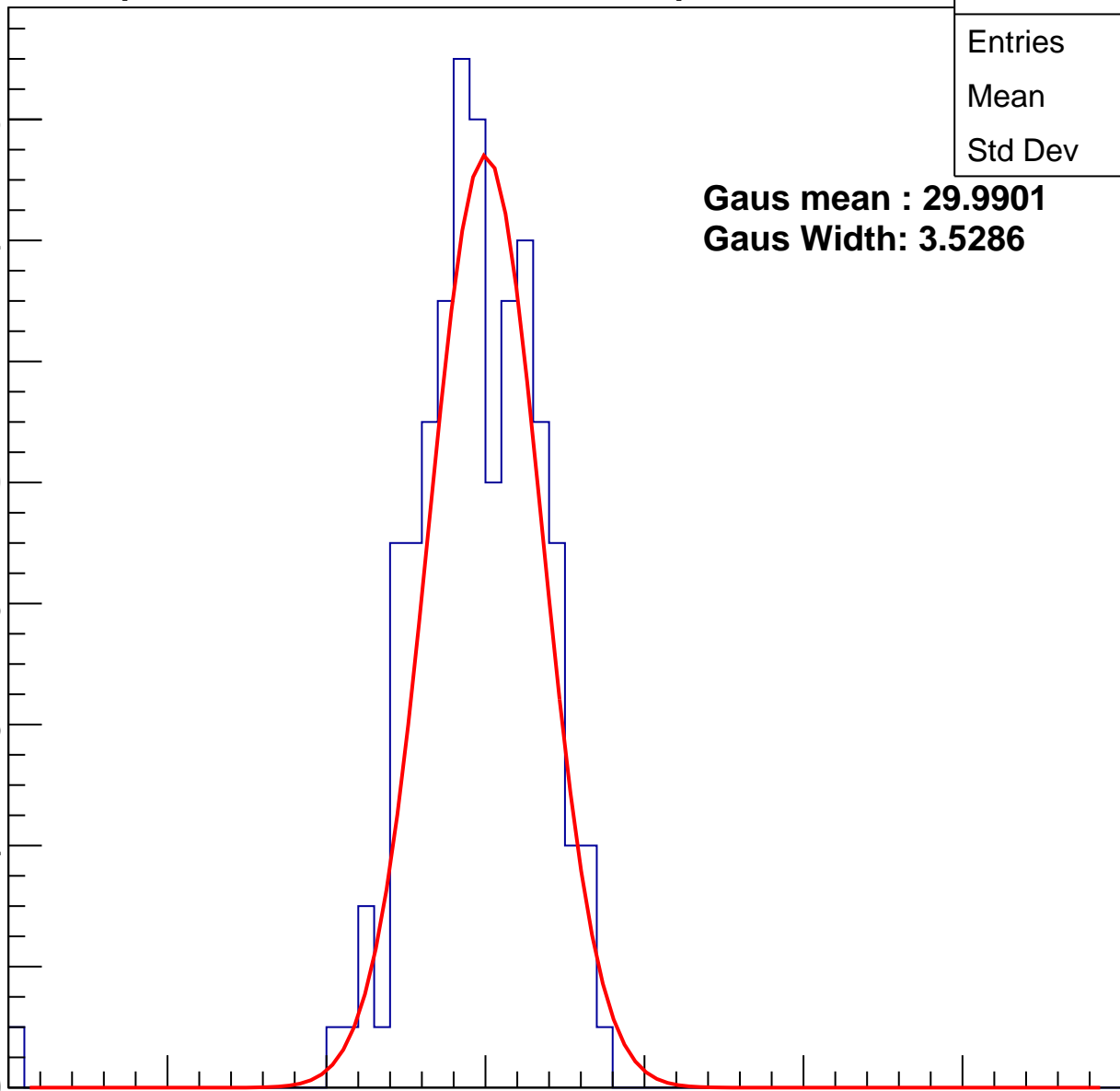
40

50

60

70

ampl



B1L001S, U22-ch73, adc1

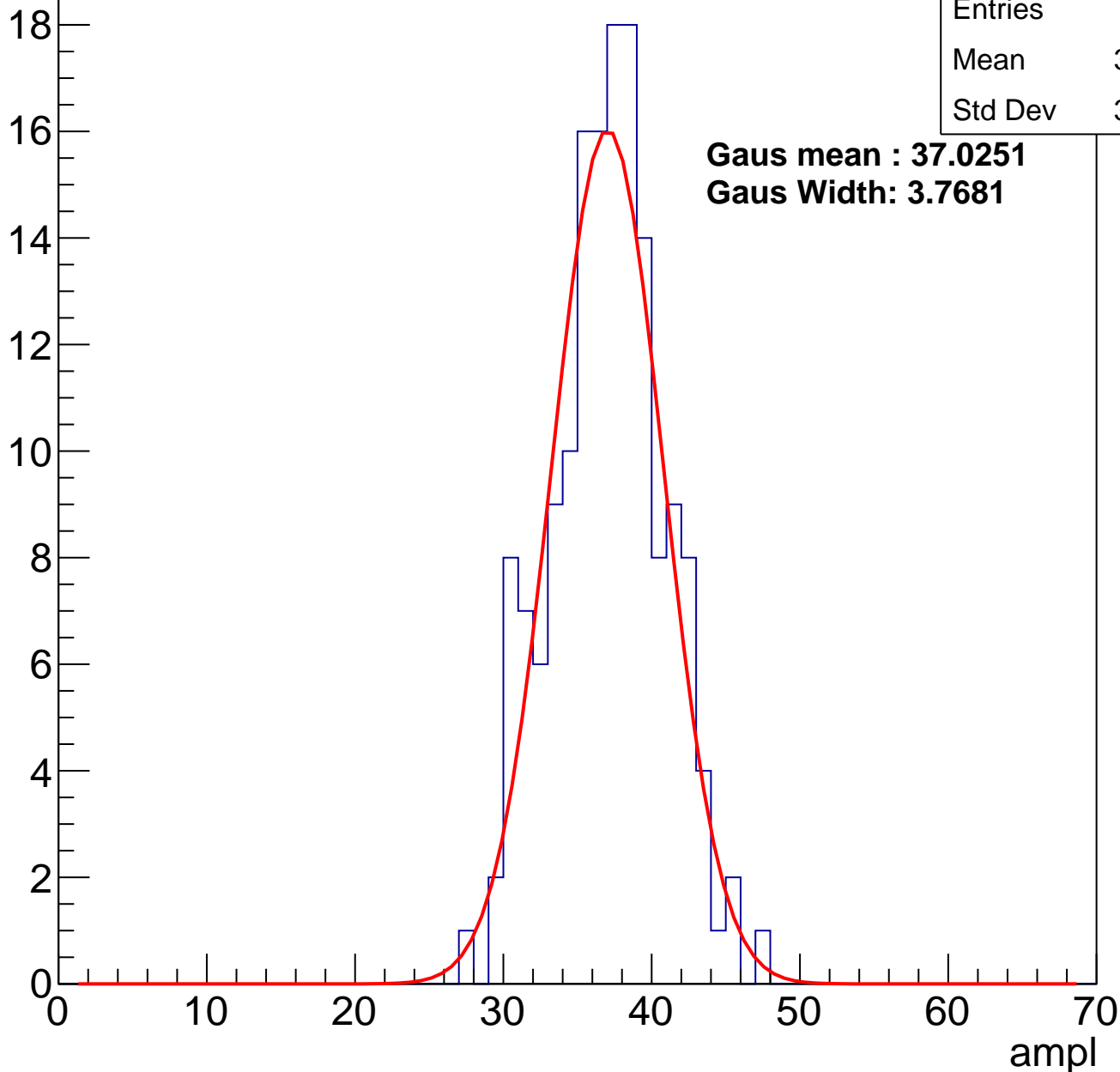
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	36.59
Std Dev	3.749

Gaus mean : 37.0251

Gaus Width: 3.7681

Entry



B1L001S, U22-ch73, adc2

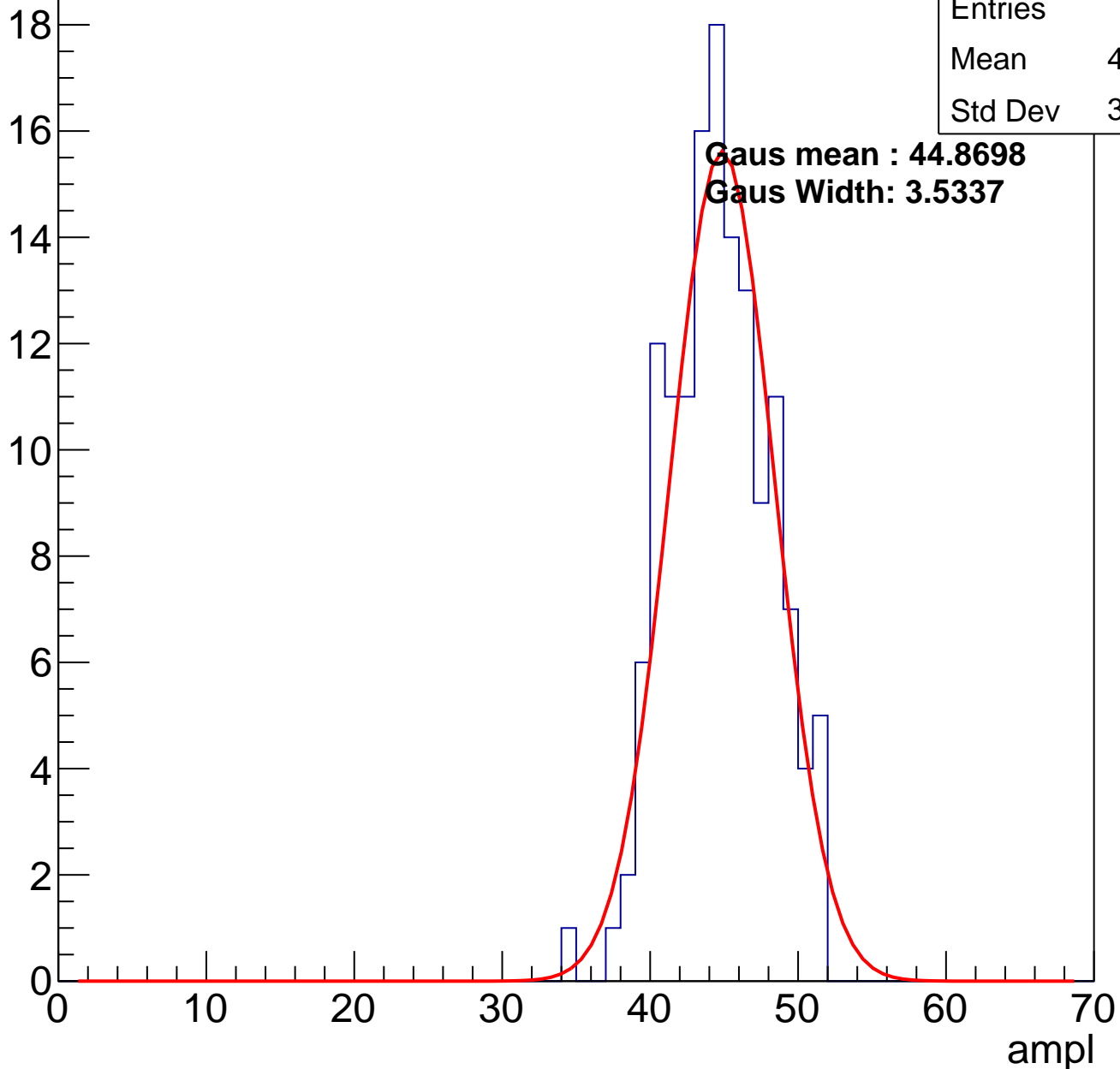
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	44.19
Std Dev	3.373

Gaus mean : 44.8698

Gaus Width: 3.5337

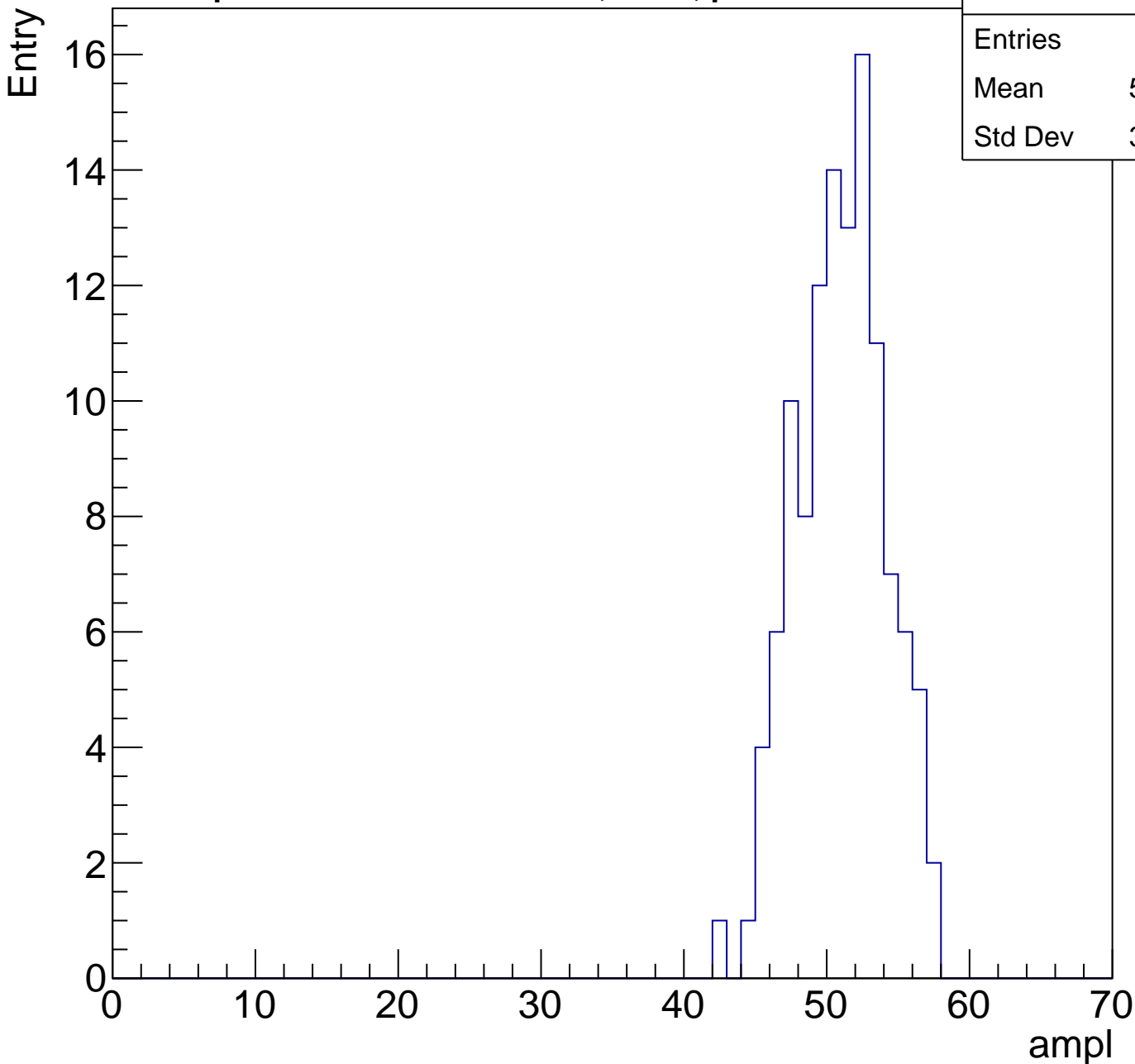
Entry



B1L001S, U22-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	50.55
Std Dev	3.094



B1L001S, U22-ch73, adc4

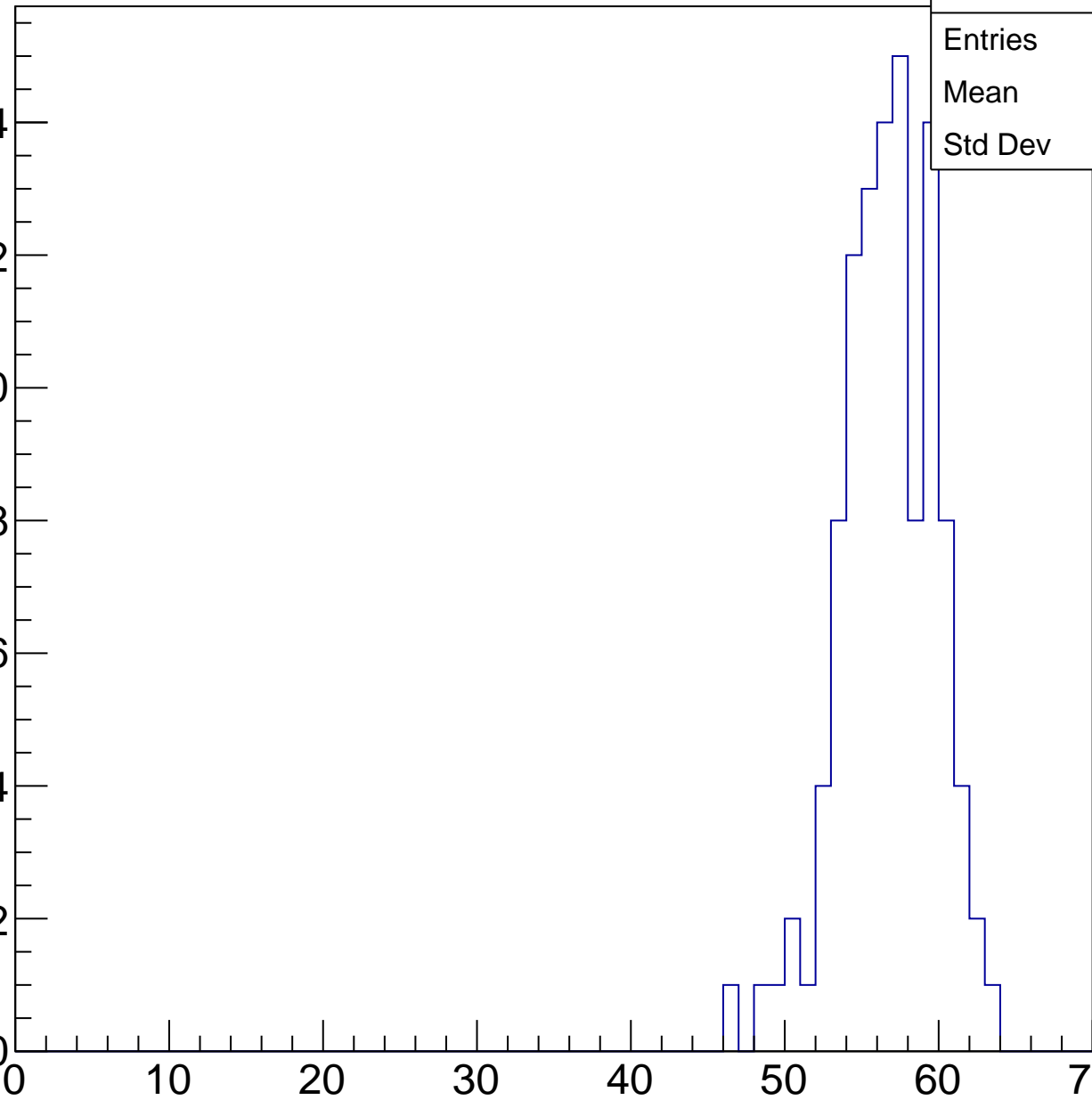
calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	56.23
Std Dev	3.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch73, adc5

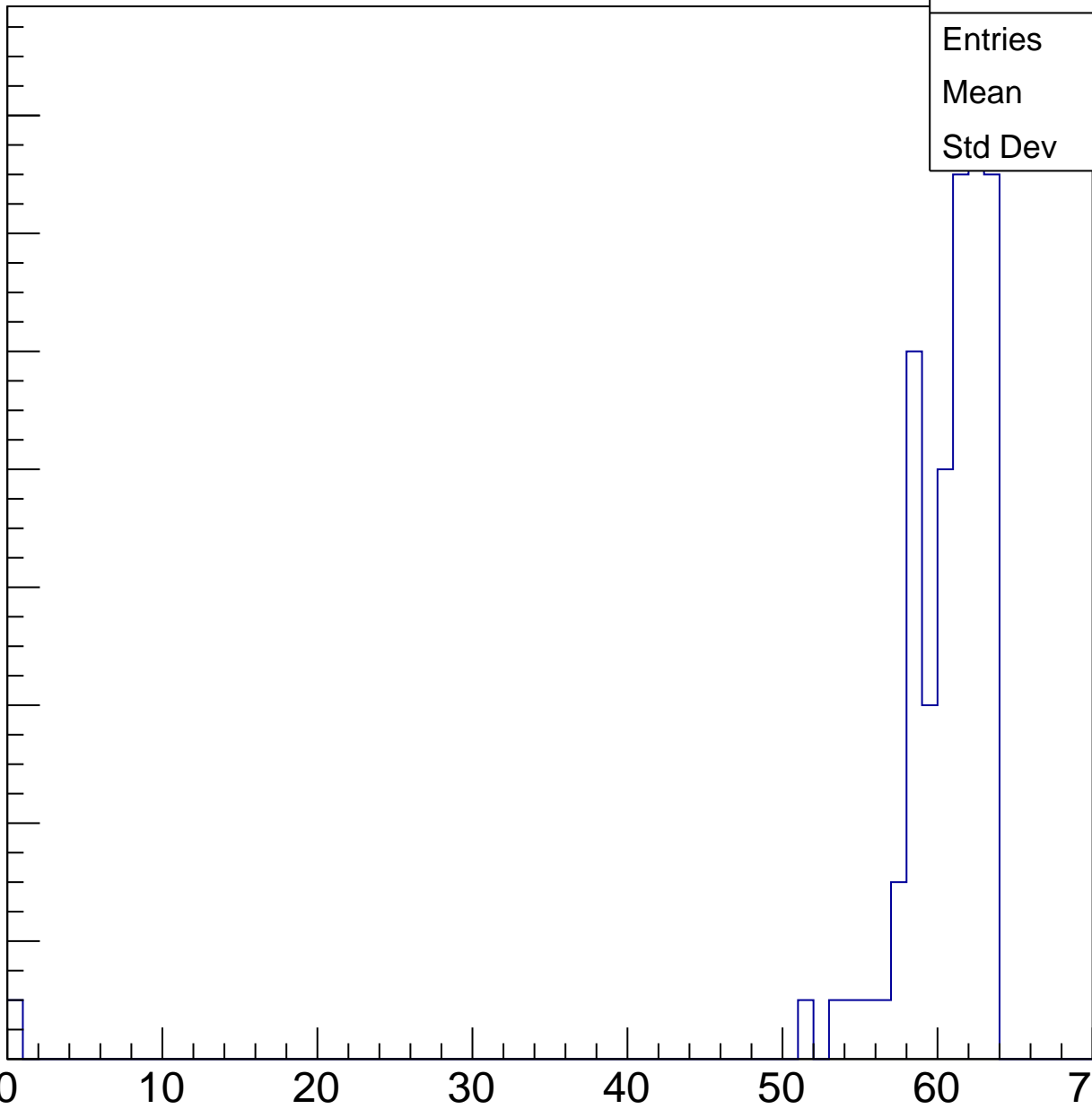
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.57
Std Dev	6.978

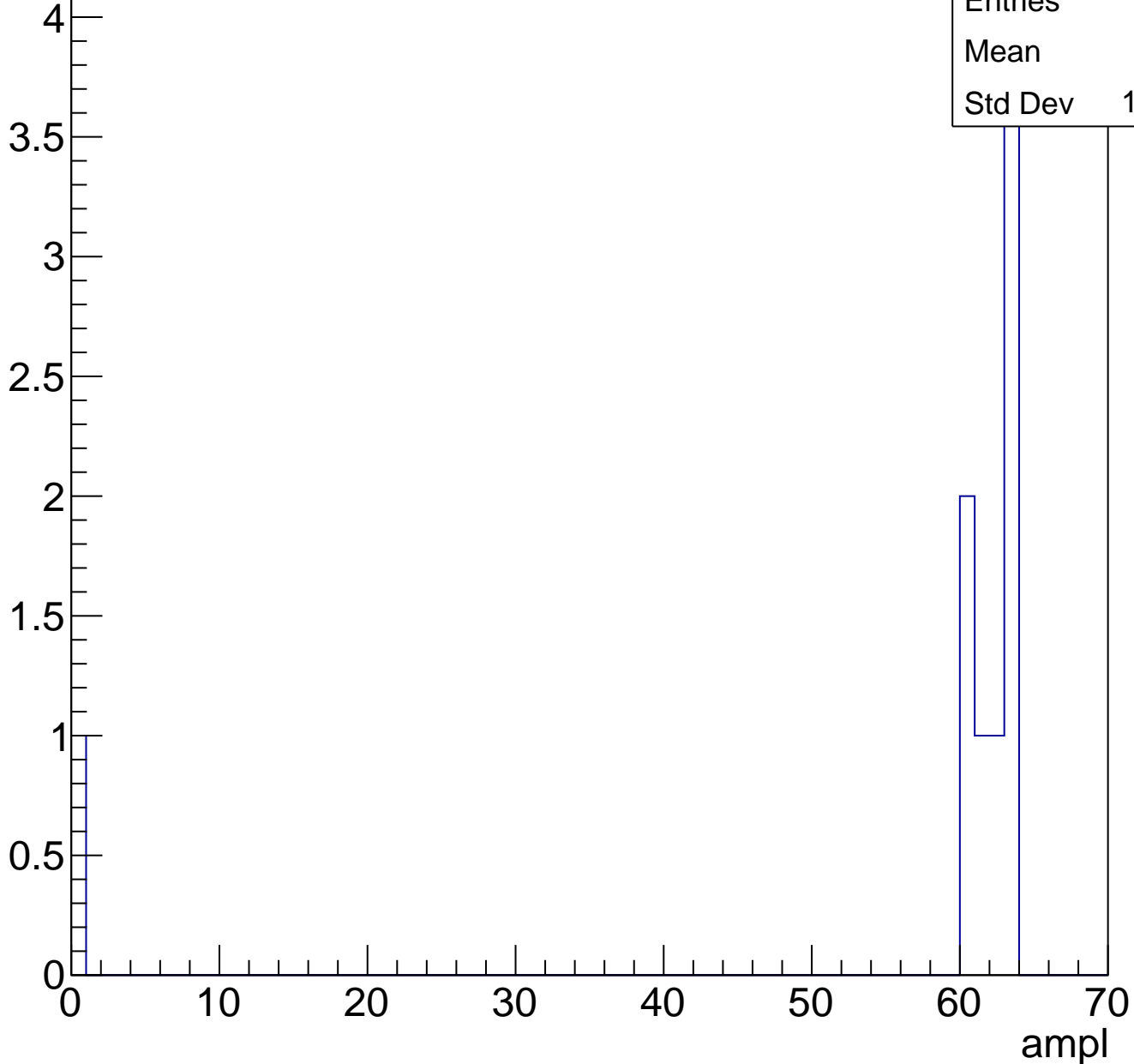
ampl



B1L001S, U22-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

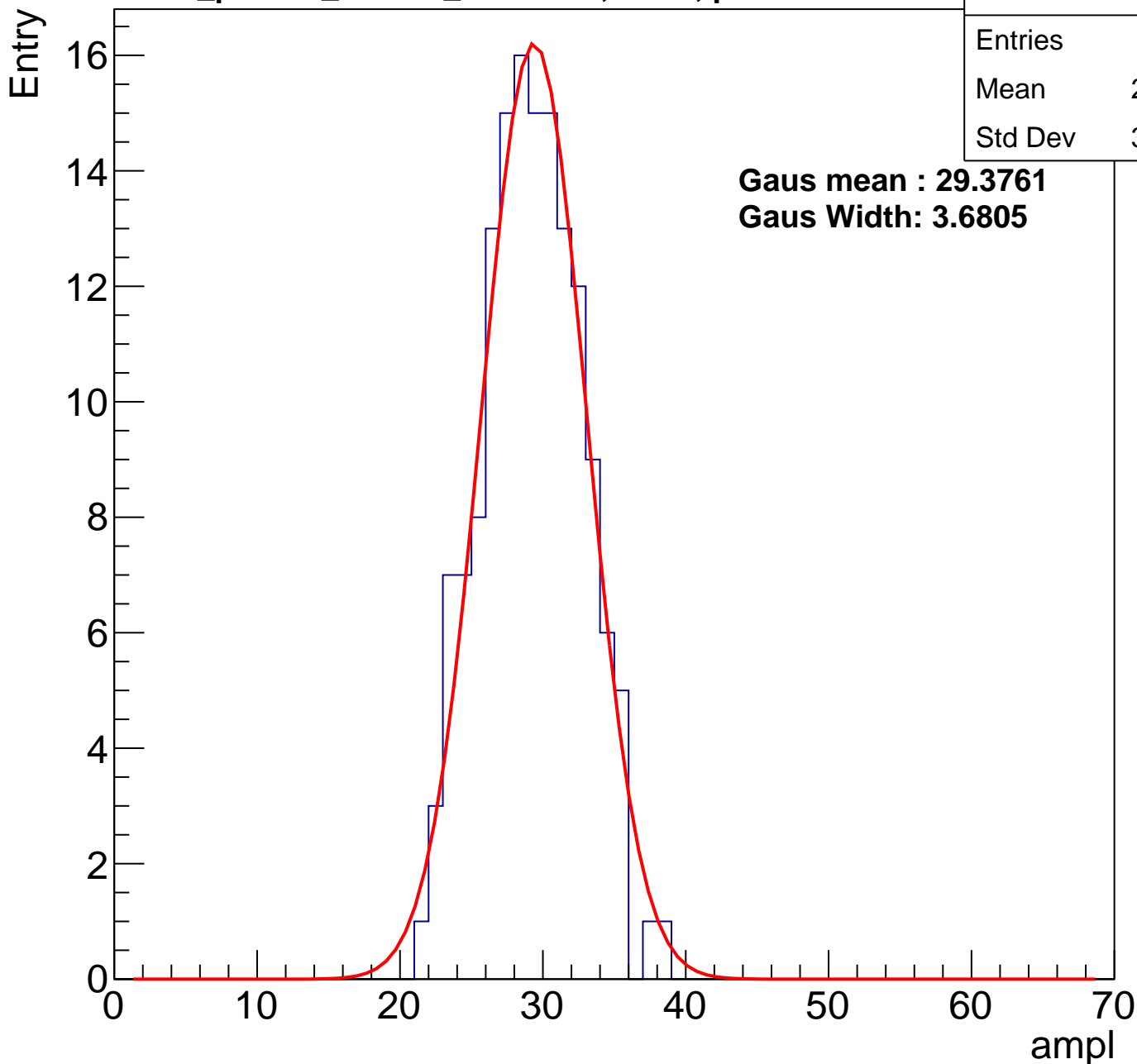
B1L001S, U22-ch74, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	28.78
Std Dev	3.446

Gaus mean : 29.3761

Gaus Width: 3.6805



B1L001S, U22-ch74, adc1

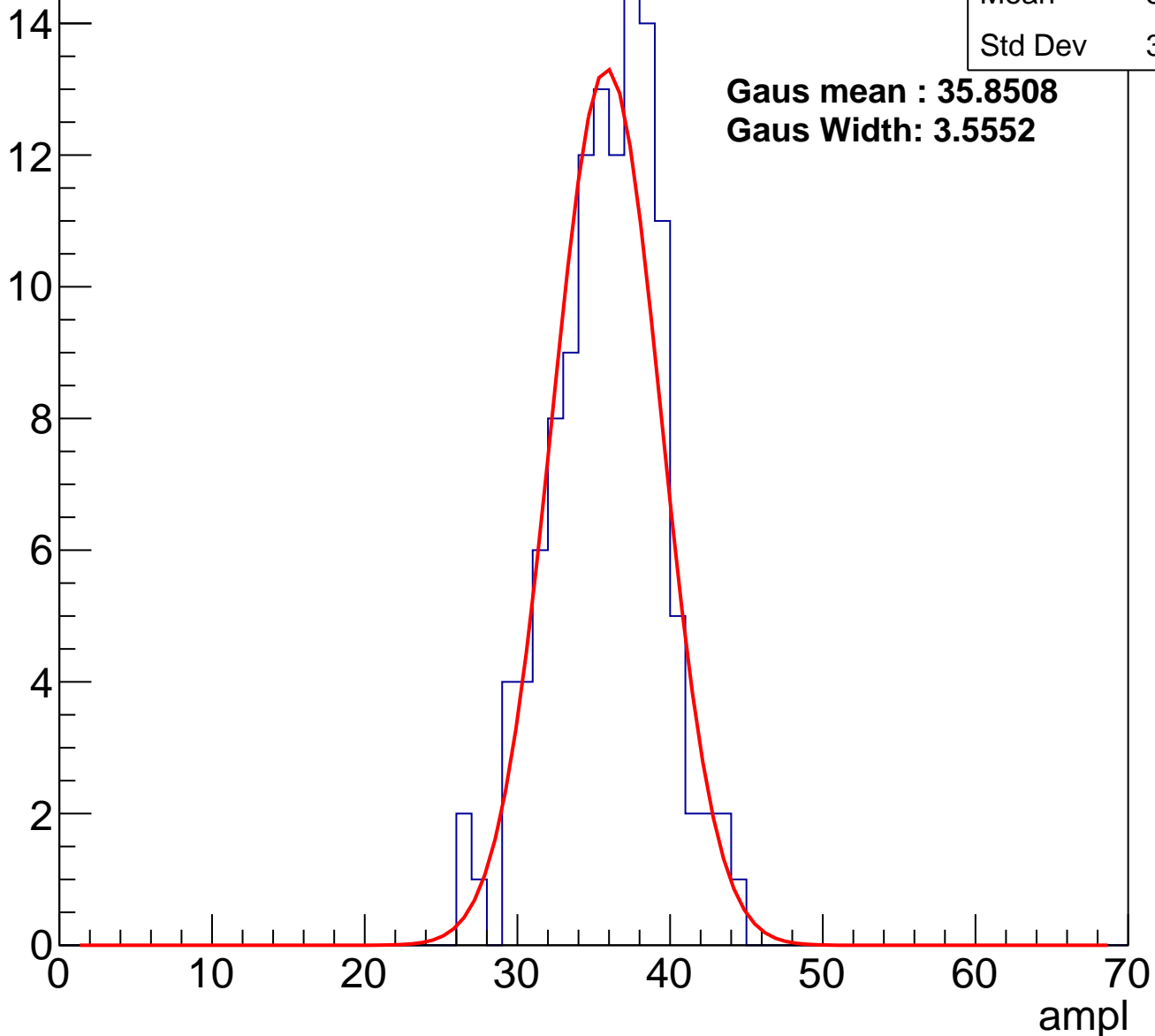
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.46
Std Dev	3.537

Gaus mean : 35.8508

Gaus Width: 3.5552

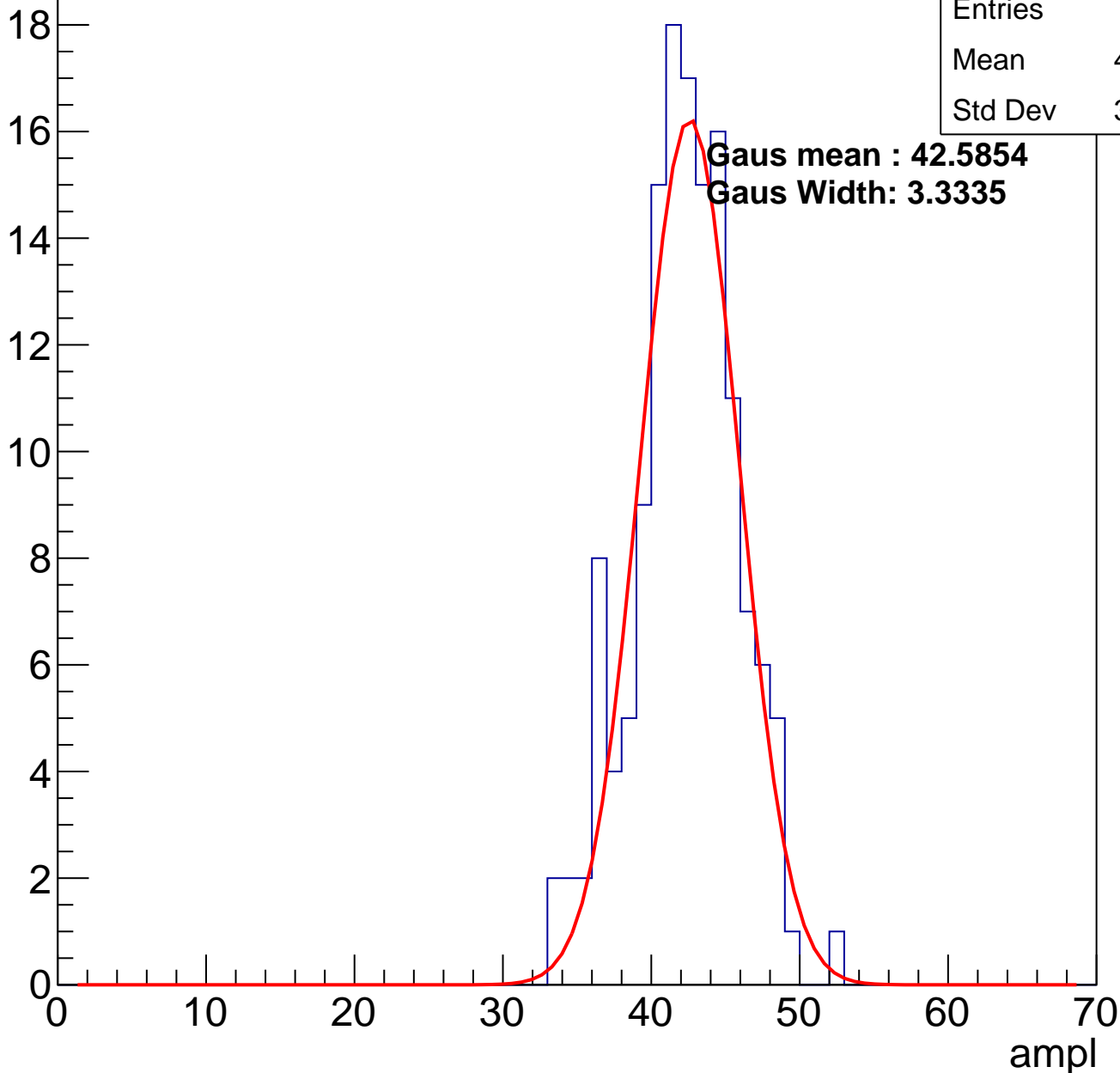
Entry



B1L001S, U22-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch74, adc3

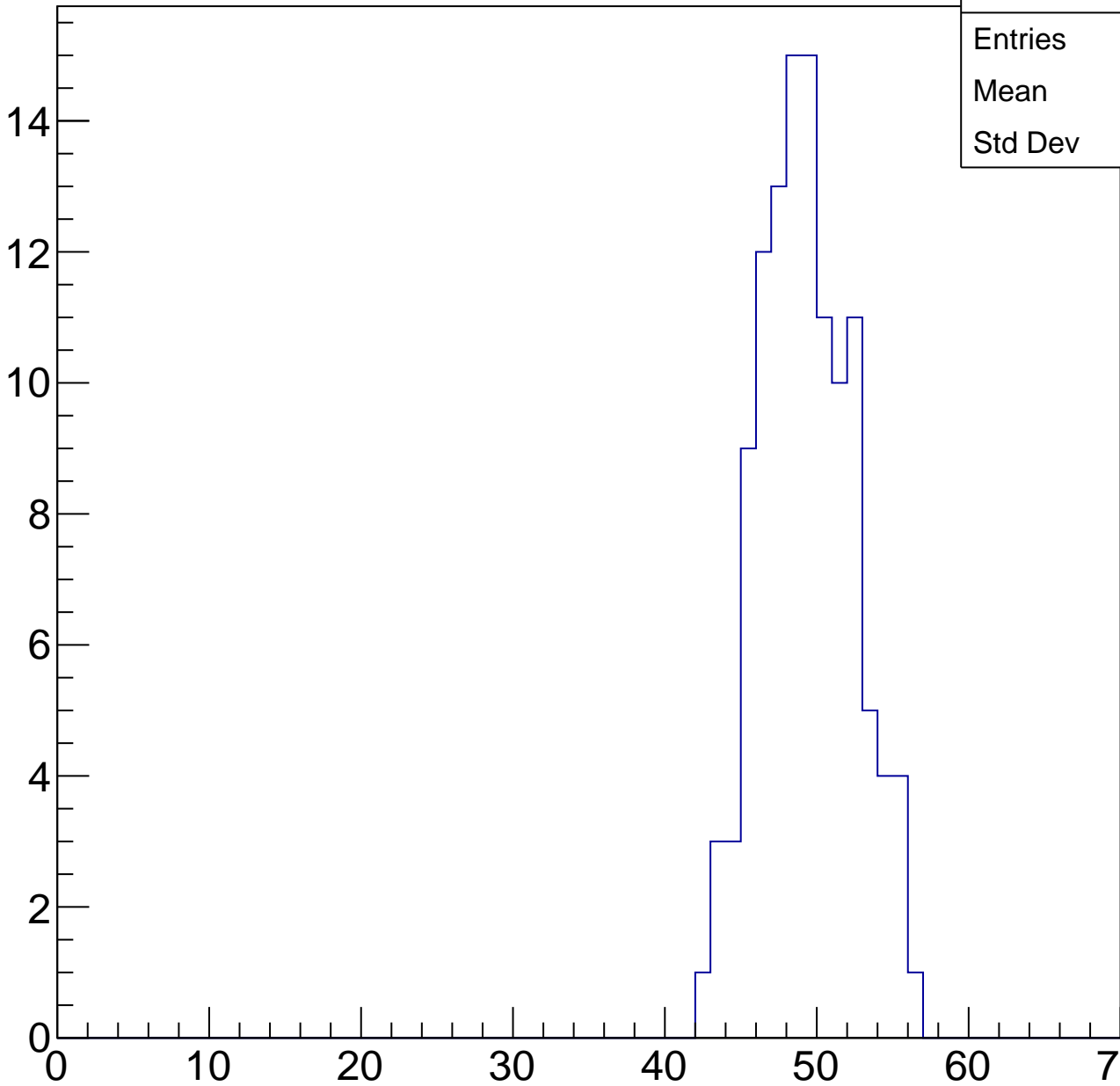
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	48.85
Std Dev	3.026

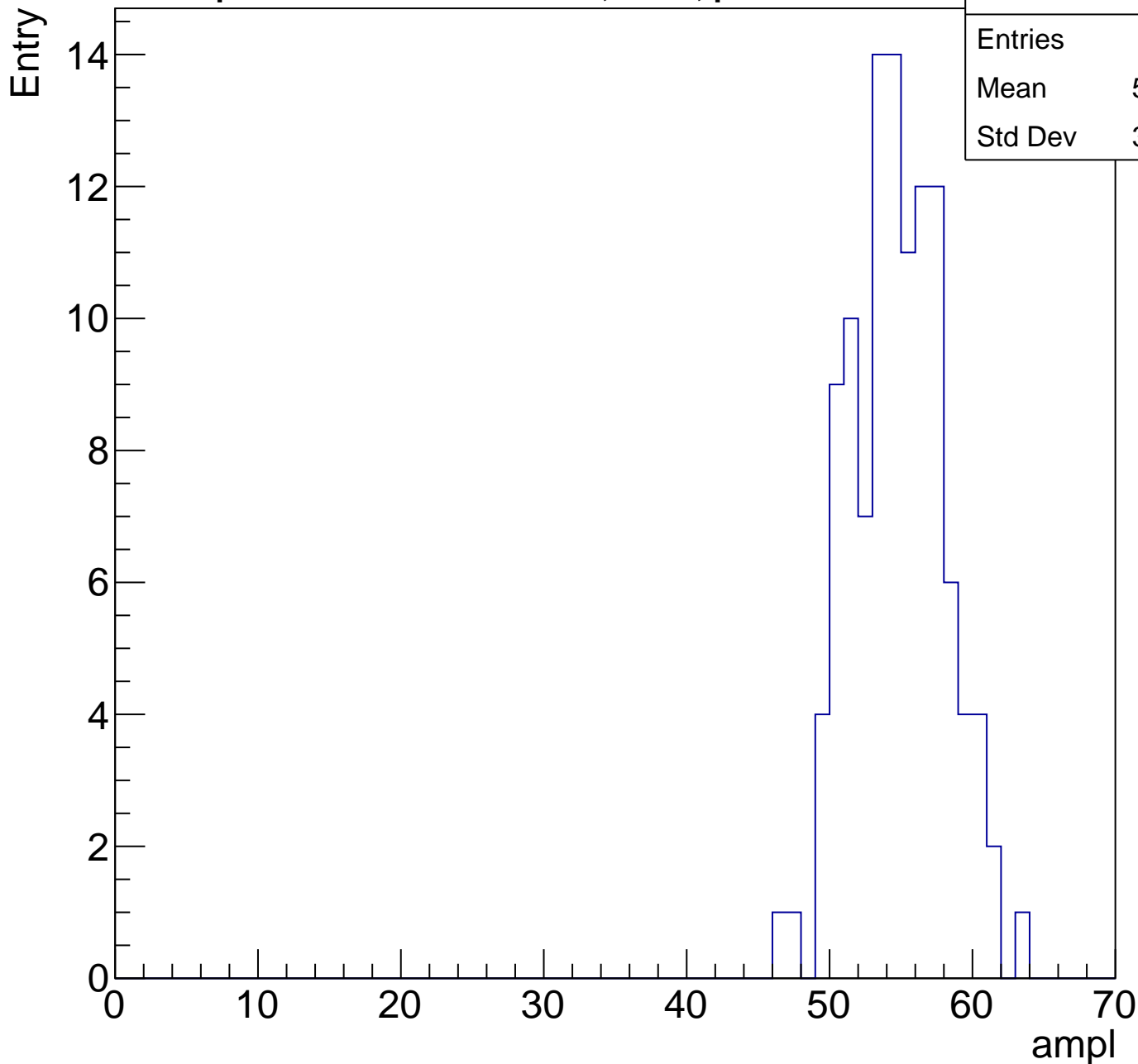
ampl



B1L001S, U22-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	54.29
Std Dev	3.226



B1L001S, U22-ch74, adc5

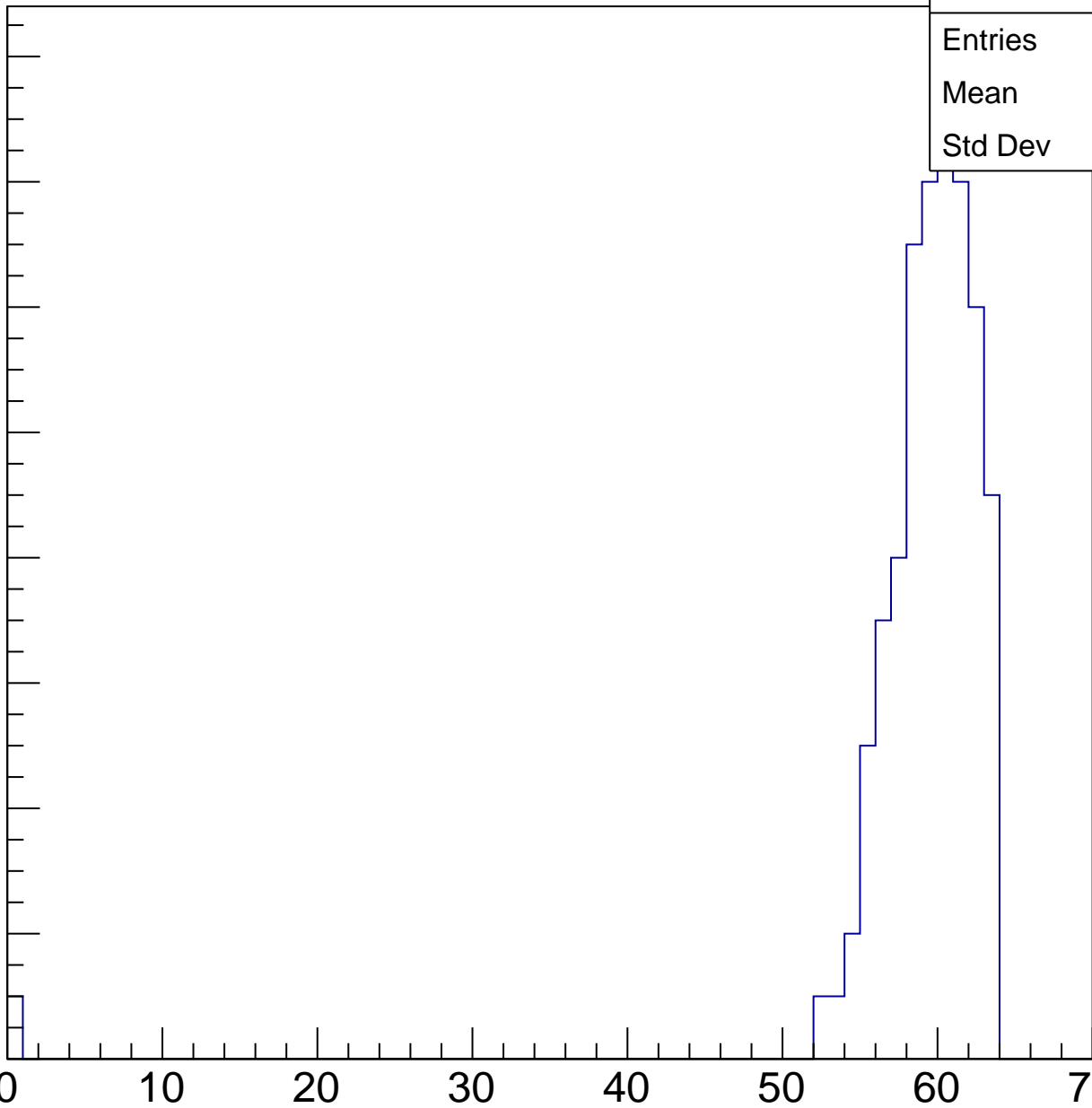
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.65
Std Dev	6.321

ampl



B1L001S, U22-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	26
Mean	59.46
Std Dev	11.97

Entry

10

8

6

4

2

0

0

10

20

30

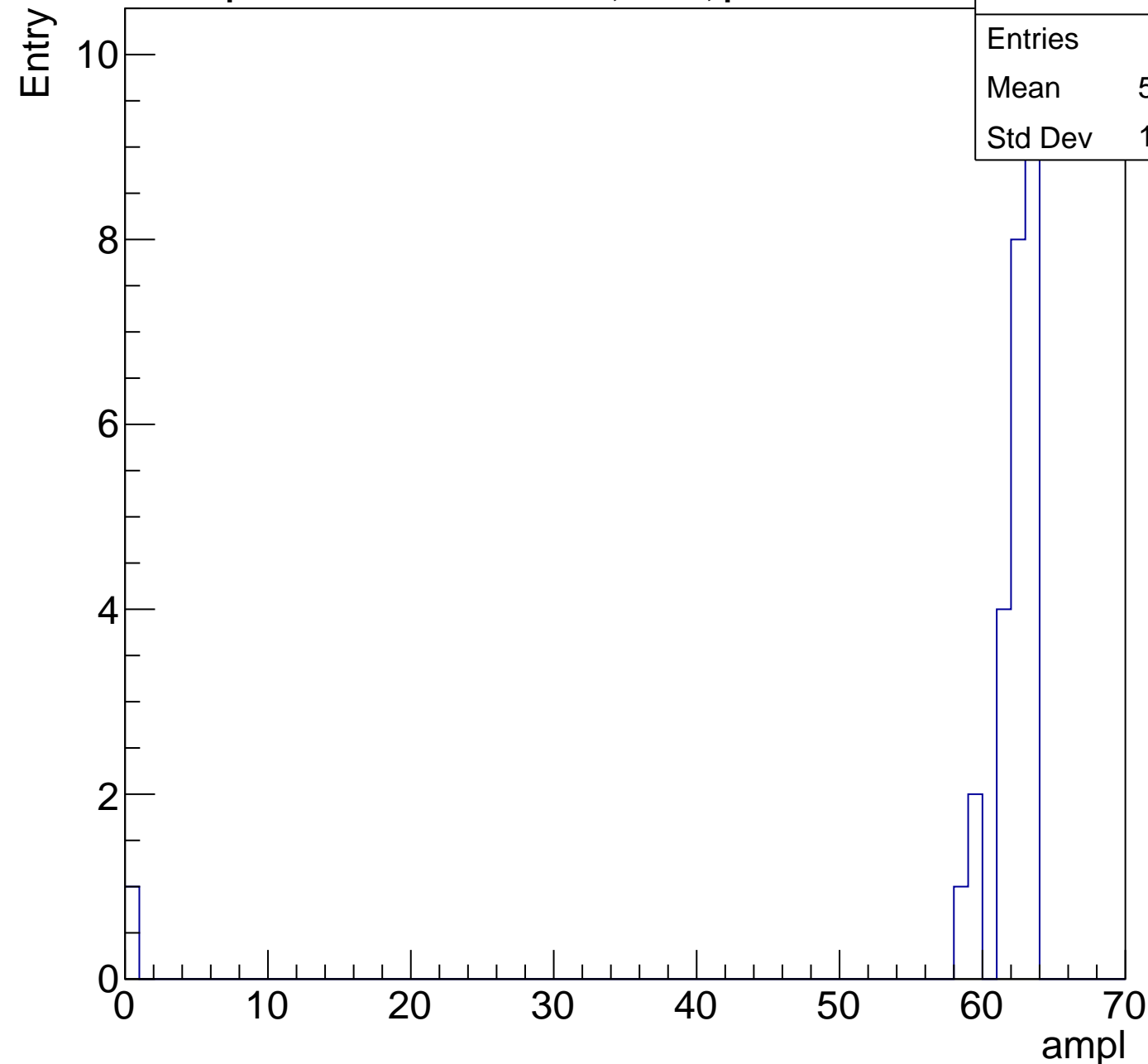
40

50

60

ampl

70



B1L001S, U22-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch75, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	27.42
Std Dev	3.869

Gaus mean : 28.1938

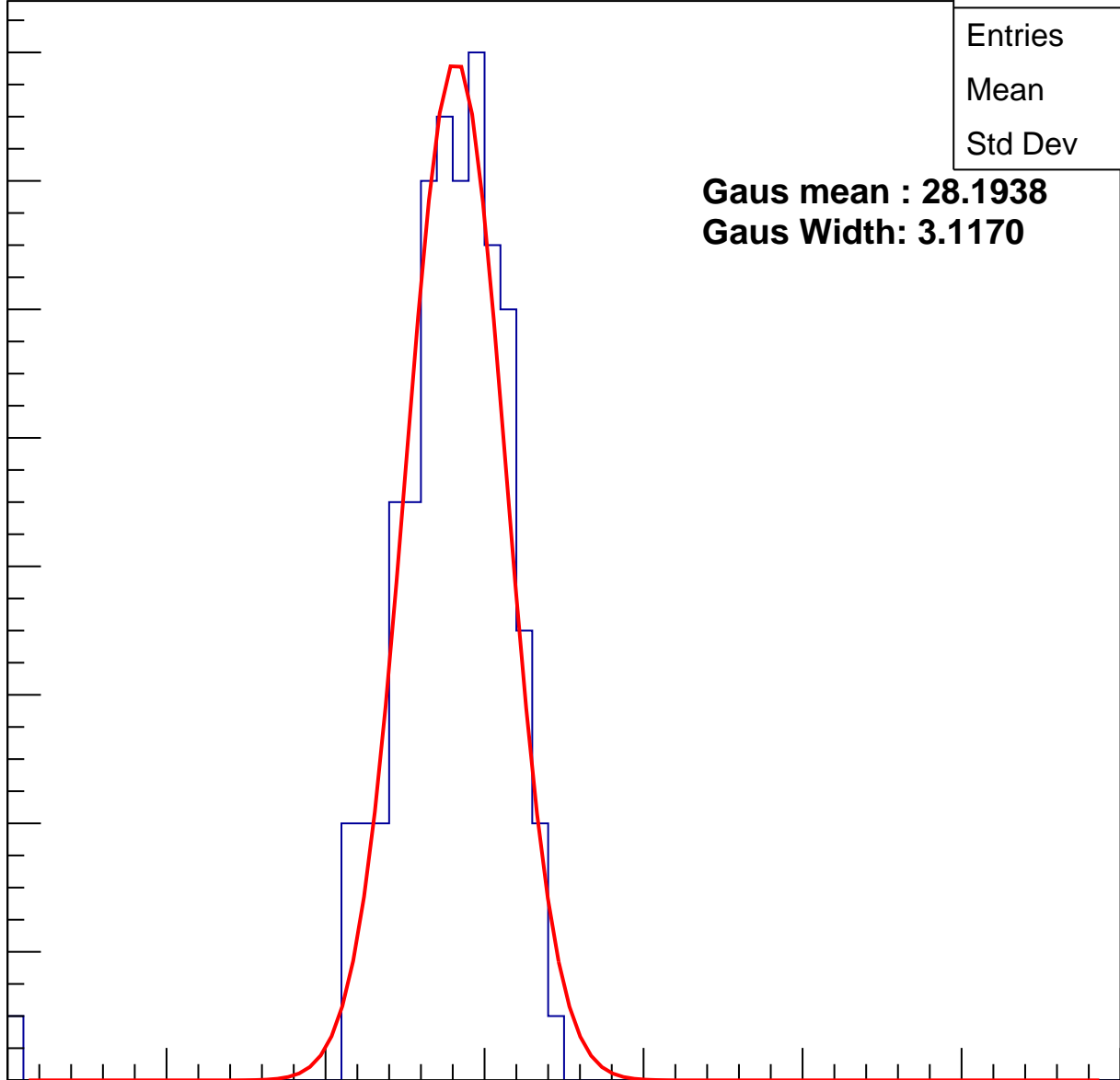
Gaus Width: 3.1170

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch75, adc1

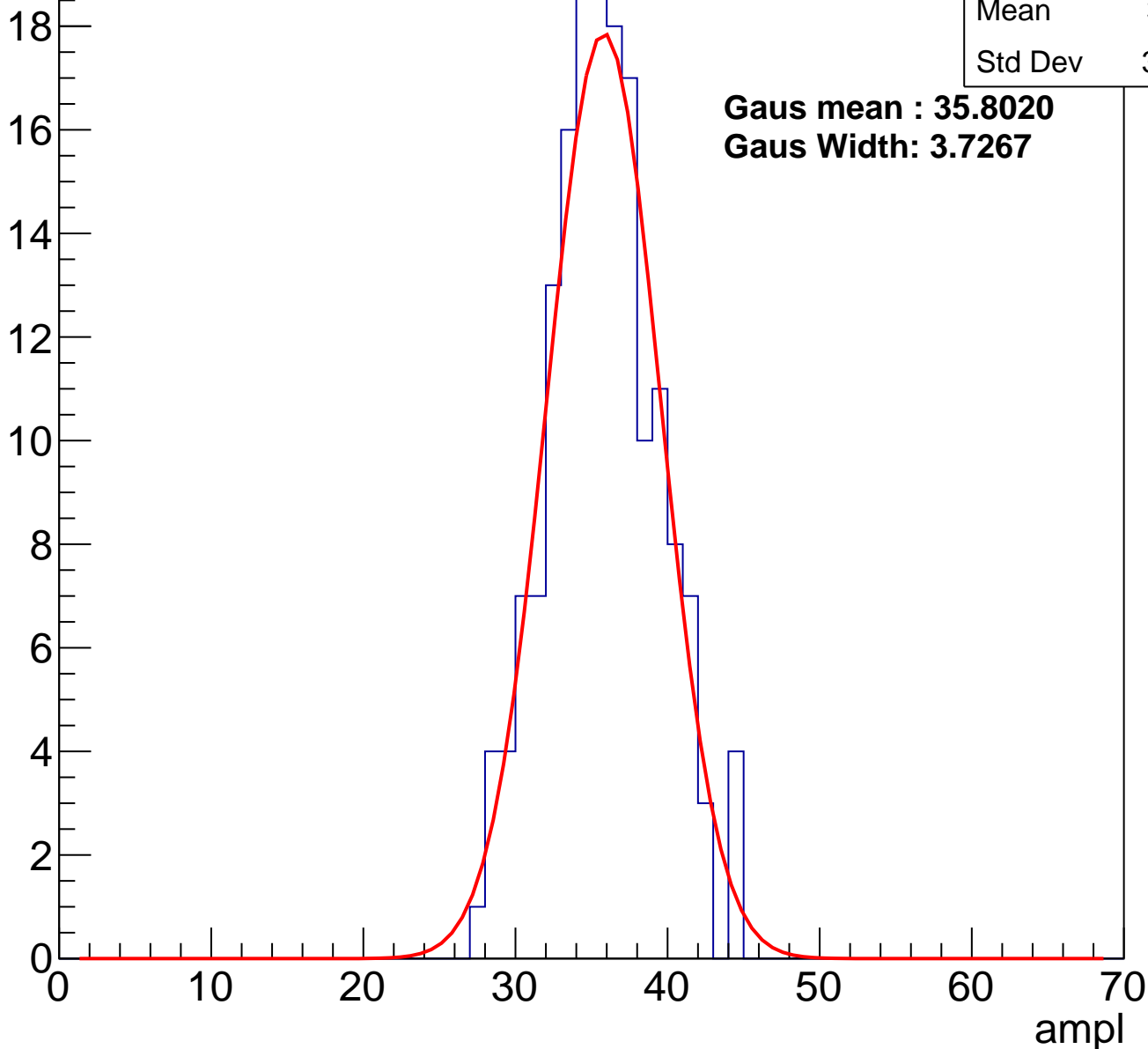
calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	35.31
Std Dev	3.577

Gaus mean : 35.8020

Gaus Width: 3.7267

Entry



B1L001S, U22-ch75, adc2

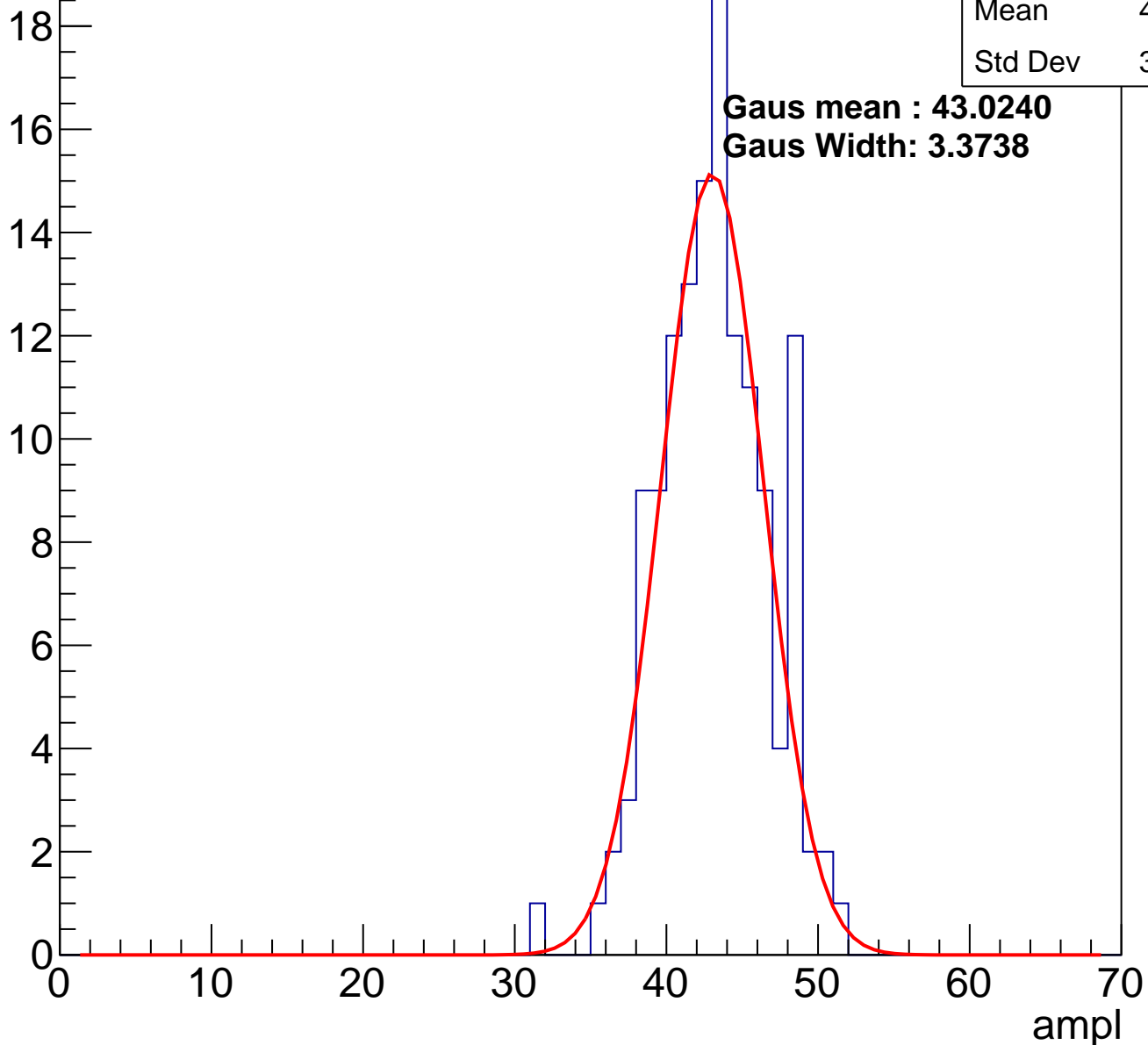
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	42.72
Std Dev	3.504

Gaus mean : 43.0240

Gaus Width: 3.3738

Entry



B1L001S, U22-ch75, adc3

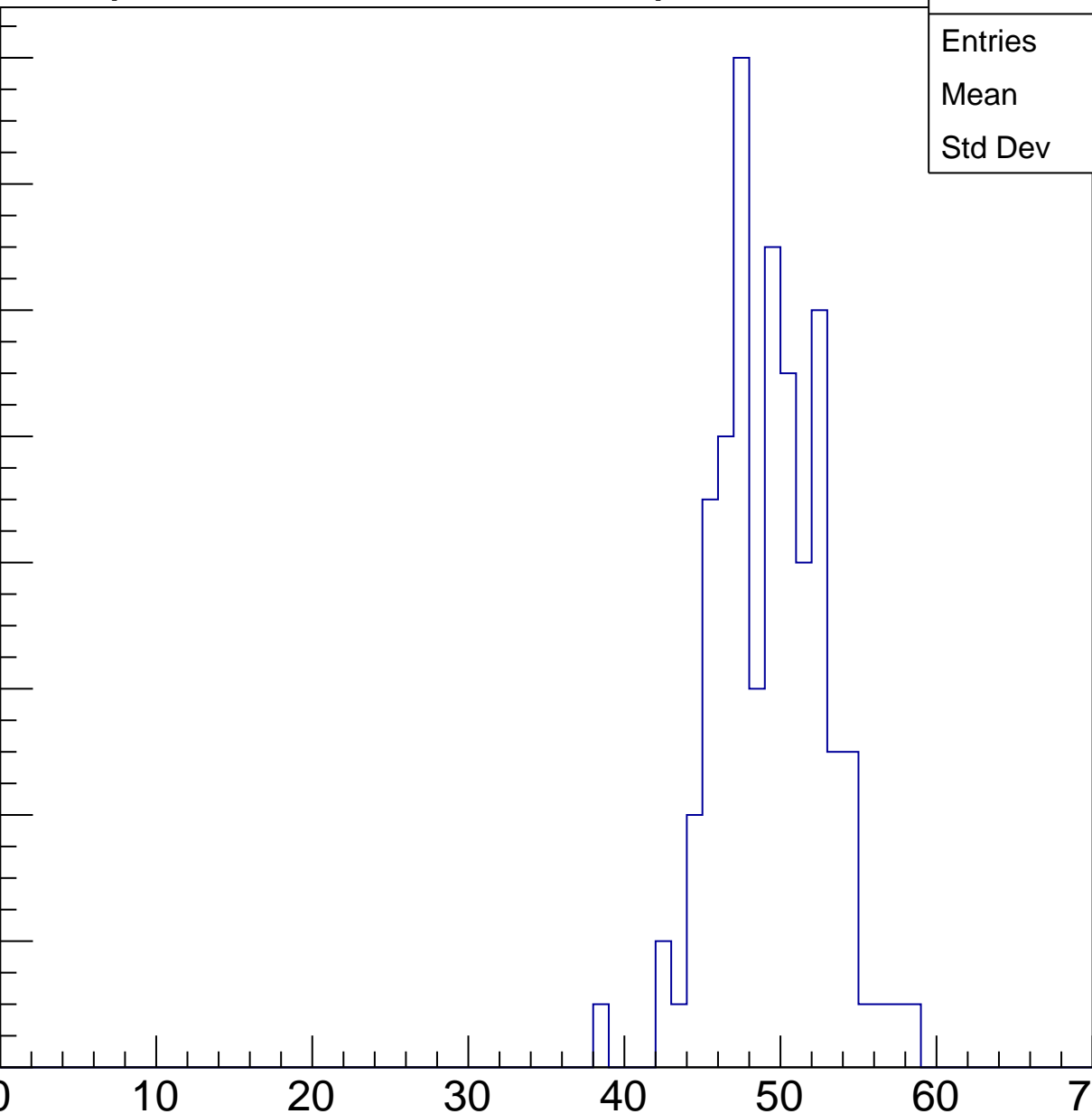
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	48.84
Std Dev	3.407

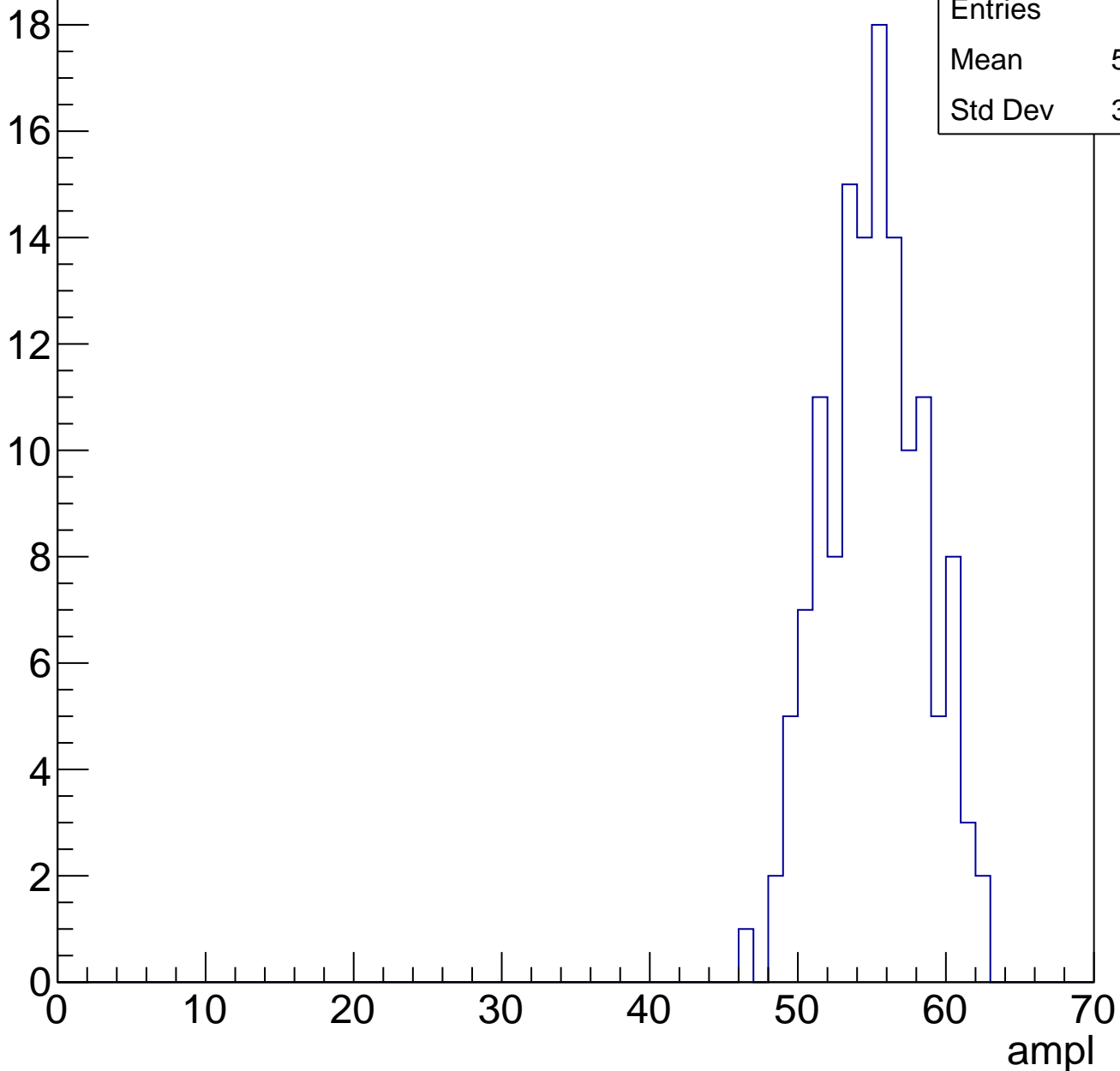
ampl



B1L001S, U22-ch75, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch75, adc5

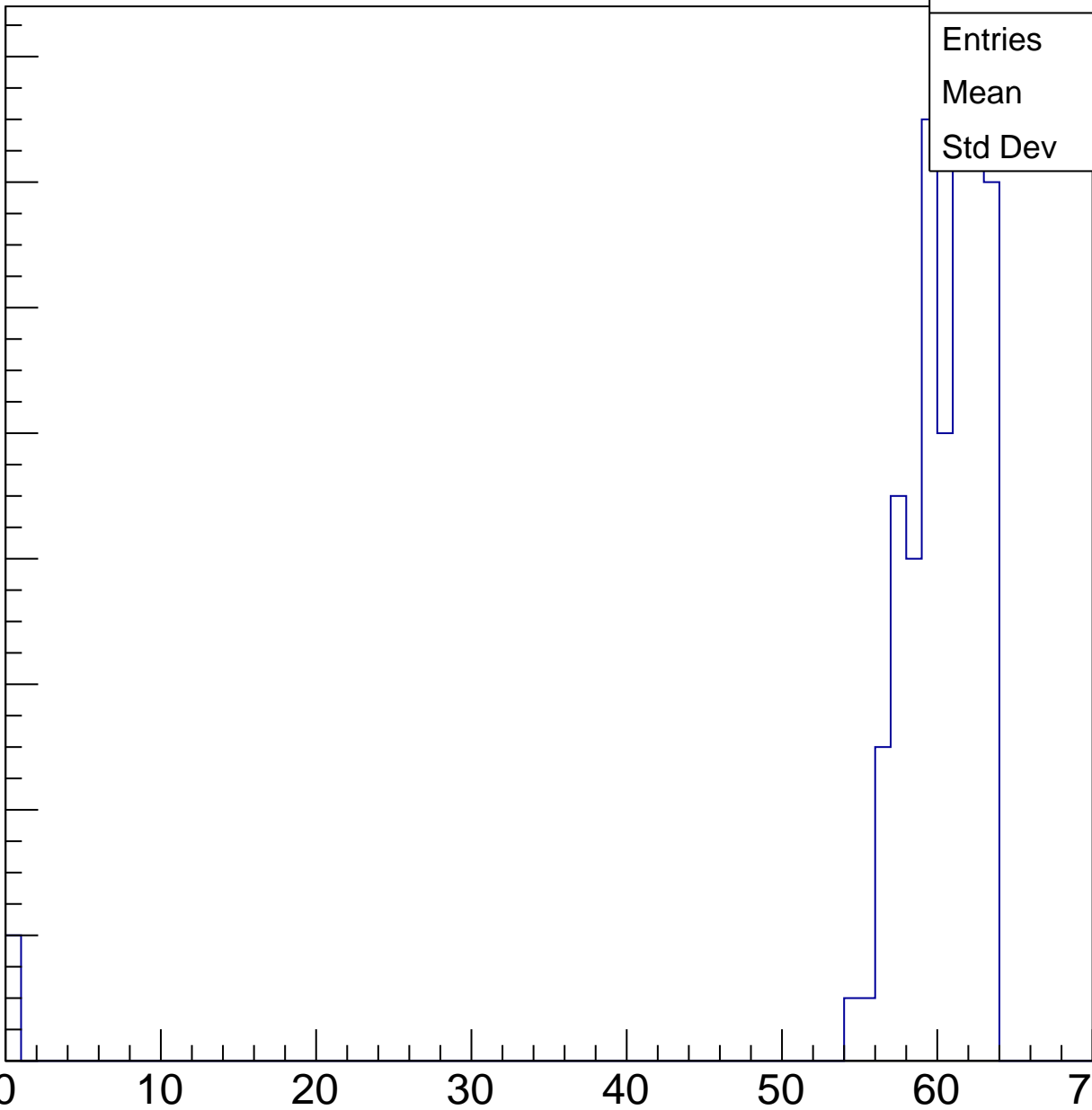
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.77
Std Dev	8.81

ampl

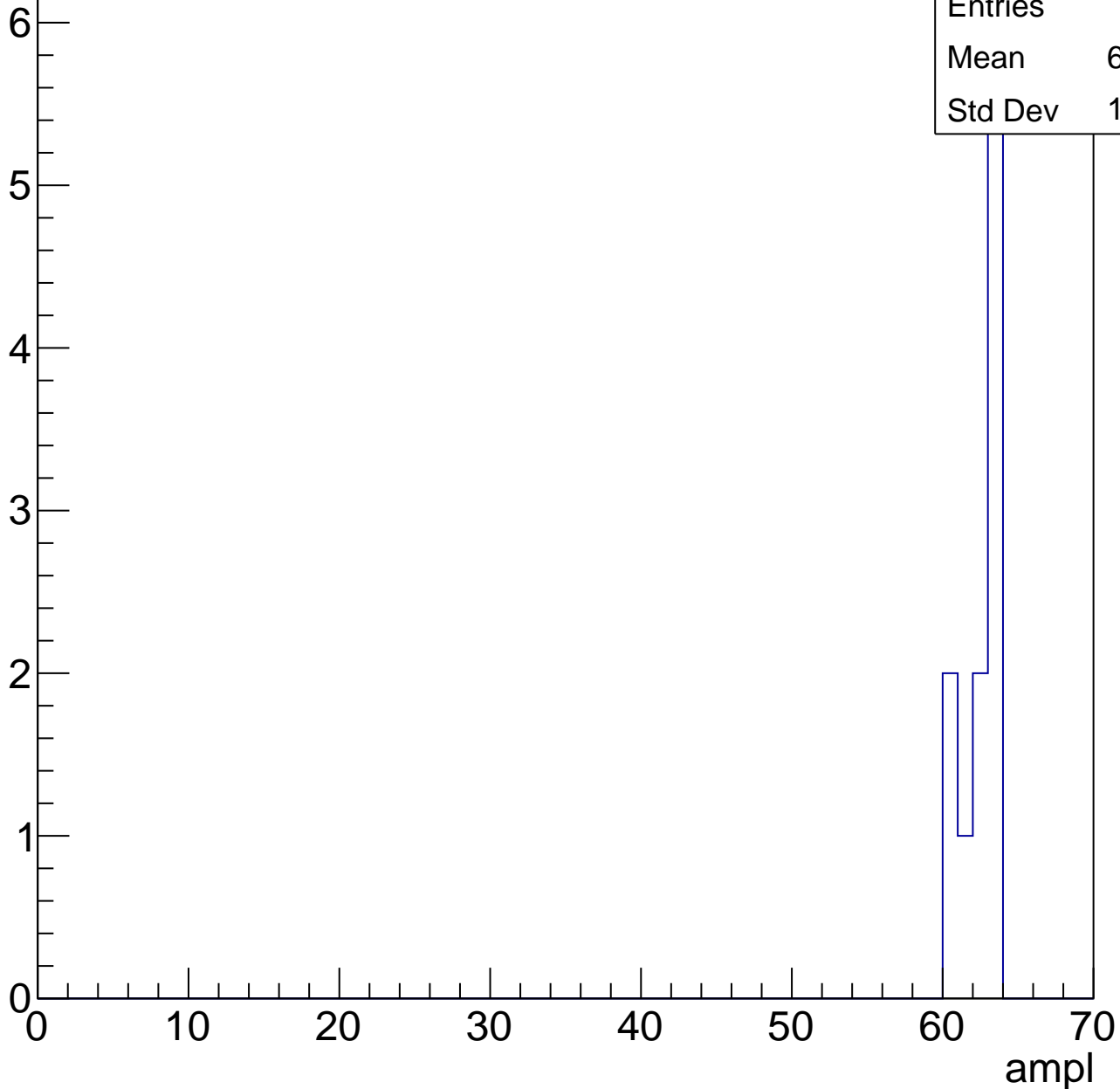


B1L001S, U22-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.09
Std Dev	1.164



B1L001S, U22-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U22-ch76, adc0

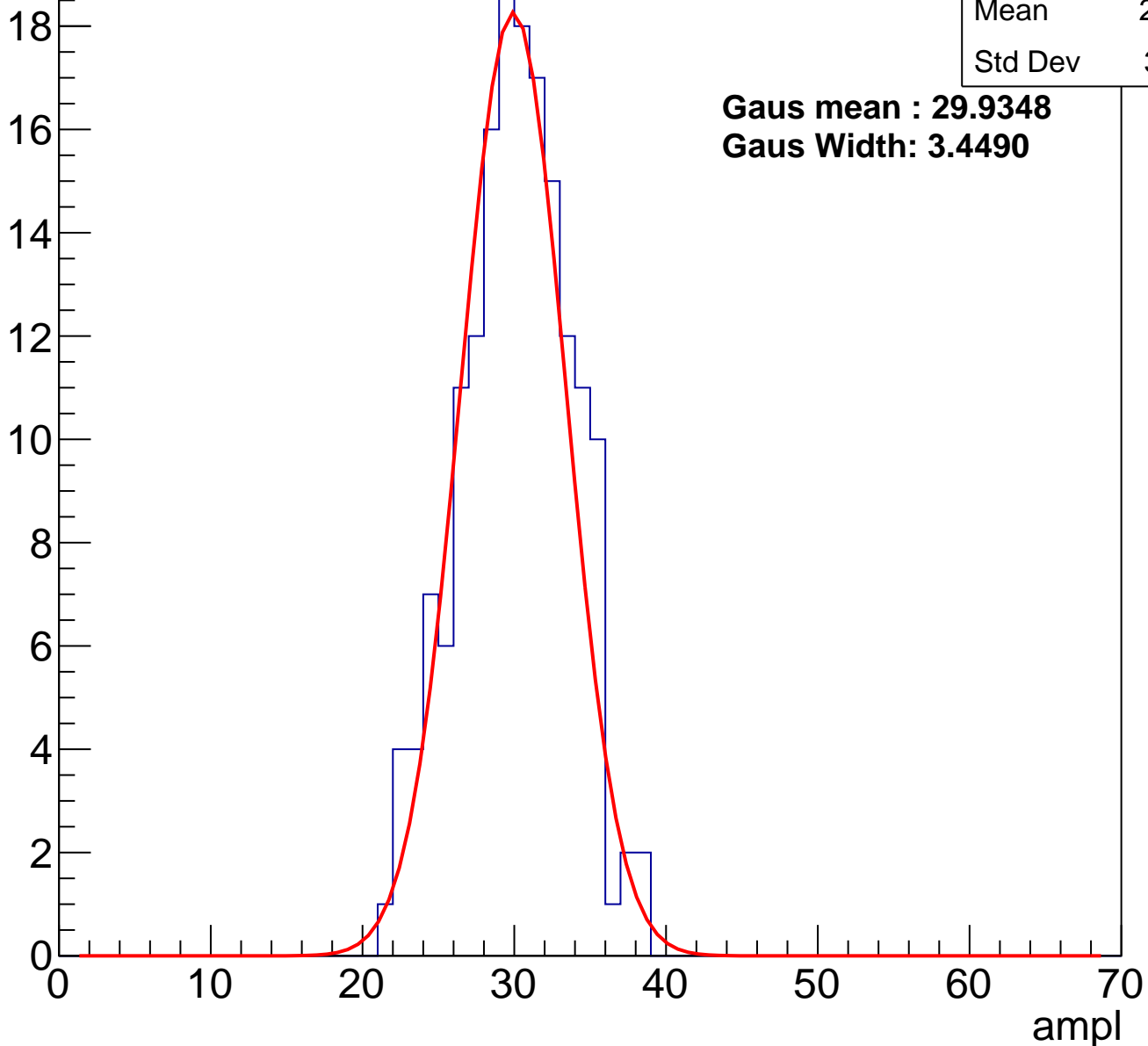
calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	29.65
Std Dev	3.581

Gaus mean : 29.9348

Gaus Width: 3.4490

Entry



B1L001S, U22-ch76, adc1

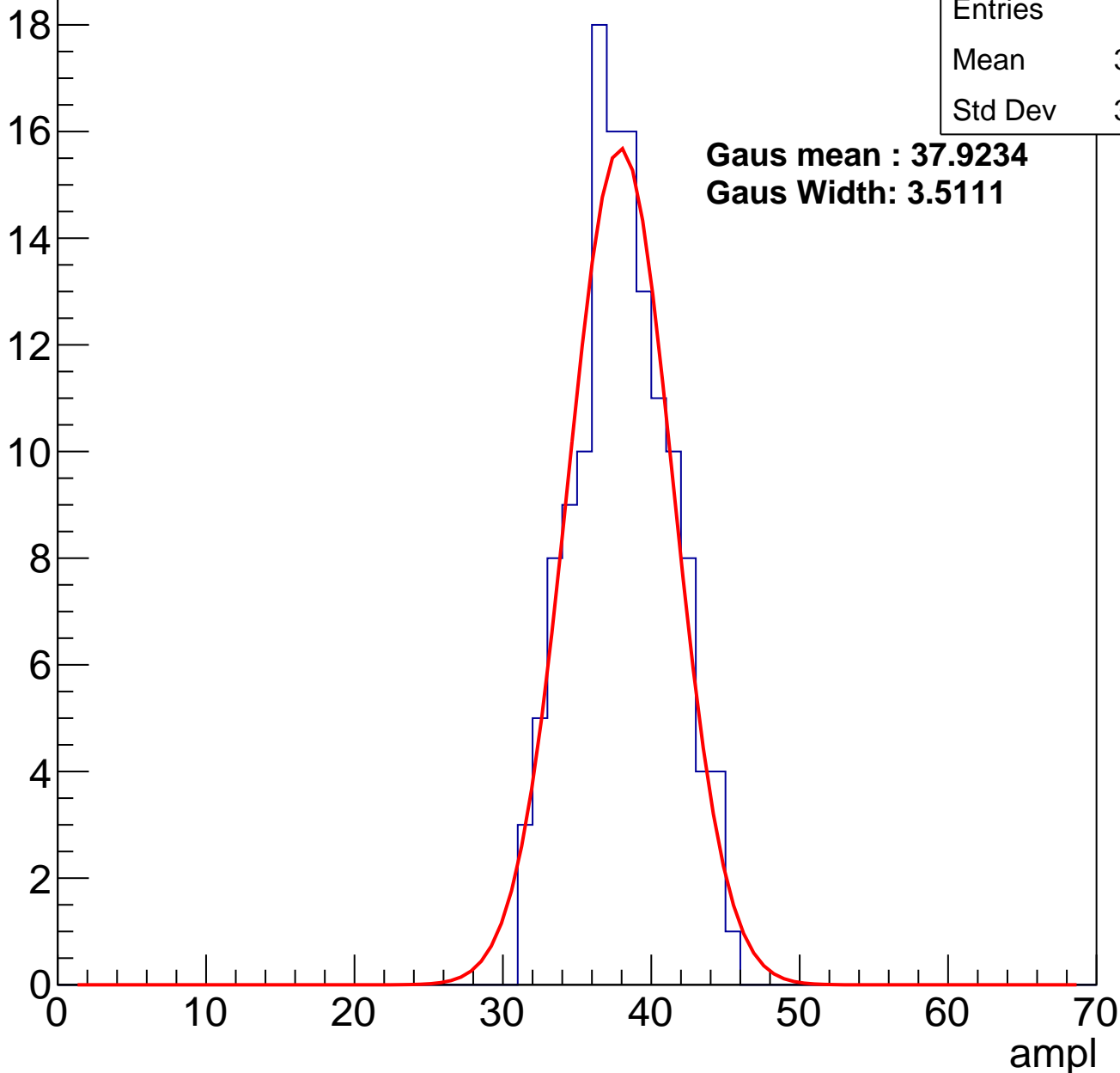
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	37.55
Std Dev	3.197

Gaus mean : 37.9234

Gaus Width: 3.5111

Entry

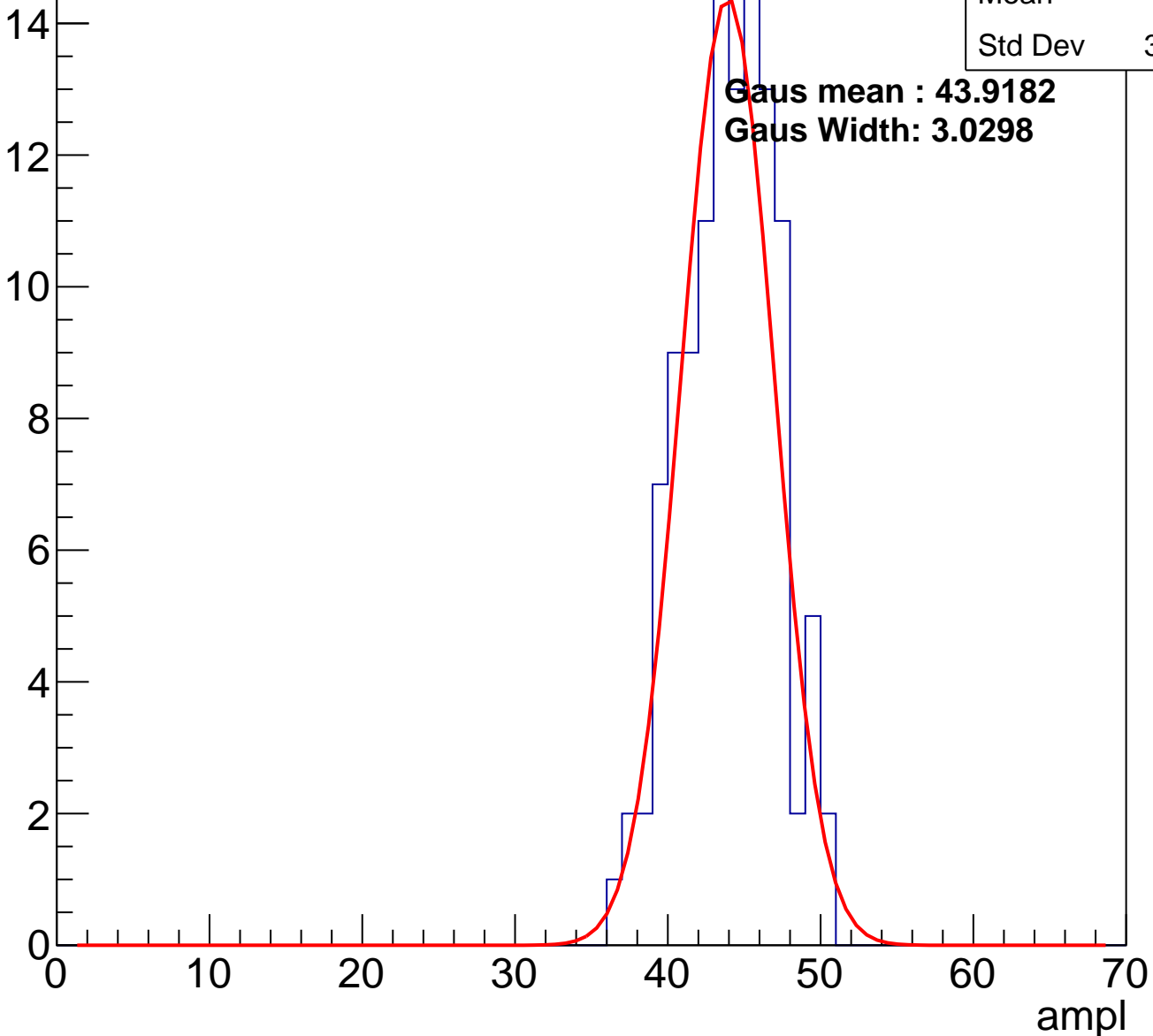


B1L001S, U22-ch76, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	43.57
Std Dev	3.019

Entry



B1L001S, U22-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	49.91
Std Dev	3.11

Entry

16

14

12

10

8

6

4

2

0

0

10

20

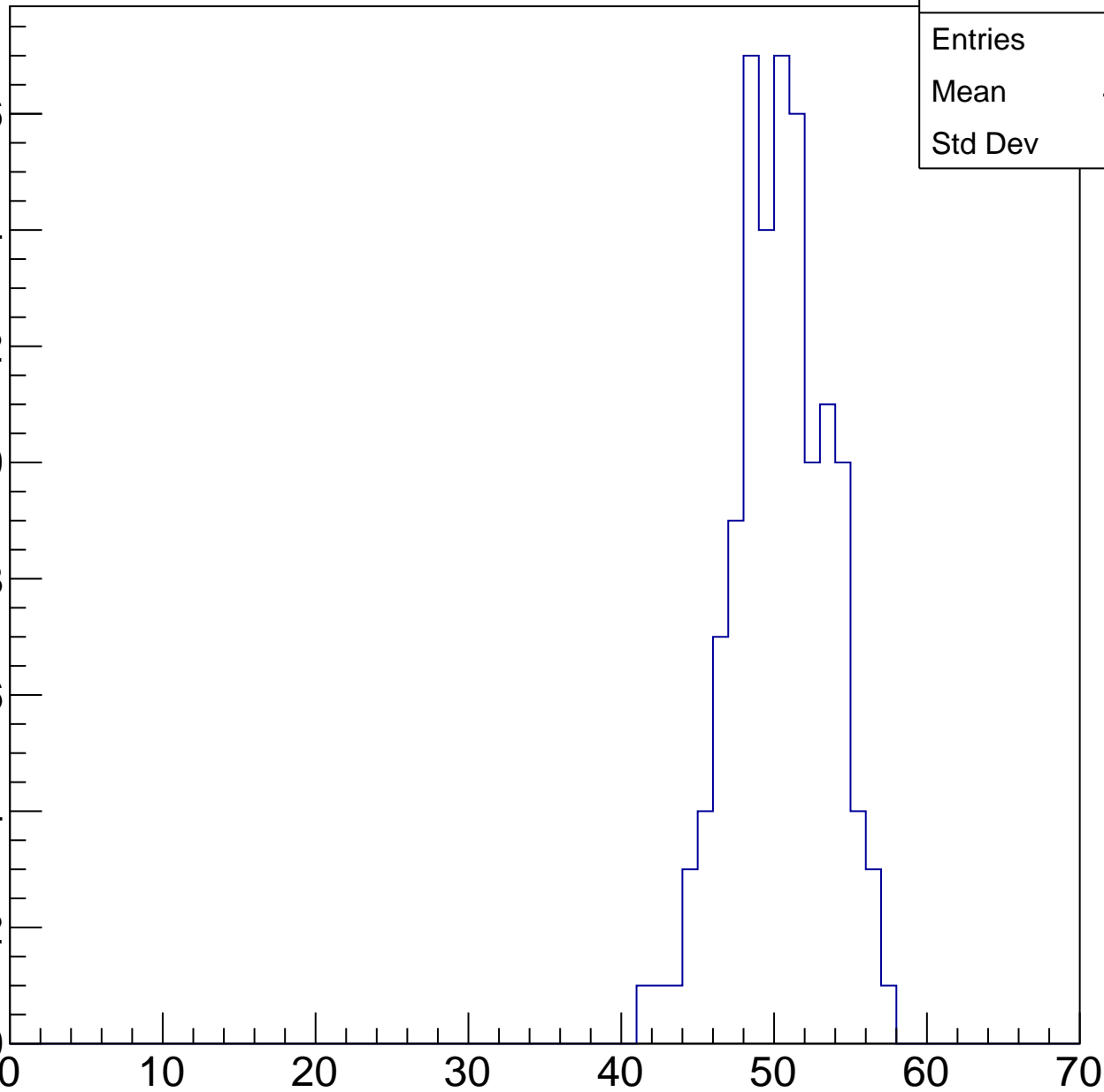
30

40

50

60

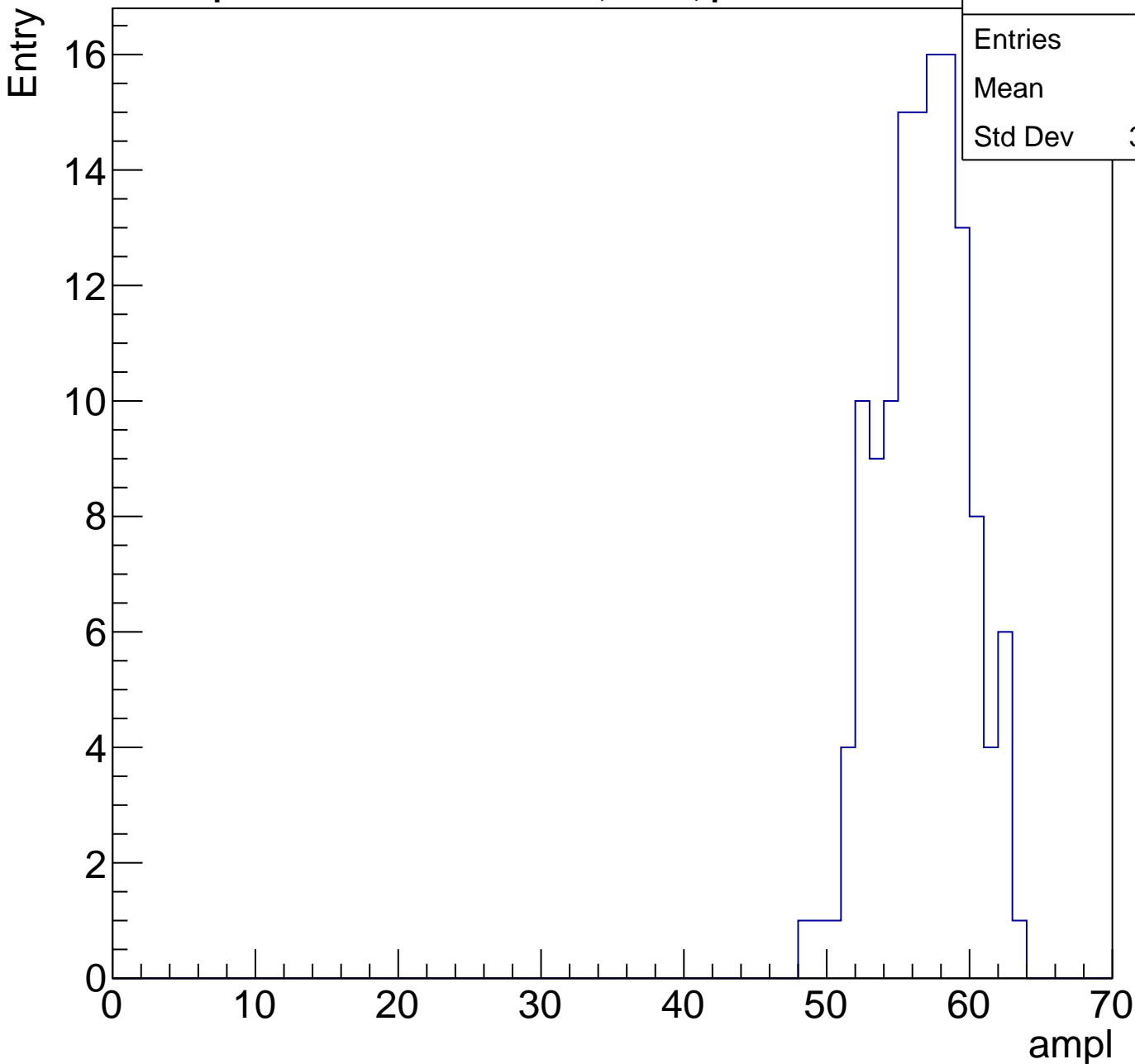
ampl



B1L001S, U22-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	56.3
Std Dev	3.068



B1L001S, U22-ch76, adc5

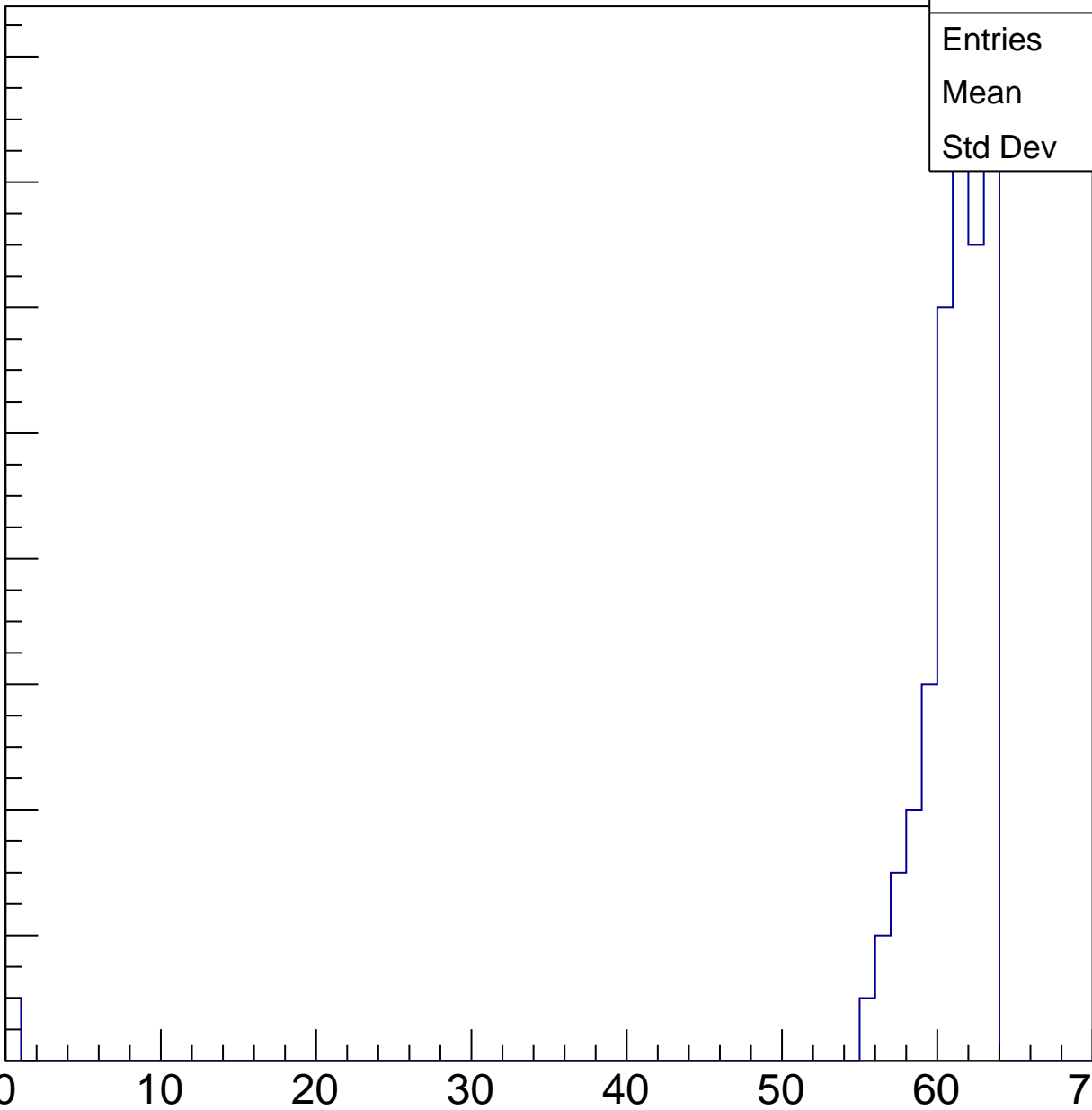
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.88
Std Dev	7.317

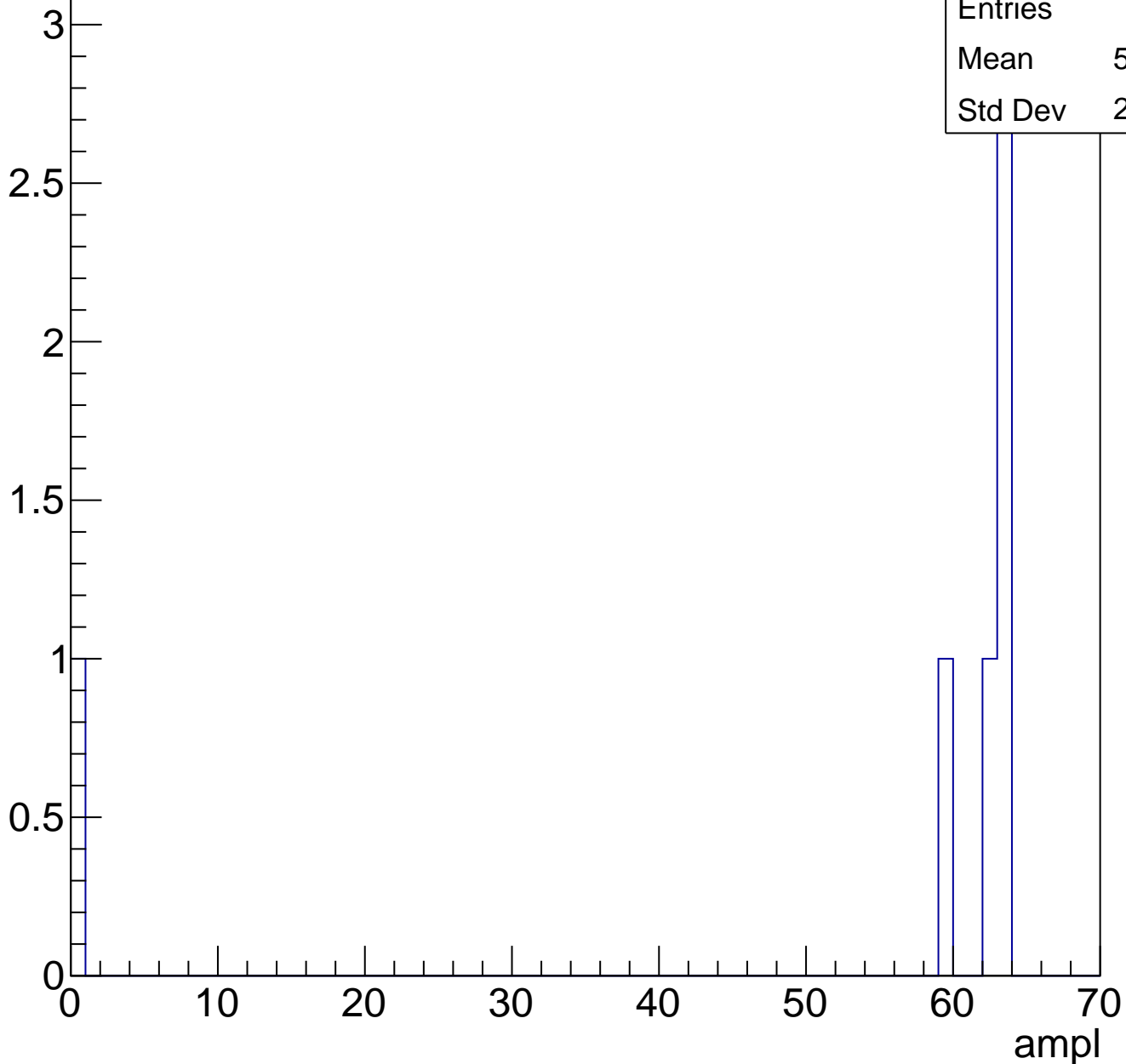
ampl



B1L001S, U22-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

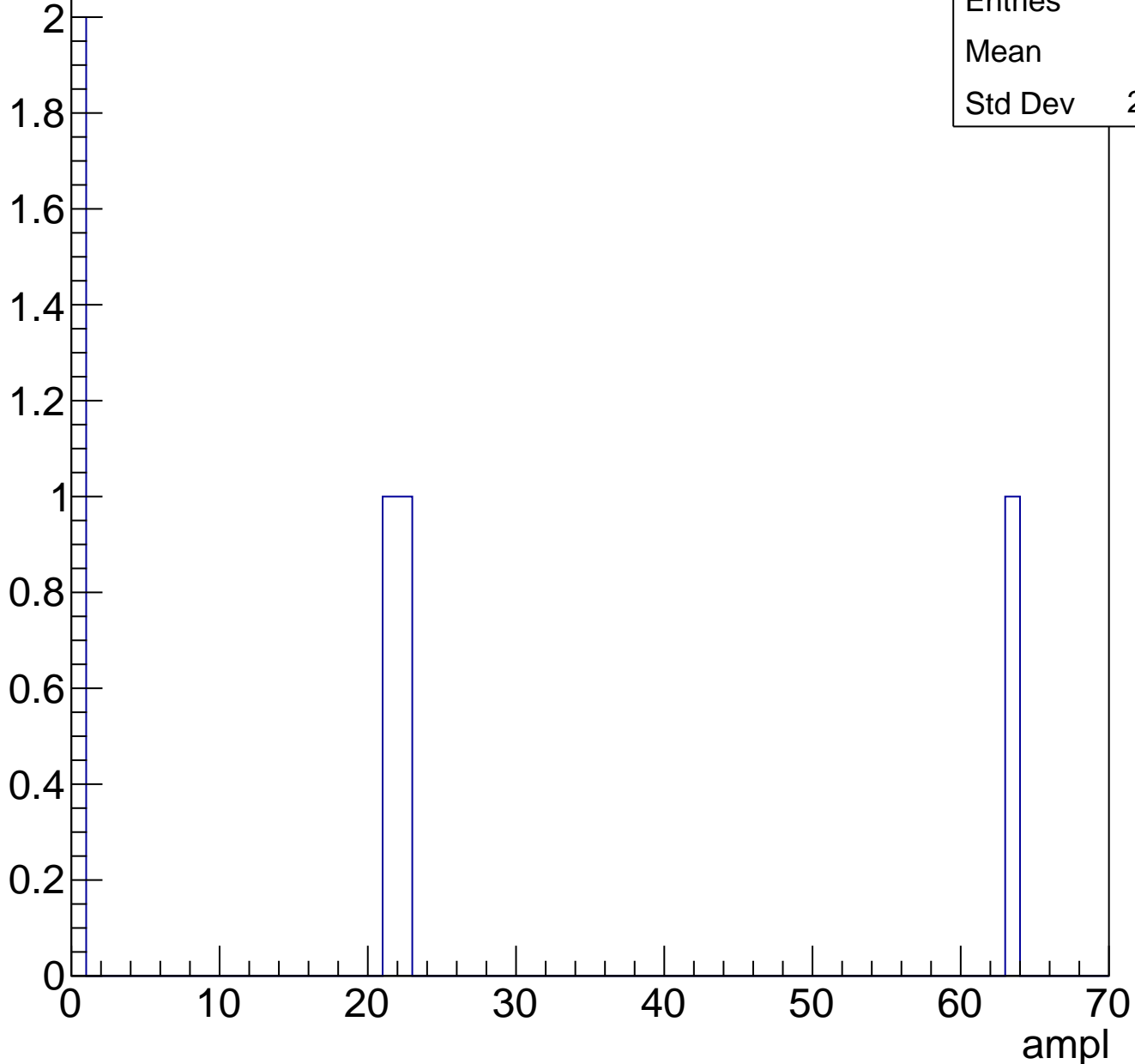
Entry



B1L001S, U22-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	21.2
Std Dev	23.01

B1L001S, U22-ch77, adc0

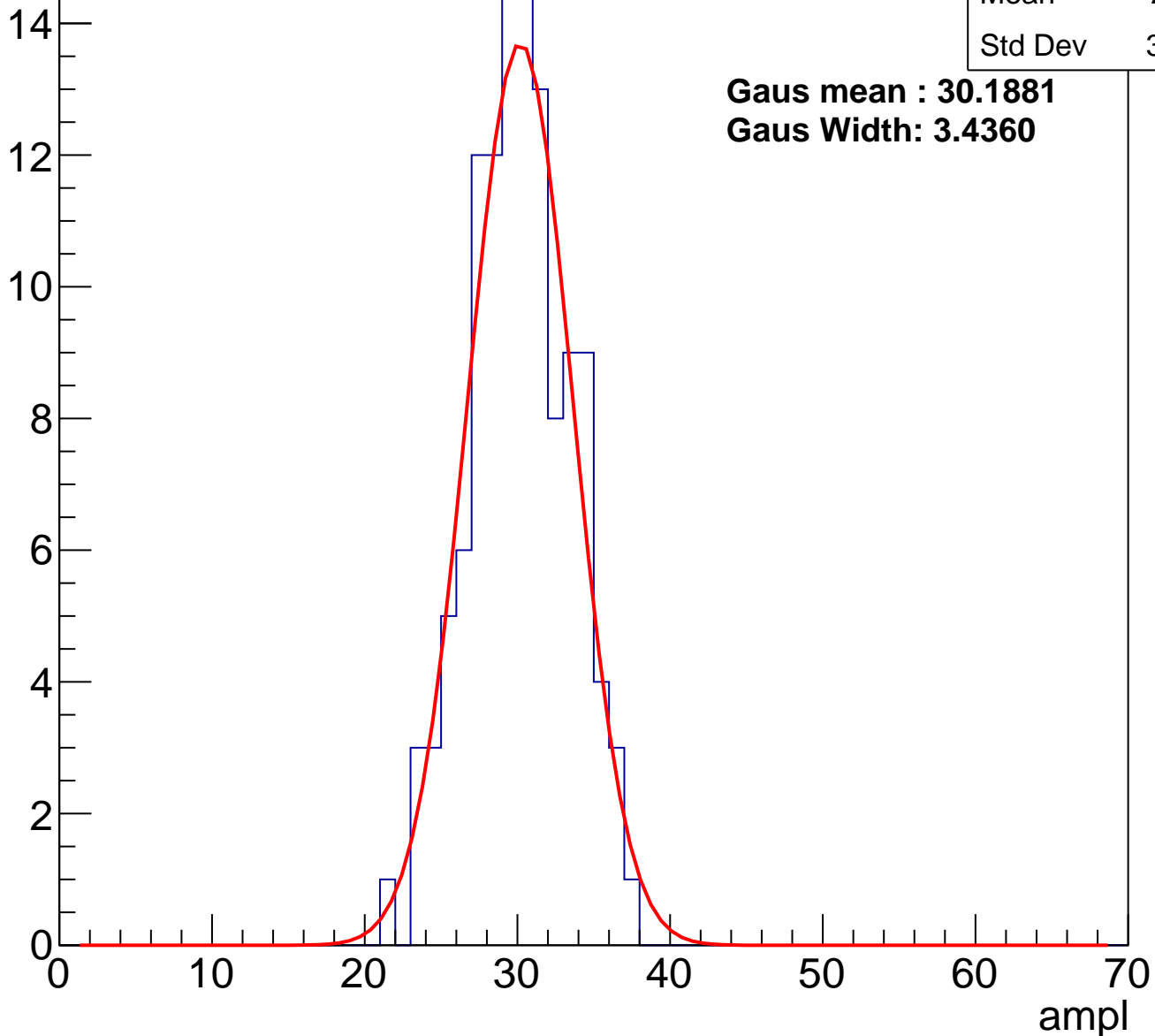
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.71
Std Dev	3.234

Gaus mean : 30.1881

Gaus Width: 3.4360

Entry



B1L001S, U22-ch77, adc1

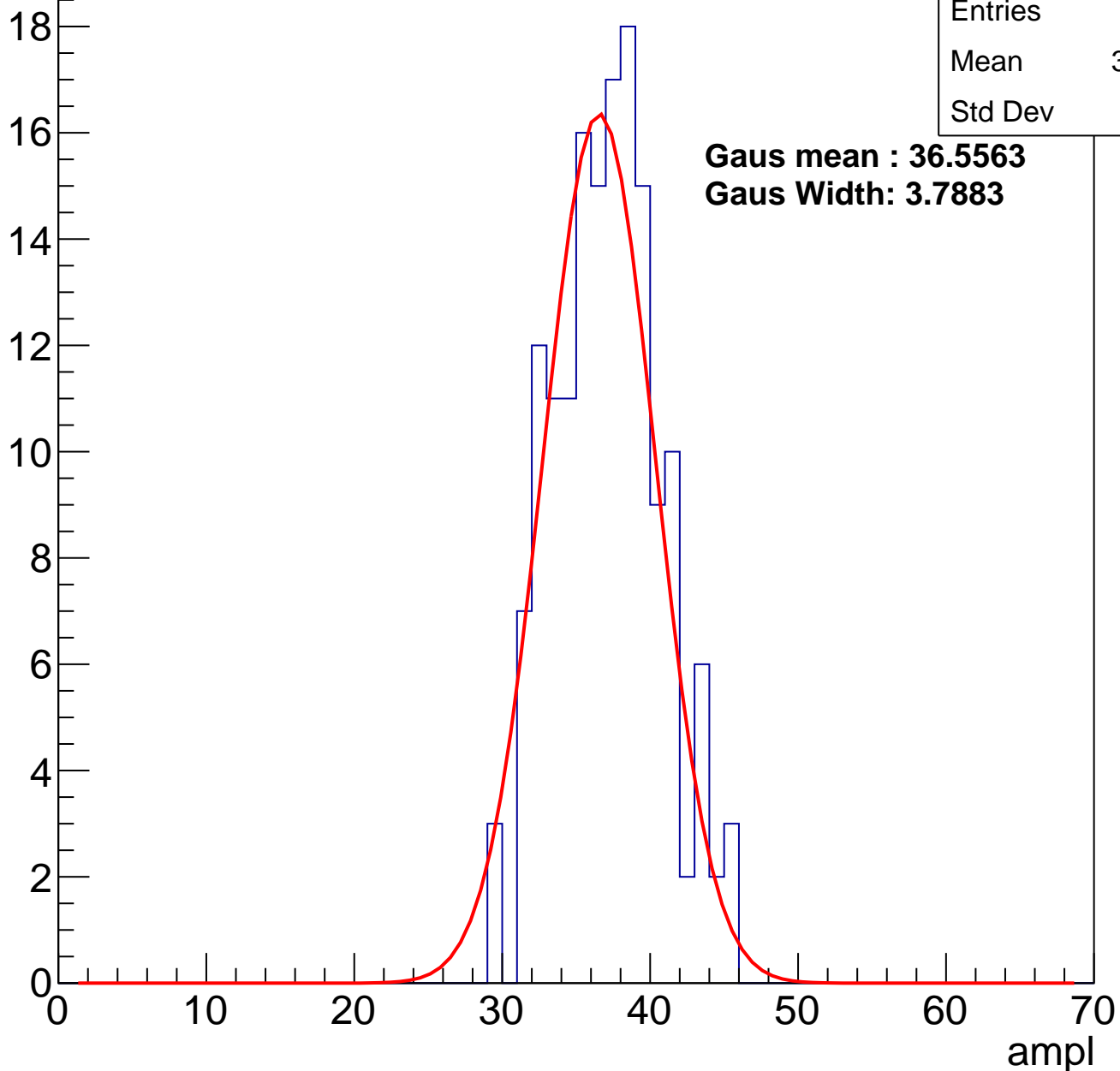
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	36.68
Std Dev	3.53

Gaus mean : 36.5563

Gaus Width: 3.7883

Entry



B1L001S, U22-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

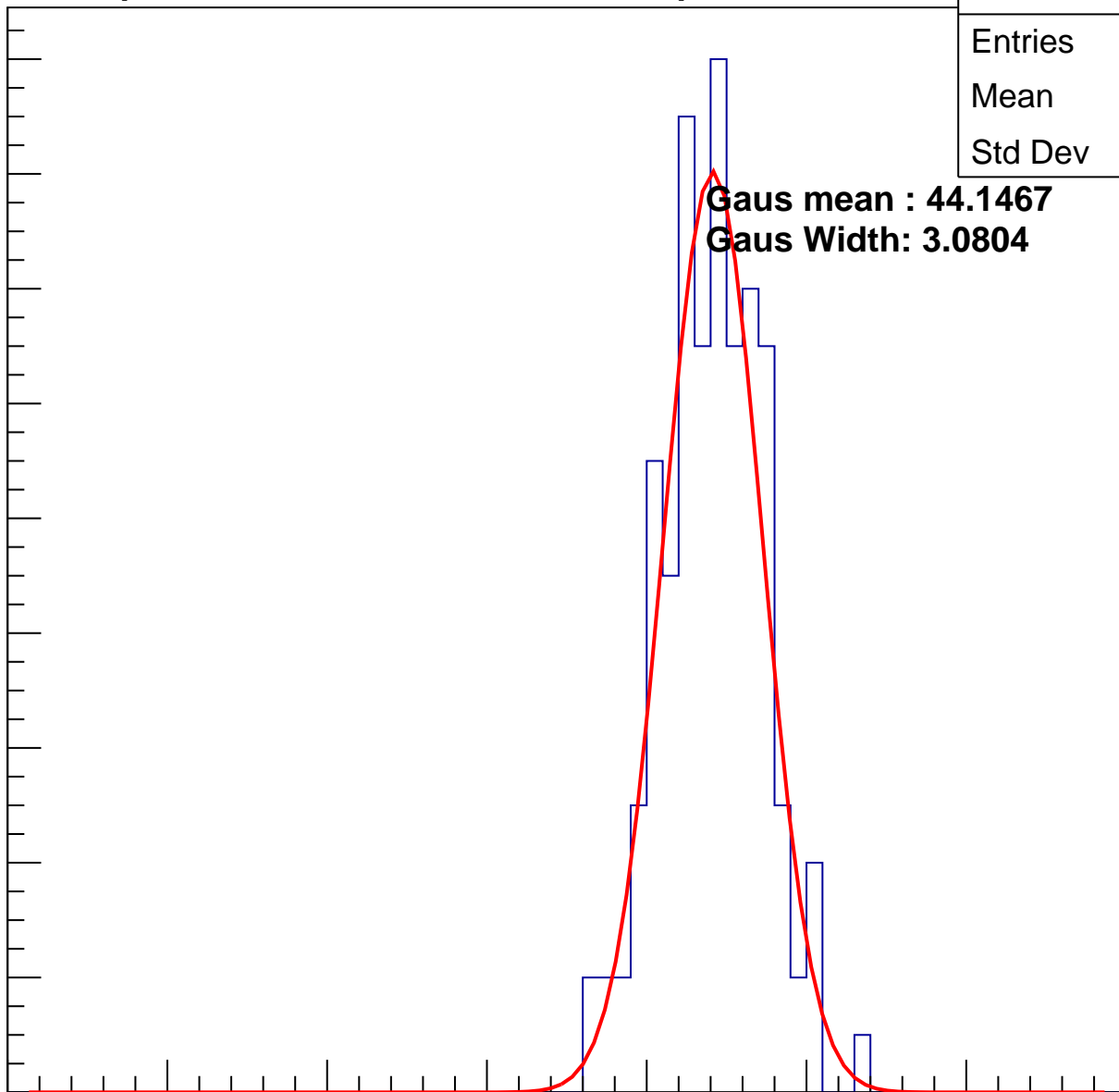
18
16
14
12
10
8
6
4
2
0

Entries	131
Mean	43.68
Std Dev	3.136

Gaus mean : 44.1467
Gaus Width: 3.0804

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

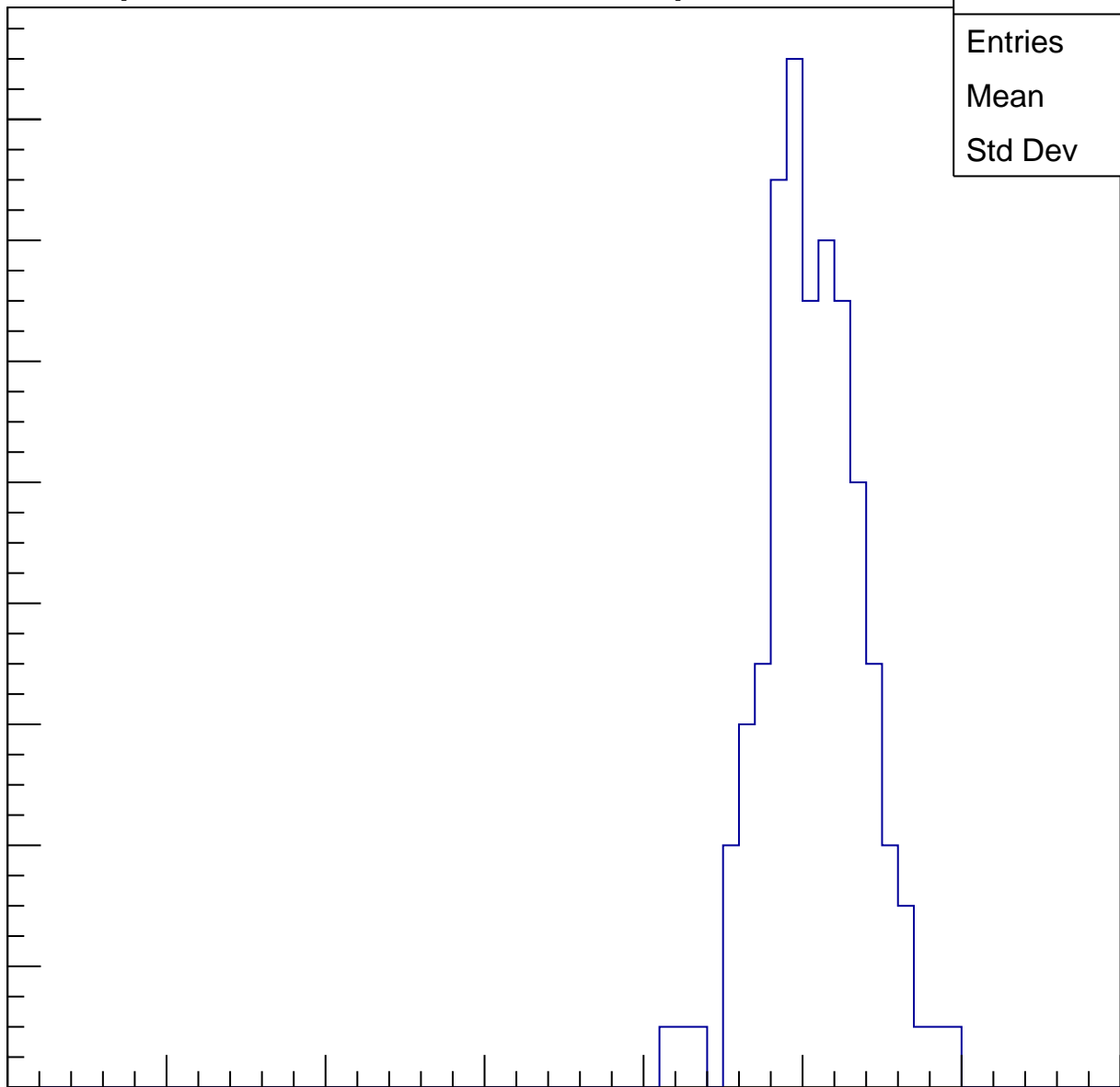
Entries	119
Mean	50.2
Std Dev	3.169

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	55.74
Std Dev	5.832

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

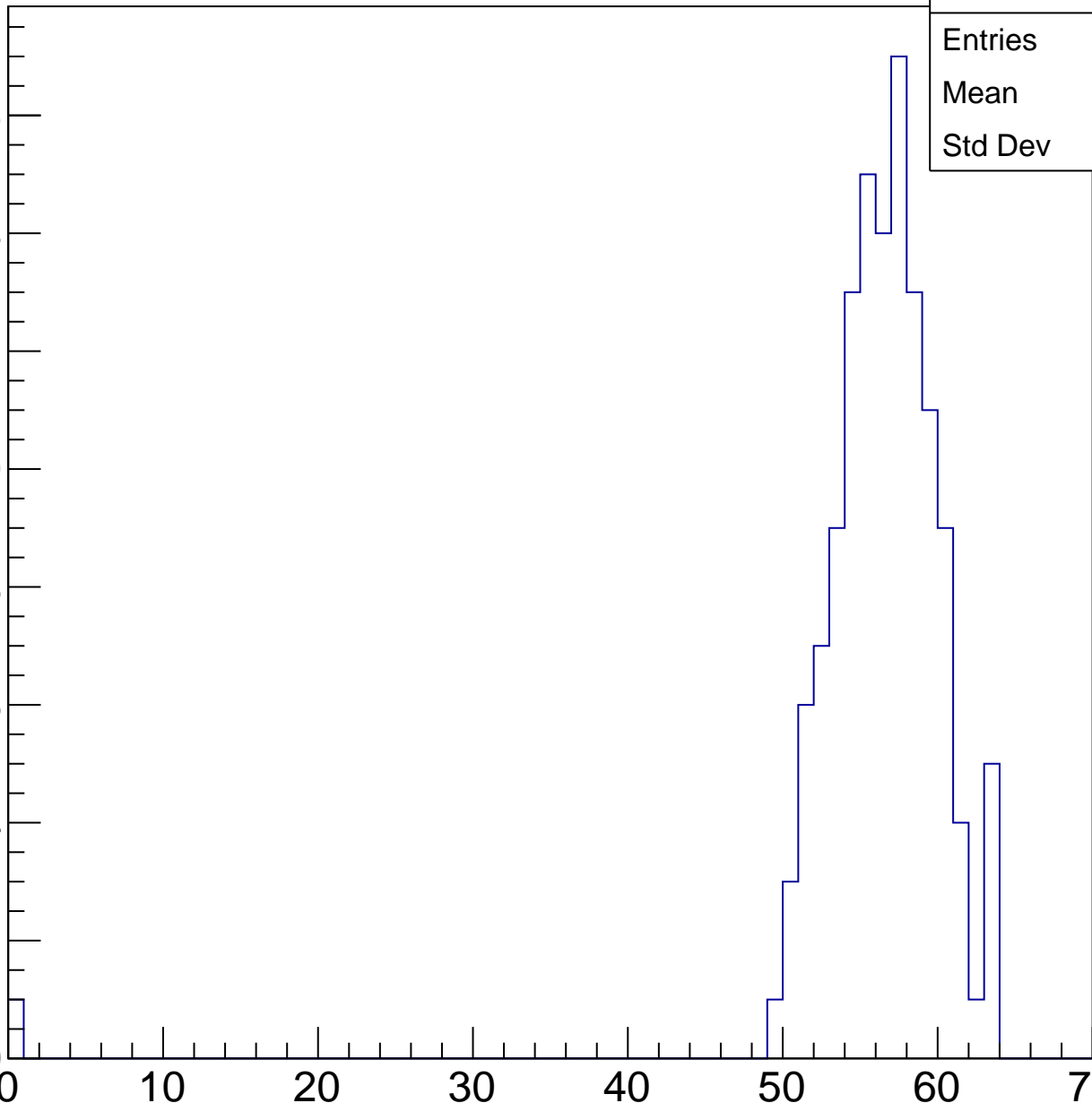
40

50

60

70

ampl



B1L001S, U22-ch77, adc5

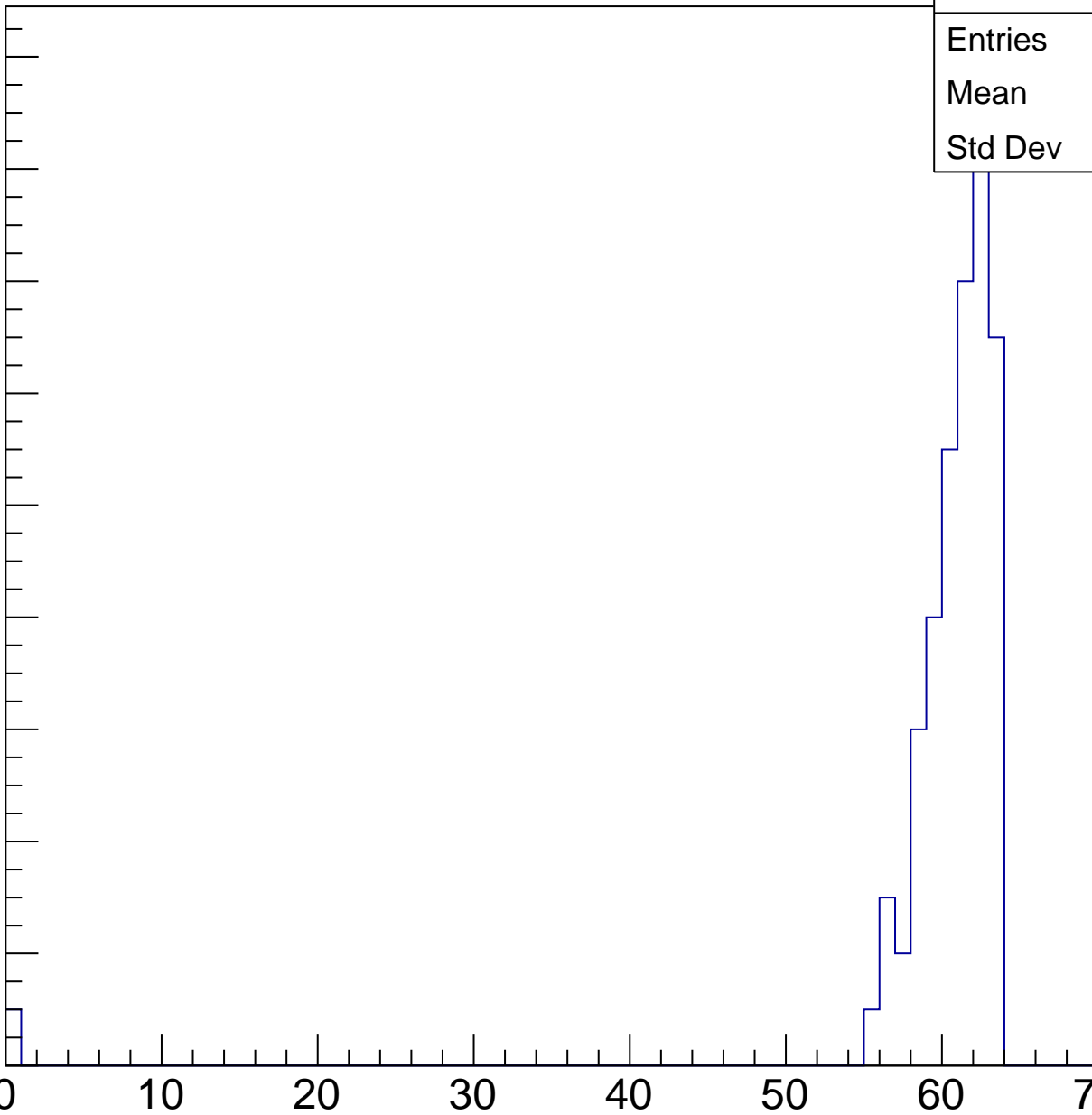
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.82
Std Dev	7.138

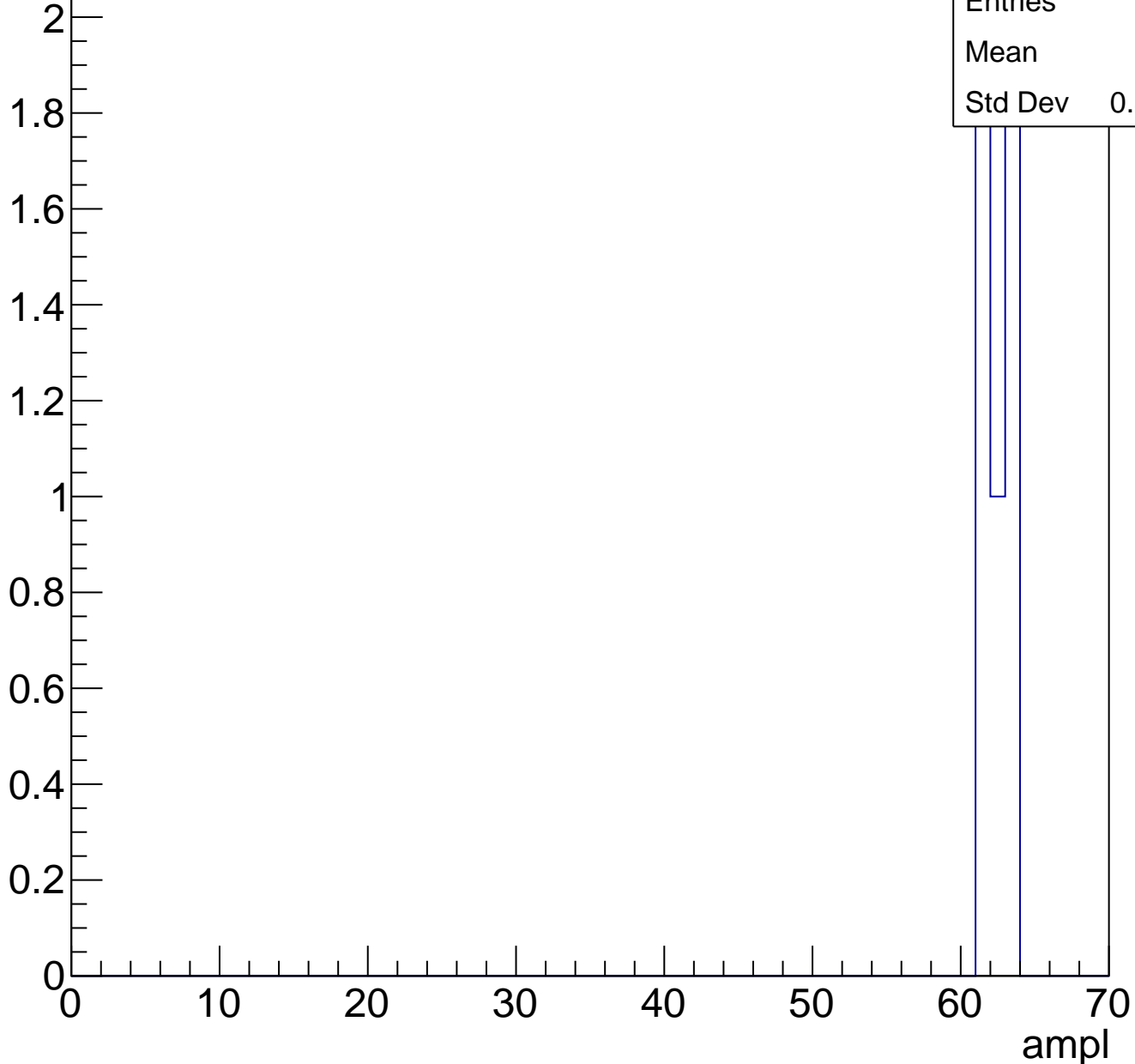
ampl



B1L001S, U22-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

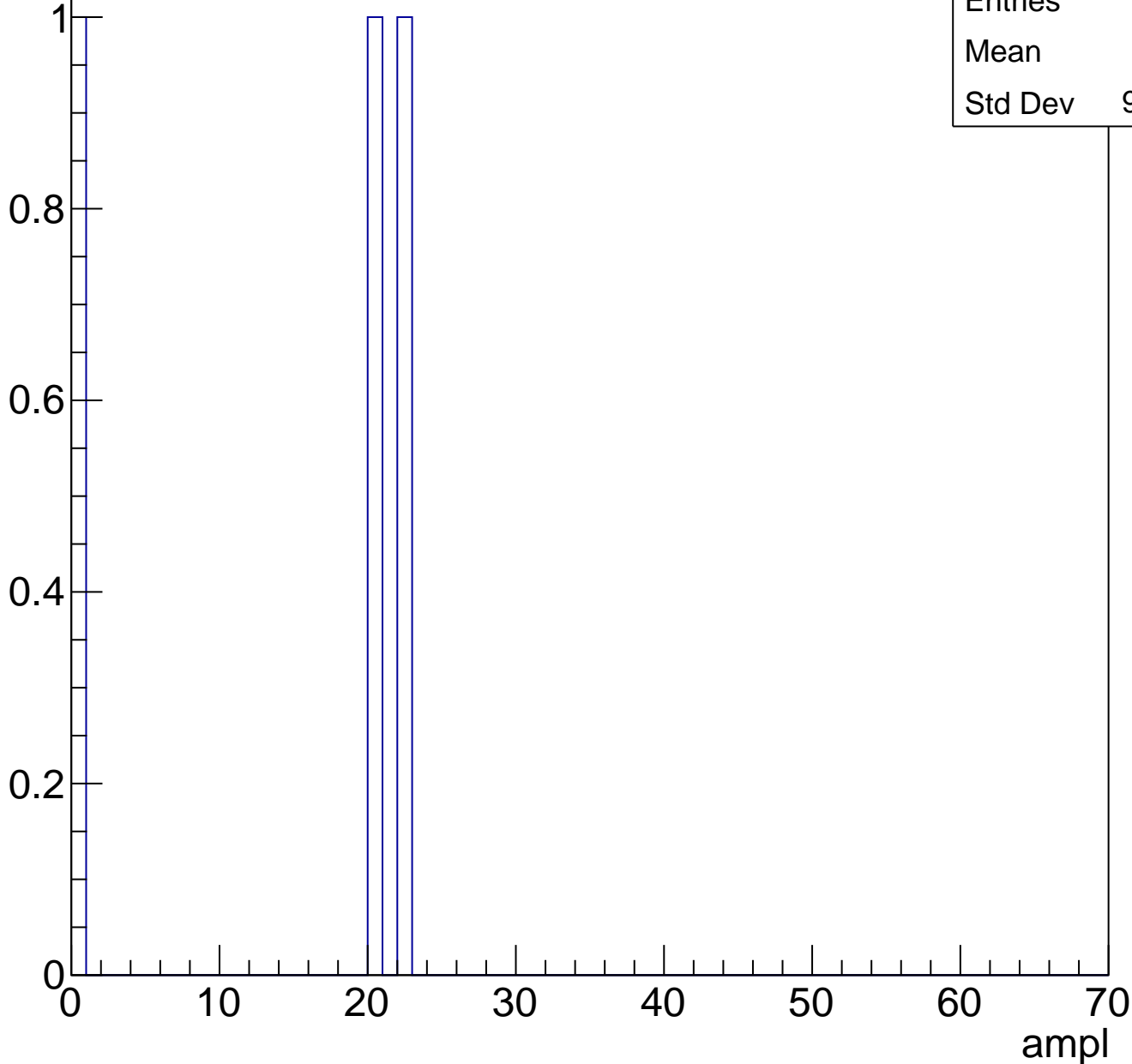
Entry



B1L001S, U22-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

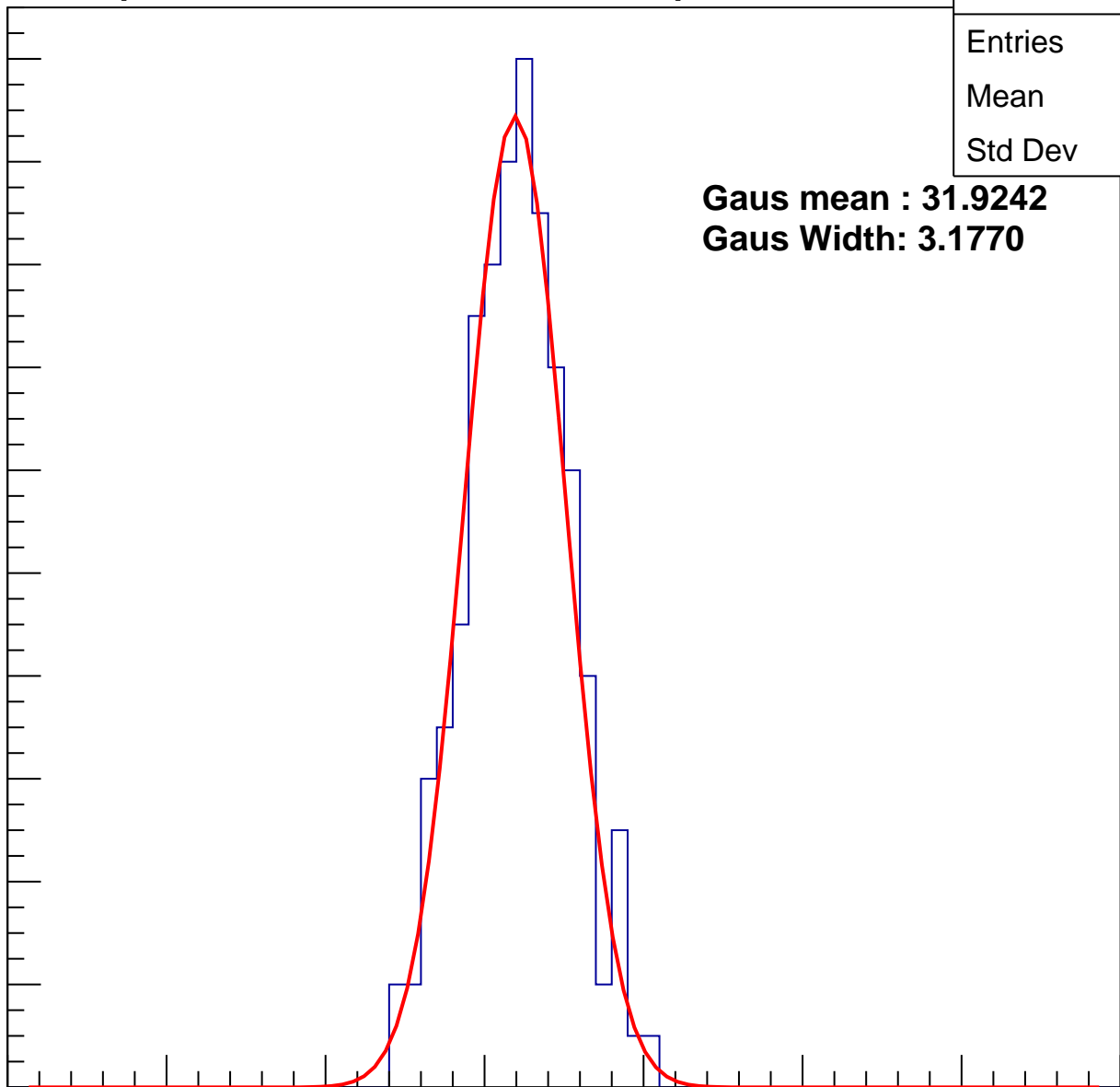
Entries	155
Mean	31.59
Std Dev	3.216

Gaus mean : 31.9242

Gaus Width: 3.1770

0 10 20 30 40 50 60 70

ampl



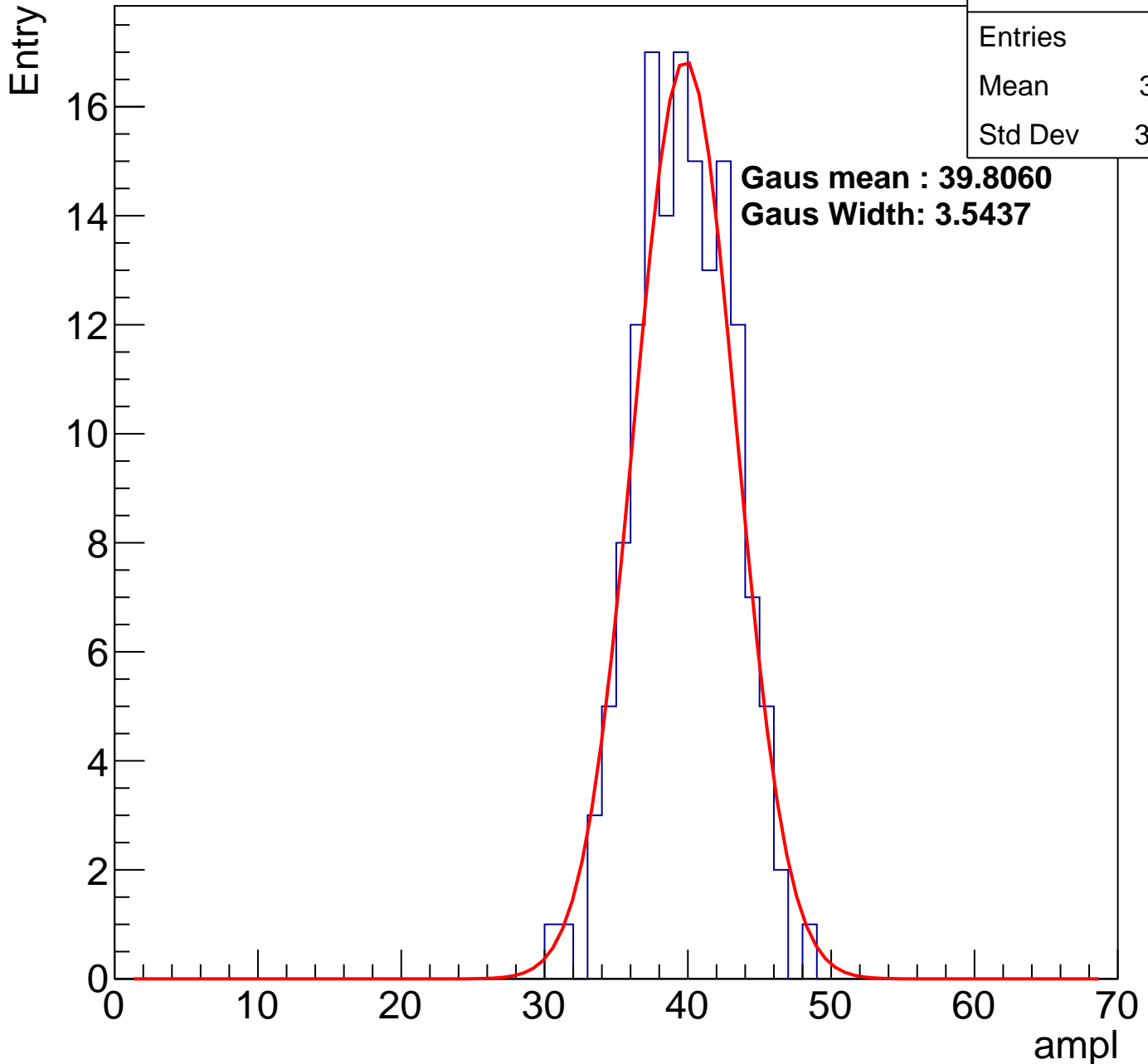
B1L001S, U22-ch78, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	39.31
Std Dev	3.298

Gaus mean : 39.8060

Gaus Width: 3.5437



B1L001S, U22-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

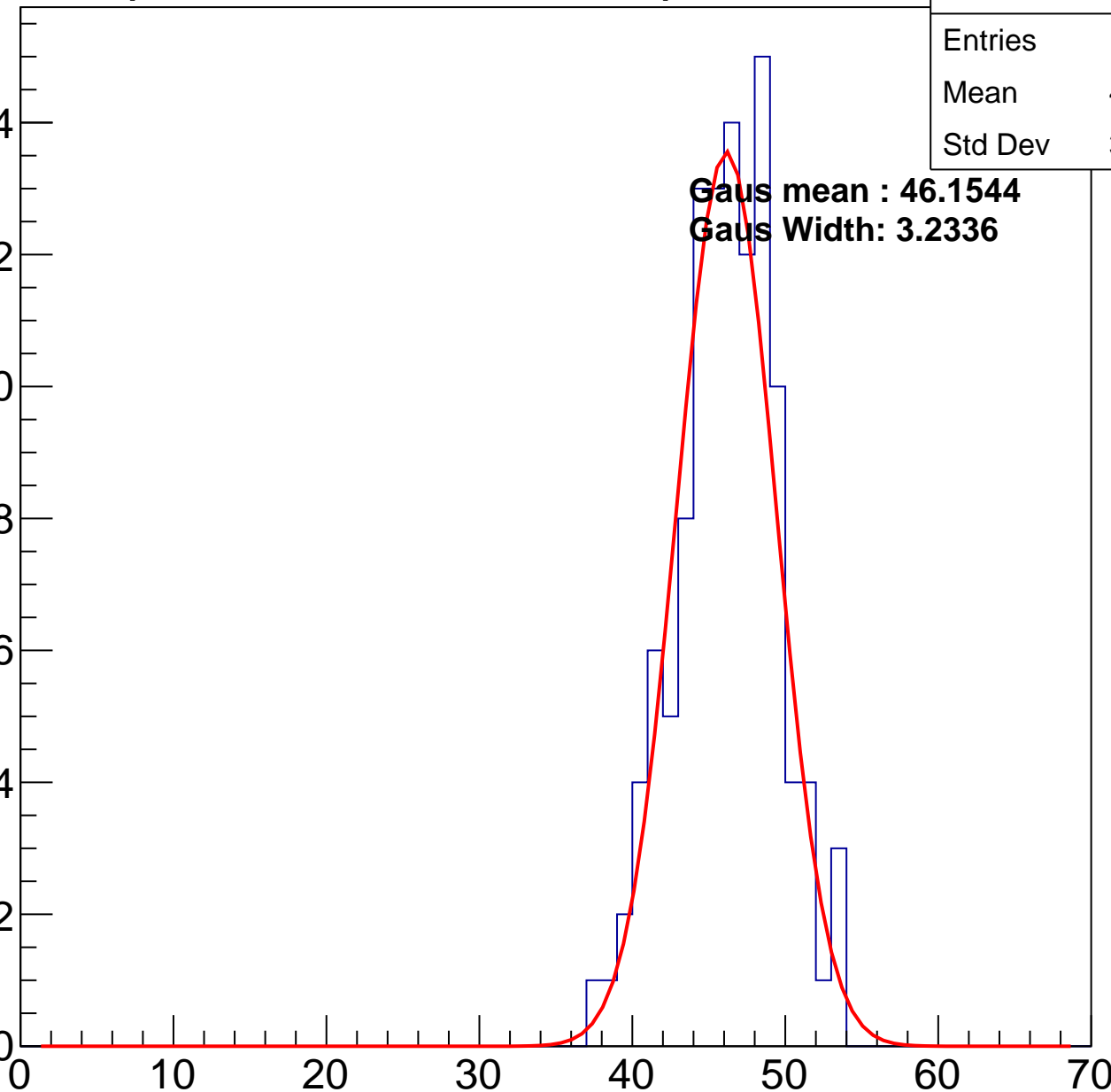
14
12
10
8
6
4
2
0

Entries	116
Mean	45.72
Std Dev	3.287

Gaus mean : 46.1544

Gaus Width: 3.2336

ampl

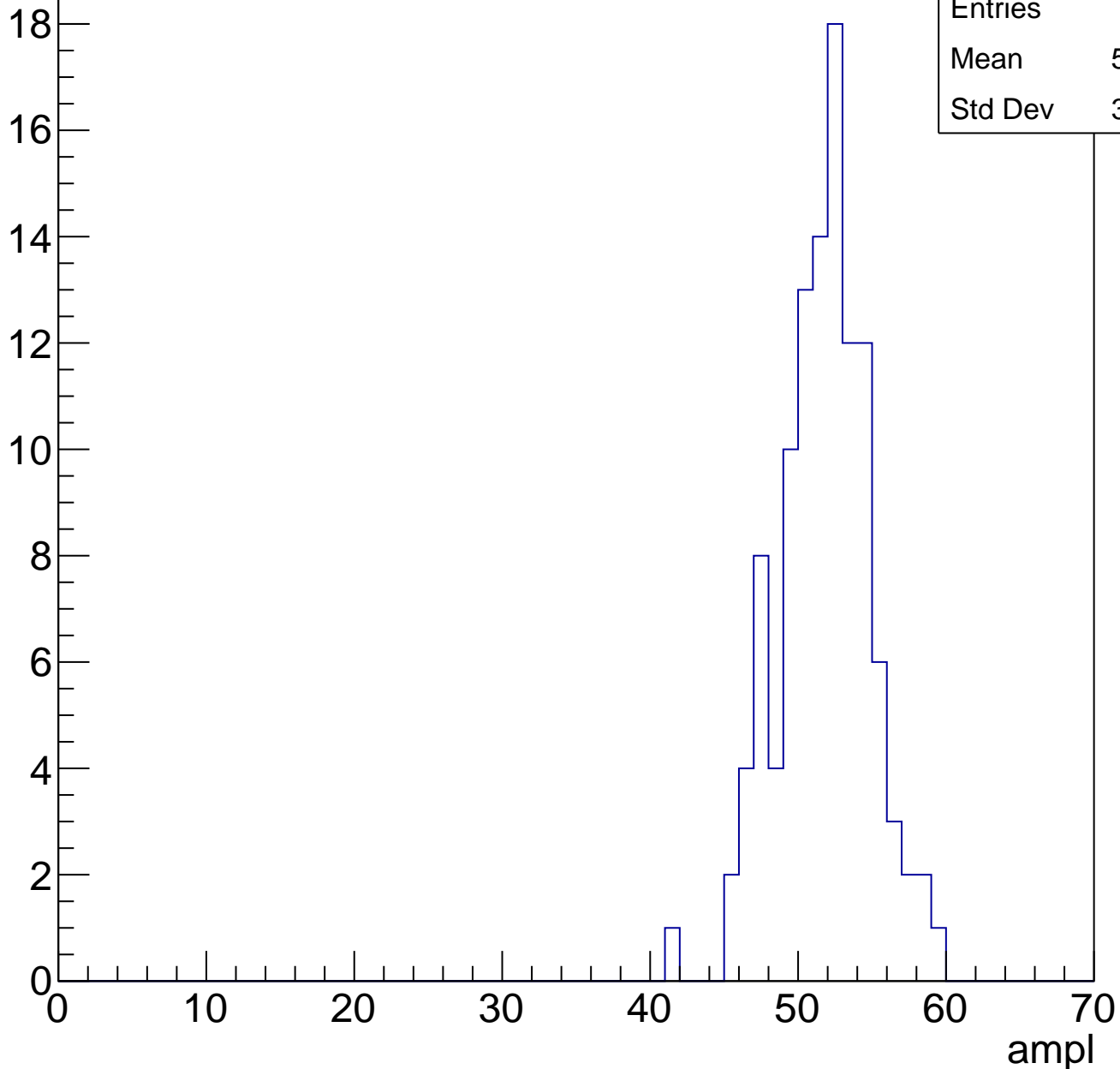


B1L001S, U22-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	51.29
Std Dev	3.075

Entry



B1L001S, U22-ch78, adc4

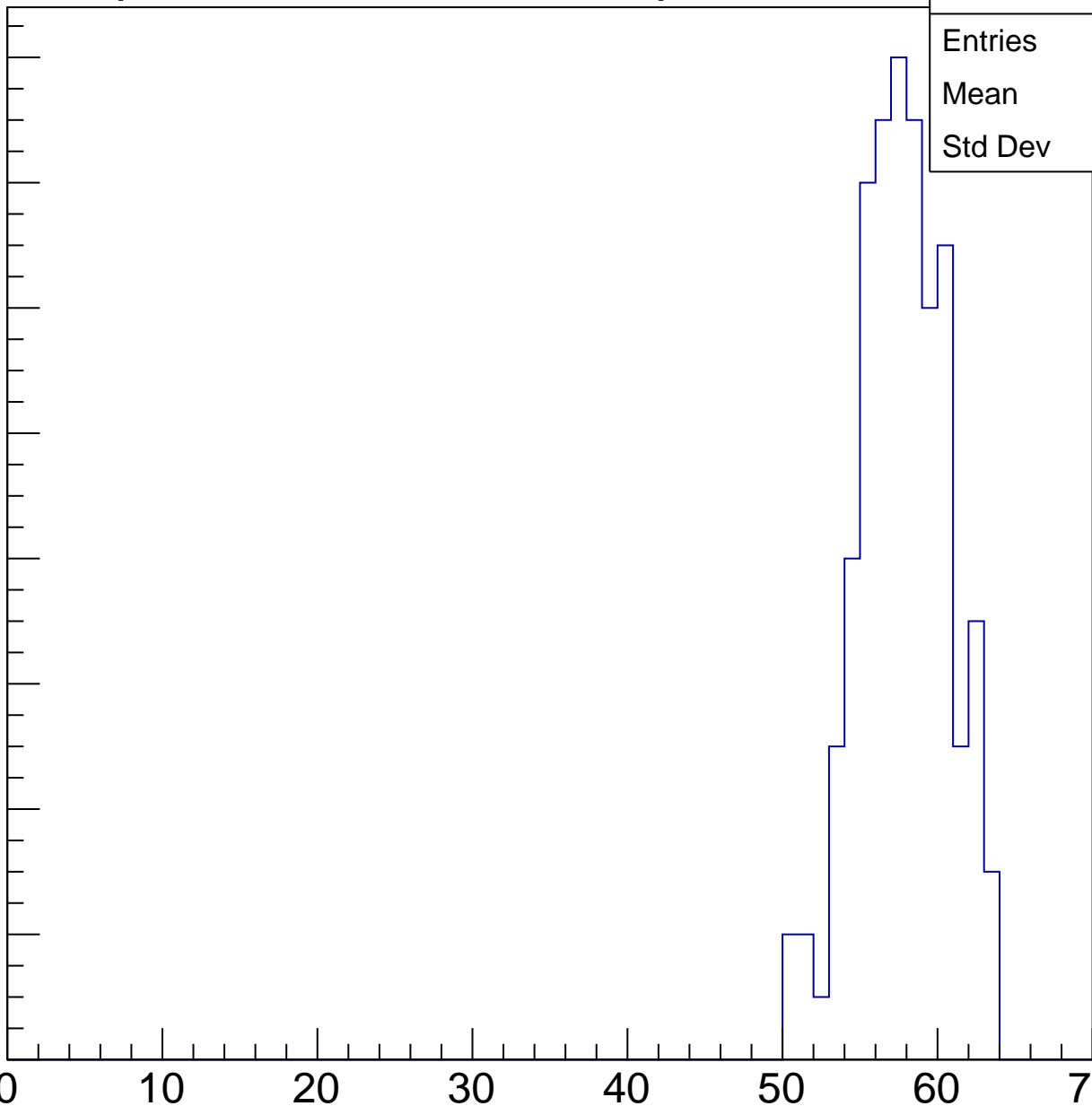
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	57.28
Std Dev	2.84

ampl



B1L001S, U22-ch78, adc5

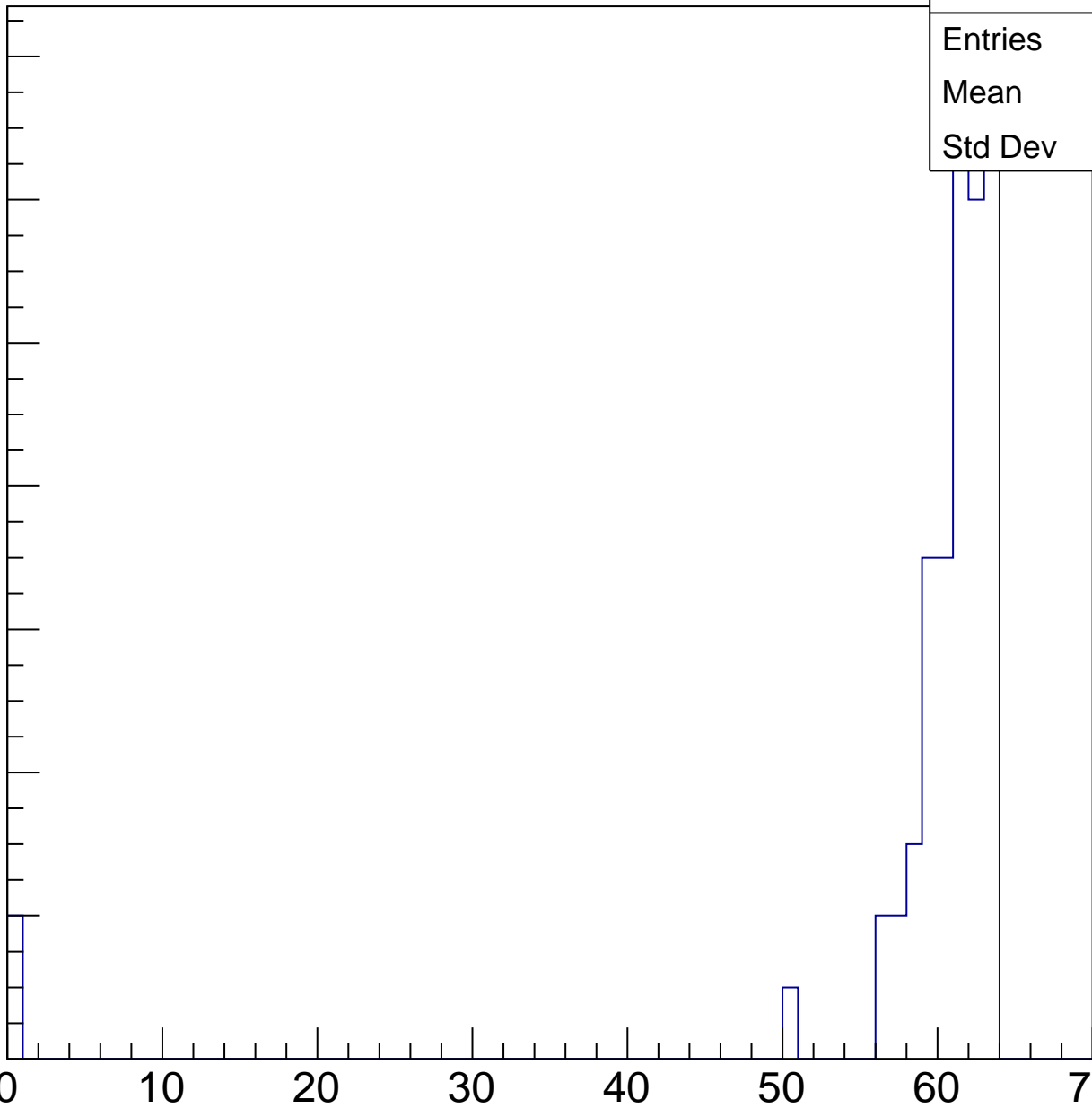
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	63
Mean	58.73
Std Dev	10.87

ampl



B1L001S, U22-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch79, adc0

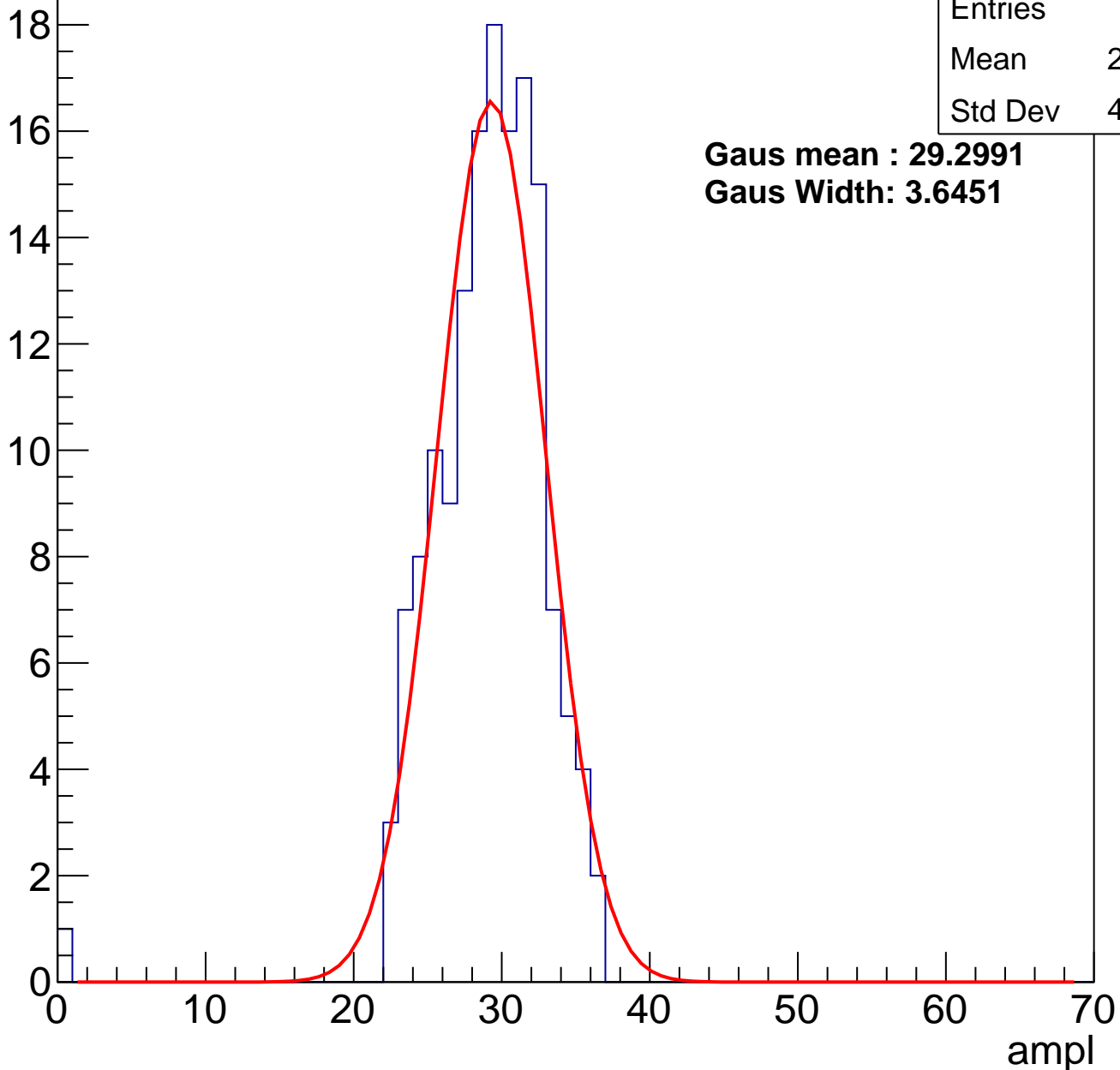
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	28.64
Std Dev	4.009

Gaus mean : 29.2991

Gaus Width: 3.6451

Entry



B1L001S, U22-ch79, adc1

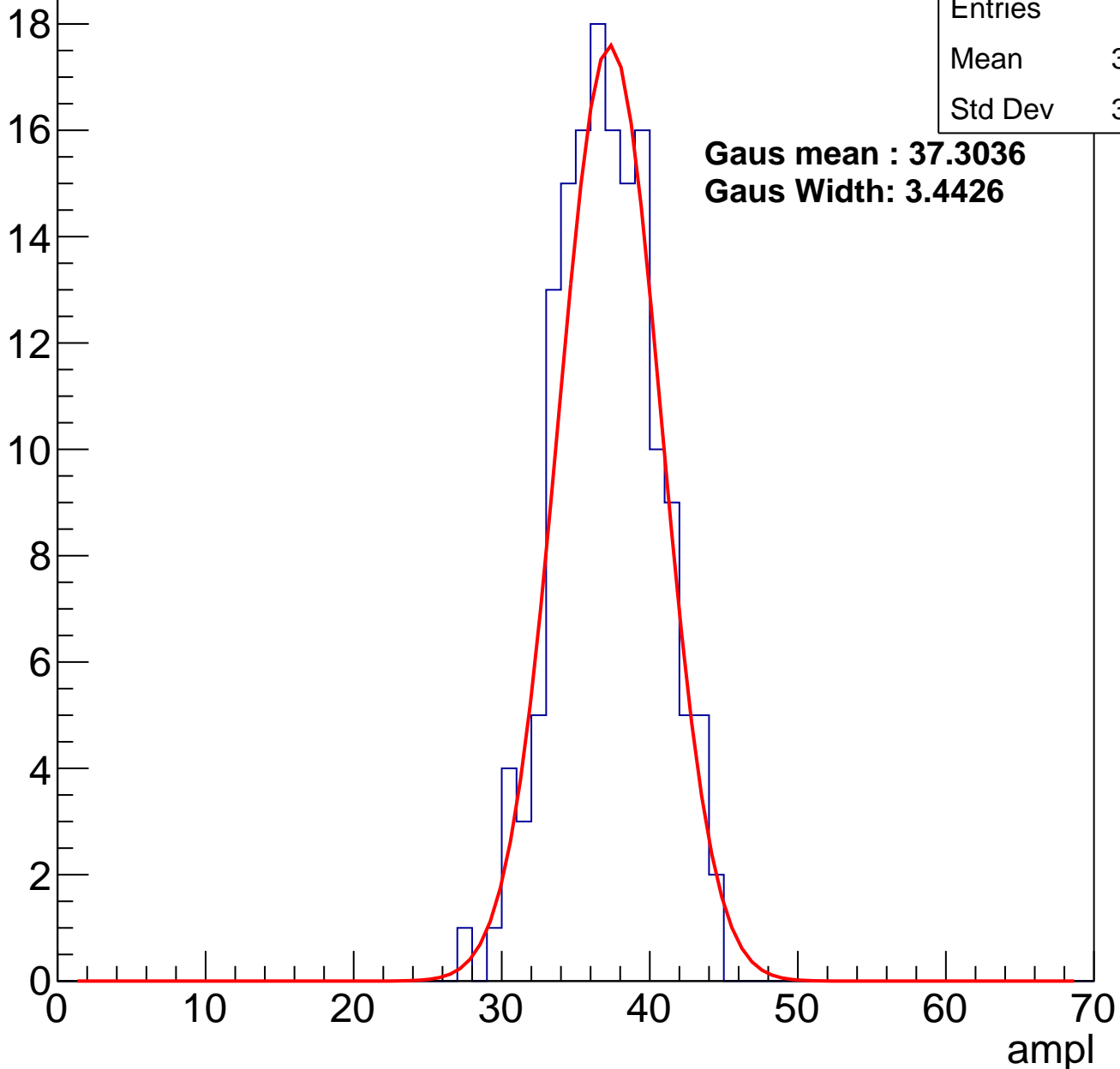
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	36.65
Std Dev	3.337

Gaus mean : 37.3036

Gaus Width: 3.4426

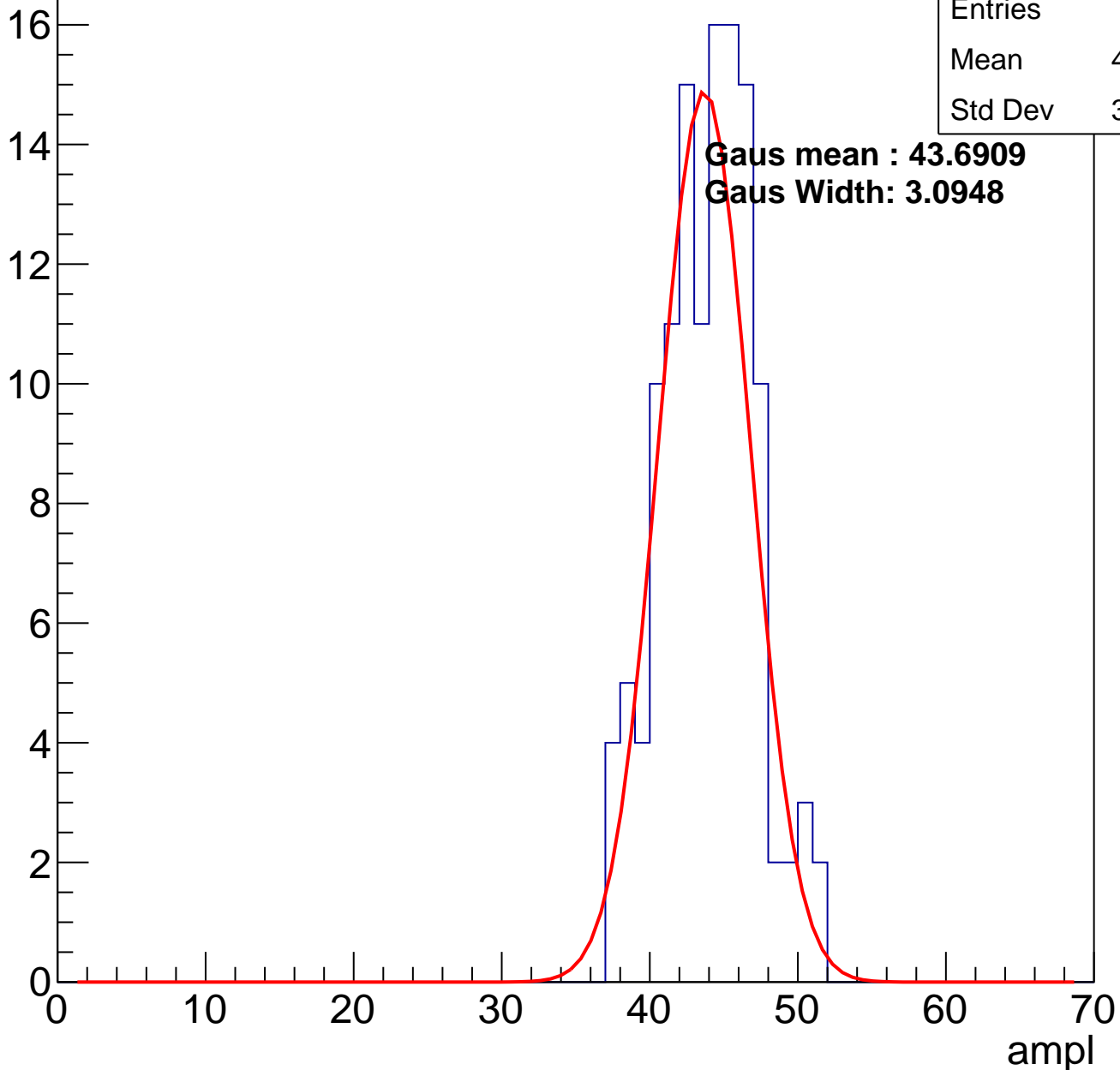
Entry



B1L001S, U22-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

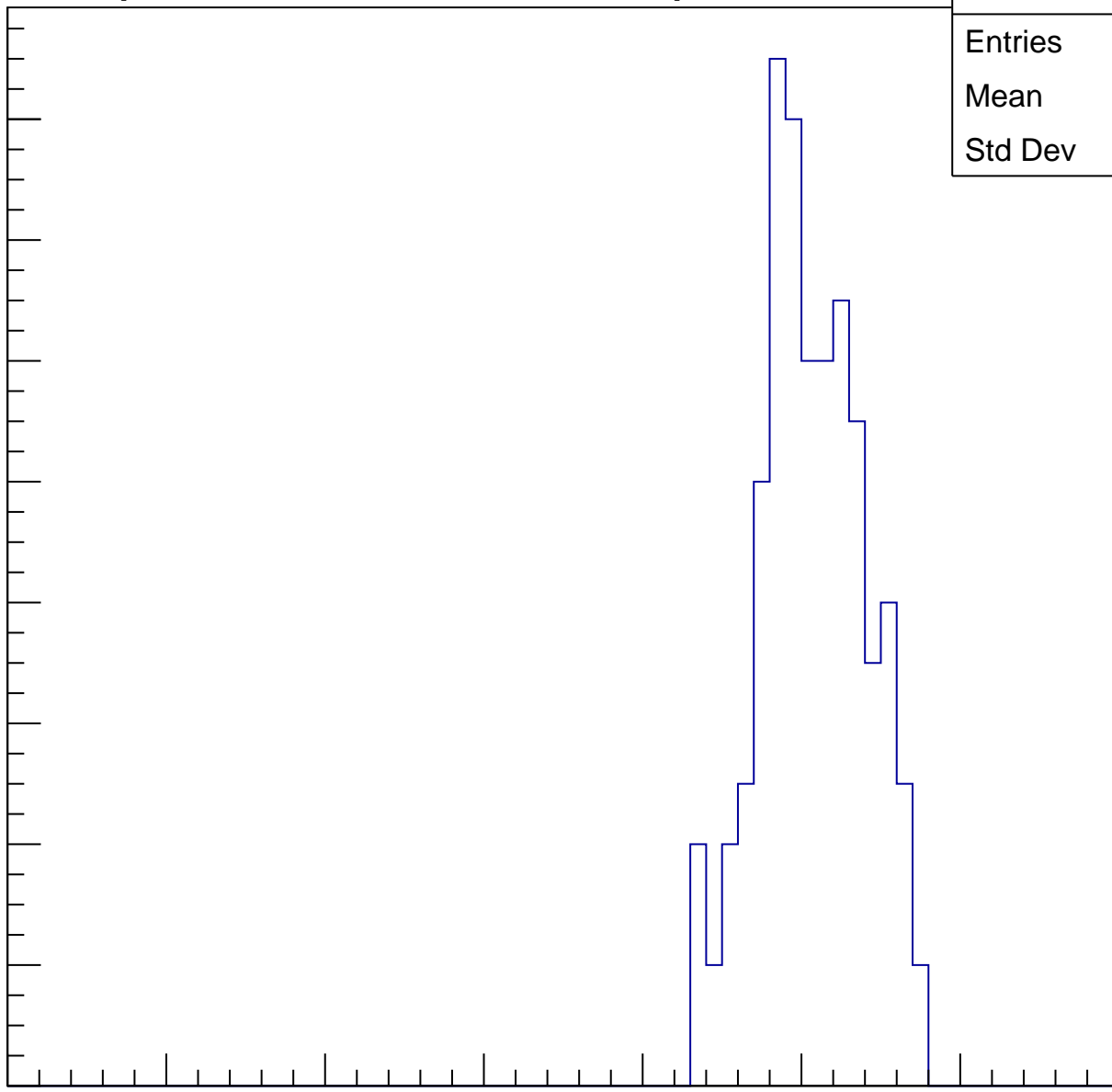
Entries	128
Mean	50.18
Std Dev	3.273

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

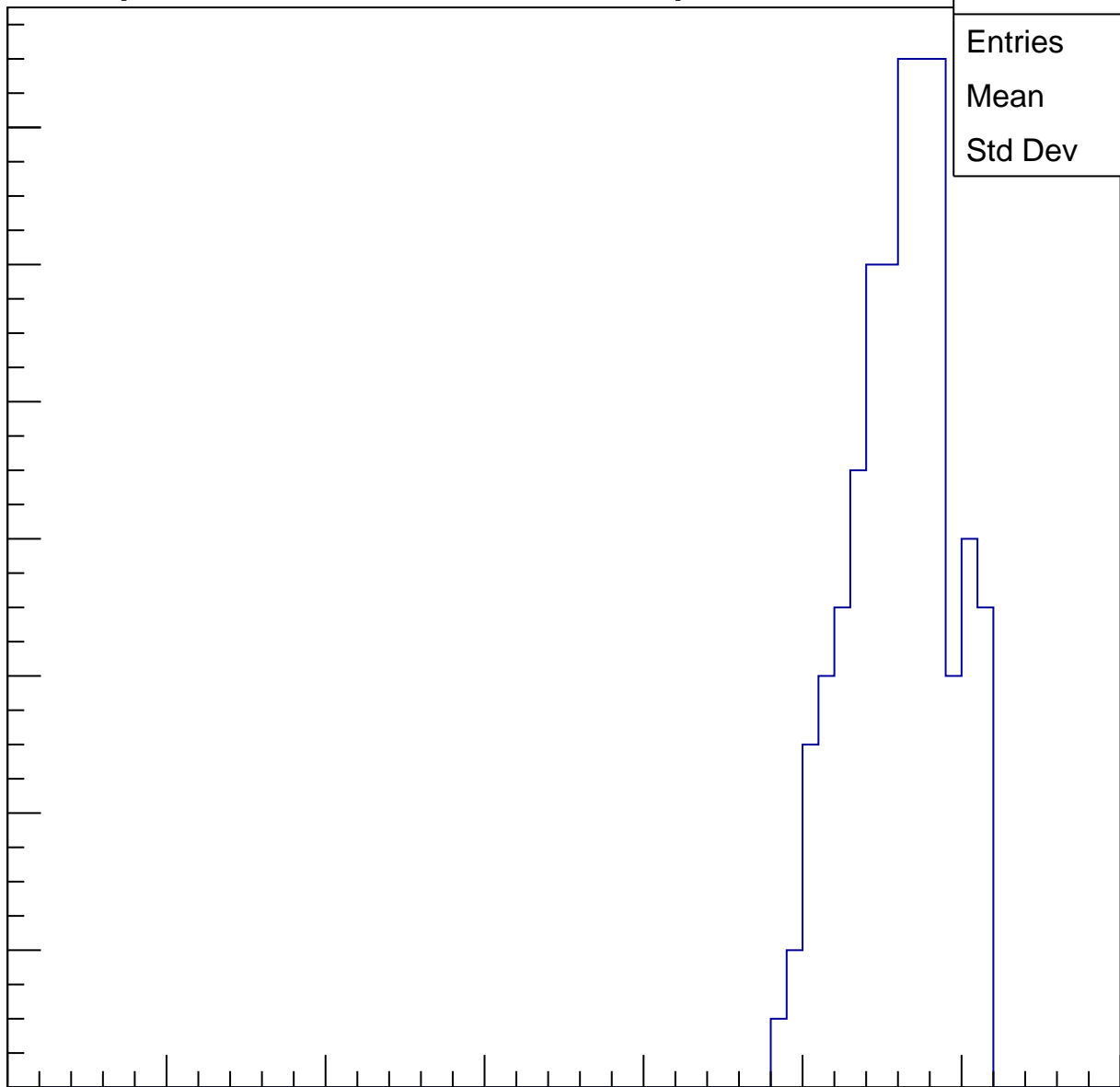
Entries	120
Mean	55.64
Std Dev	3.111

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch79, adc5

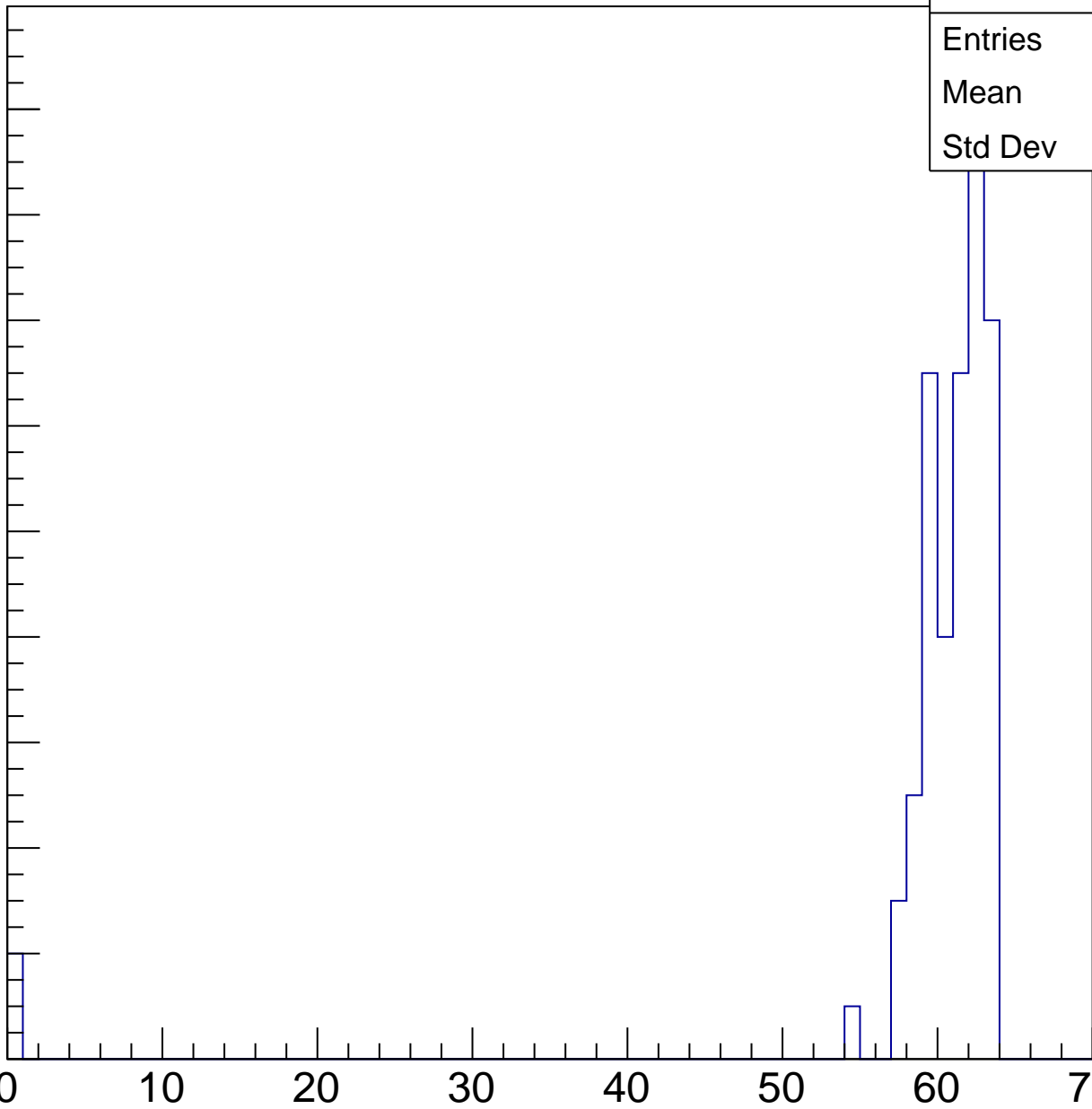
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.17
Std Dev	9.778

ampl

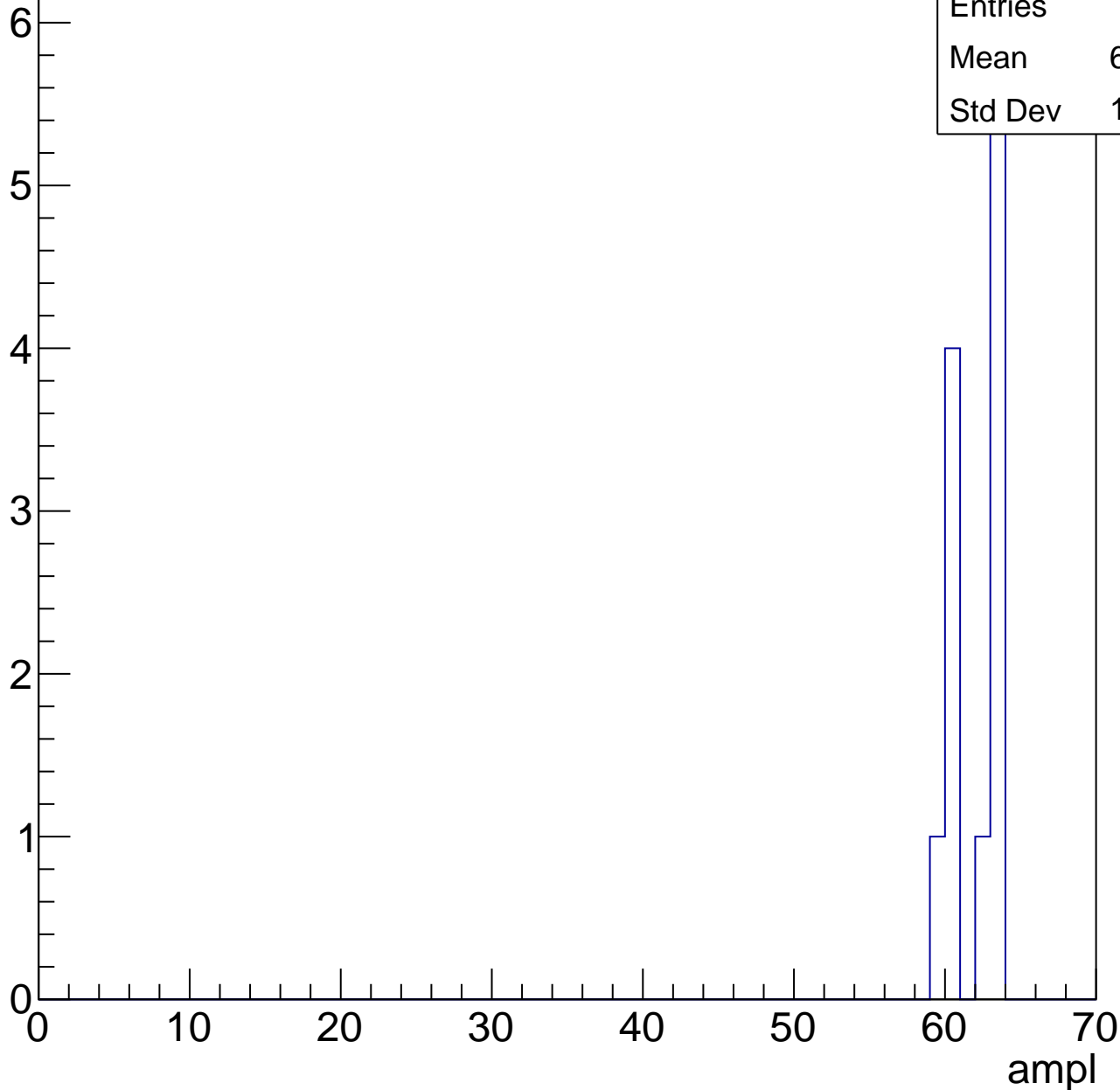


B1L001S, U22-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

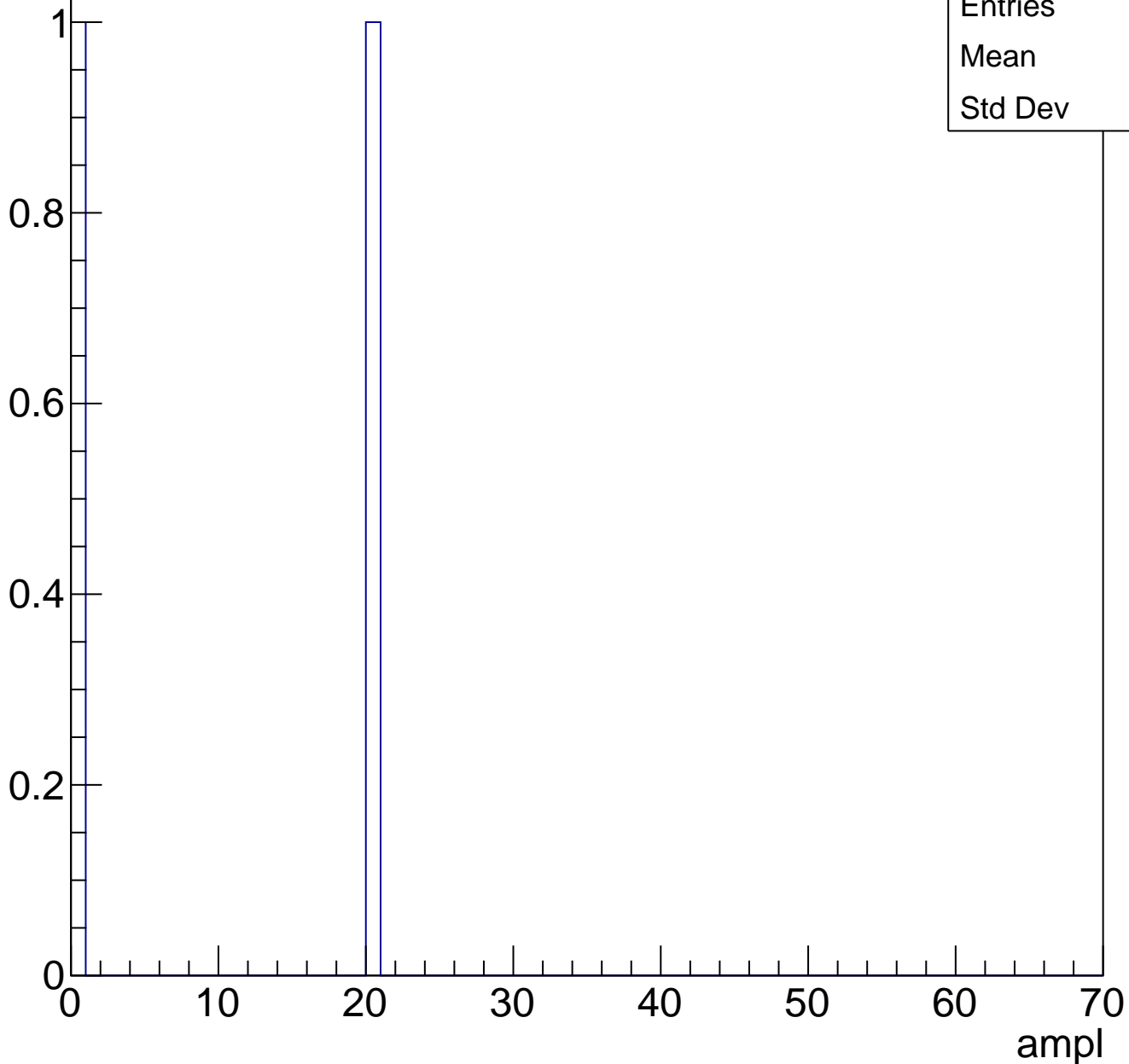
Entries	12
Mean	61.58
Std Dev	1.552



B1L001S, U22-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	10
Std Dev	10

B1L001S, U22-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	31.47
Std Dev	3.275

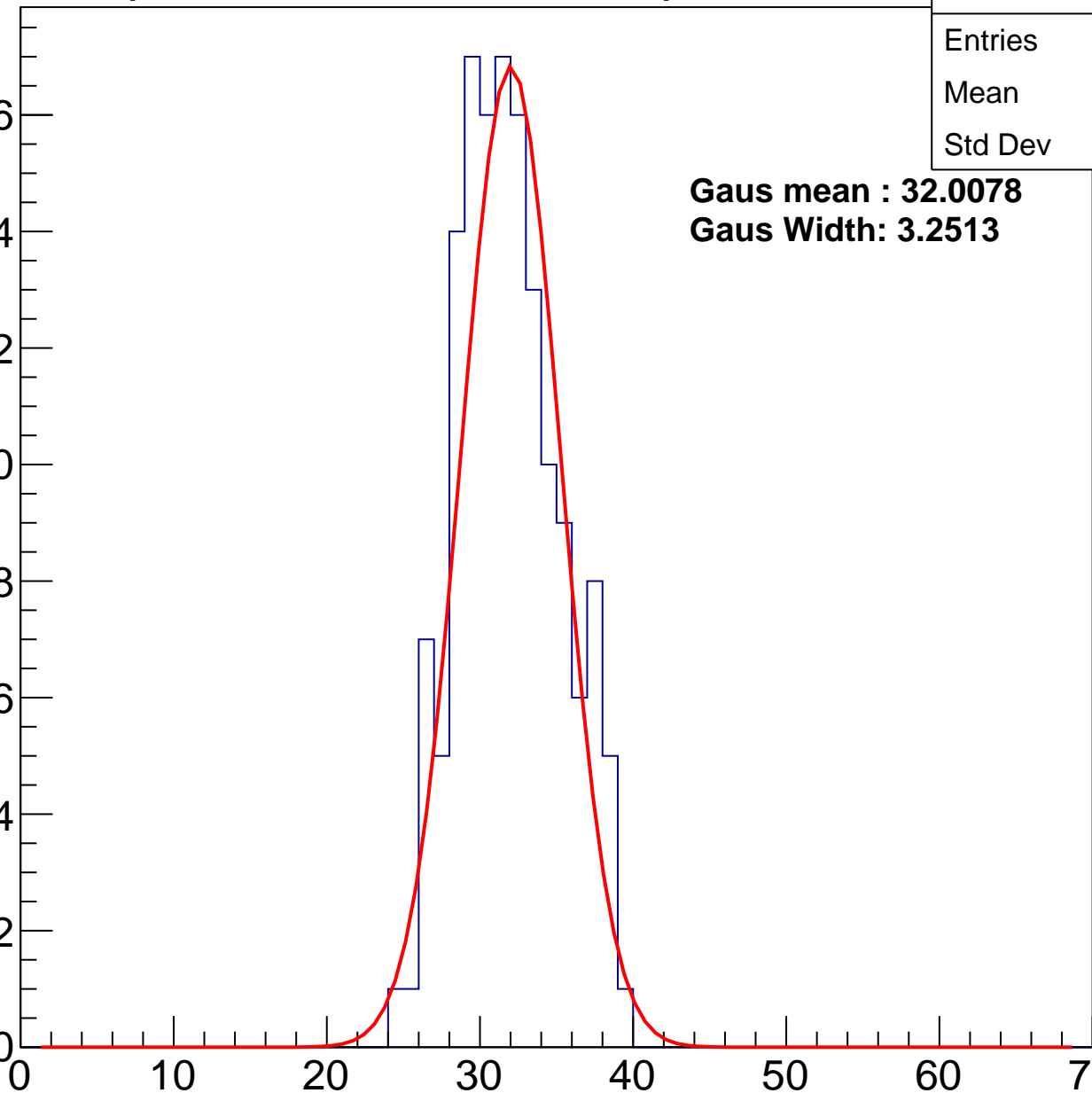
Gaus mean : 32.0078

Gaus Width: 3.2513

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch80, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	102
Mean	37.13
Std Dev	2.989

Gaus mean : 37.4917

Gaus Width: 2.9657

0

B1L001S, U22-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	43.71
Std Dev	3.396

Entry

Gaus mean : 44.3851

Gaus Width: 3.2476

ampl

16
14
12
10
8
6
4
2
0

B1L001S, U22-ch80, adc3

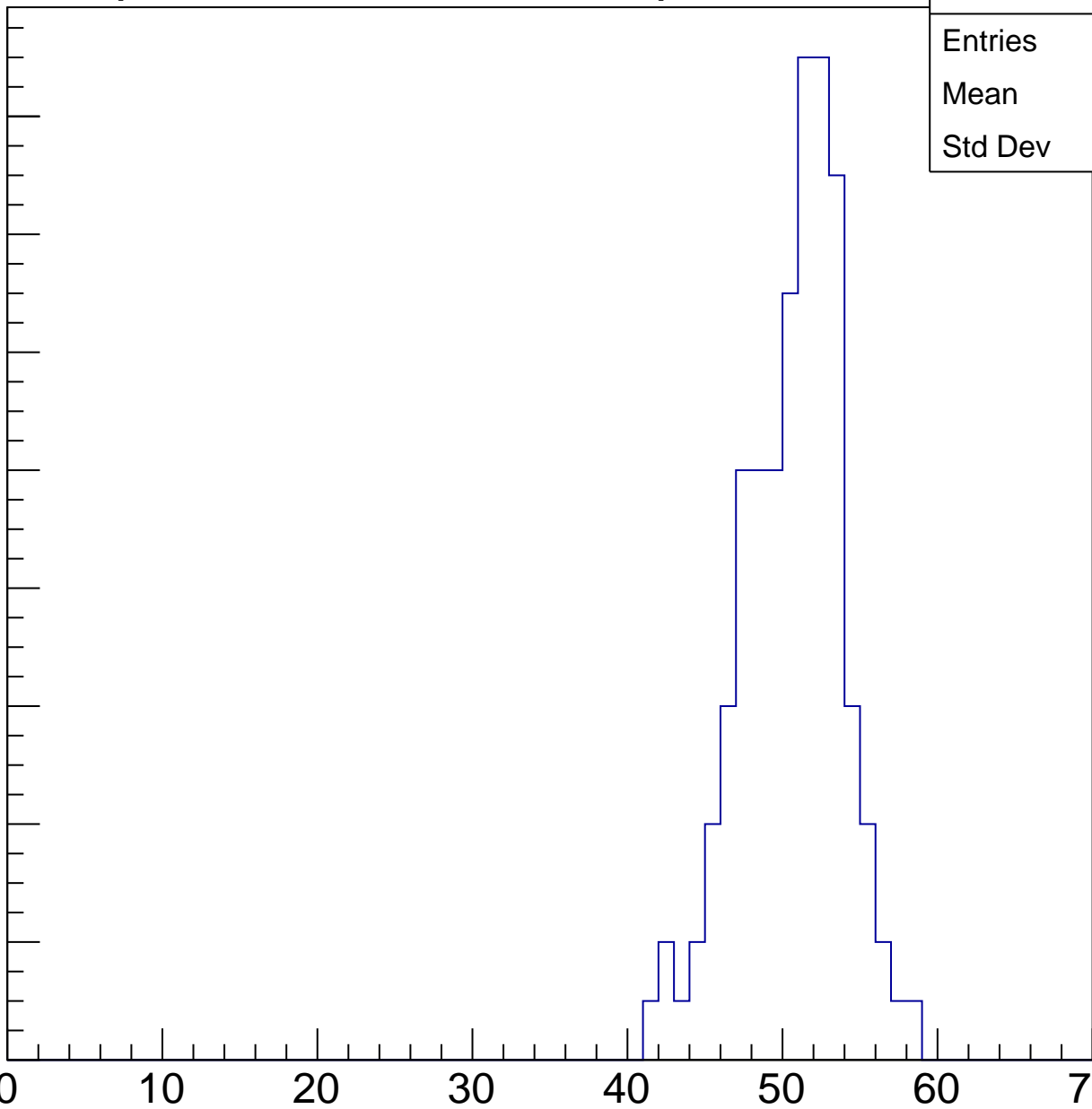
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	50.16
Std Dev	3.219

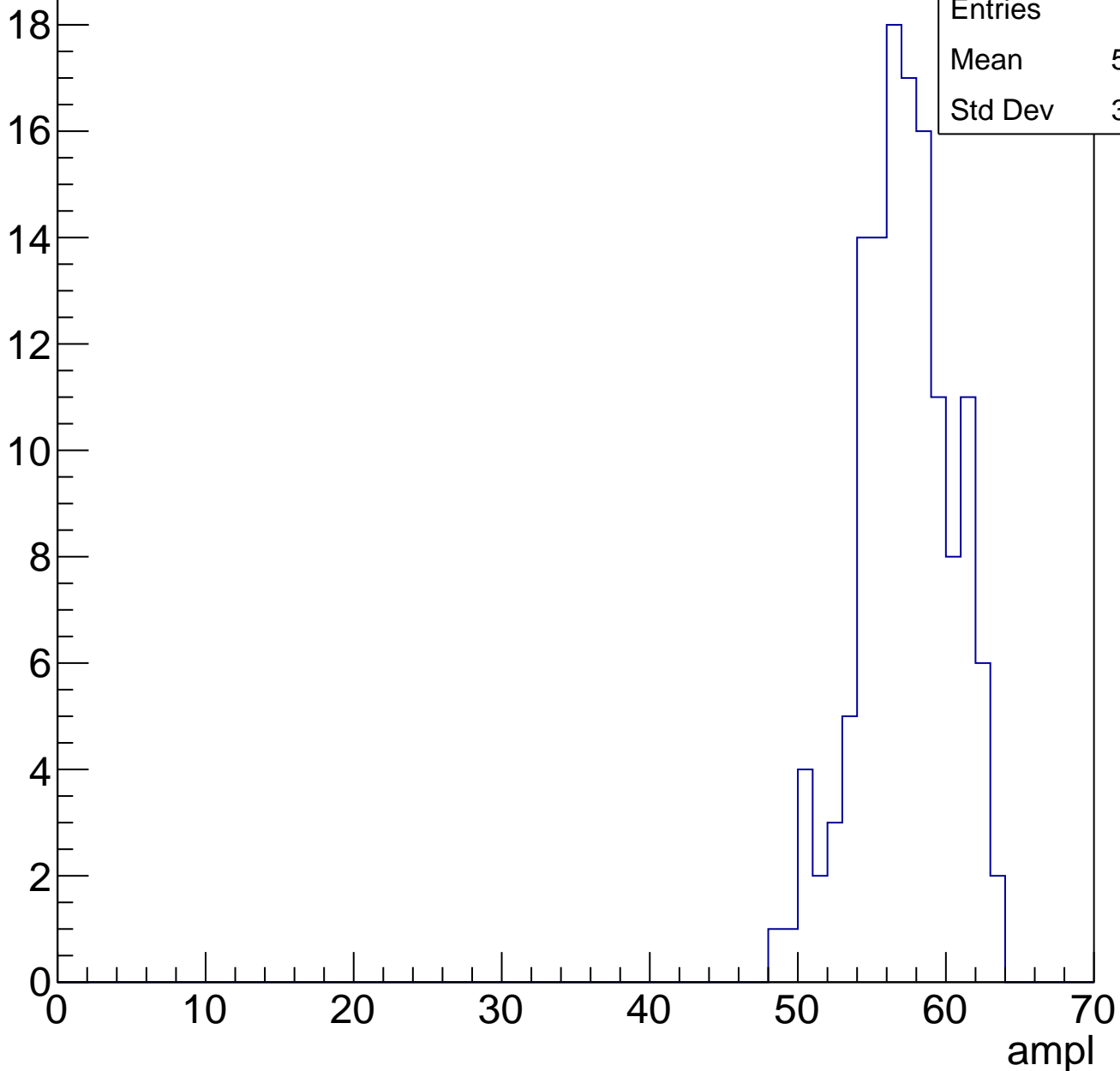
ampl



B1L001S, U22-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch80, adc5

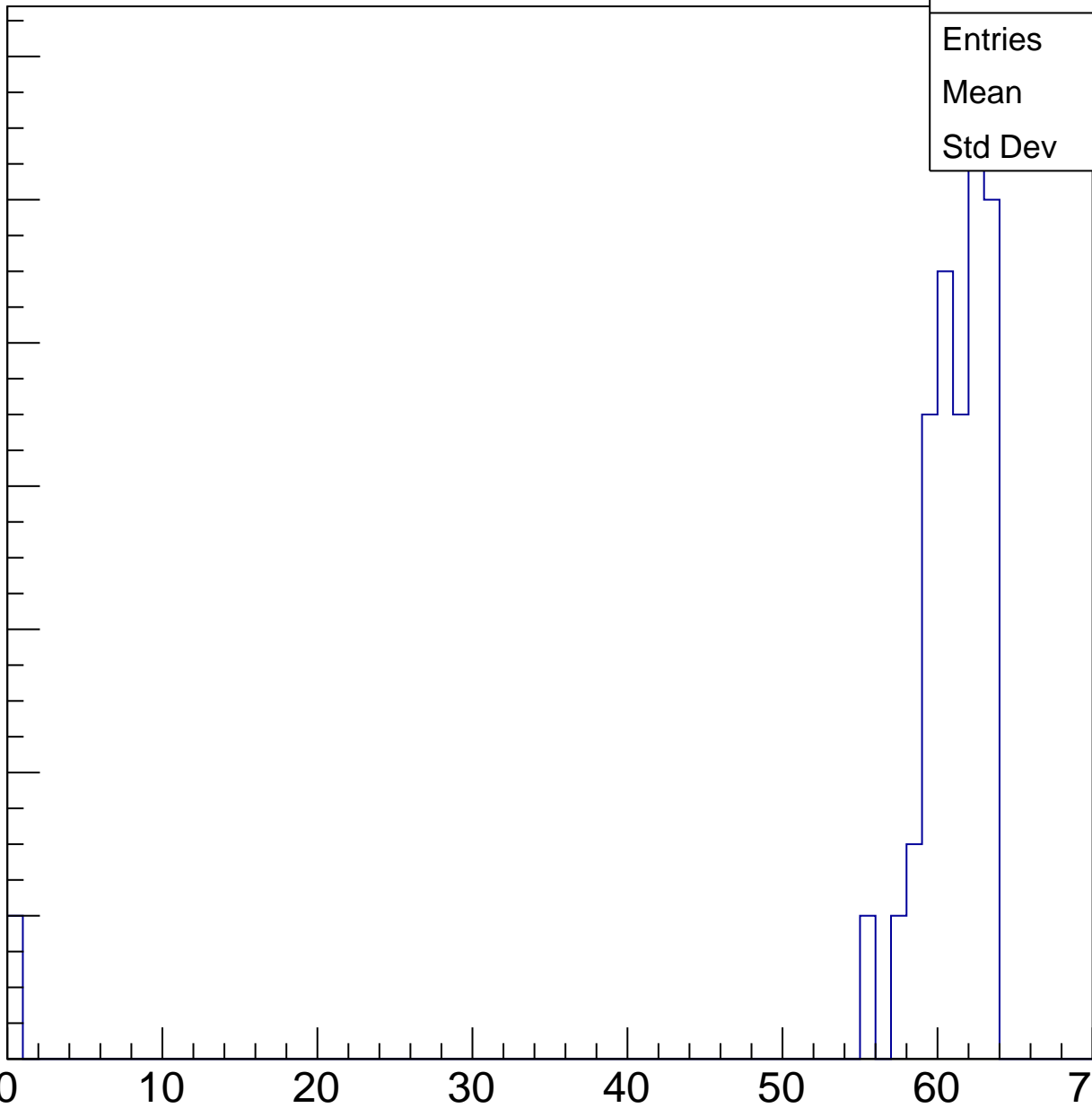
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	58.78
Std Dev	10.73

ampl

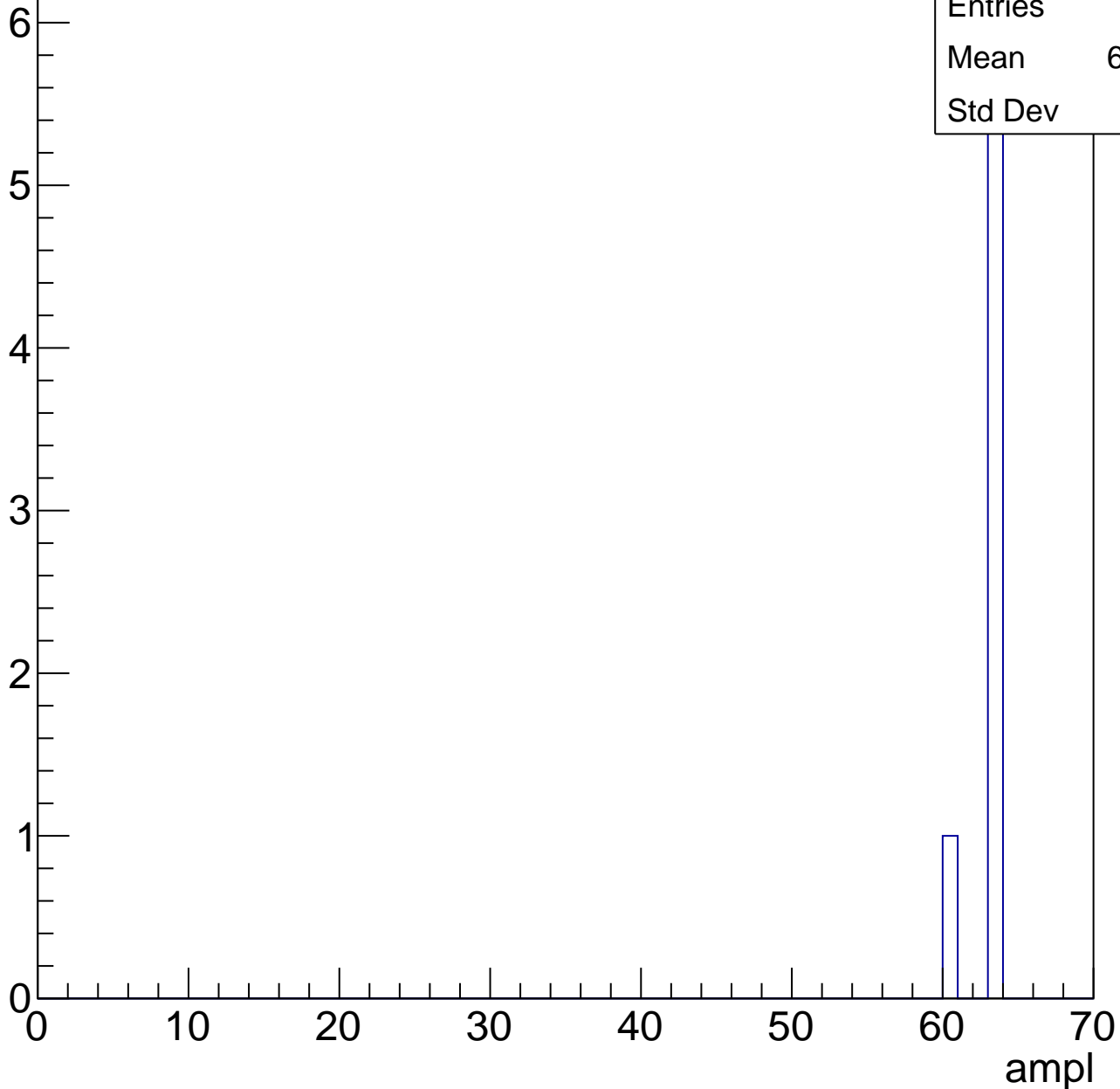


B1L001S, U22-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

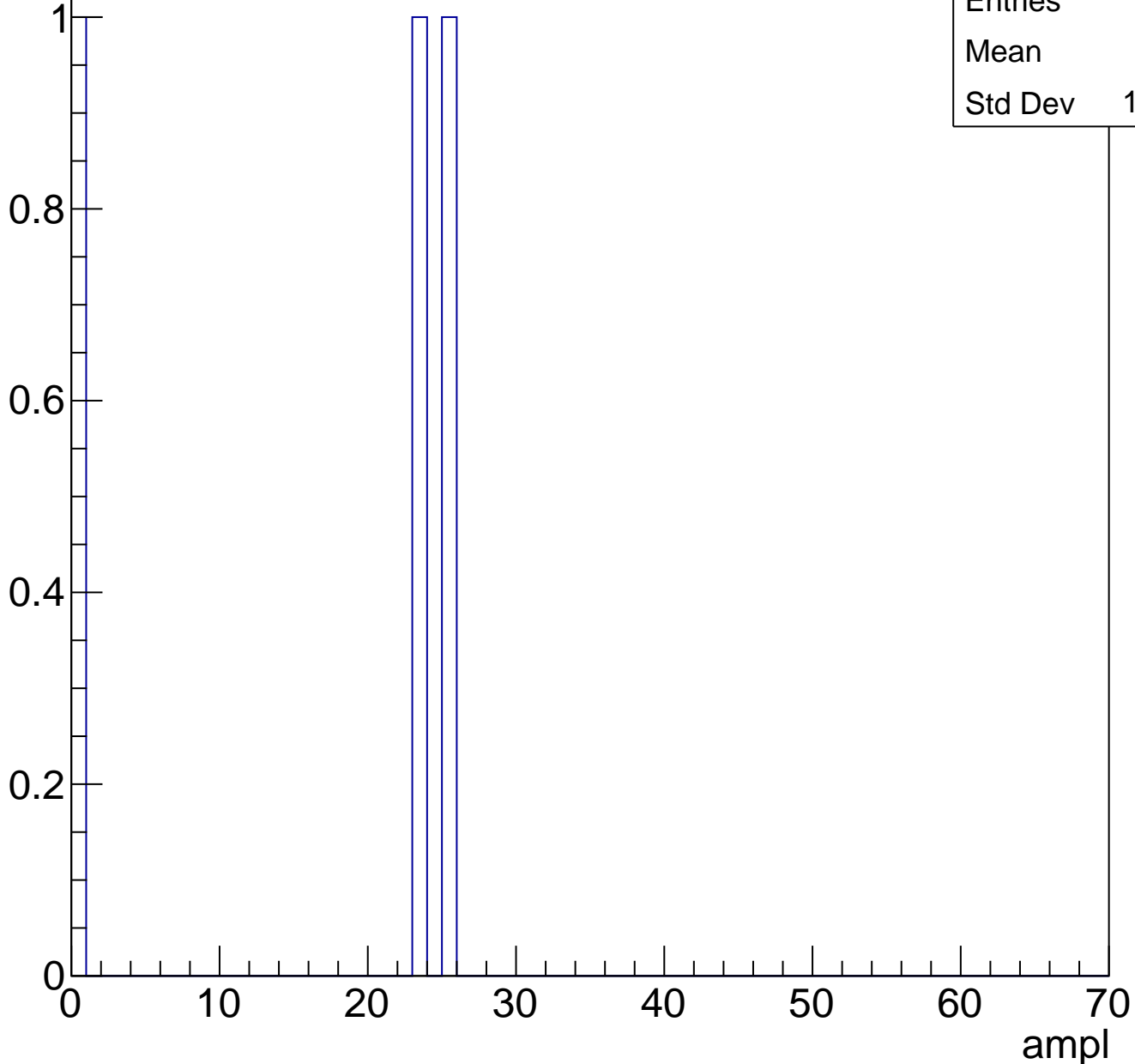
Entries	7
Mean	62.57
Std Dev	1.05



B1L001S, U22-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch81, adc0

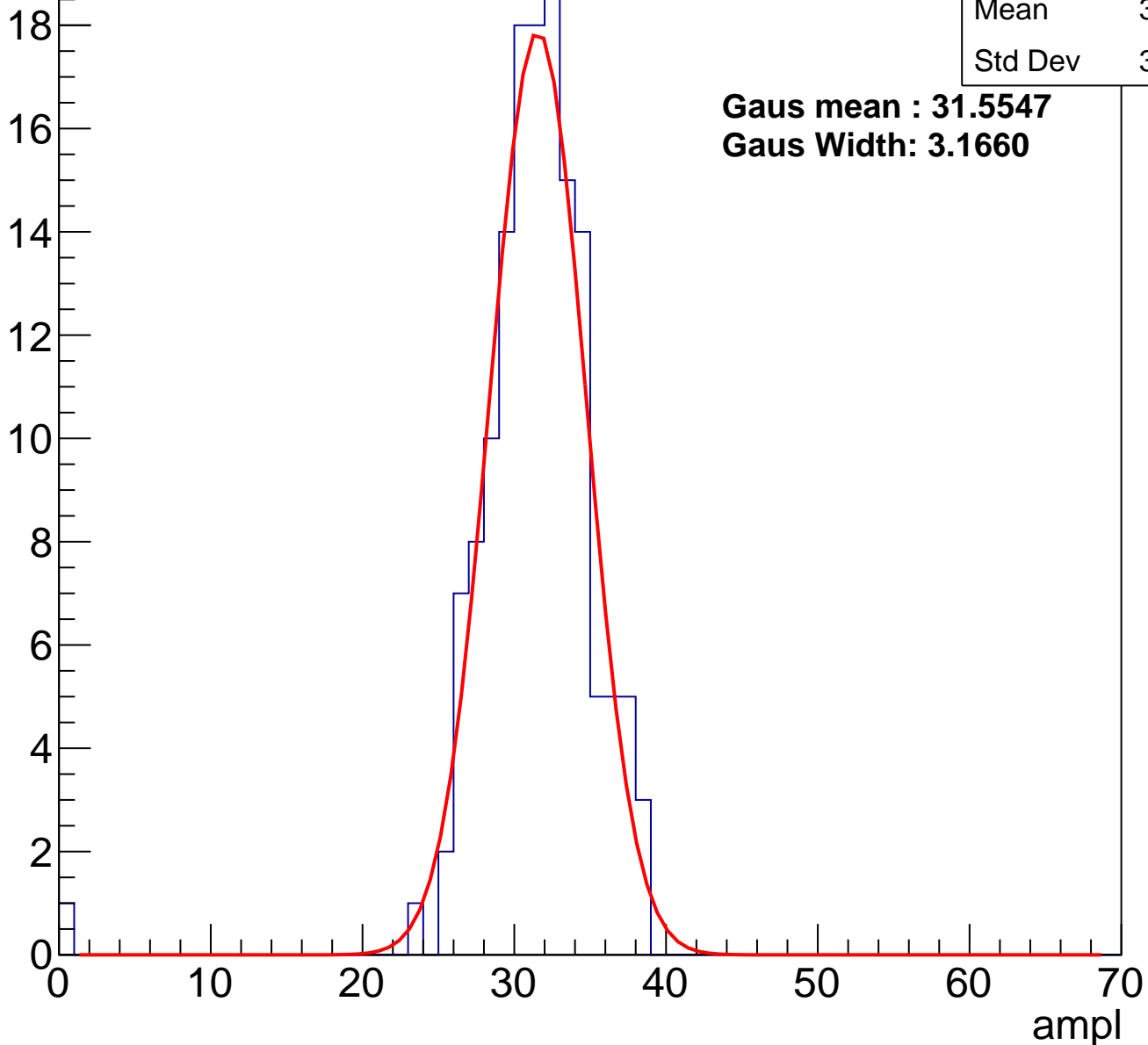
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	30.95
Std Dev	3.983

Gaus mean : 31.5547

Gaus Width: 3.1660

Entry



B1L001S, U22-ch81, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

140

Mean

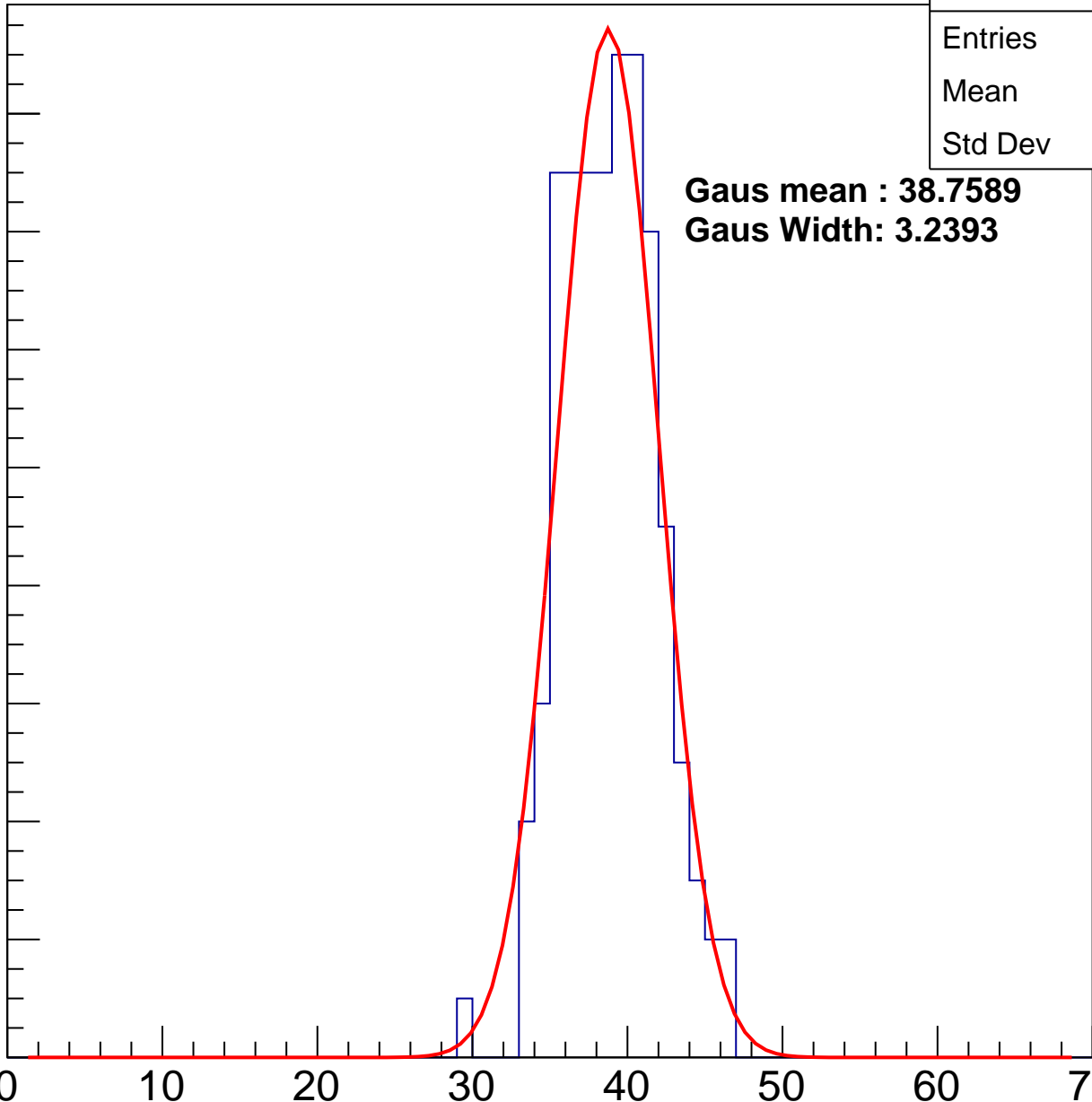
38.42

Std Dev

3.019

Gaus mean : 38.7589

Gaus Width: 3.2393



B1L001S, U22-ch81, adc2

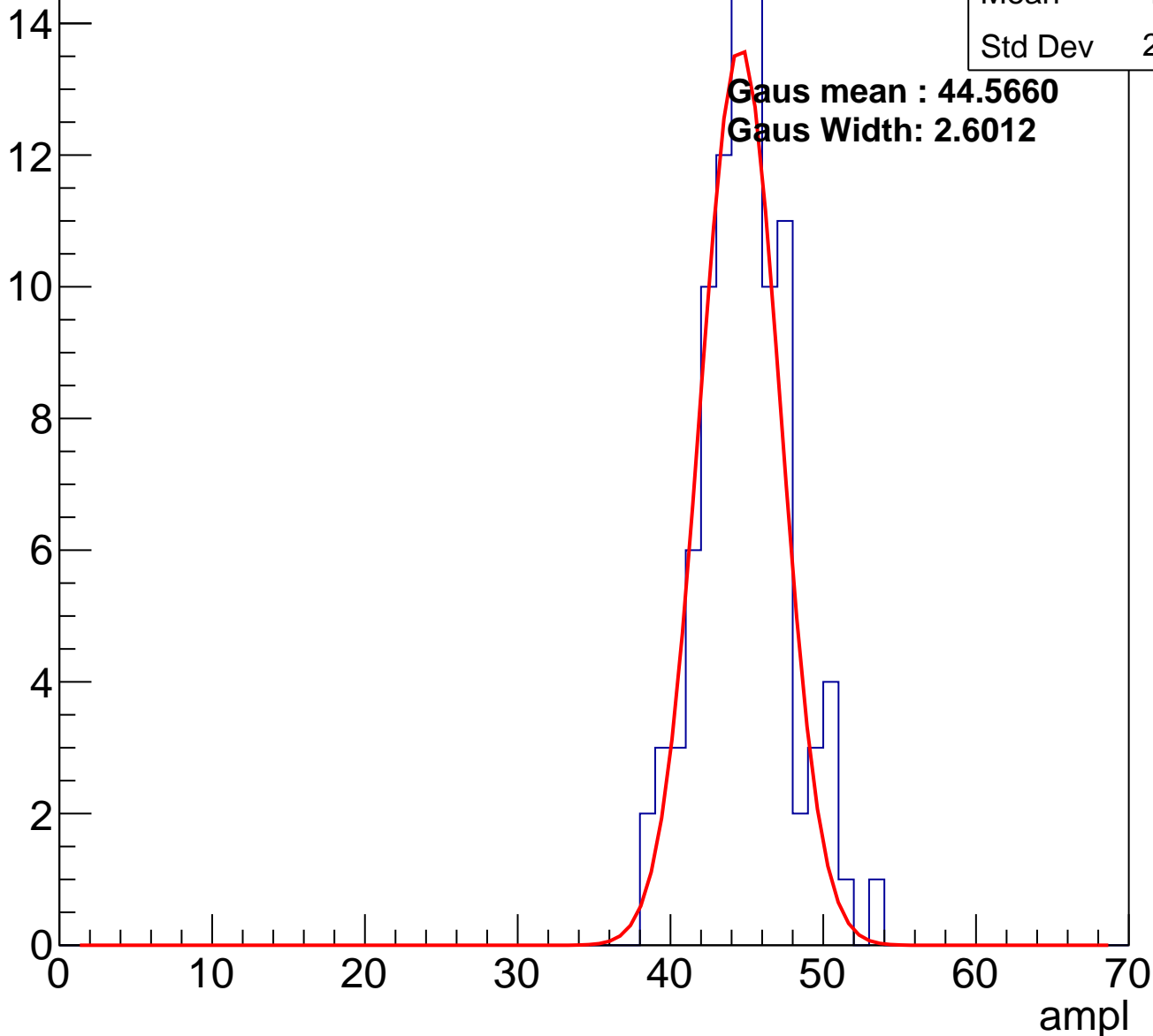
calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	44.43
Std Dev	2.889

Gaus mean : 44.5660

Gaus Width: 2.6012

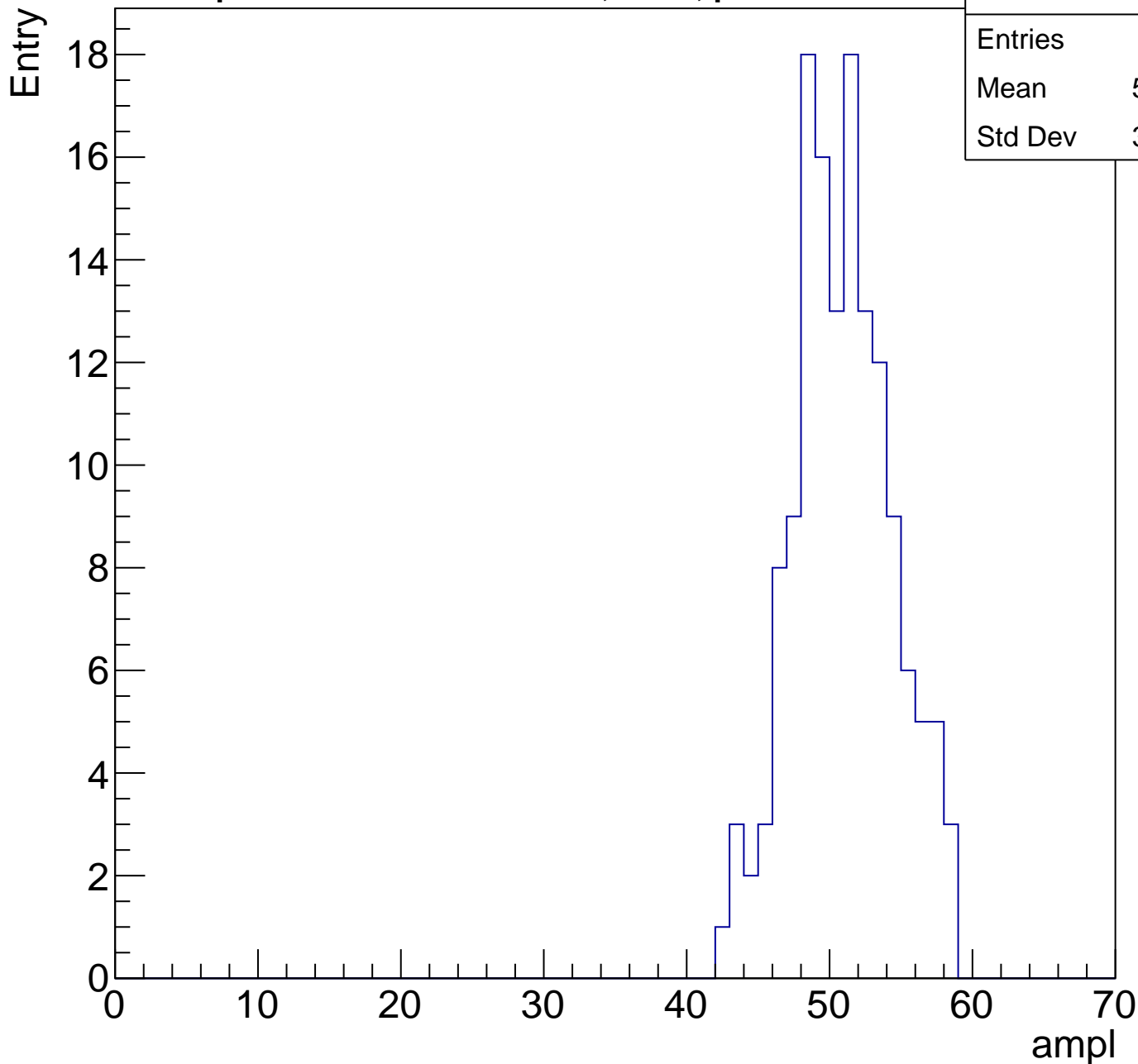
Entry



B1L001S, U22-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

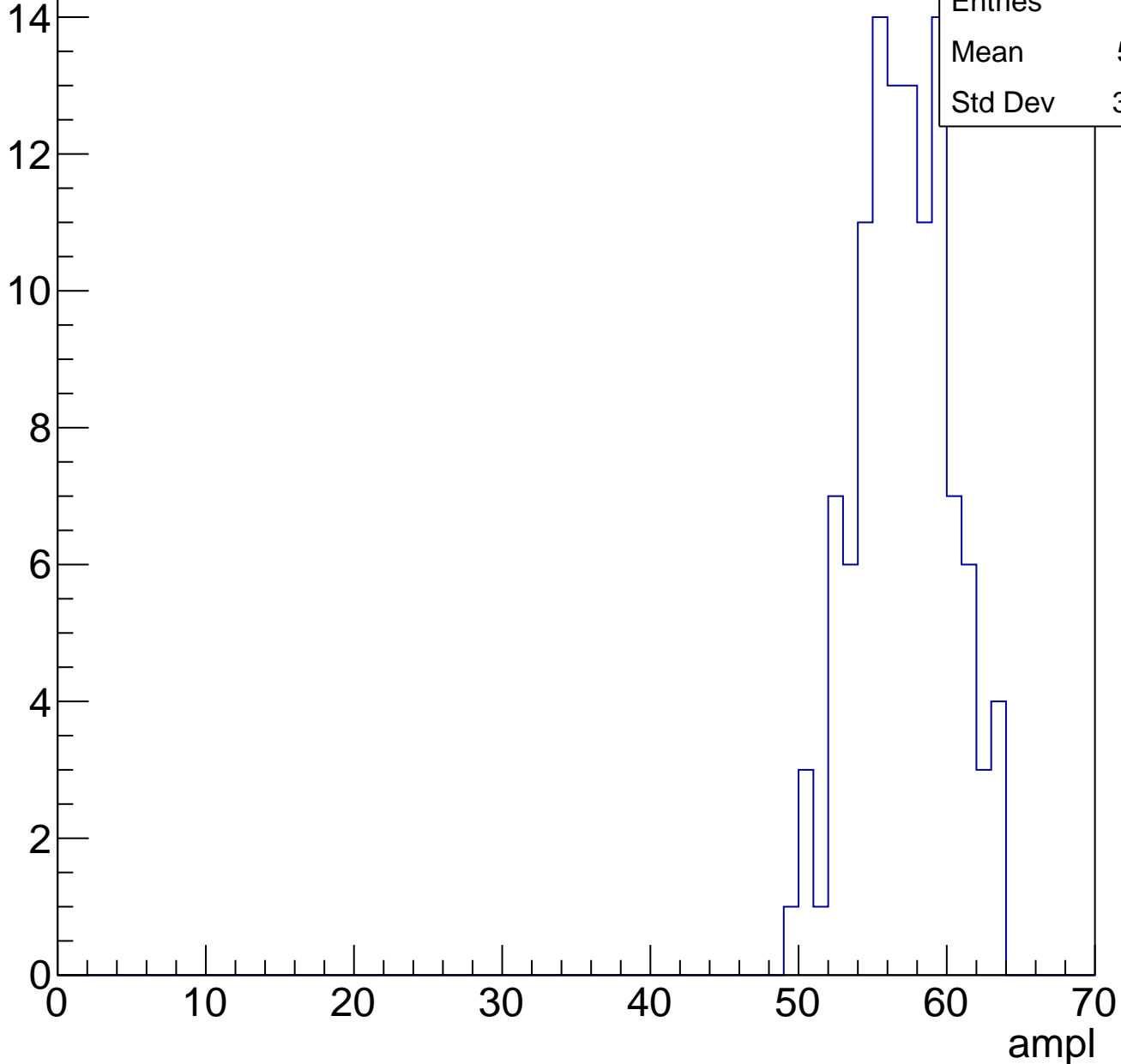
Entries	144
Mean	50.47
Std Dev	3.448



B1L001S, U22-ch81, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch81, adc5

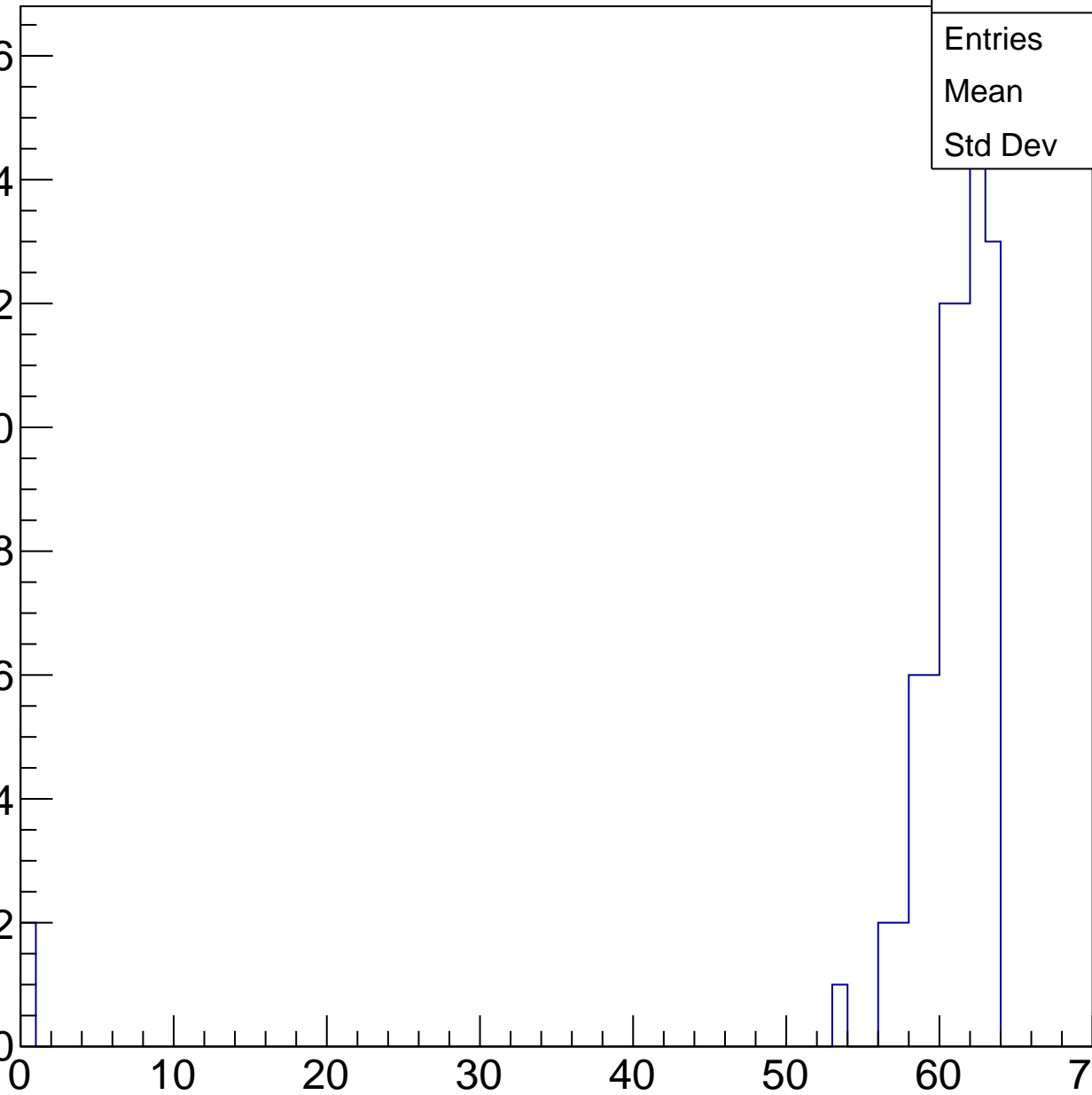
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	58.94
Std Dev	10.17

ampl



B1L001S, U22-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U22-ch82, adc0

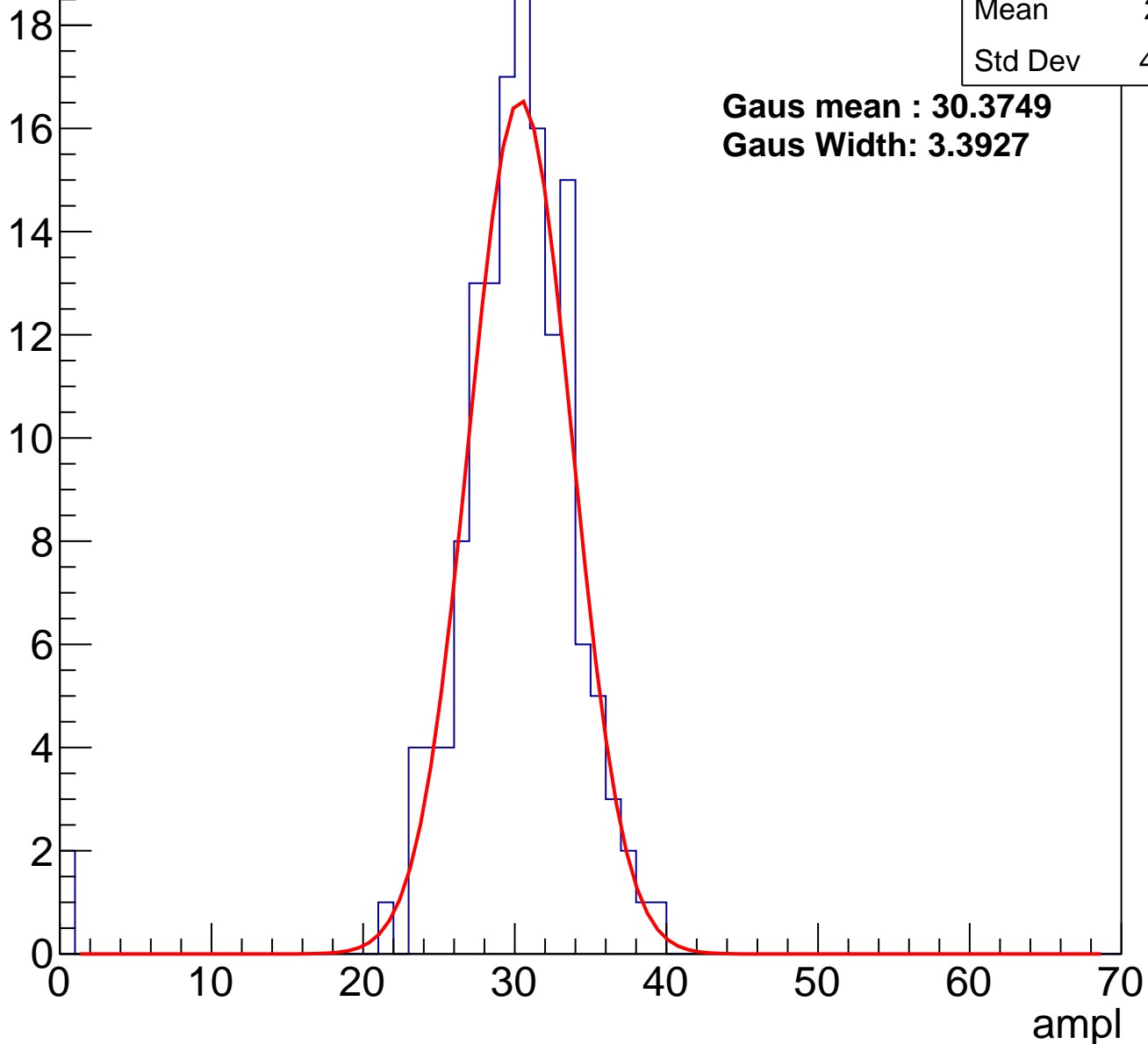
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.51
Std Dev	4.805

Gaus mean : 30.3749

Gaus Width: 3.3927

Entry



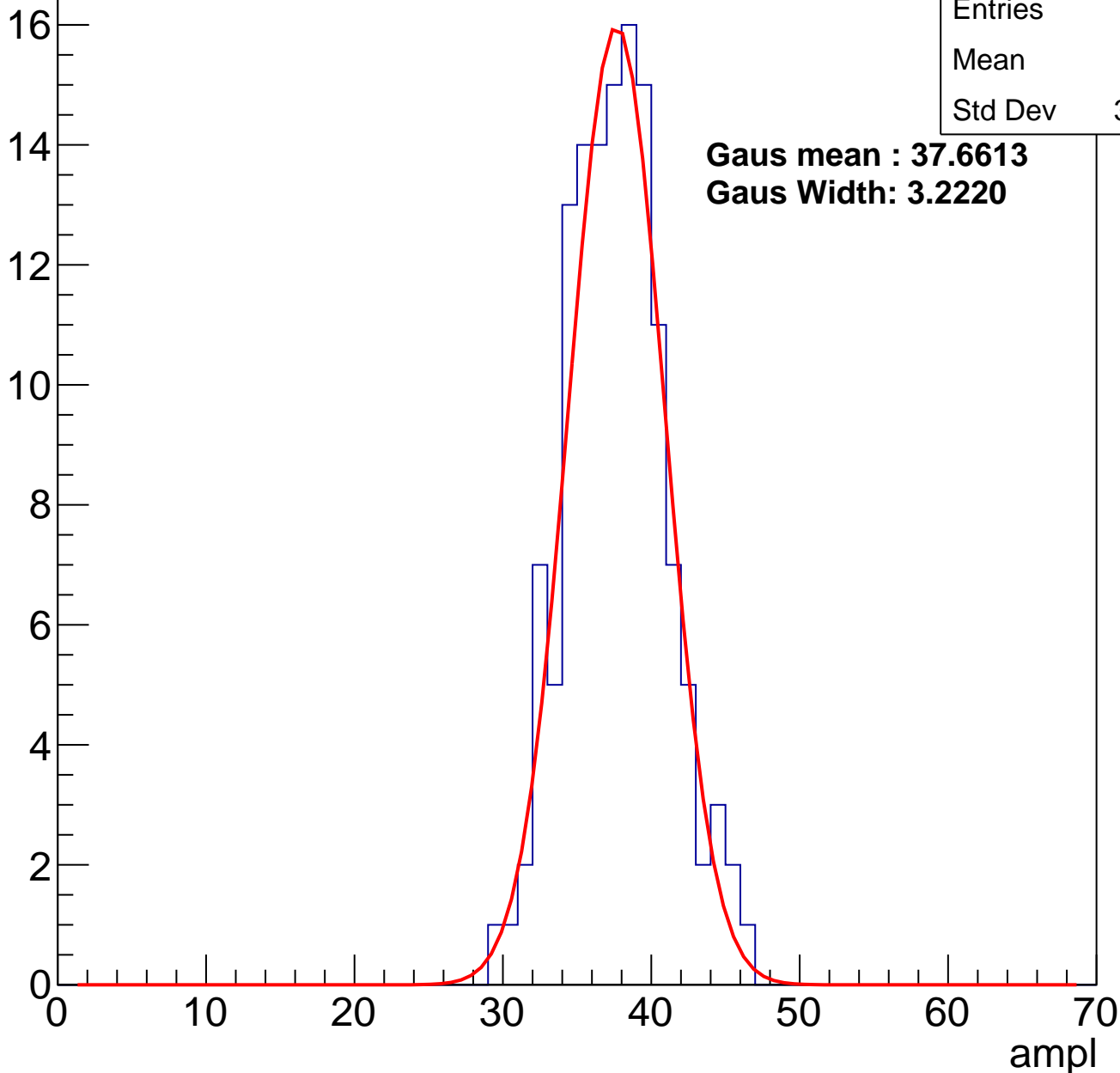
B1L001S, U22-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	37.2
Std Dev	3.303

Gaus mean : 37.6613
Gaus Width: 3.2220

Entry



B1L001S, U22-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

43.28

Std Dev

3.41

Gaus mean : 44.1120

Gaus Width: 3.5988

0

10

20

30

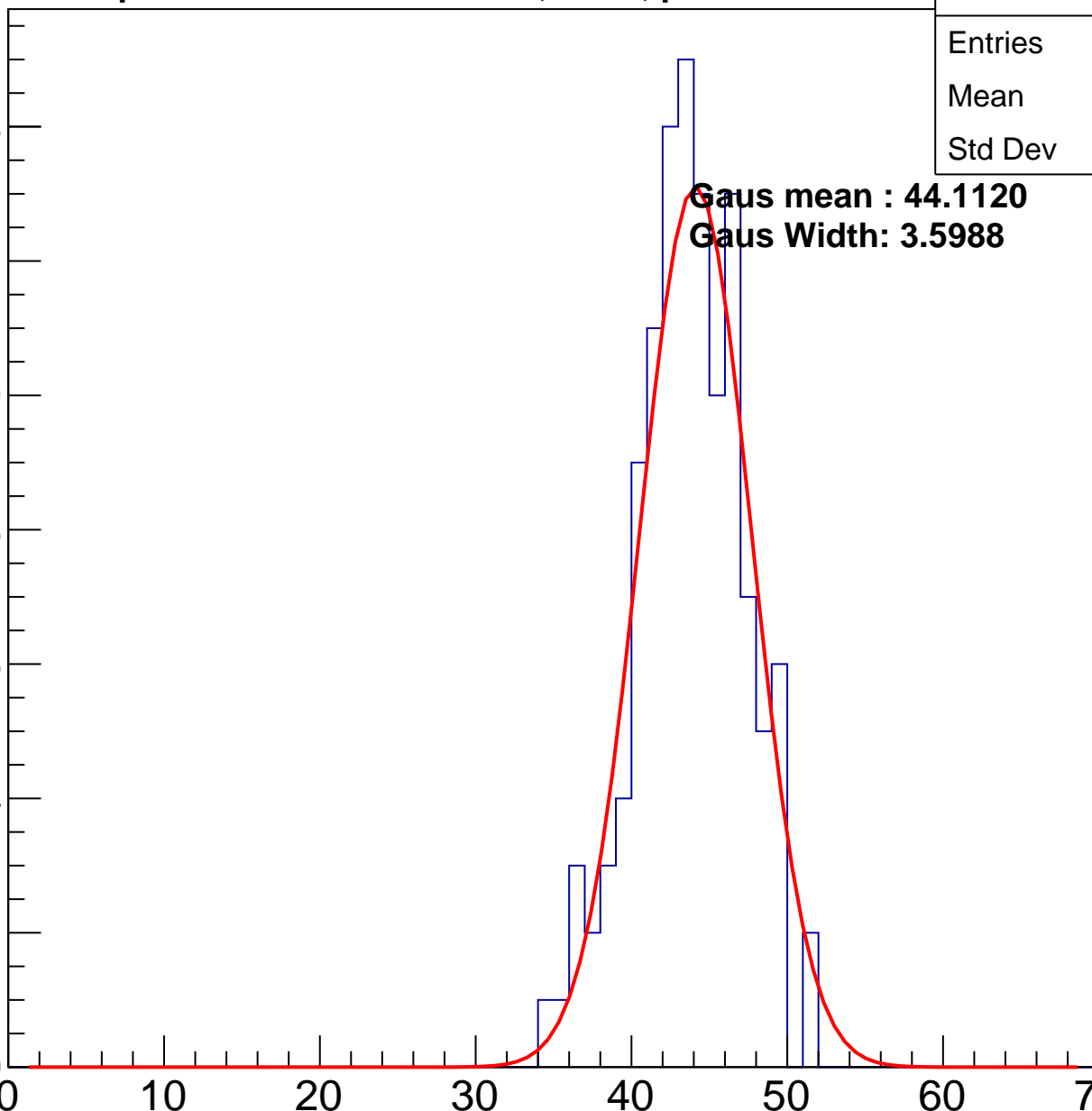
40

50

60

70

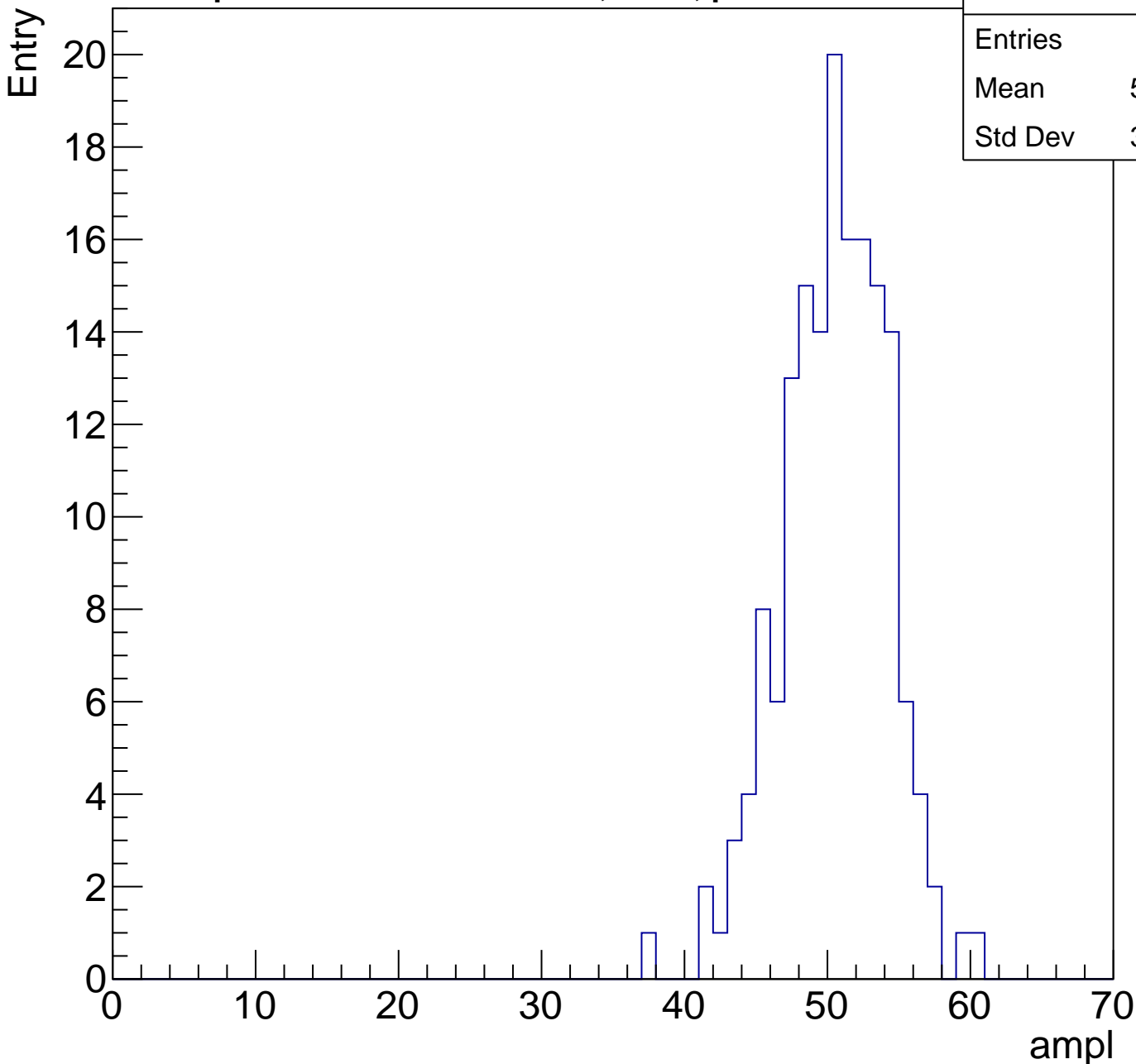
ampl



B1L001S, U22-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	50.03
Std Dev	3.672



B1L001S, U22-ch82, adc4

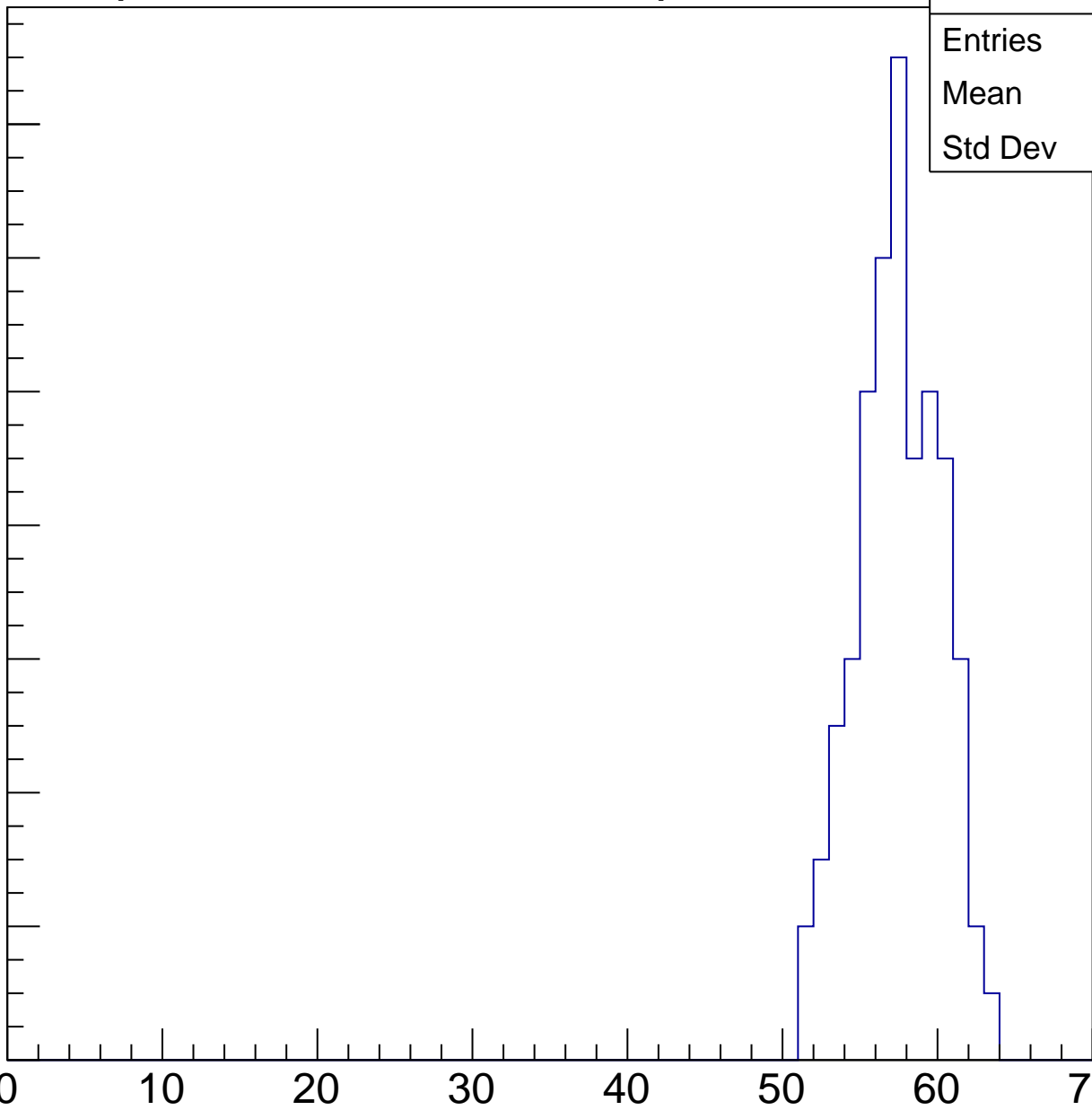
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	56.99
Std Dev	2.677

ampl



B1L001S, U22-ch82, adc5

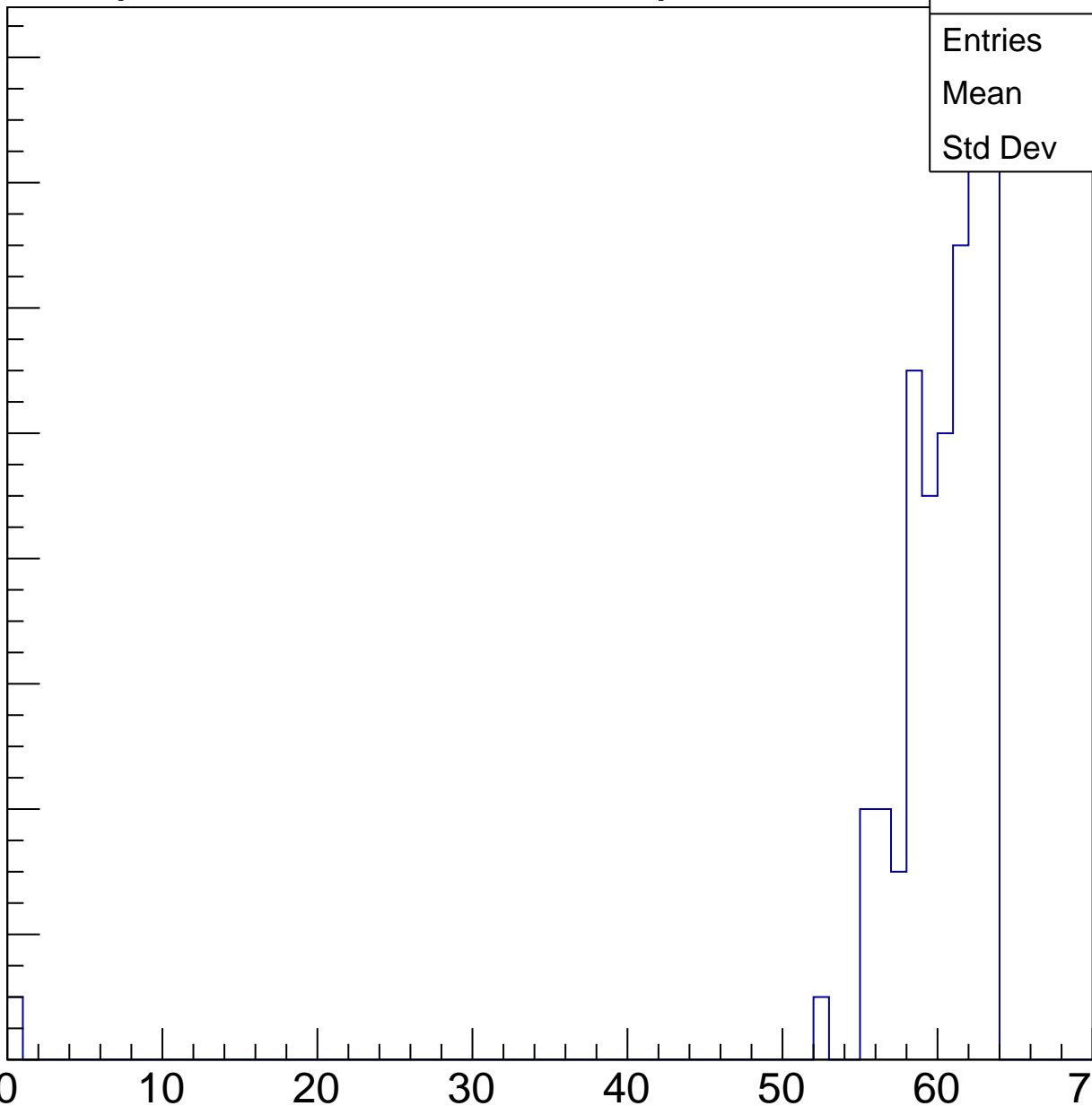
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.42
Std Dev	6.825

ampl



B1L001S, U22-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch83, adc0

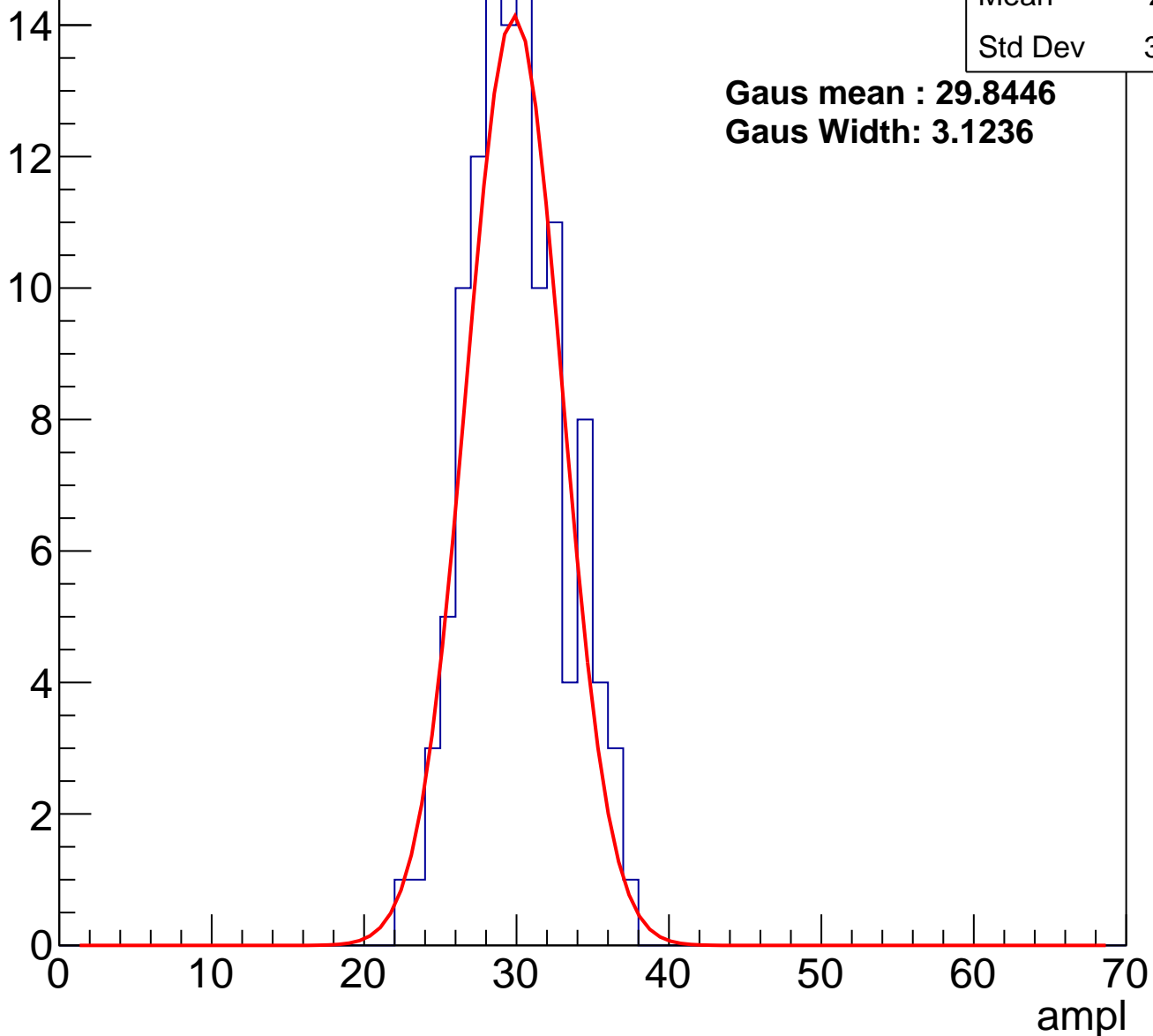
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	29.51
Std Dev	3.115

Gaus mean : 29.8446

Gaus Width: 3.1236

Entry

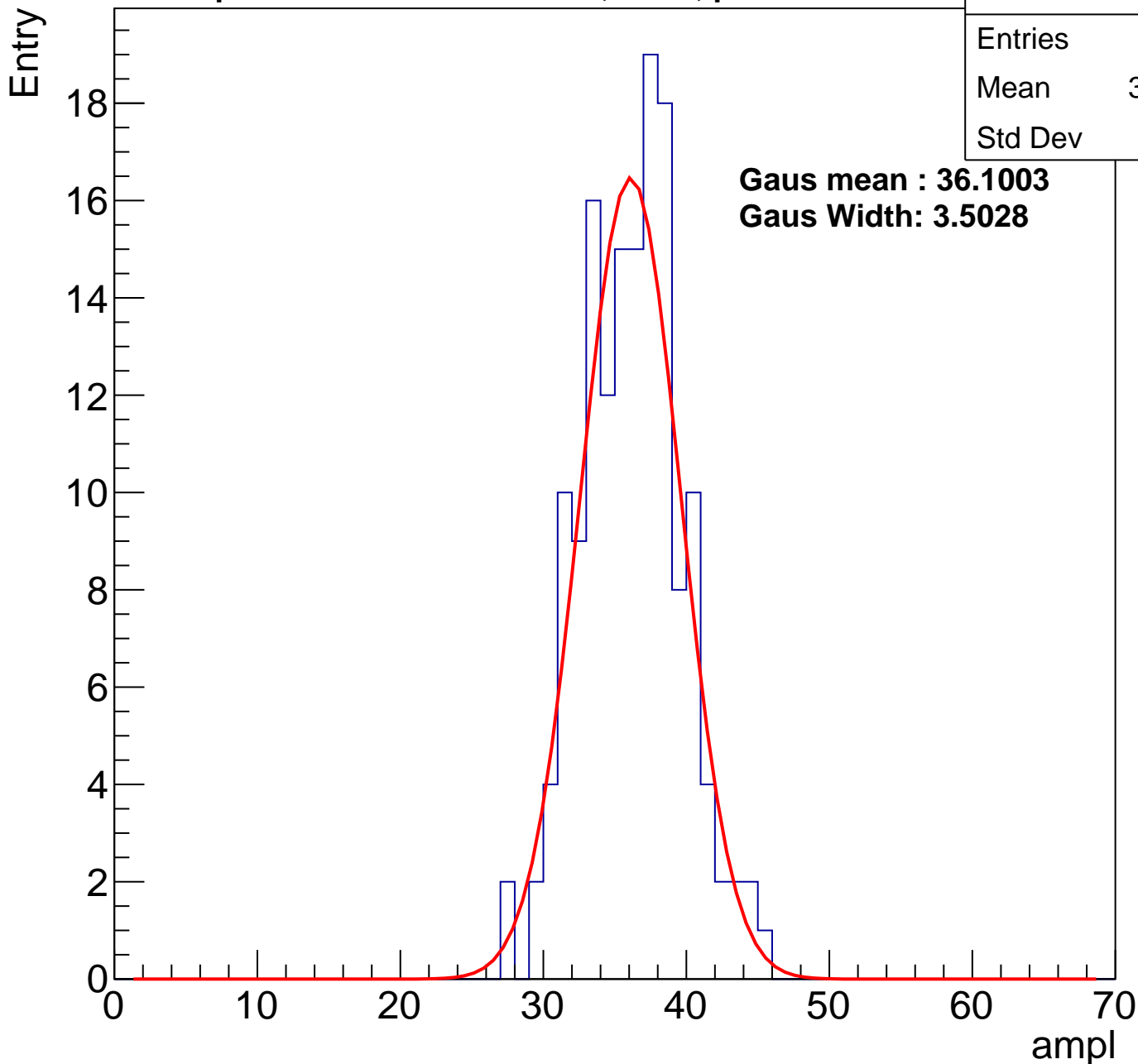


B1L001S, U22-ch83, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.74
Std Dev	3.45

Gaus mean : 36.1003
Gaus Width: 3.5028



B1L001S, U22-ch83, adc2

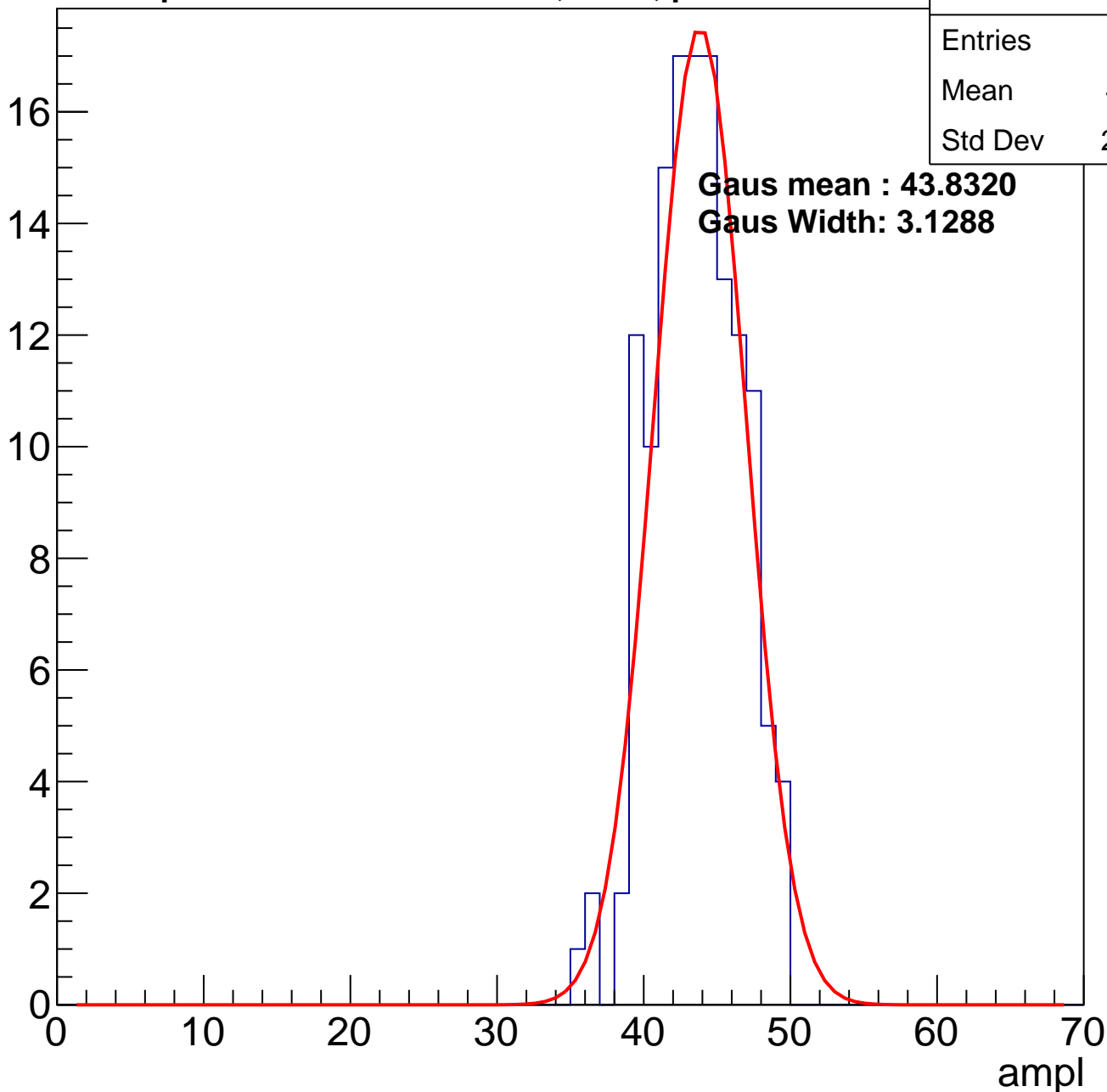
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	43.11
Std Dev	2.938

Gaus mean : 43.8320

Gaus Width: 3.1288

Entry



B1L001S, U22-ch83, adc3

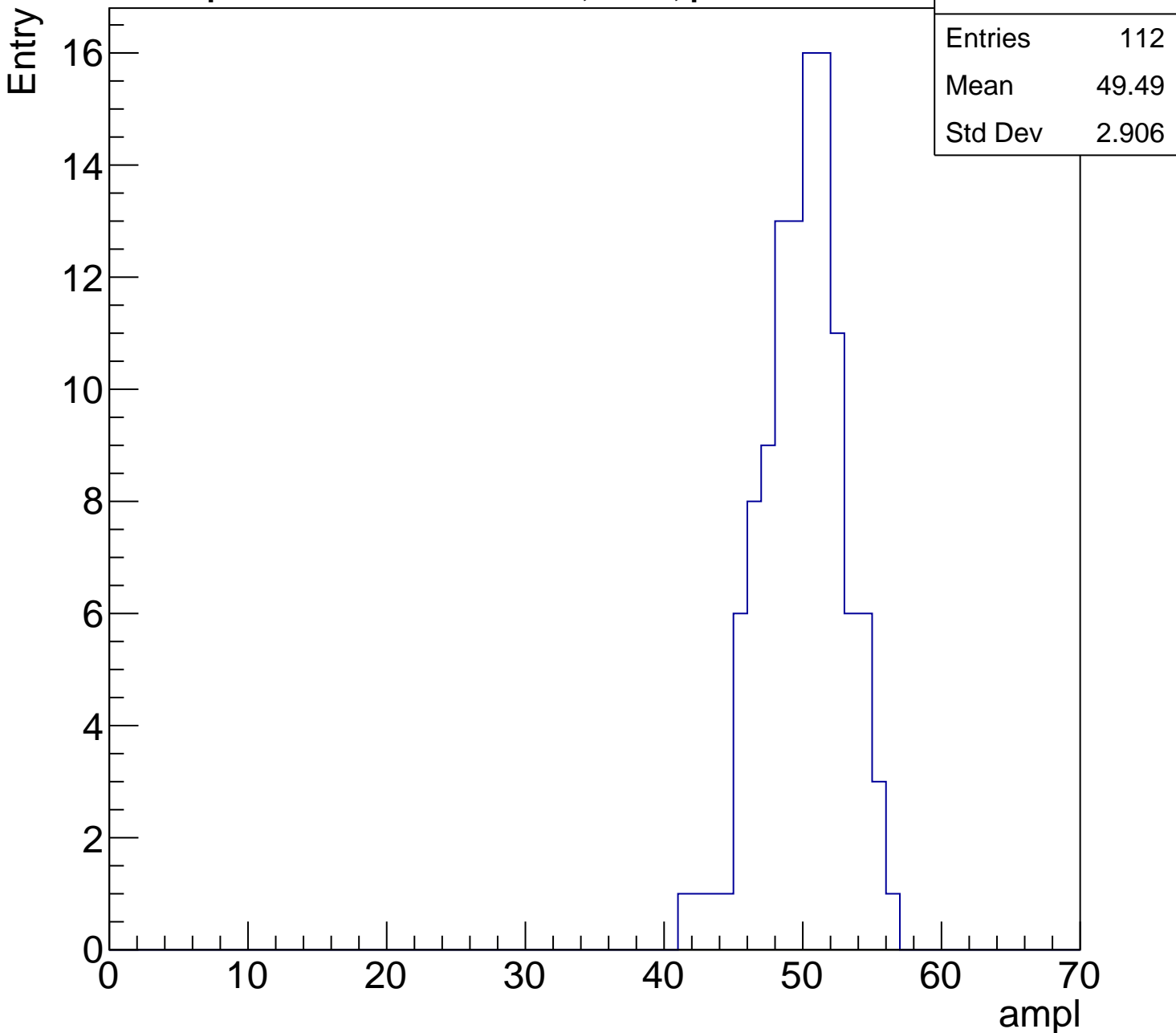
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	49.49
Std Dev	2.906

ampl



B1L001S, U22-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

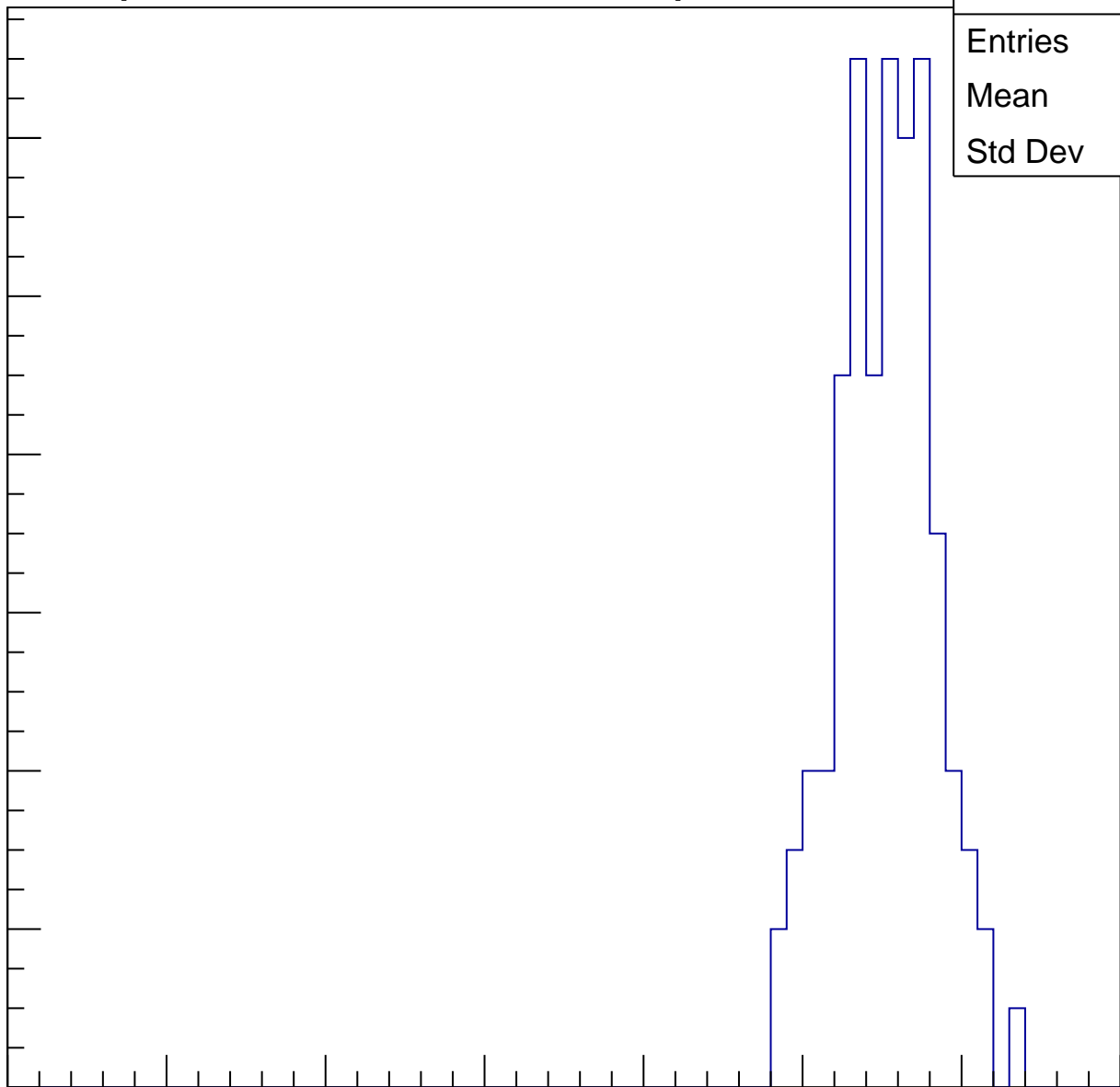
Entries	99
Mean	54.8
Std Dev	3.032

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

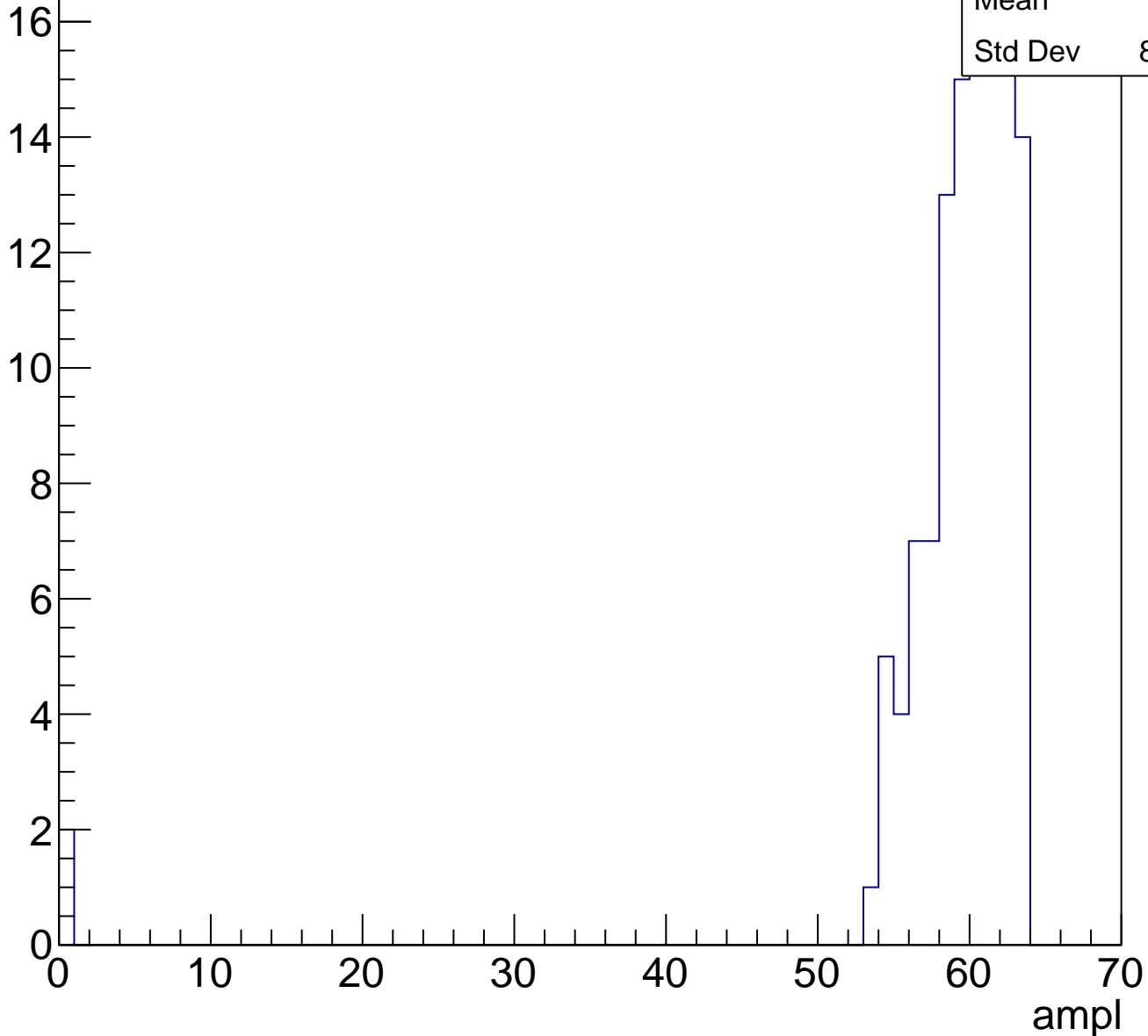
ampl



B1L001S, U22-ch83, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

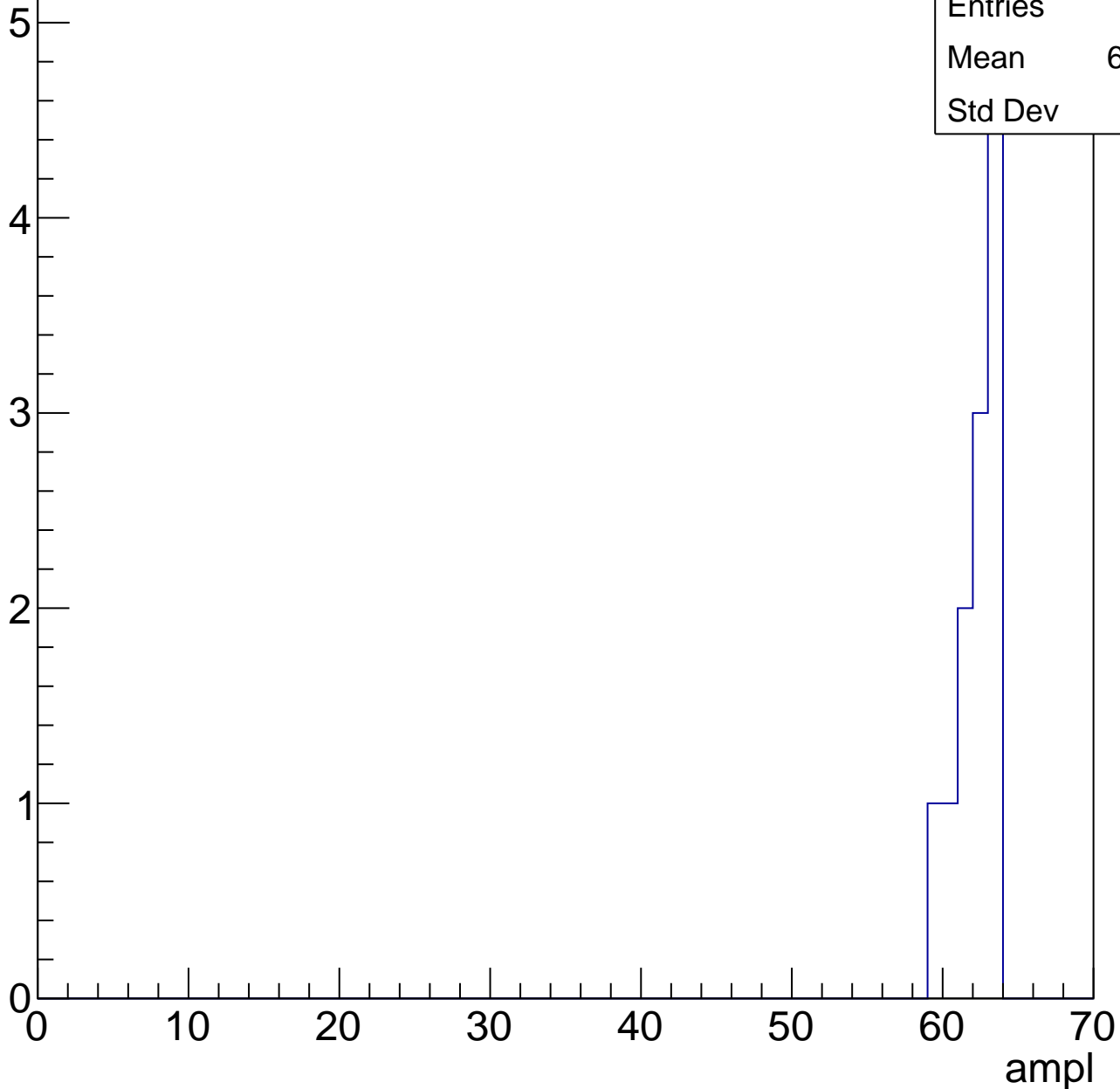


B1L001S, U22-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.83
Std Dev	1.28



B1L001S, U22-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

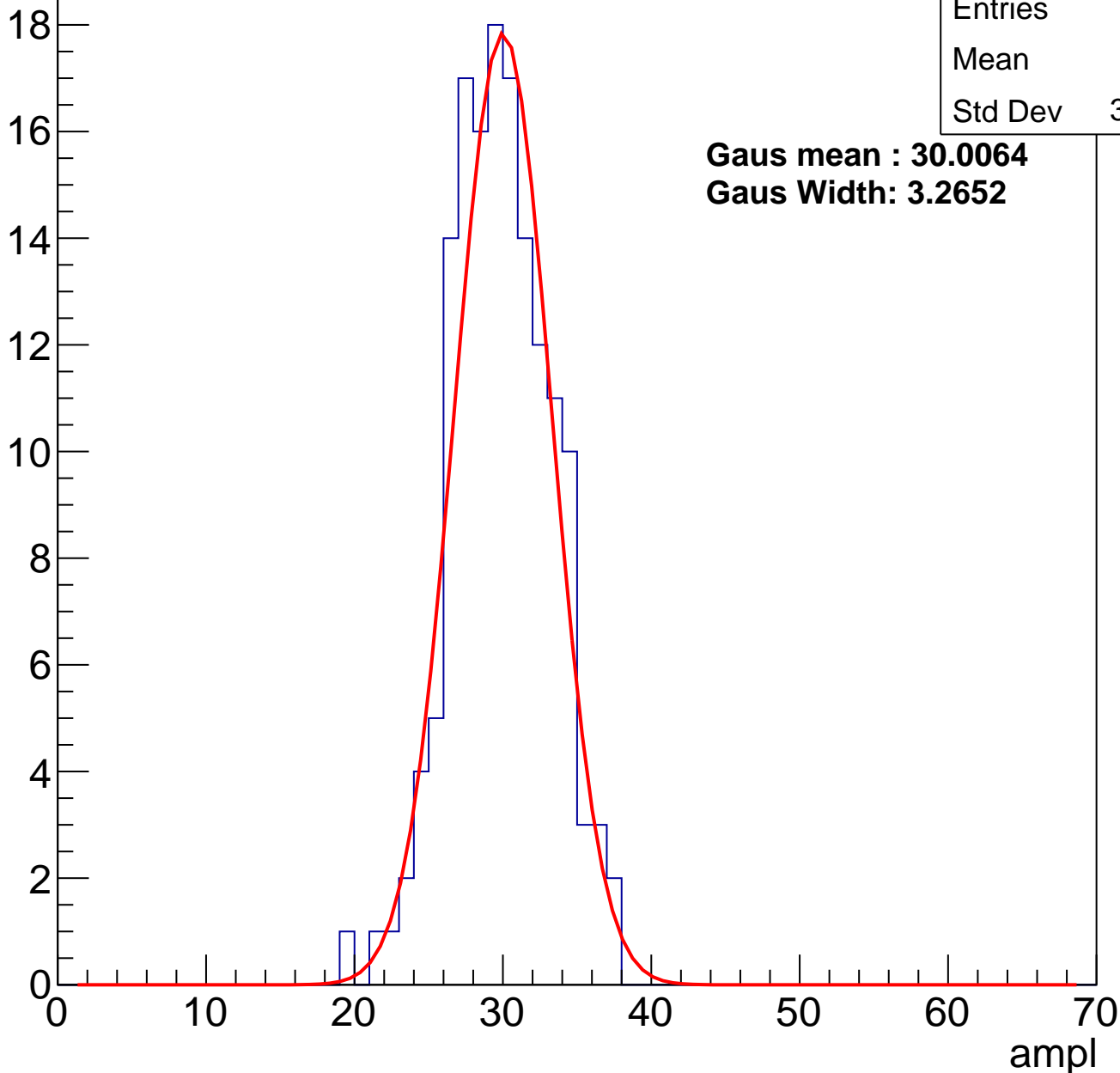
ampl



B1L001S, U22-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch84, adc1

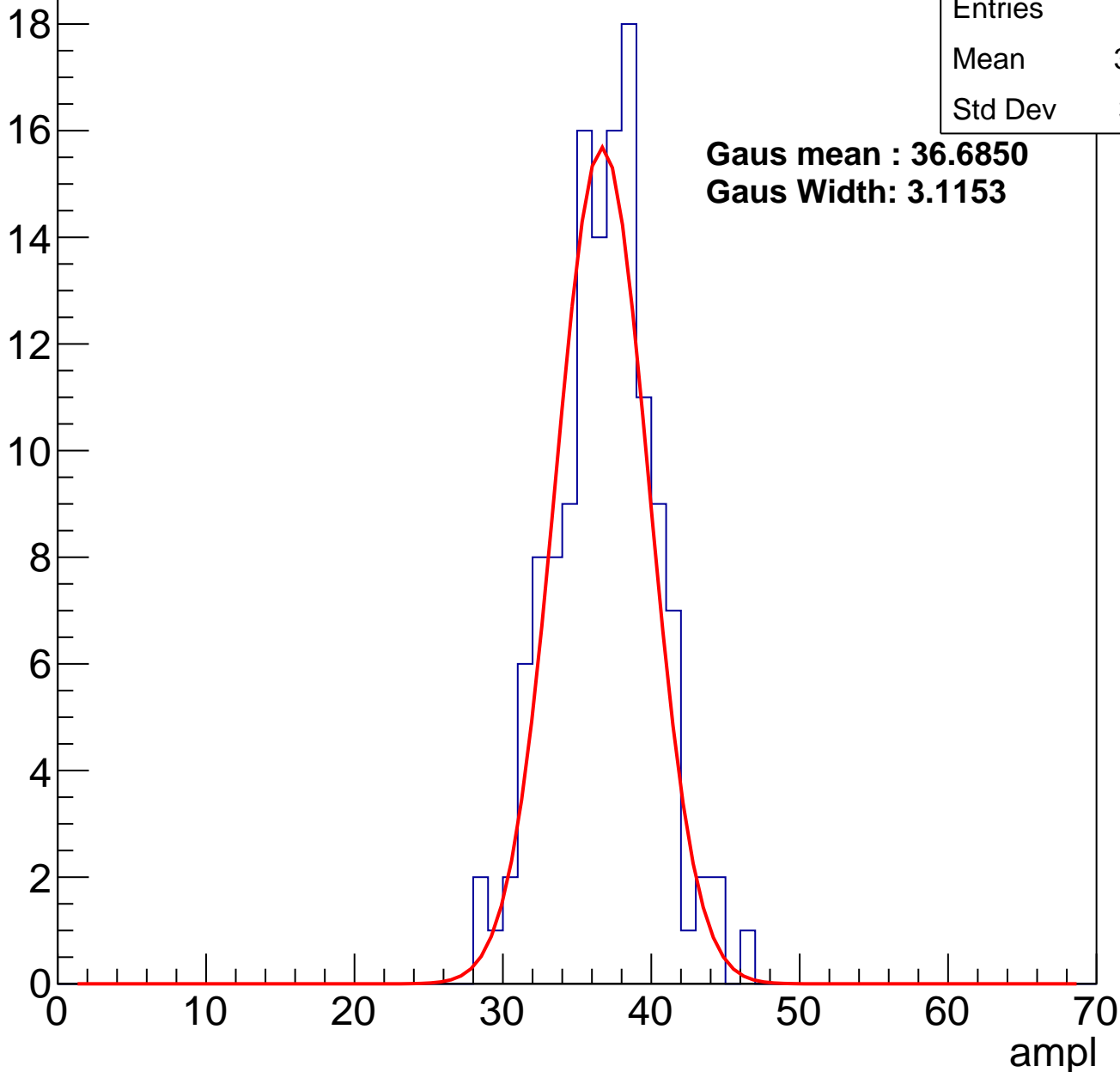
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.35
Std Dev	3.361

Gaus mean : 36.6850

Gaus Width: 3.1153

Entry



B1L001S, U22-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	43.18
Std Dev	3.375

Gaus mean : 43.7371

Gaus Width: 3.1803

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

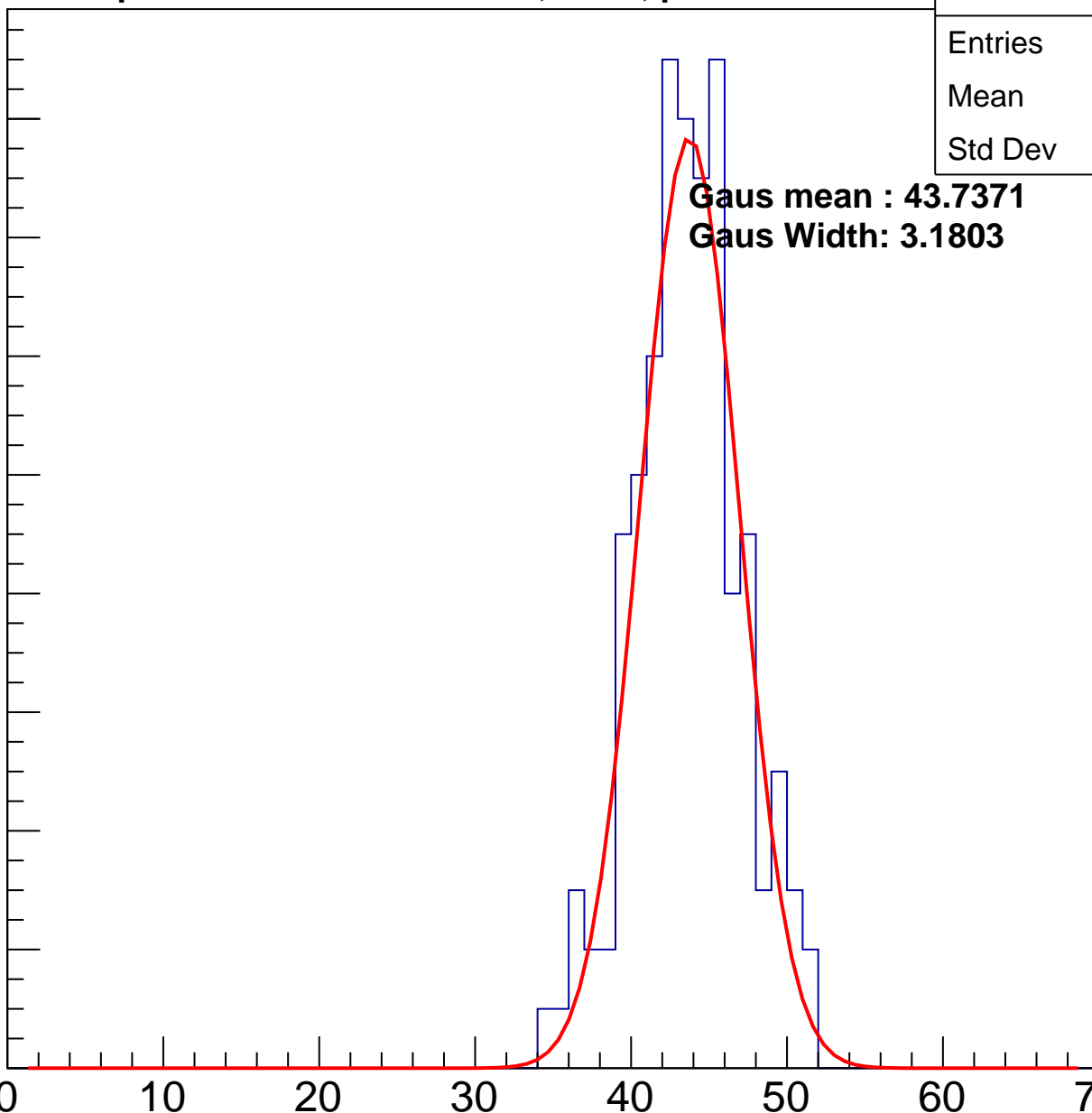
40

50

60

70

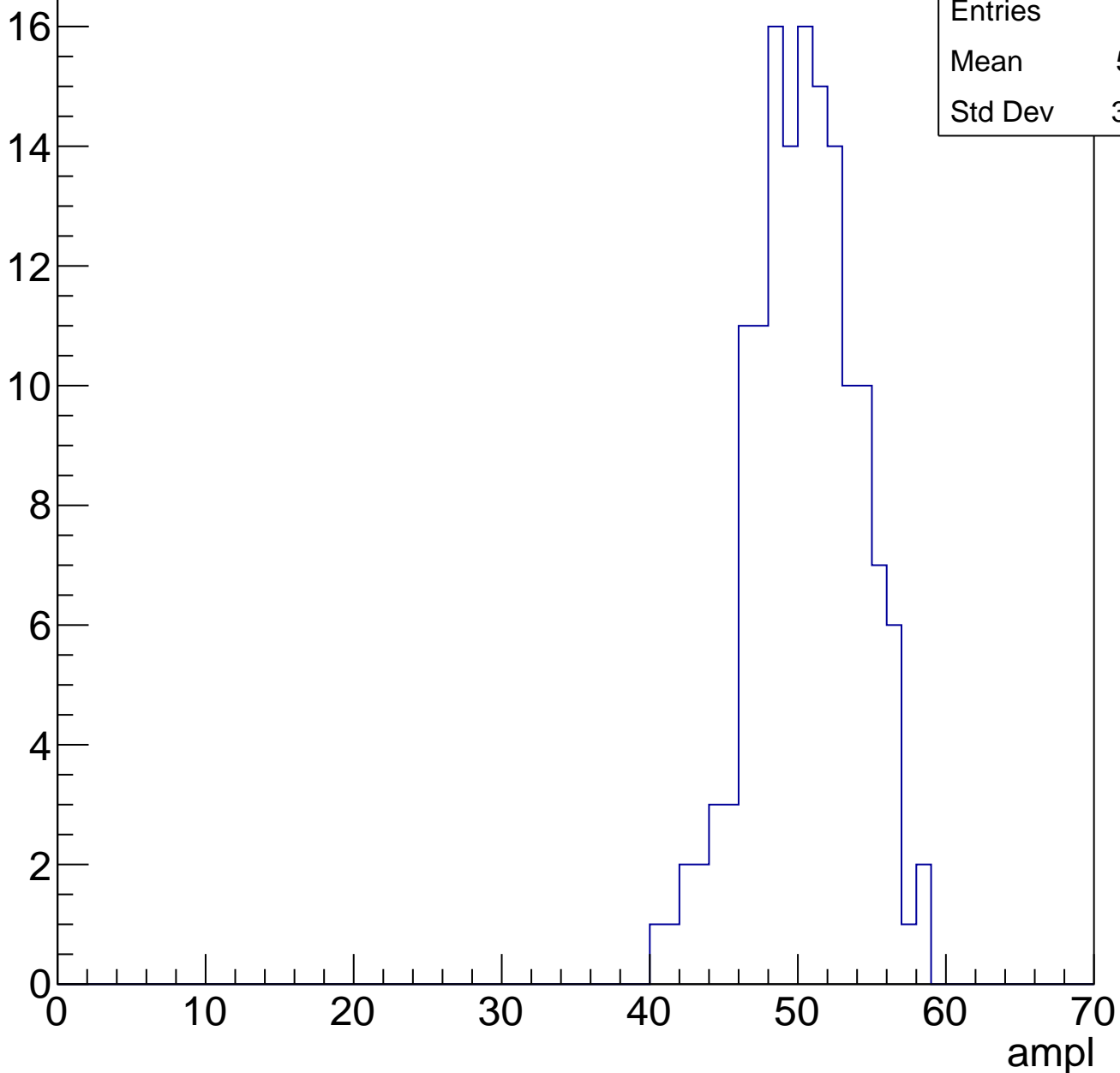
ampl



B1L001S, U22-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

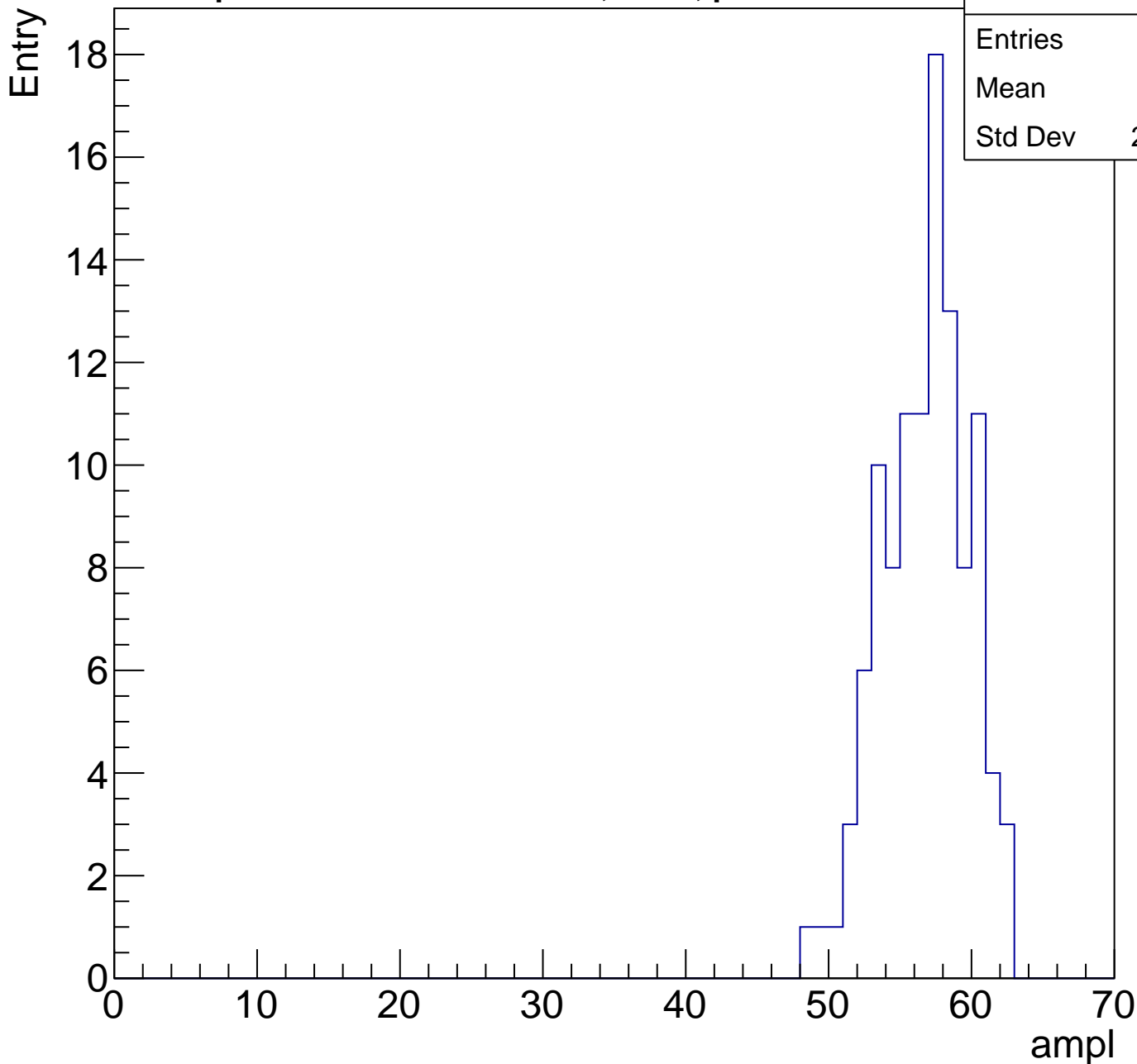
Entry



B1L001S, U22-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	56.3
Std Dev	2.982



B1L001S, U22-ch84, adc5

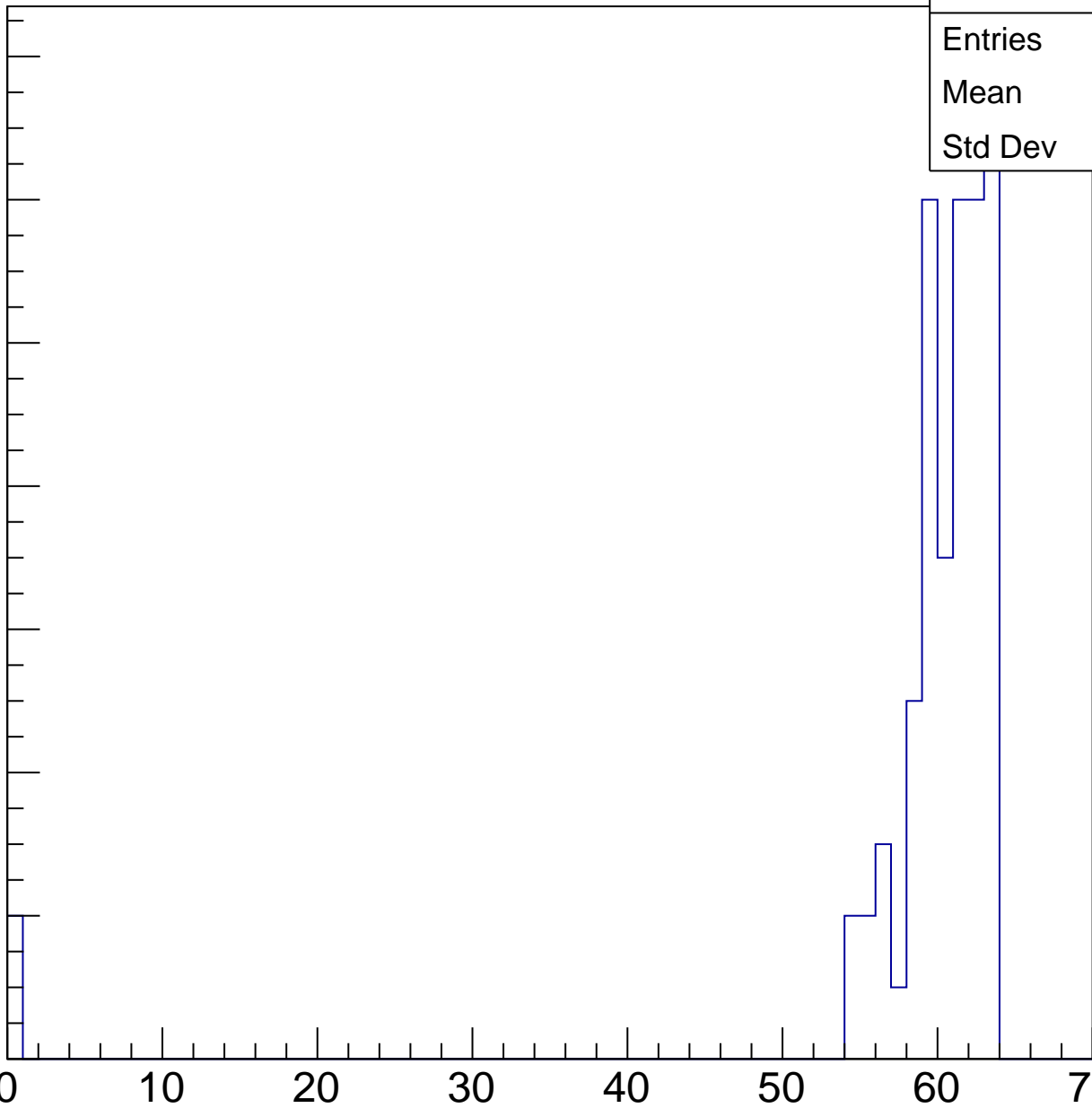
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.6
Std Dev	10.18

ampl

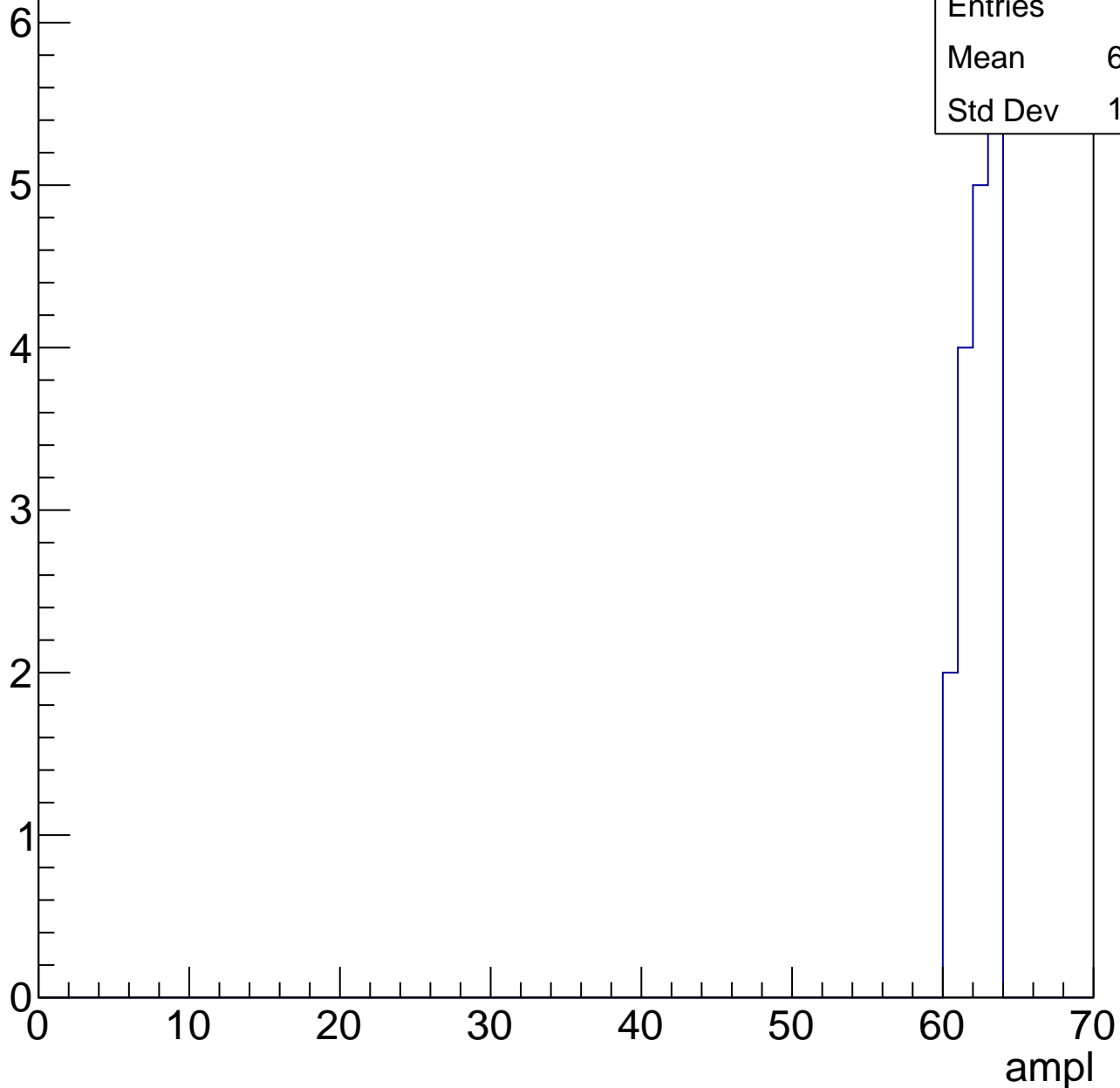


B1L001S, U22-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	61.88
Std Dev	1.022



B1L001S, U22-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch85, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.75
Std Dev	3.367

Gaus mean : 31.2287

Gaus Width: 3.4472

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

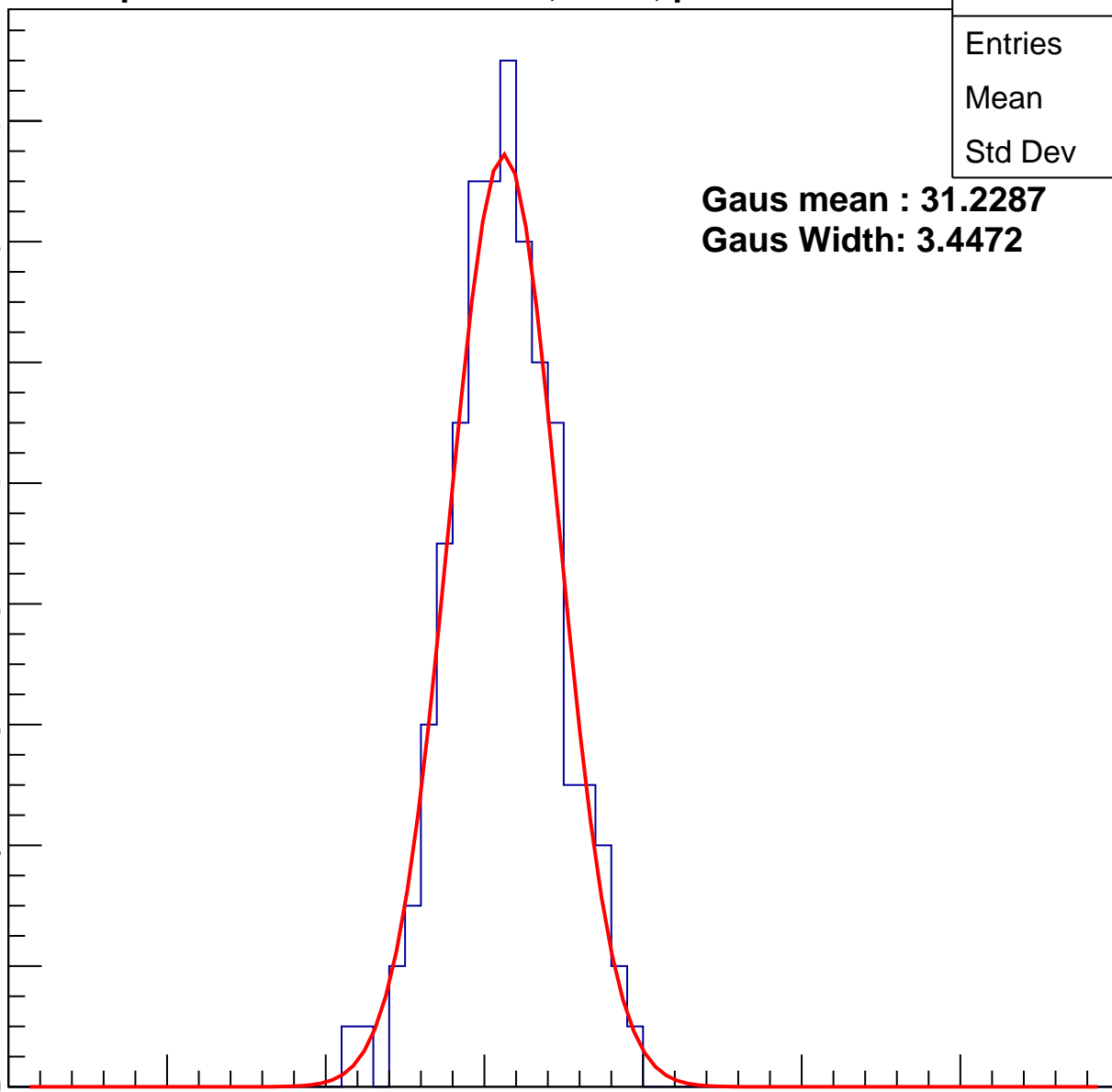
40

50

60

70

ampl



B1L001S, U22-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.99
Std Dev	3.446

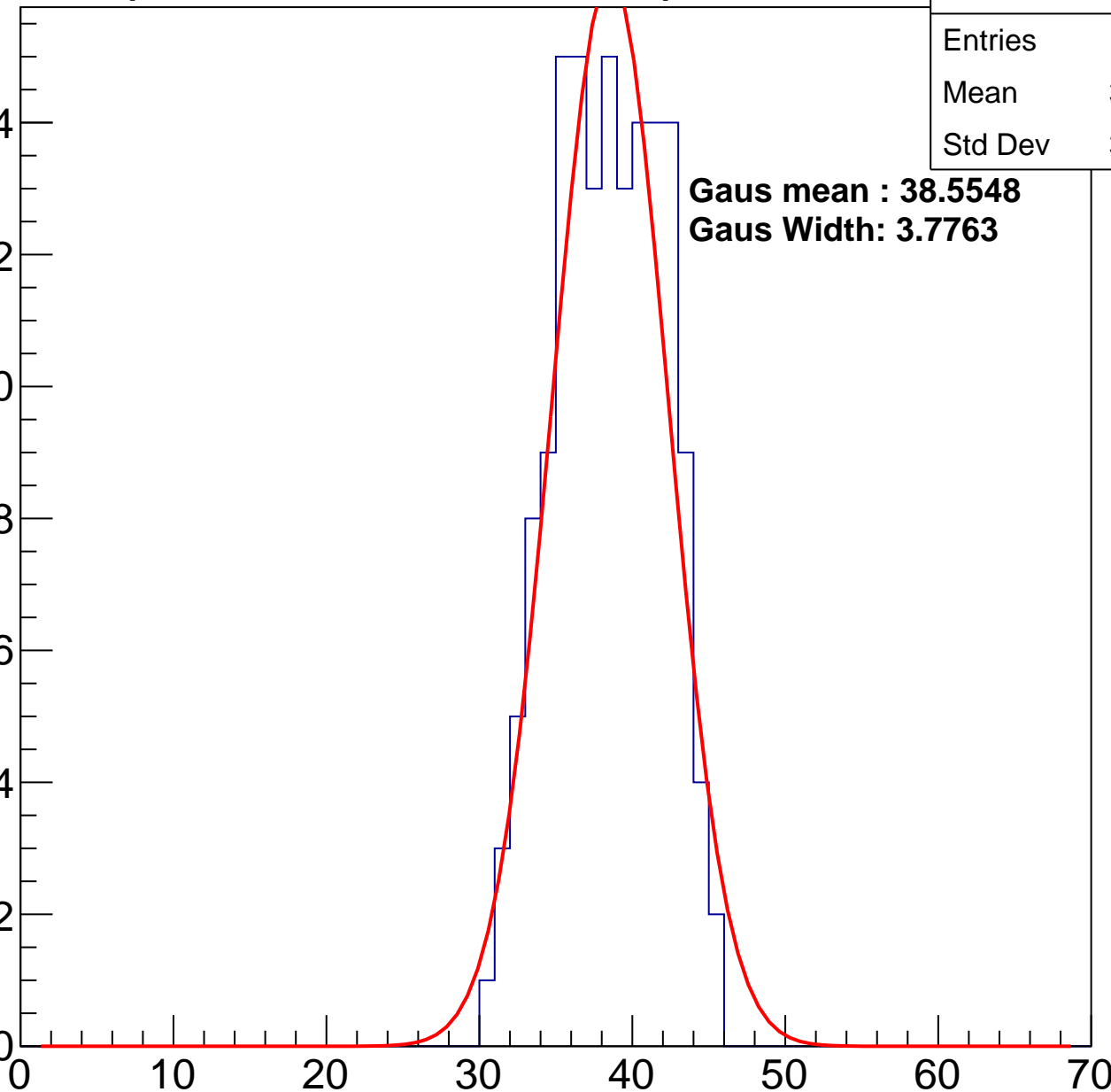
Gaus mean : 38.5548

Gaus Width: 3.7763

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	45.33
Std Dev	3.886

Gaus mean : 45.7796

Gaus Width: 3.9469

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

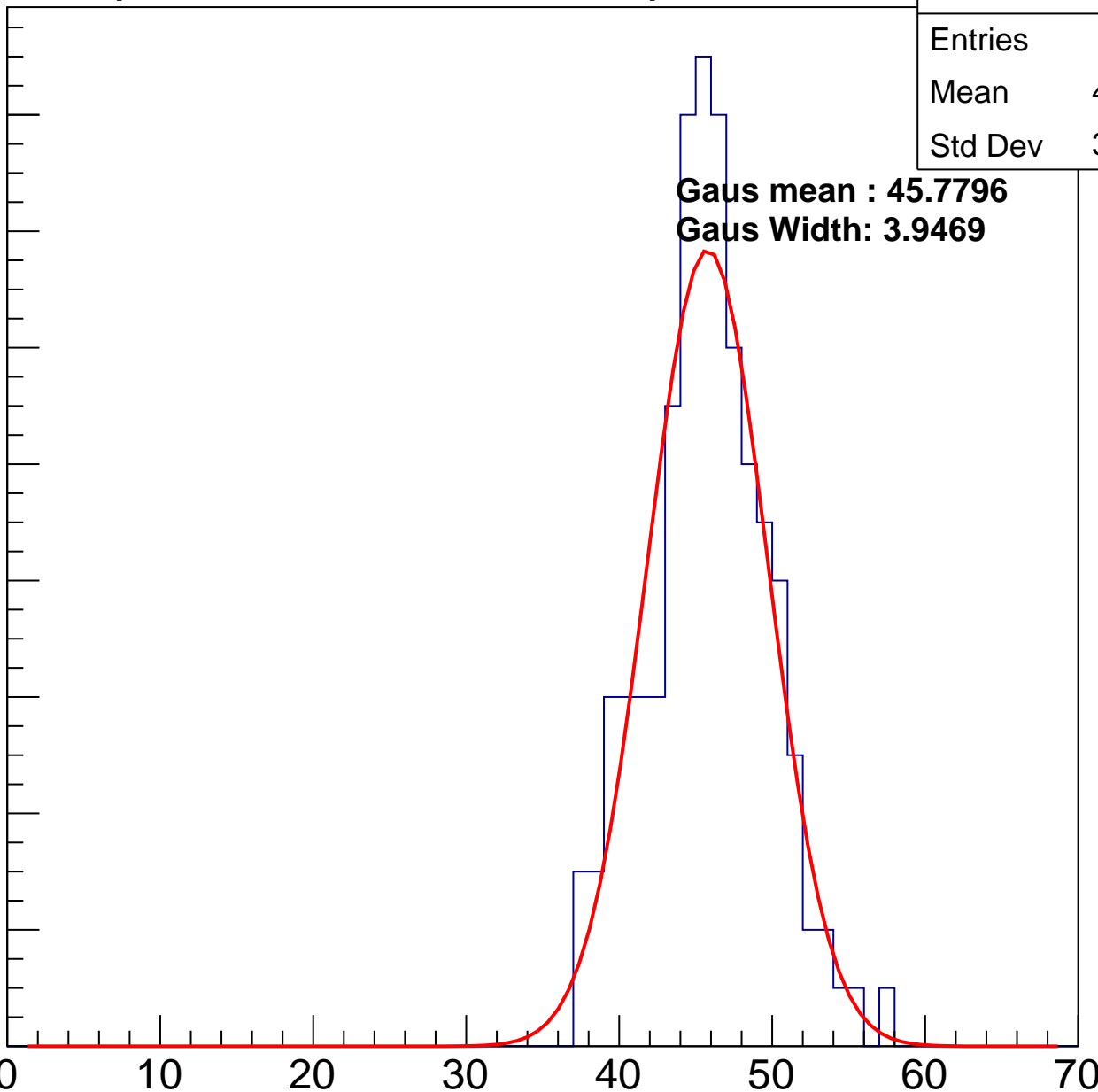
40

50

60

70

ampl



B1L001S, U22-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	113
Mean	51.77
Std Dev	3.446

B1L001S, U22-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

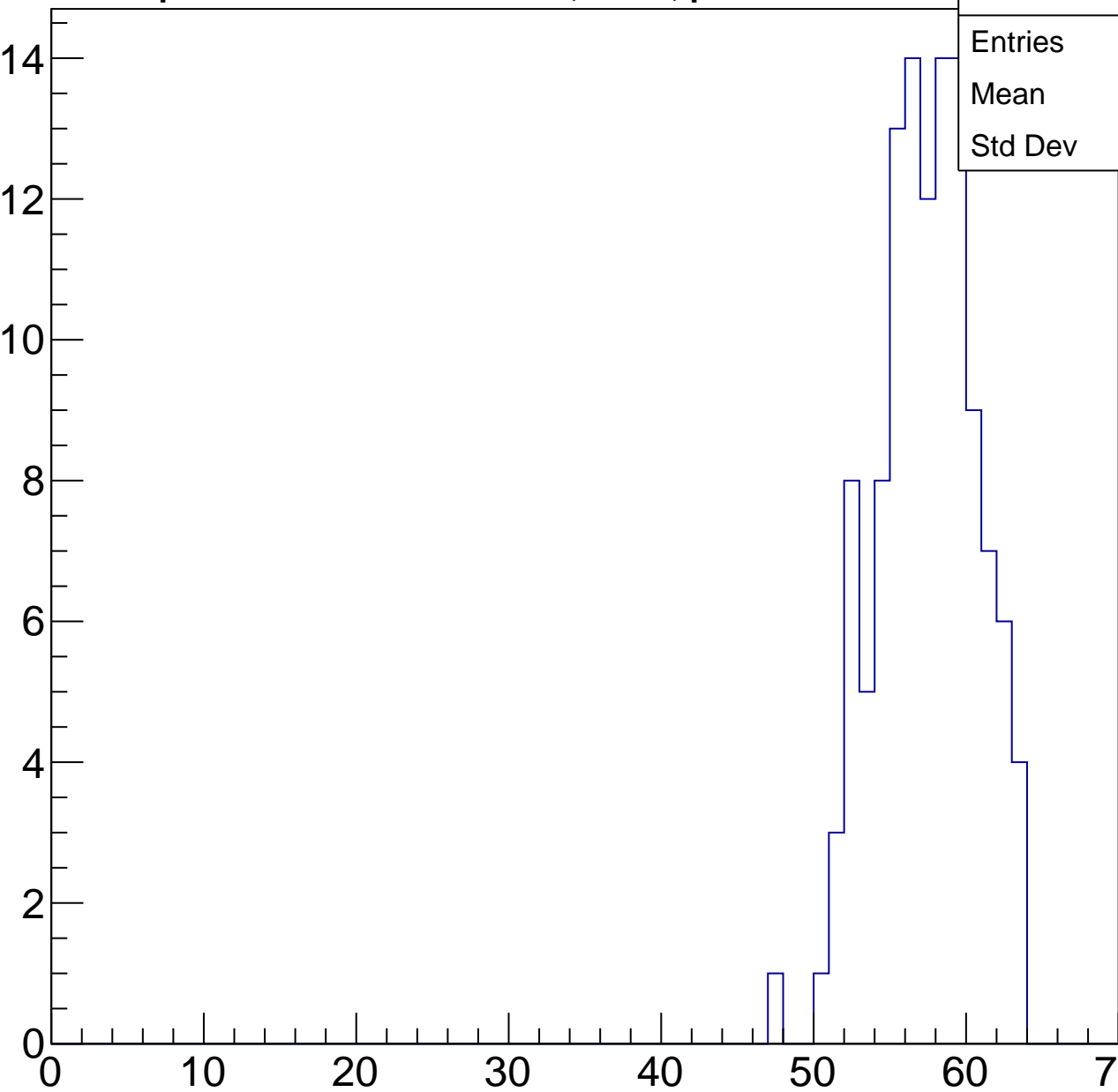
Entries	119
Mean	56.93
Std Dev	3.217

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch85, adc5

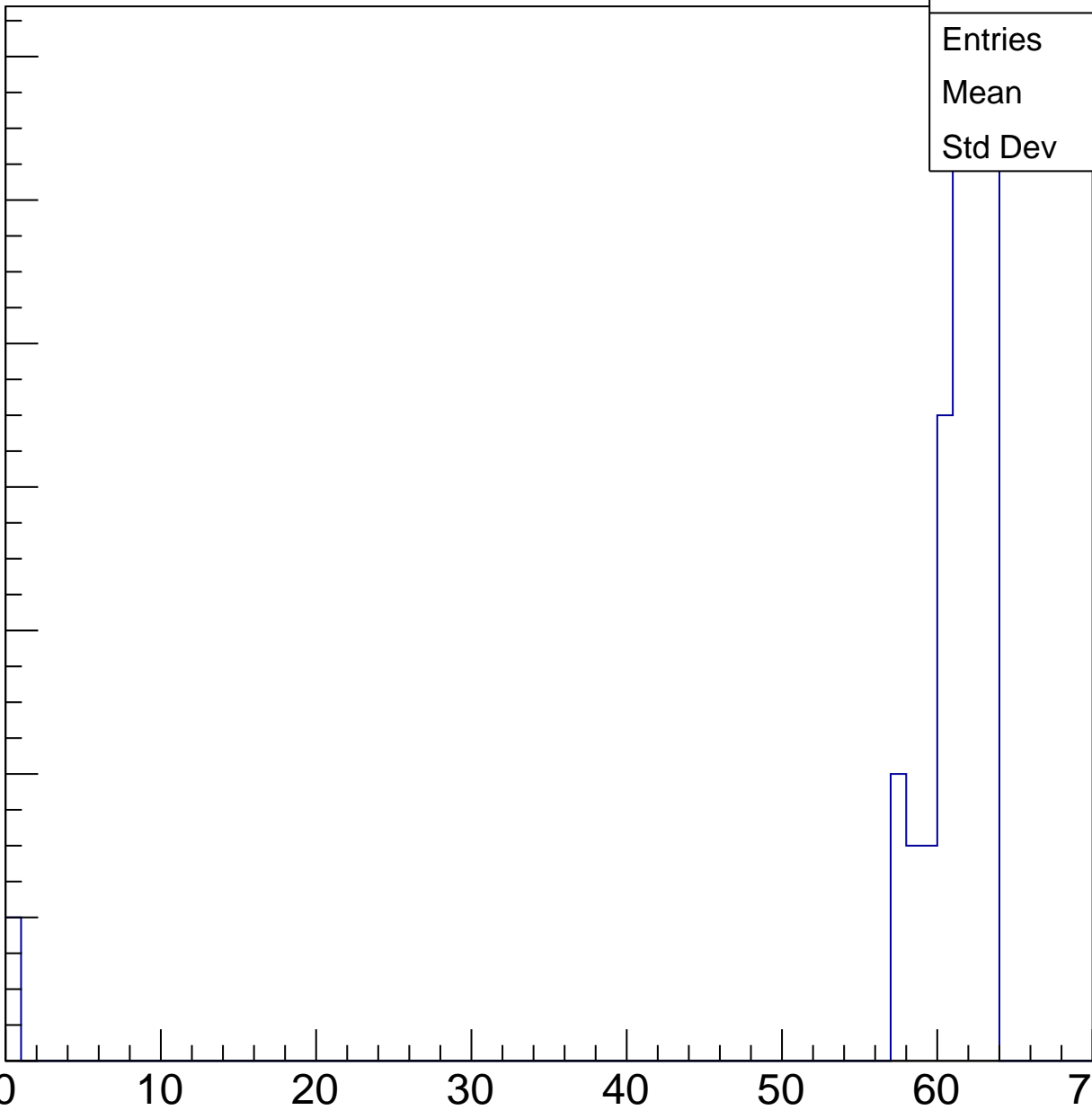
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	59
Std Dev	11

ampl



B1L001S, U22-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U22-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	28.85
Std Dev	3.5

Gaus mean : 29.3772

Gaus Width: 3.5738

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.69
Std Dev	3.429

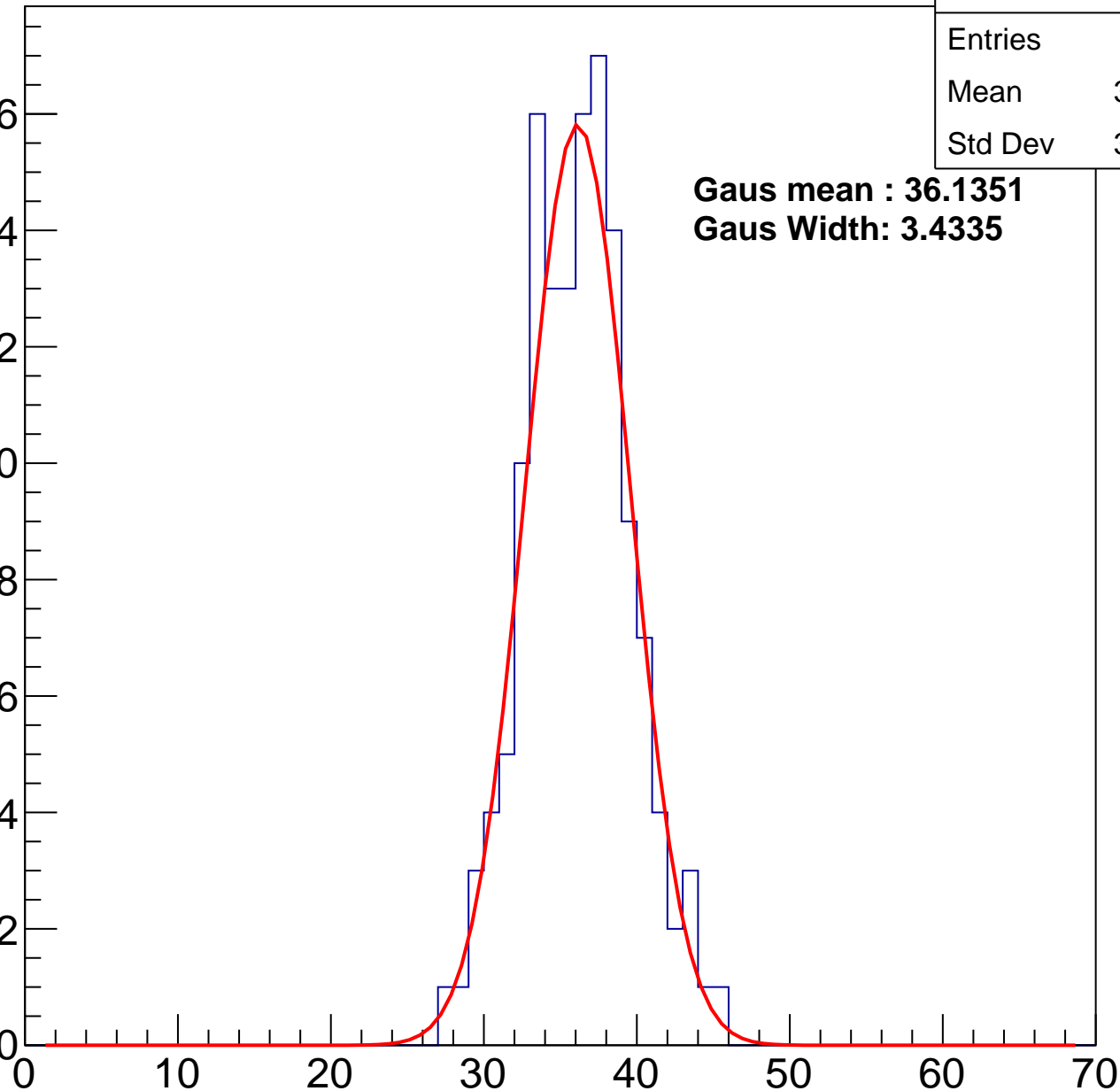
Gaus mean : 36.1351

Gaus Width: 3.4335

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	42.41
Std Dev	3.196

Gaus mean : 43.0838

Gaus Width: 3.2256

Entry

14

12

10

8

6

4

2

0

0

10

20

30

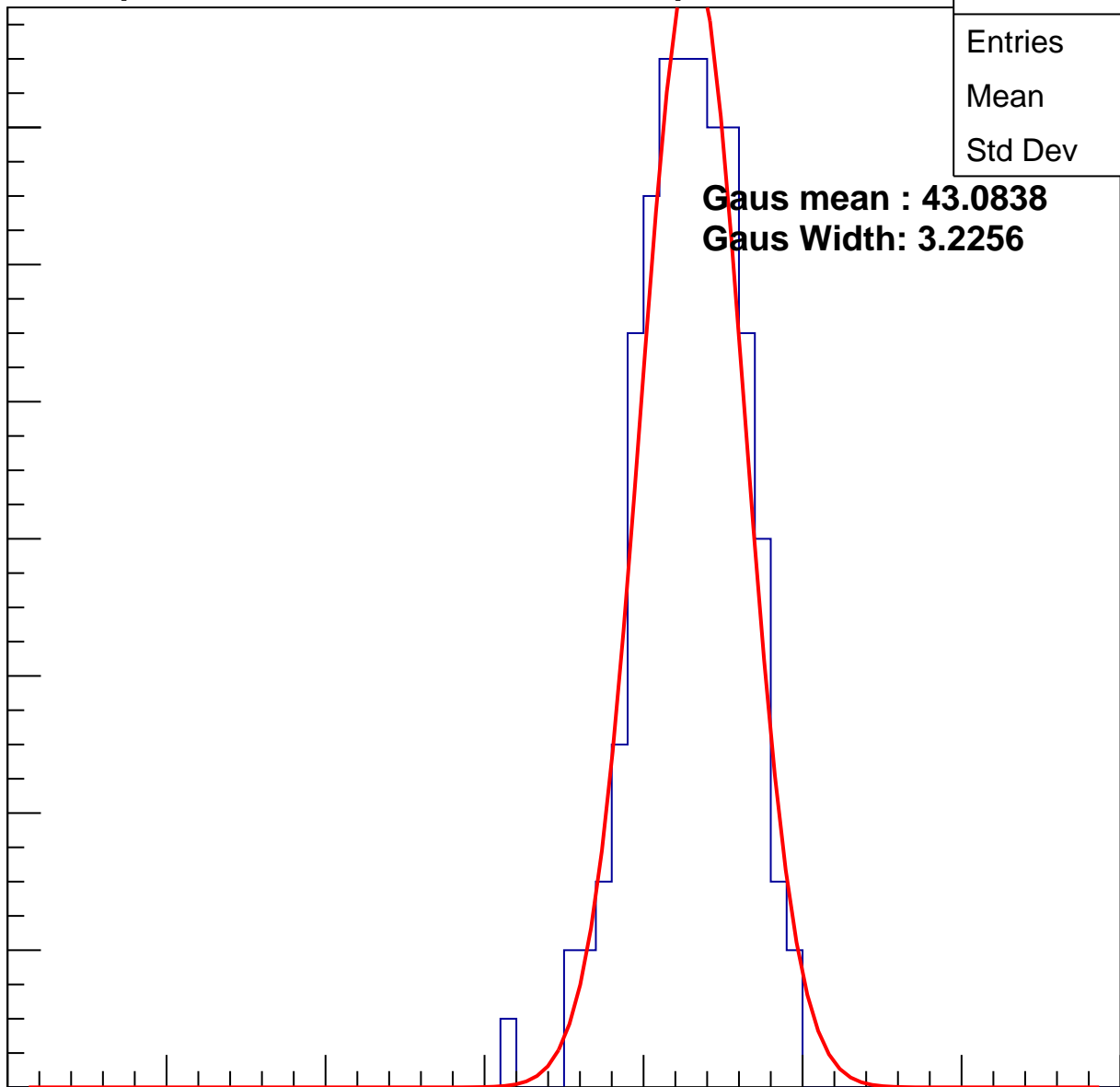
40

50

60

70

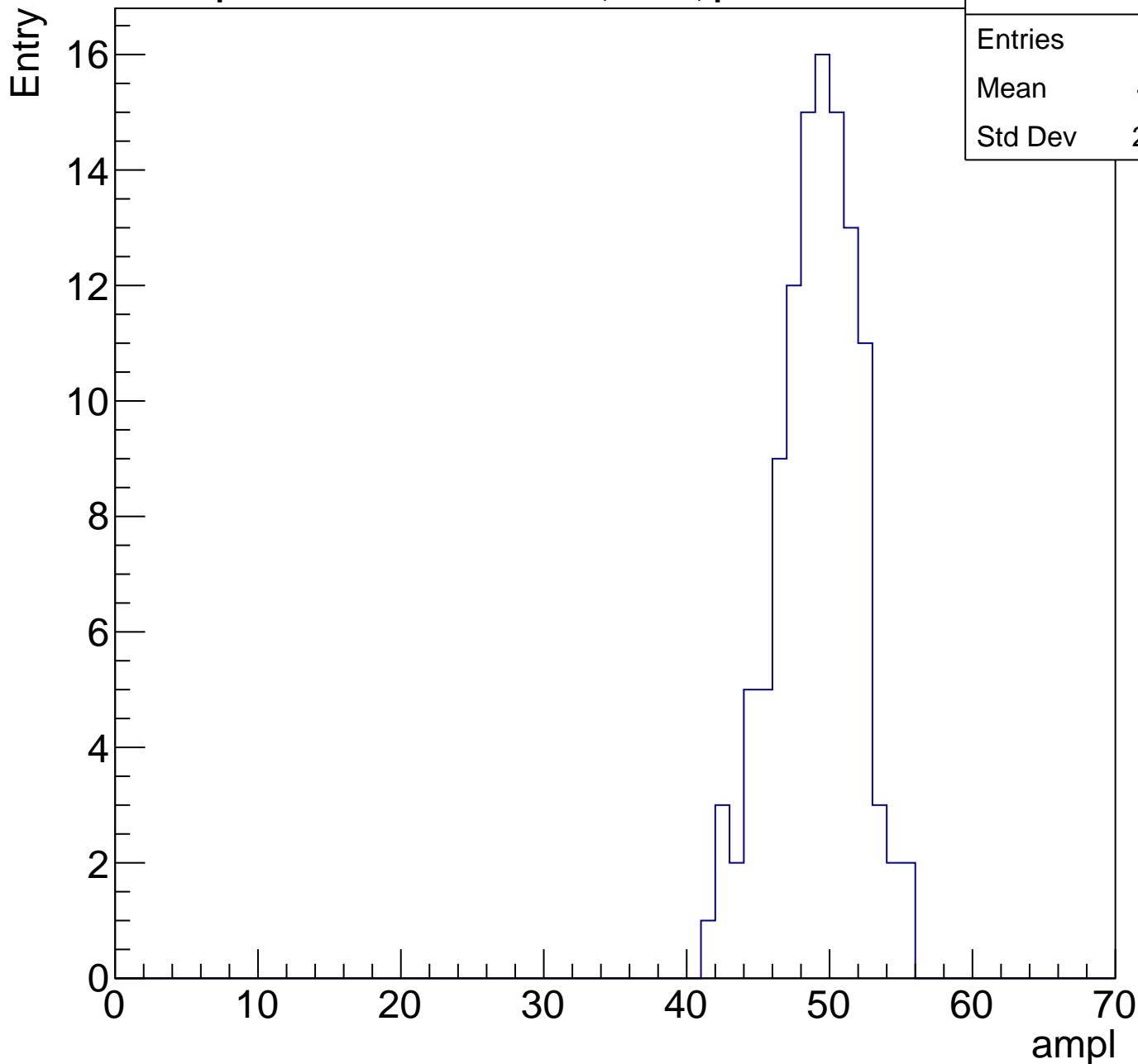
ampl



B1L001S, U22-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

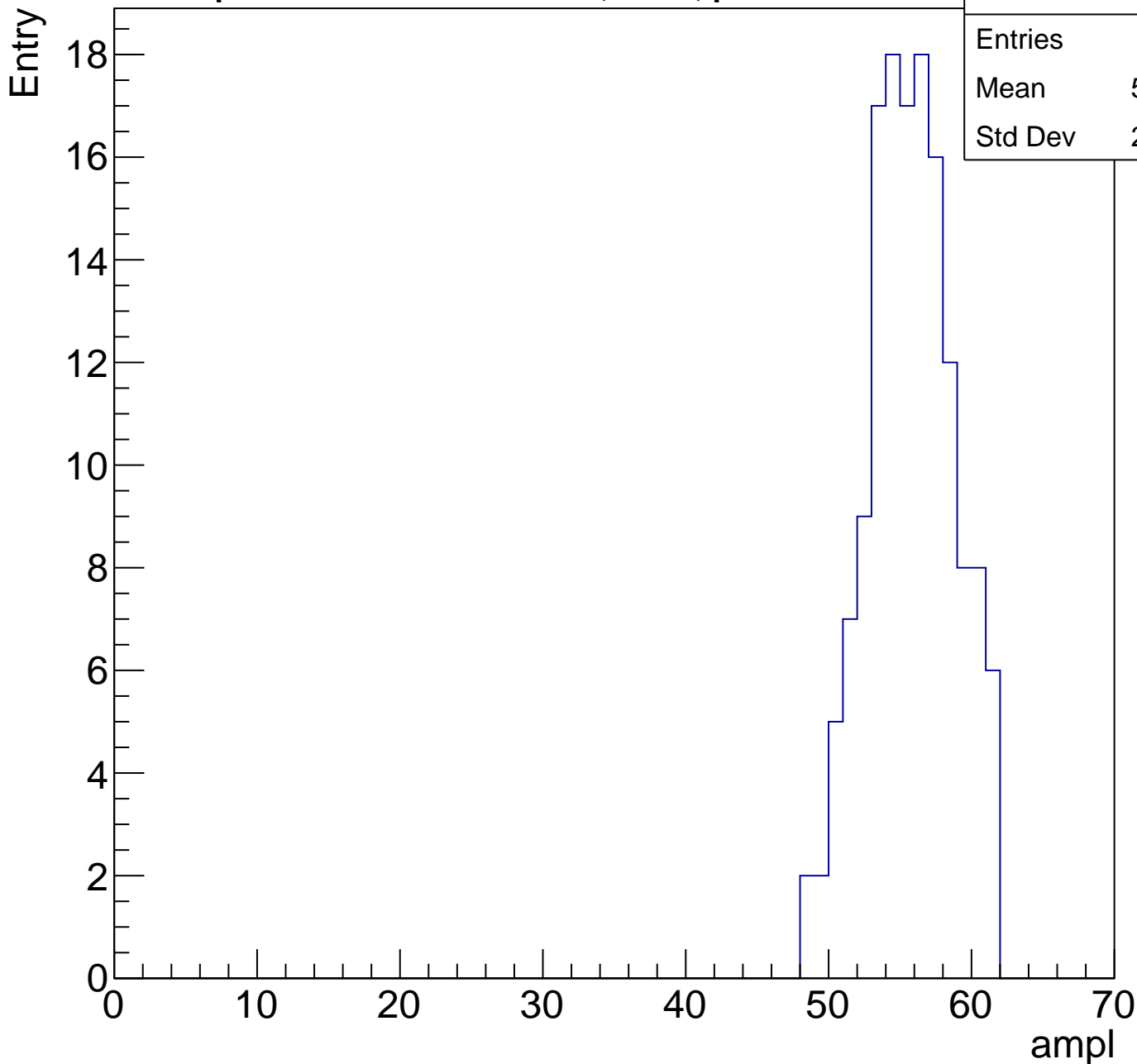
Entries	114
Mean	48.61
Std Dev	2.888



B1L001S, U22-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	55.25
Std Dev	2.993



B1L001S, U22-ch86, adc5

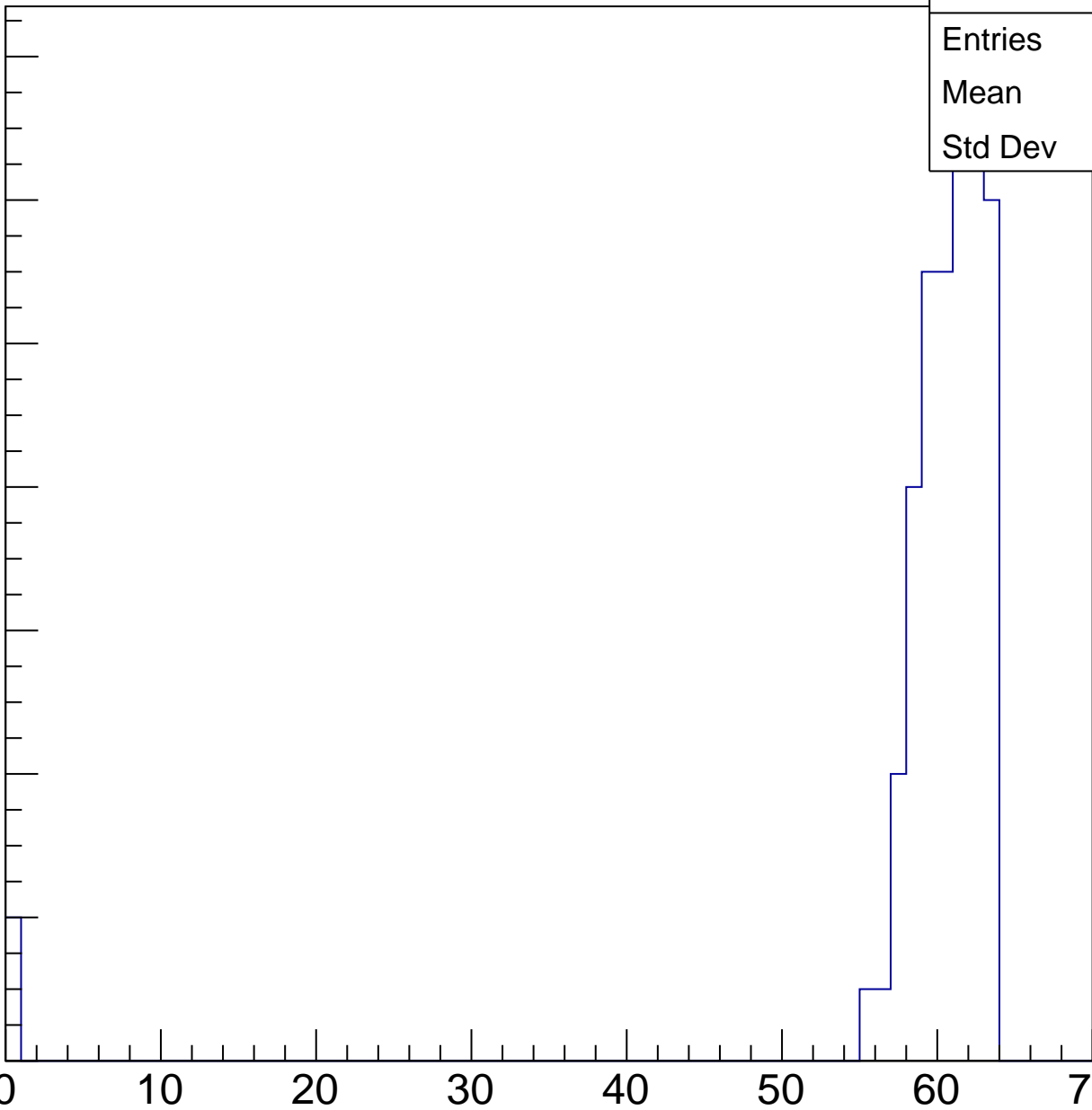
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.82
Std Dev	9.796

ampl

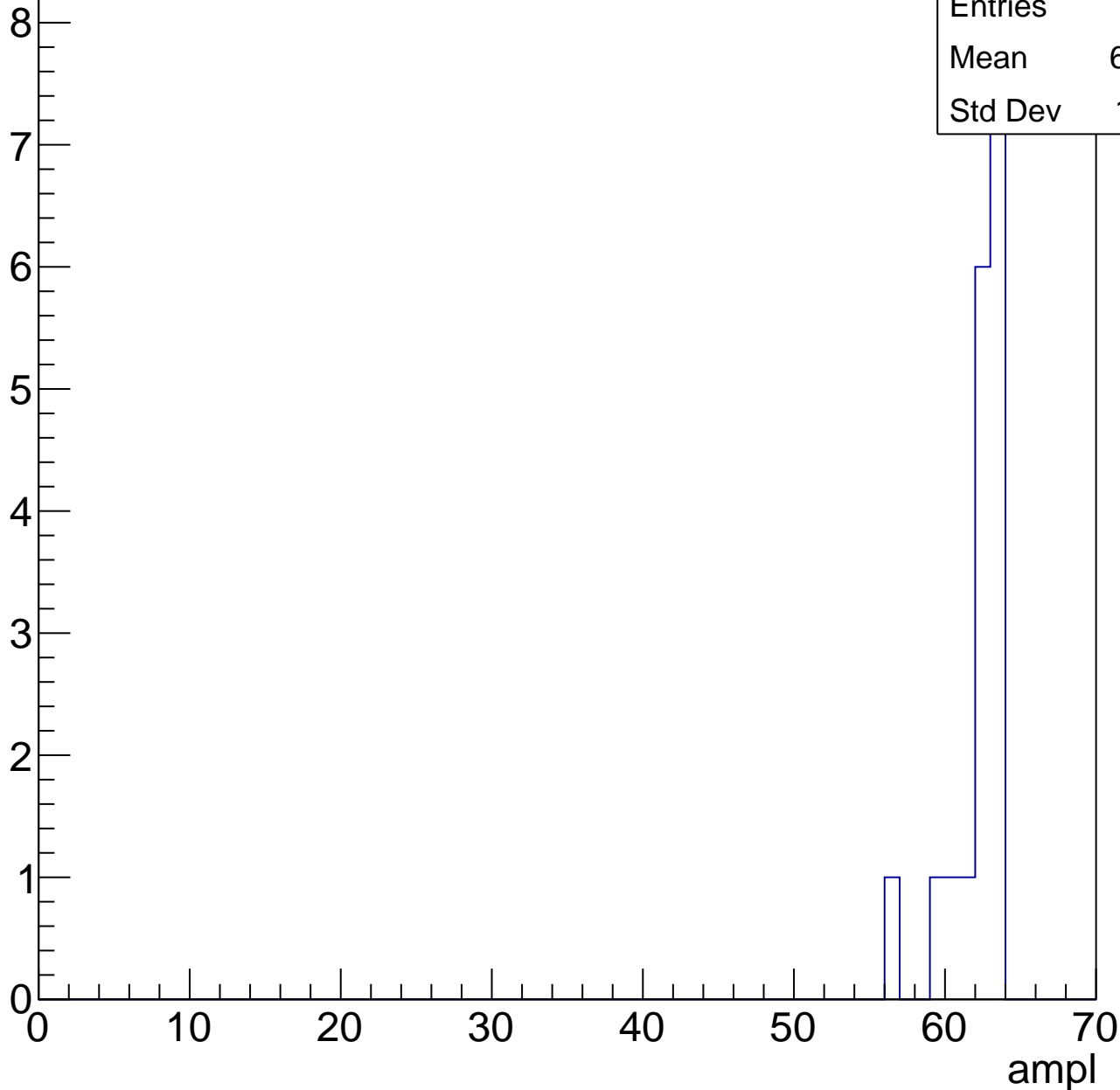


B1L001S, U22-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	61.78
Std Dev	1.781



B1L001S, U22-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



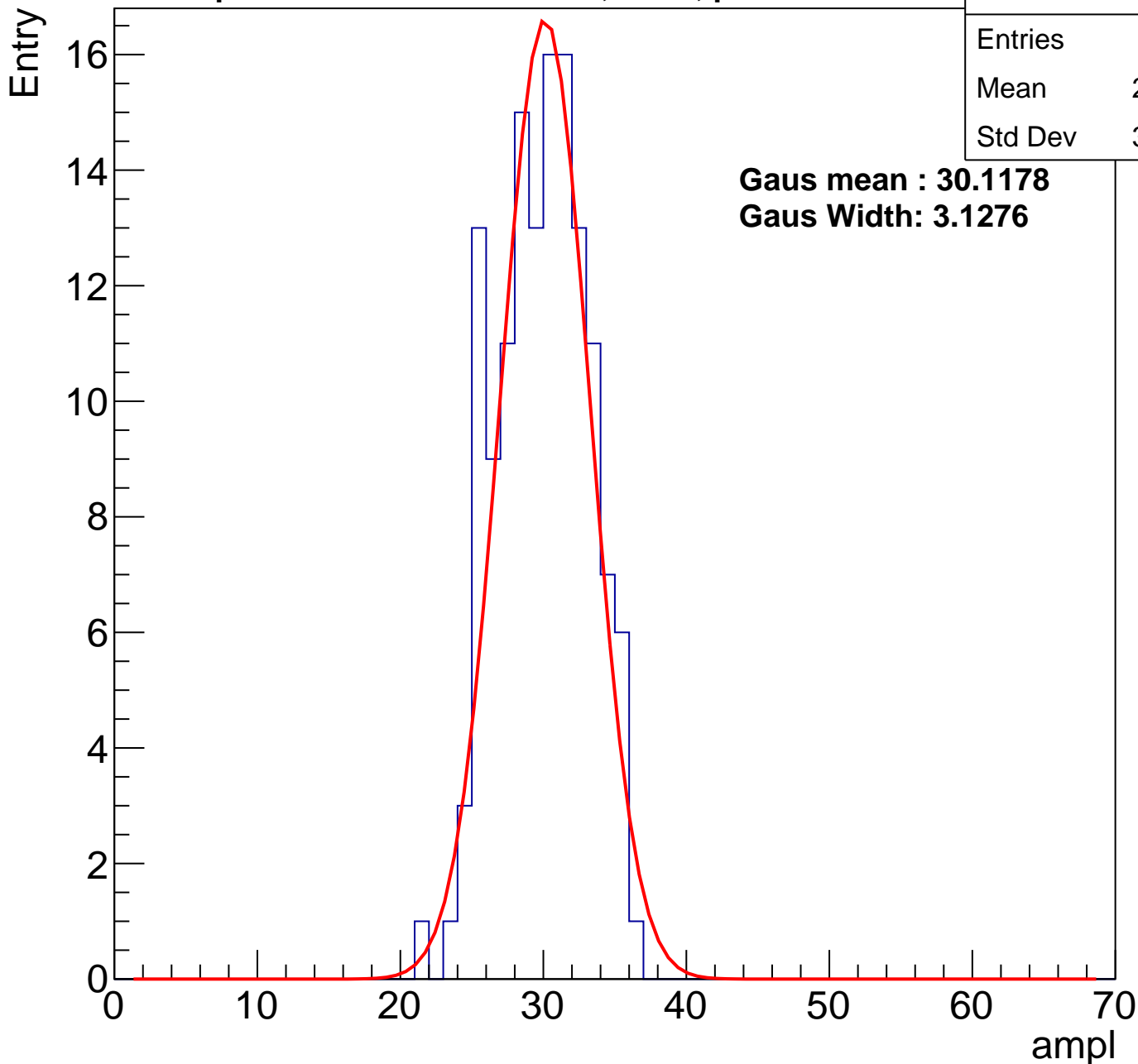
B1L001S, U22-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	29.47
Std Dev	3.099

Gaus mean : 30.1178

Gaus Width: 3.1276



B1L001S, U22-ch87, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	35.46
Std Dev	3.223

Gaus mean : 36.0274

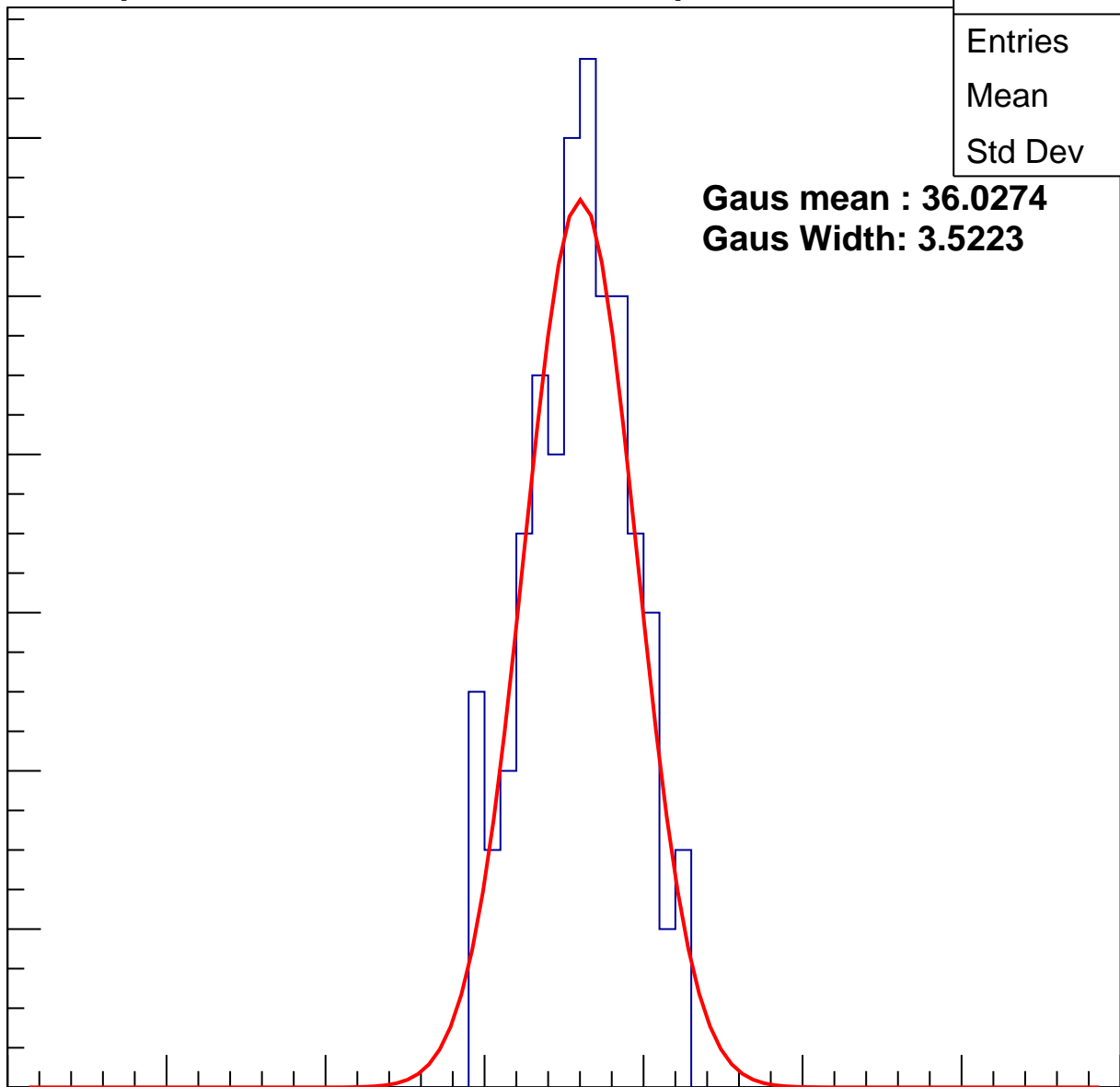
Gaus Width: 3.5223

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	41.8
Std Dev	3.821

Gaus mean : 42.2320

Gaus Width: 3.8620

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

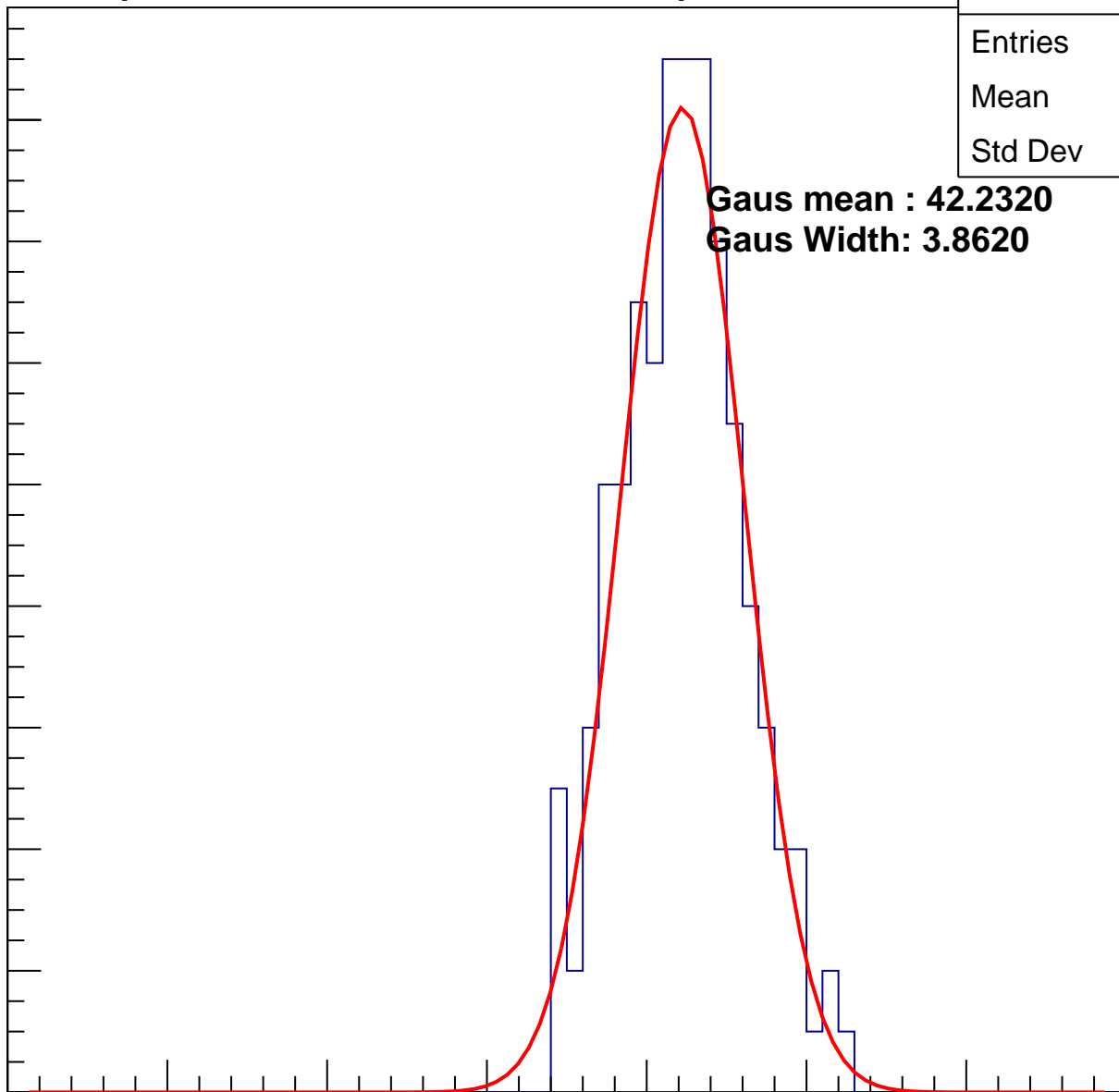
40

50

60

70

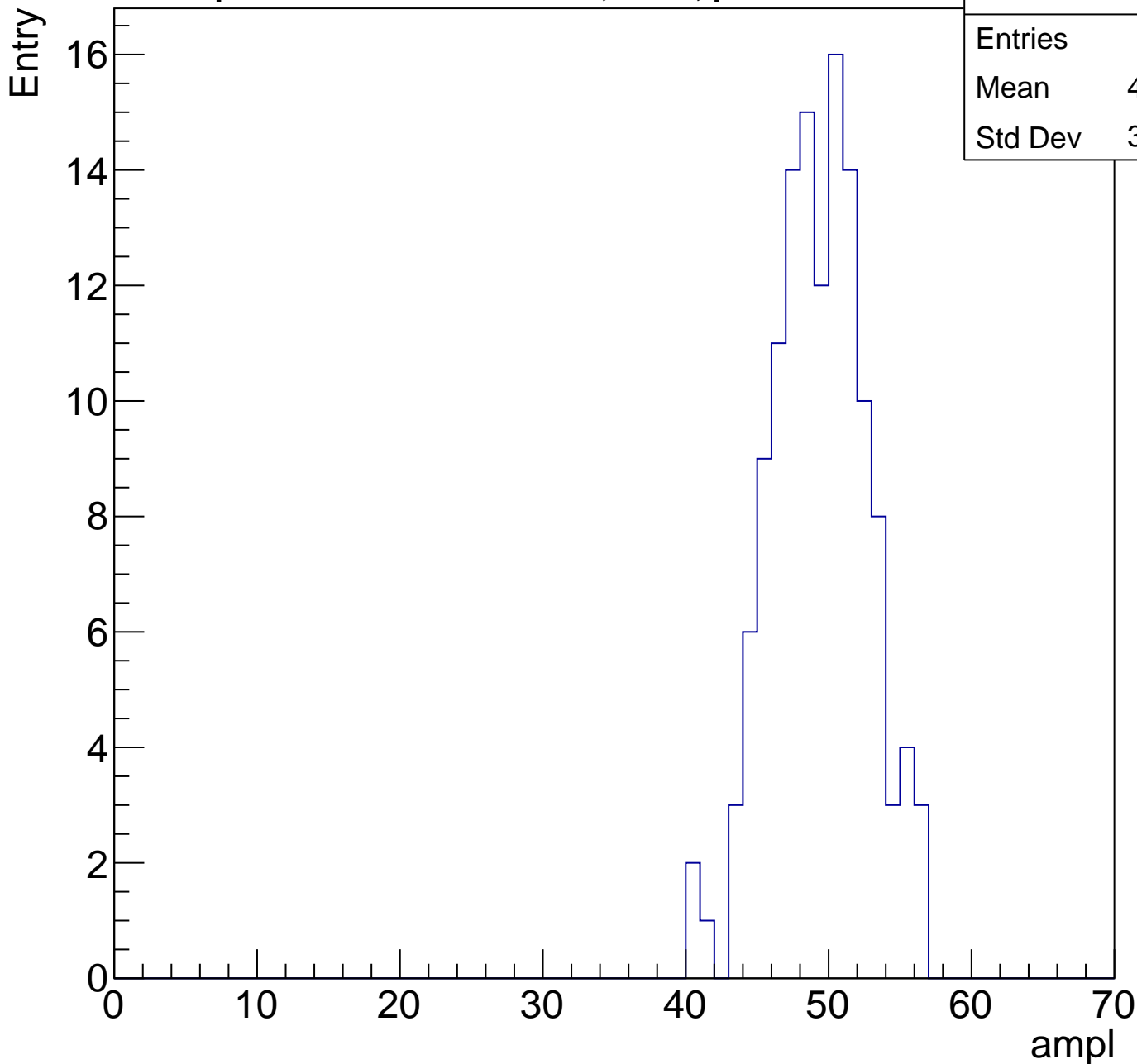
ampl



B1L001S, U22-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	48.85
Std Dev	3.328



B1L001S, U22-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

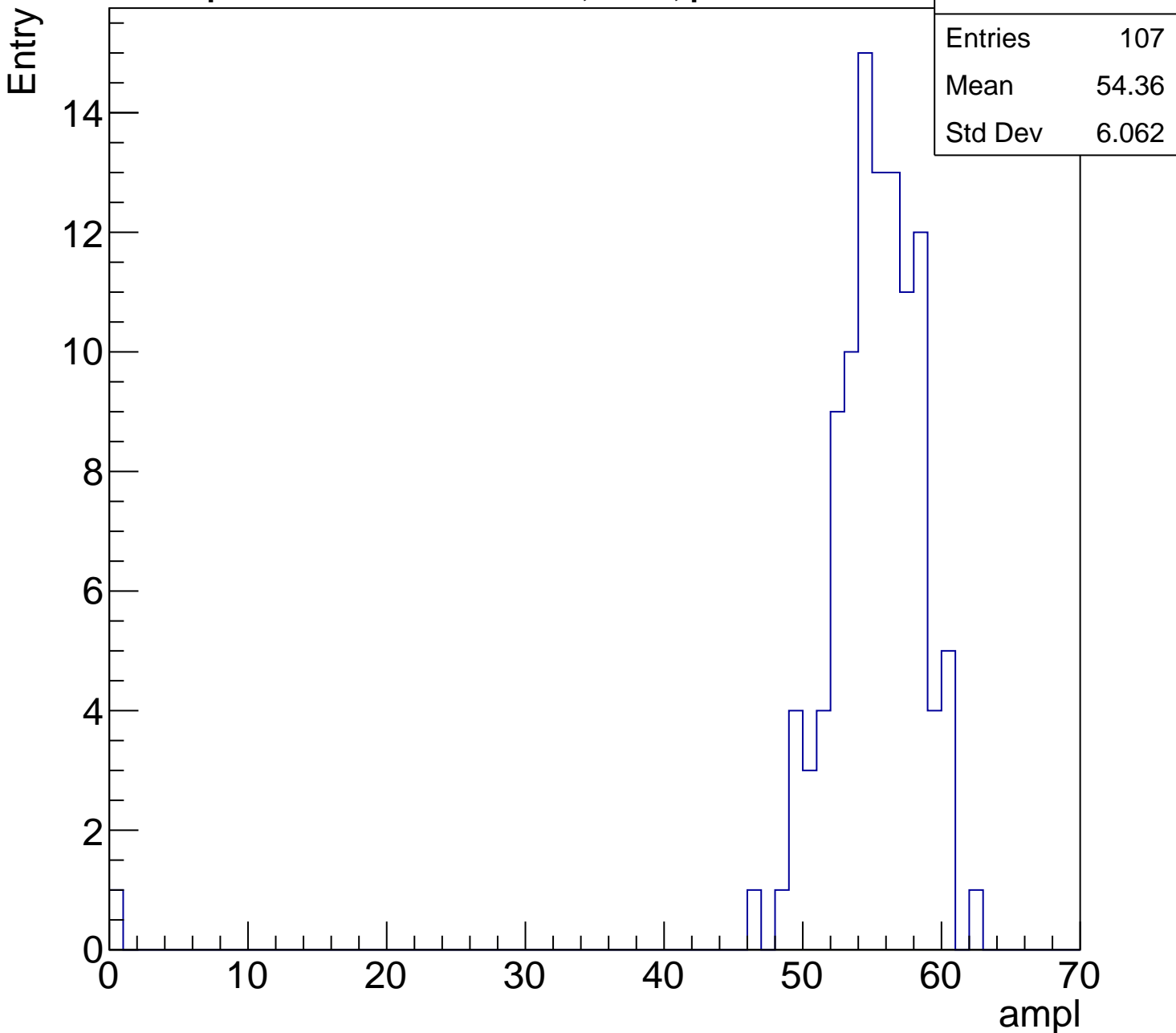
Entries	107
Mean	54.36
Std Dev	6.062

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch87, adc5

calib_packv5_042523_0143.root, FC#2, port C2

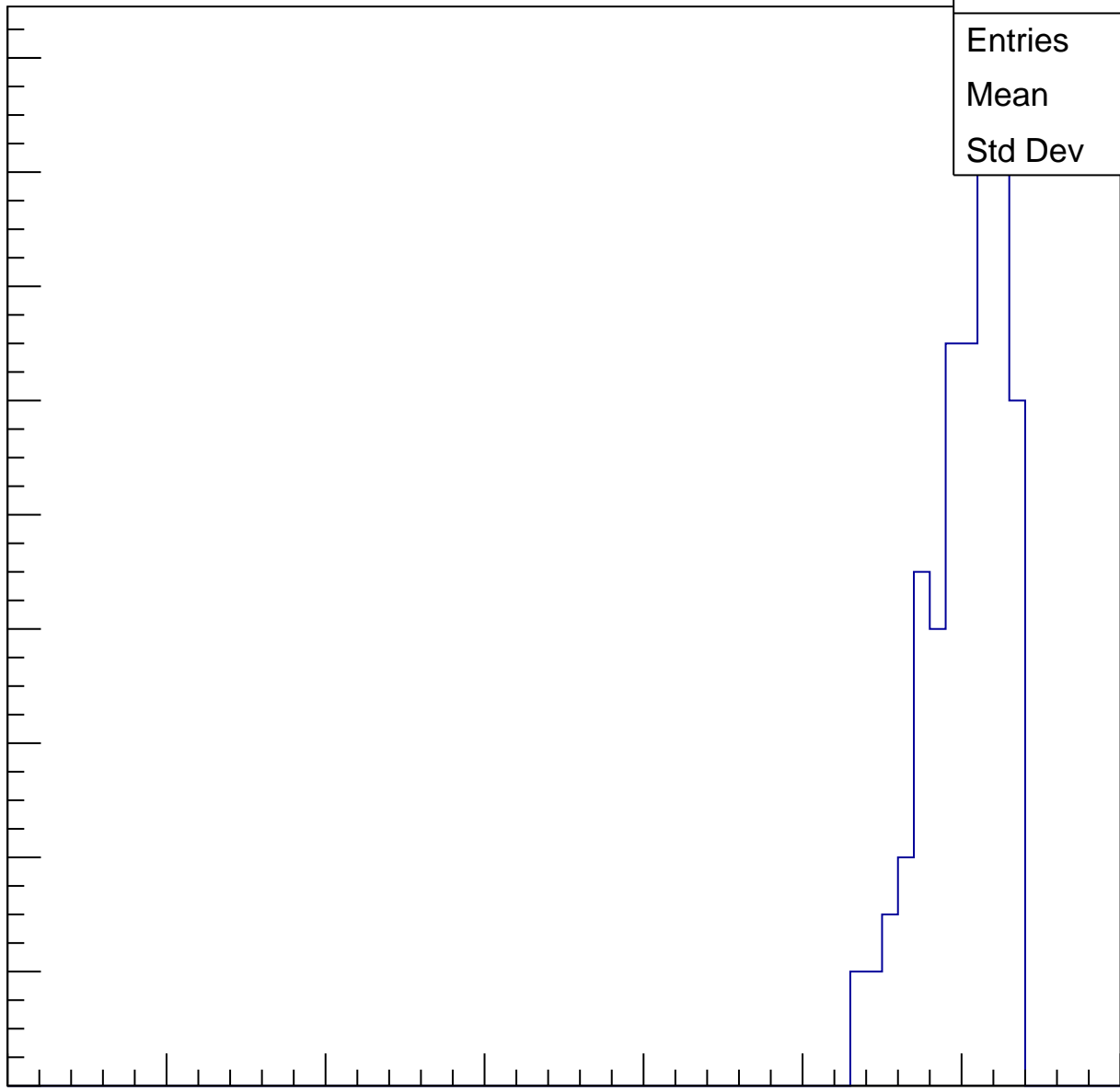
Entries	101
Mean	59.75
Std Dev	2.483

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

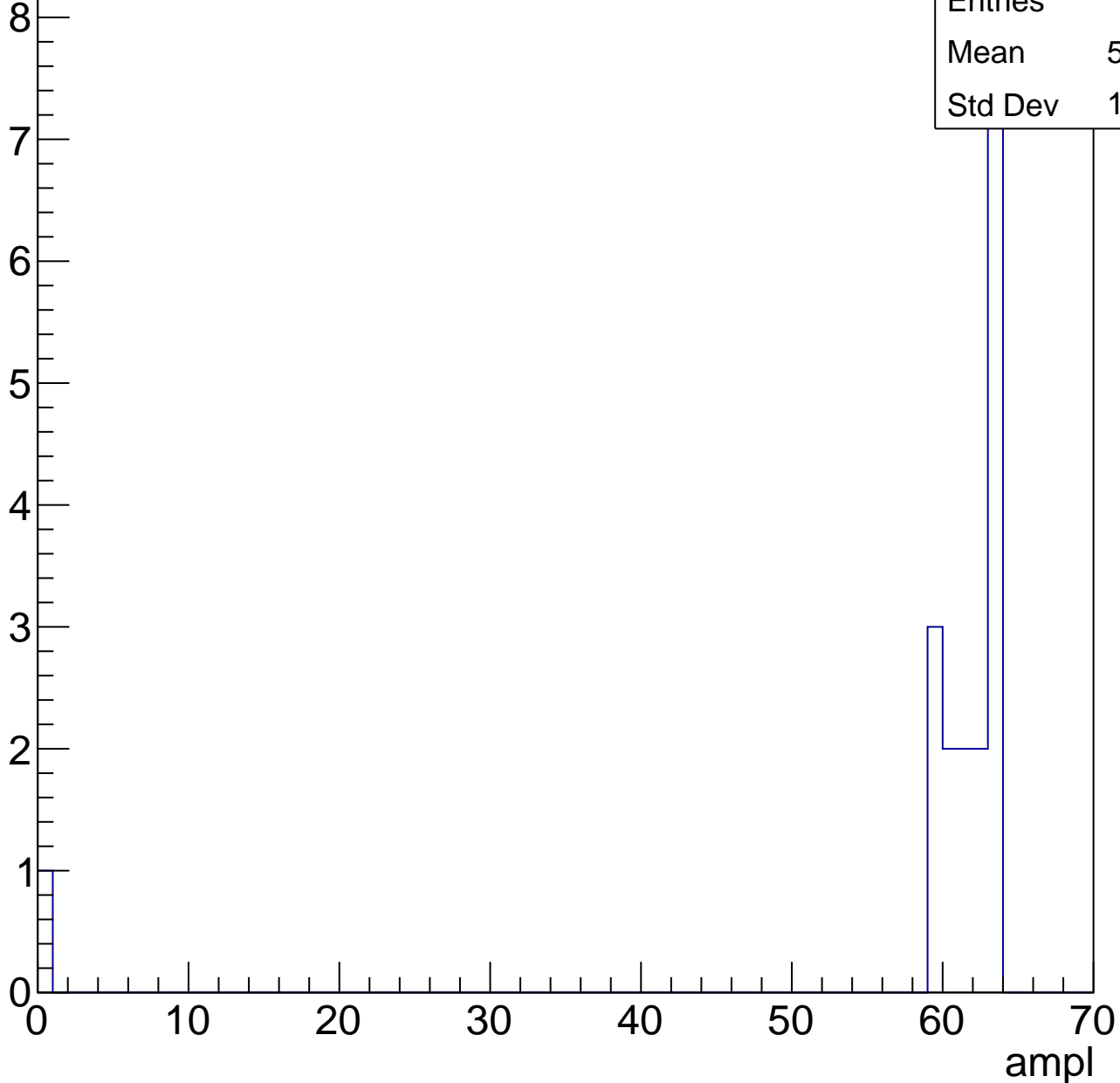


B1L001S, U22-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.17
Std Dev	14.19



B1L001S, U22-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



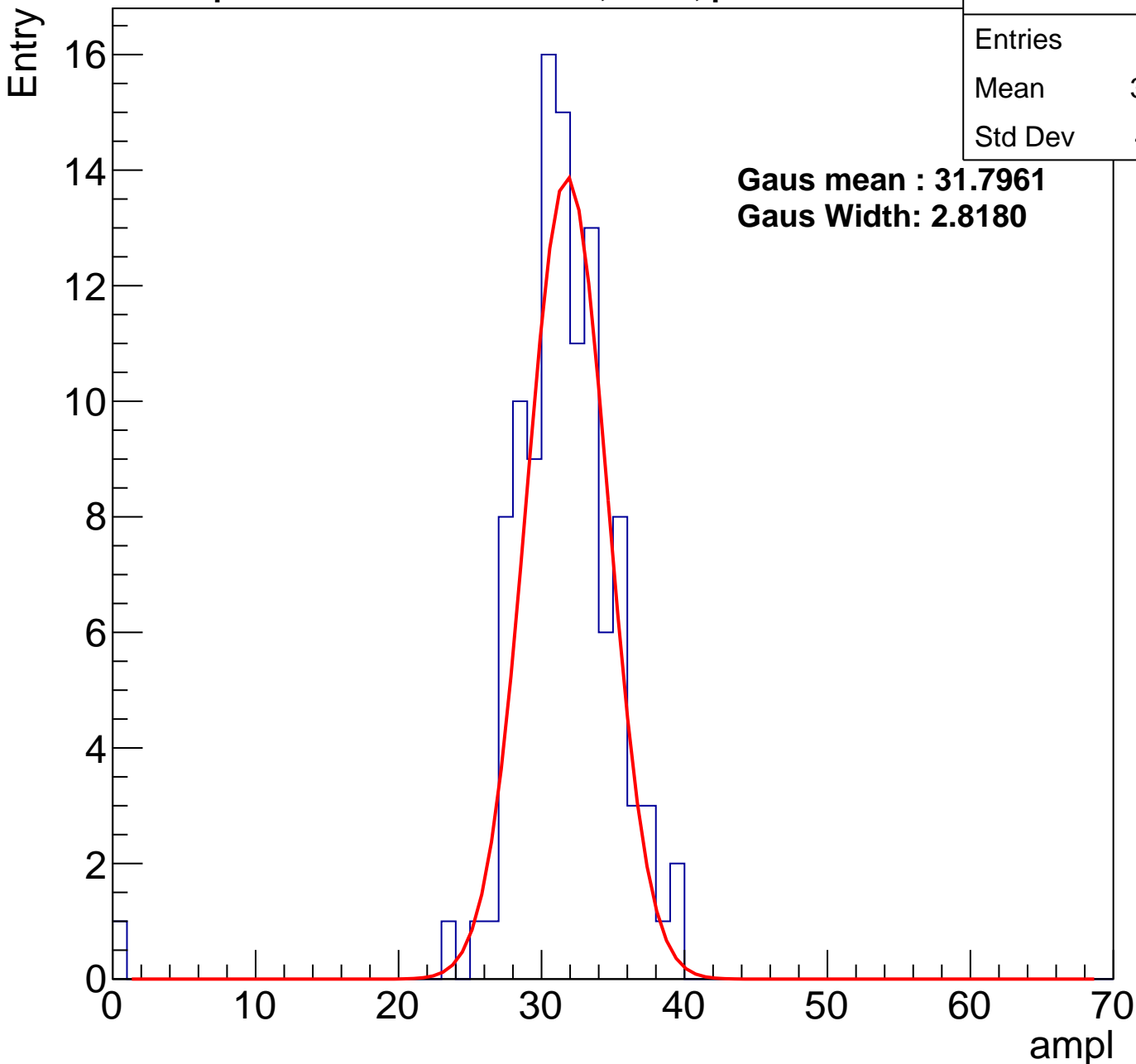
Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch88, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	30.97
Std Dev	4.241

Gaus mean : 31.7961
Gaus Width: 2.8180



B1L001S, U22-ch88, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	37.61
Std Dev	3.371

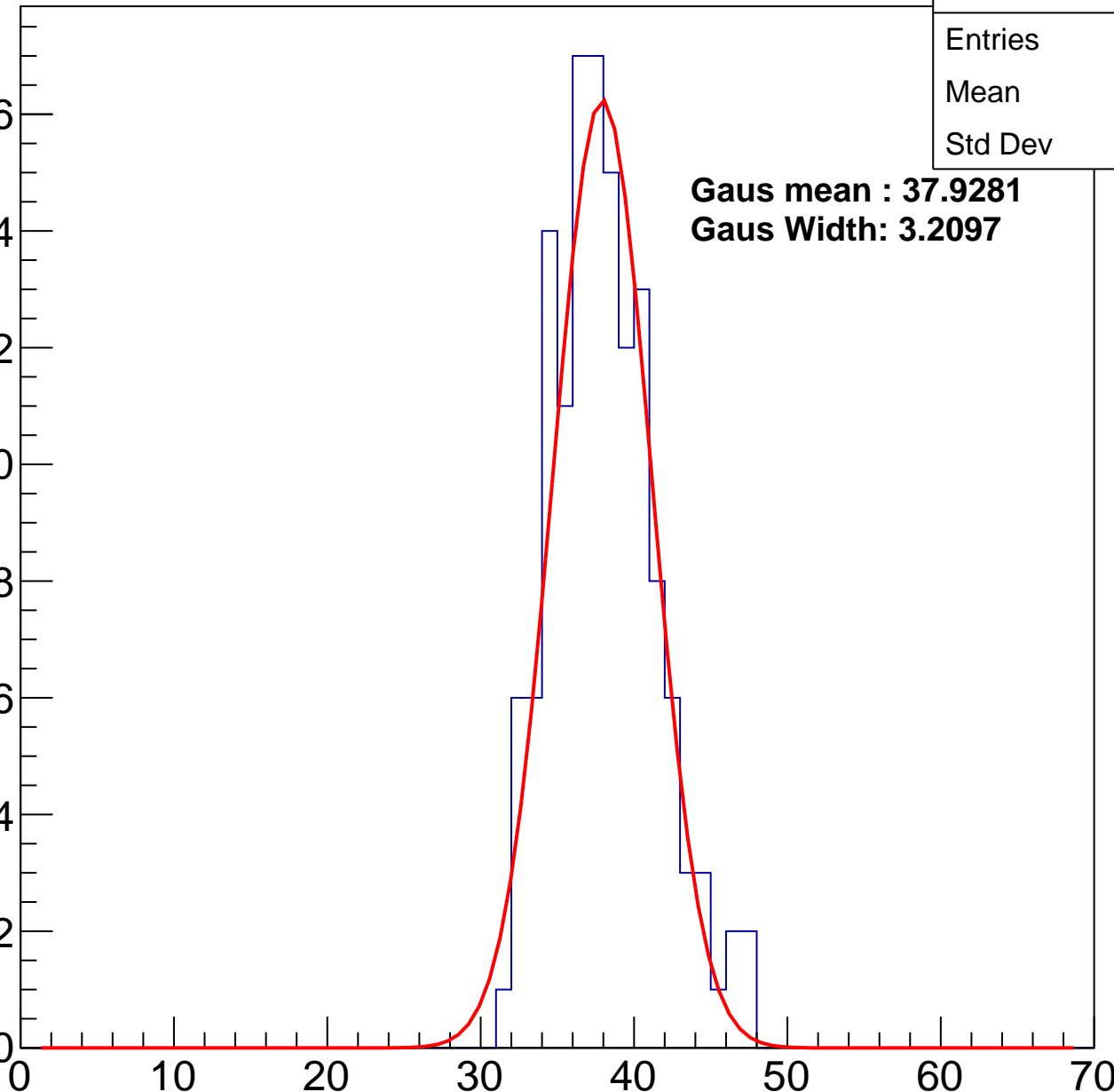
Gaus mean : 37.9281

Gaus Width: 3.2097

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

144

Mean

44.34

Std Dev

3.538

Gaus mean : 44.6608

Gaus Width: 3.5502

0

10

20

30

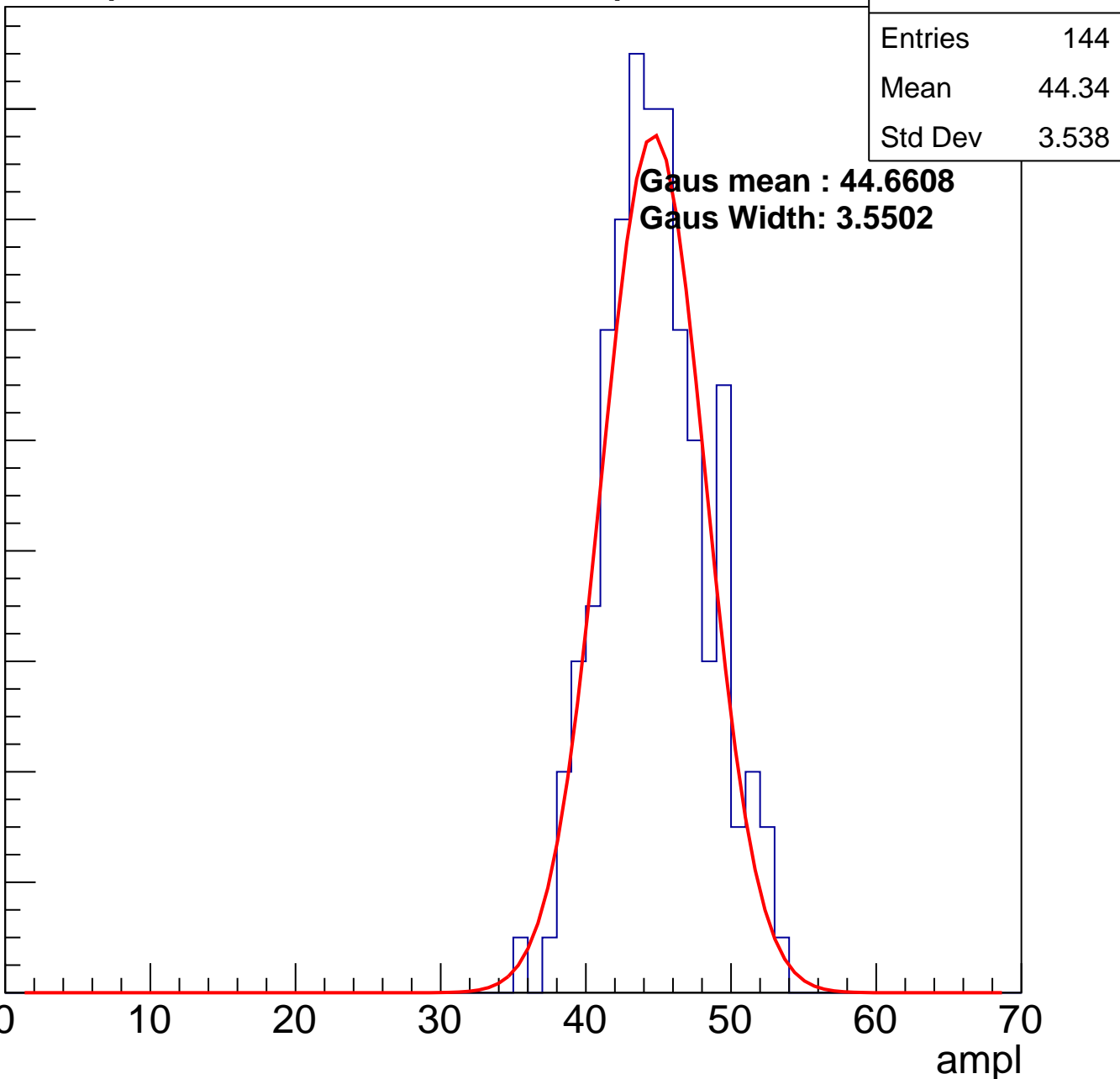
40

50

60

70

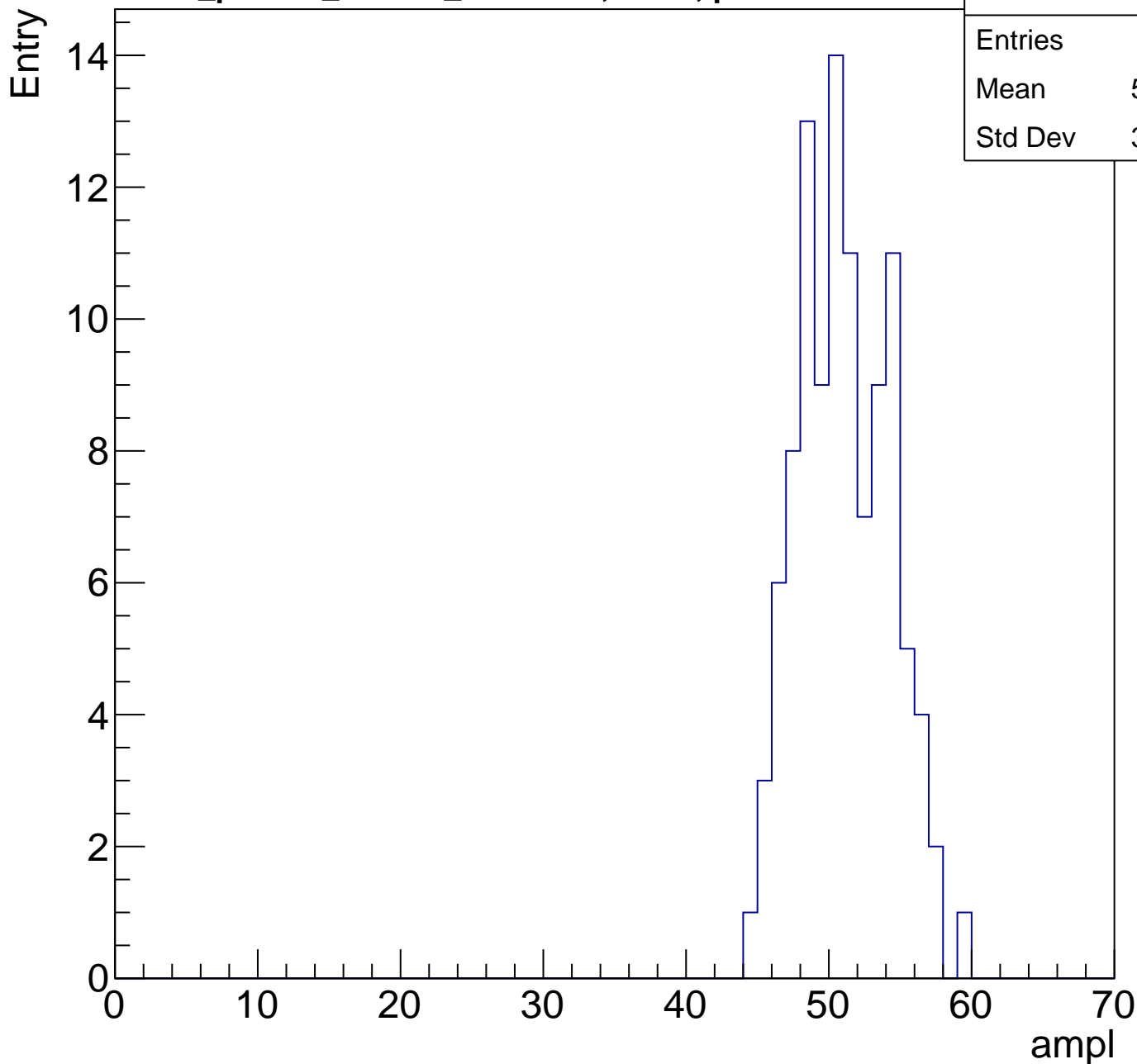
ampl



B1L001S, U22-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

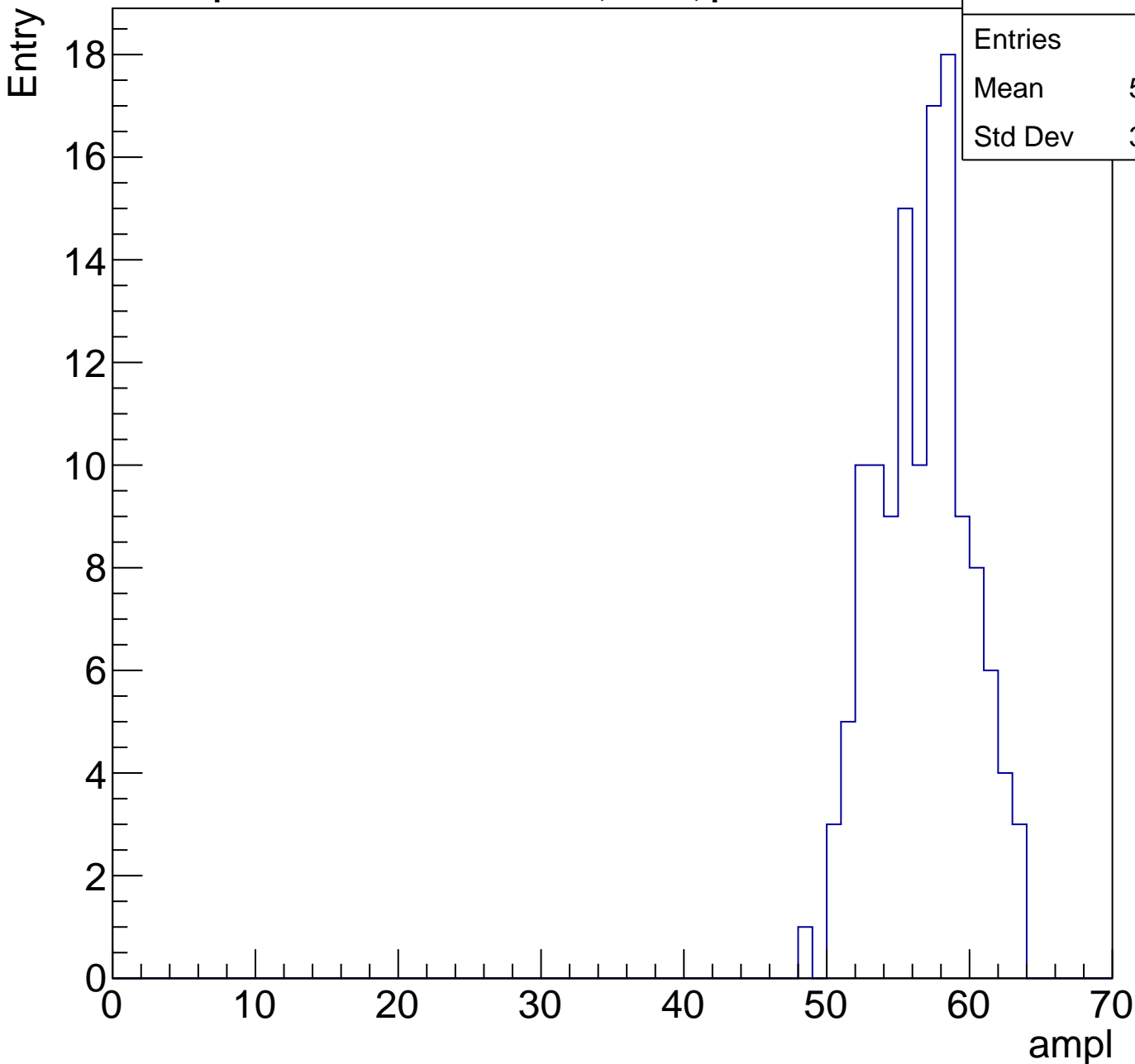
Entries	104
Mean	50.62
Std Dev	3.169



B1L001S, U22-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	56.26
Std Dev	3.222



B1L001S, U22-ch88, adc5

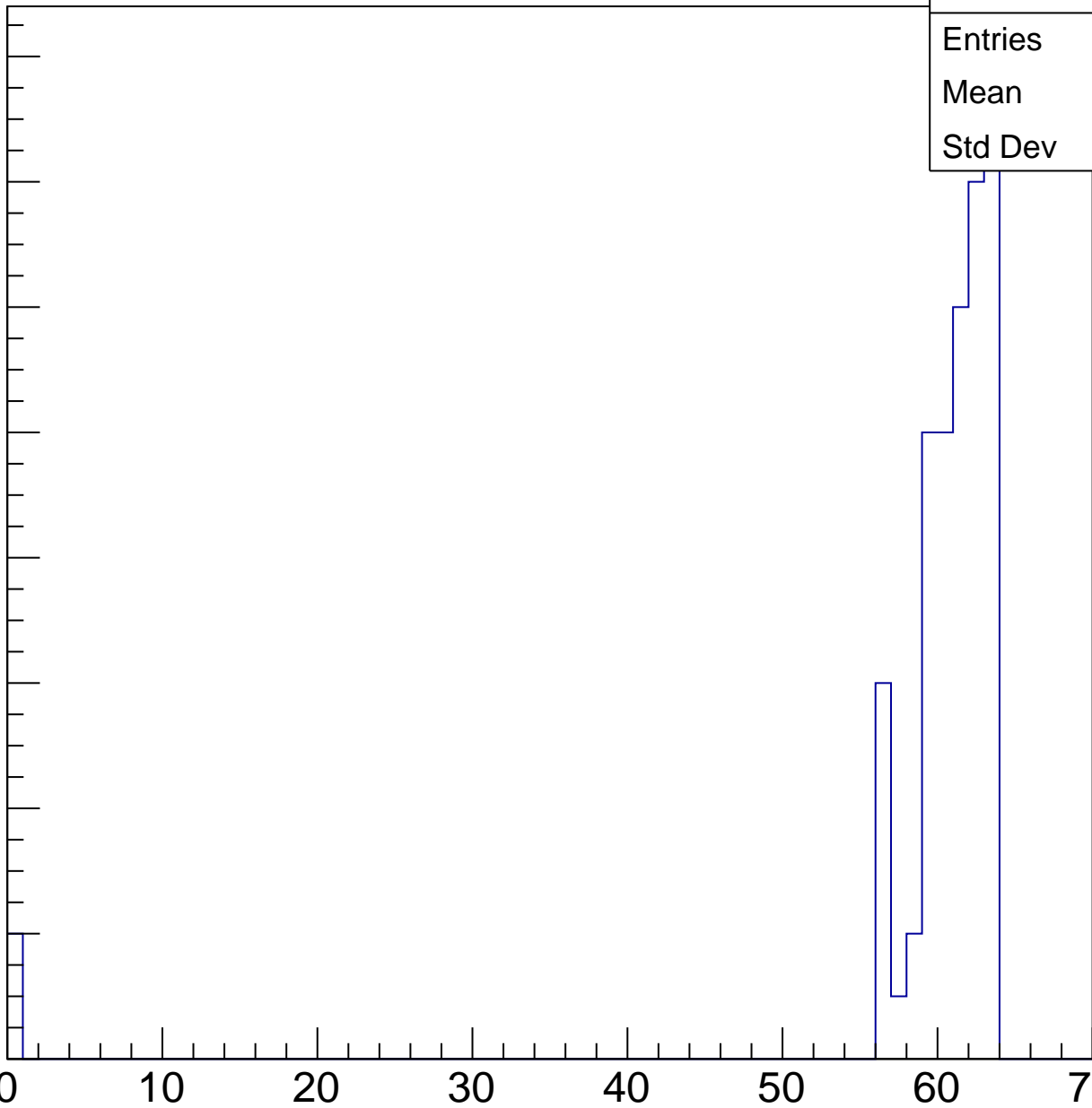
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59
Std Dev	10.11

ampl



B1L001S, U22-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

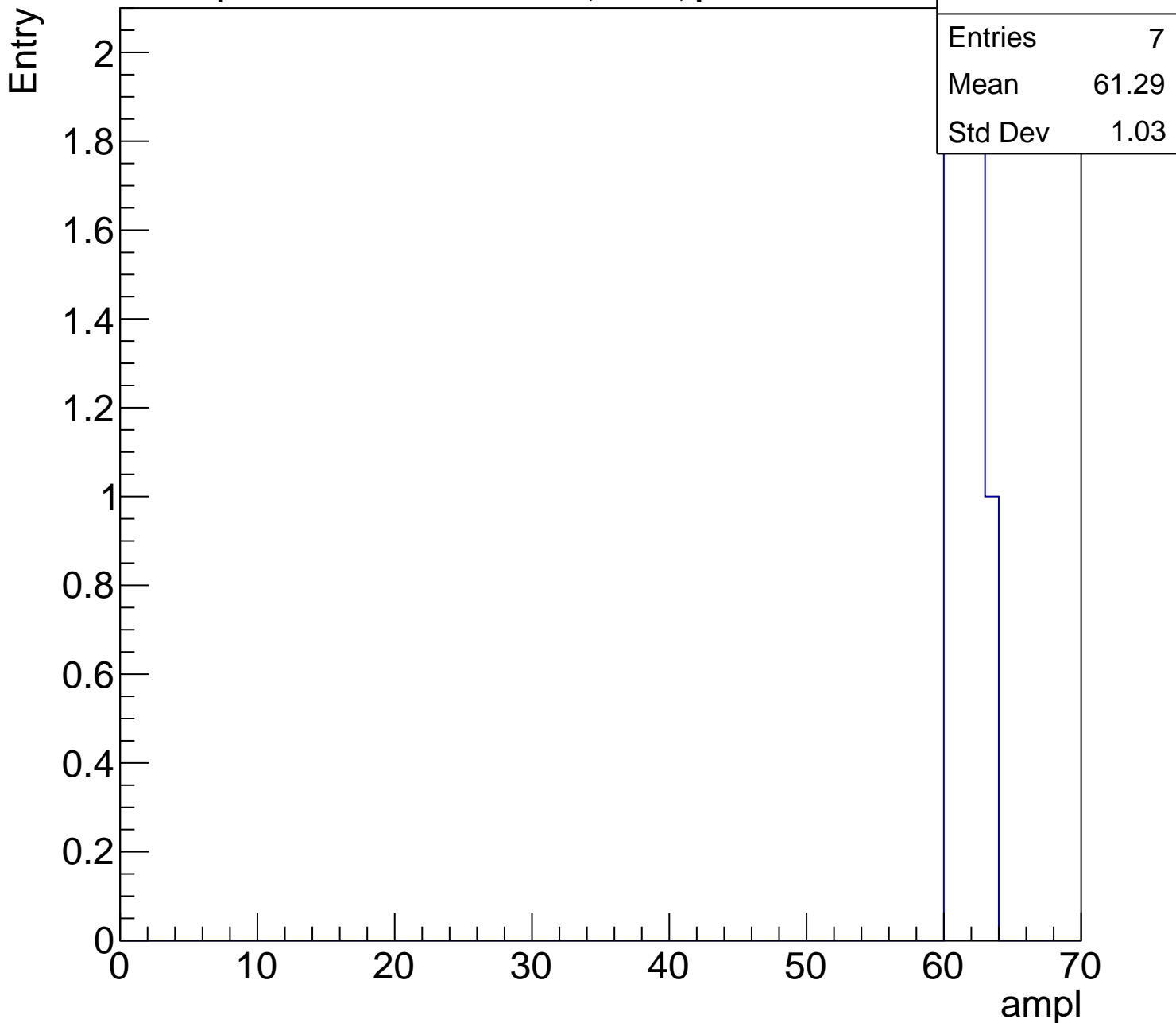
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.03

0 10 20 30 40 50 60 70

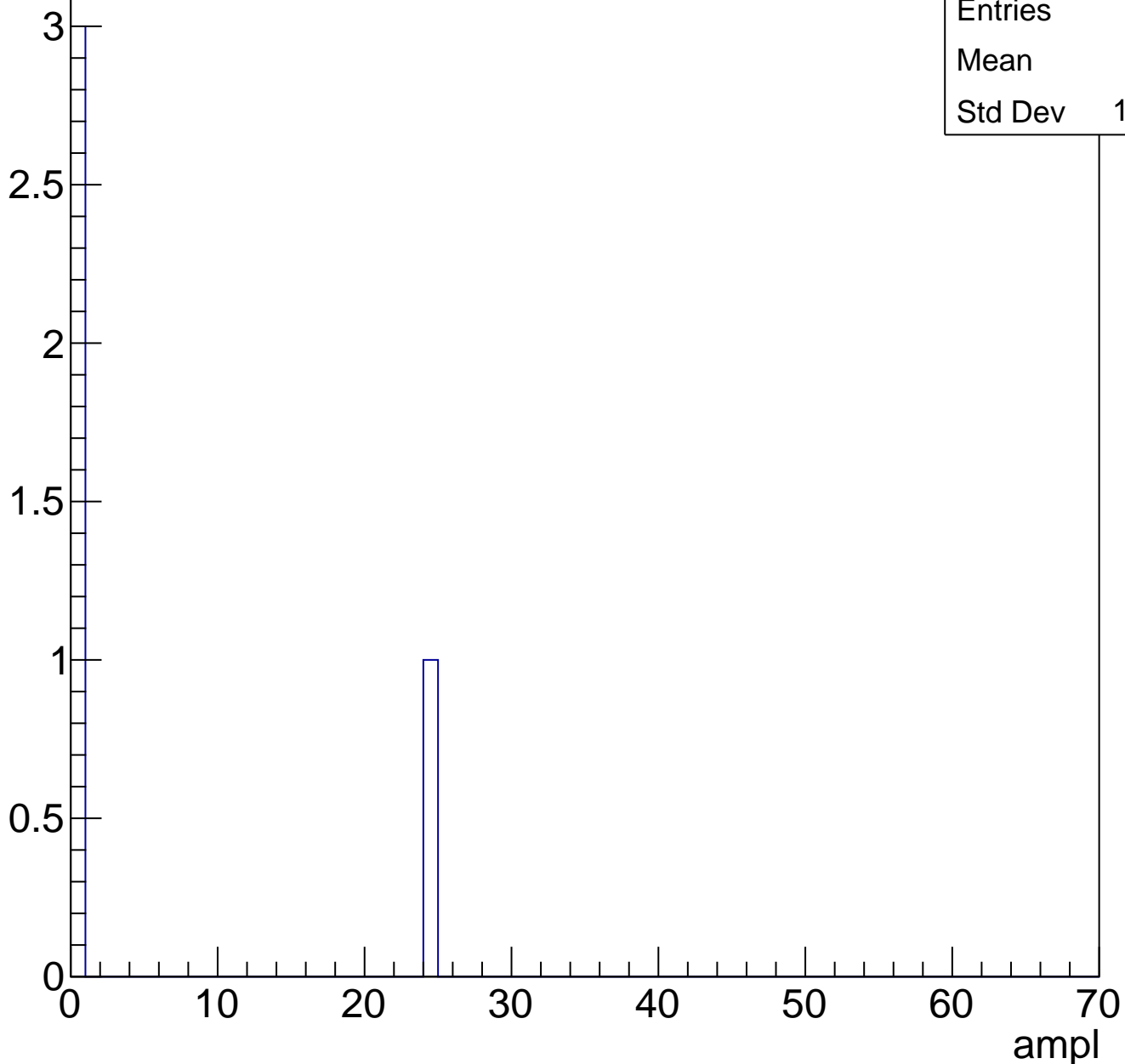
ampl



B1L001S, U22-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	6
Std Dev	10.39

B1L001S, U22-ch89, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.36
Std Dev	3.081

Gaus mean : 29.6563

Gaus Width: 3.3294

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

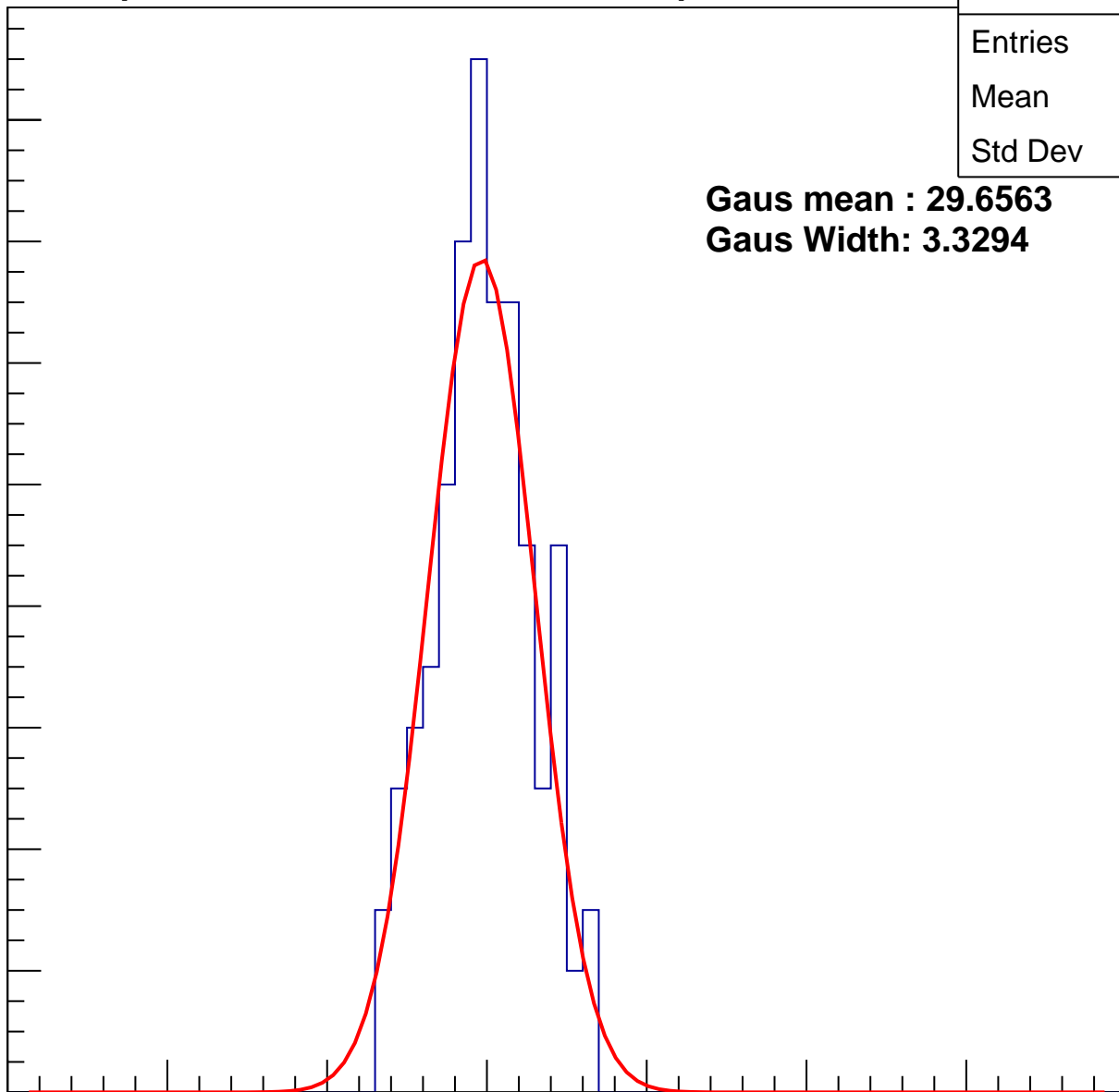
40

50

60

70

ampl



B1L001S, U22-ch89, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

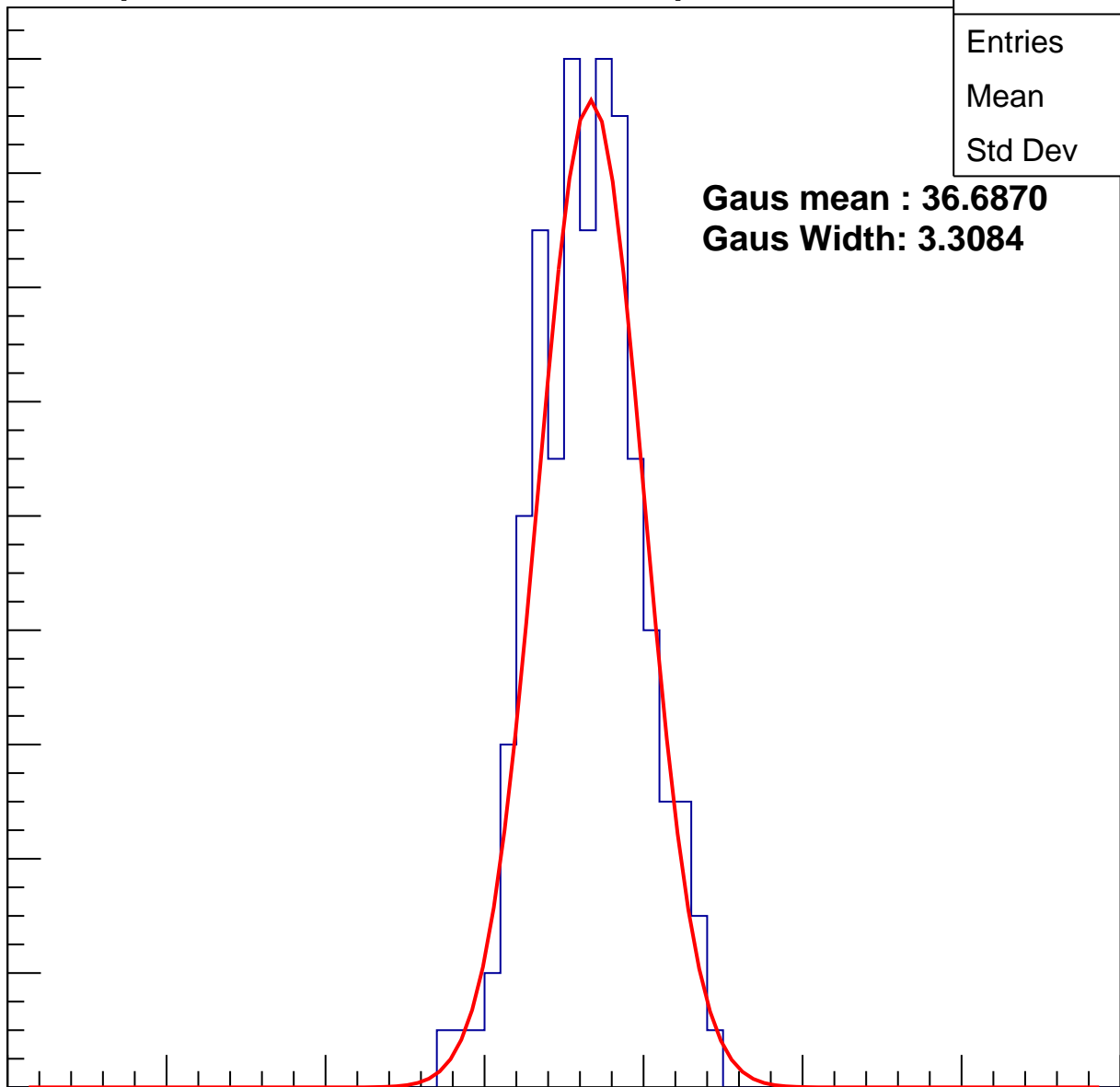
Entries	148
Mean	36.07
Std Dev	3.289

Gaus mean : 36.6870

Gaus Width: 3.3084

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	43.09
Std Dev	3.037

Gaus mean : 43.5328

Gaus Width: 3.0379

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

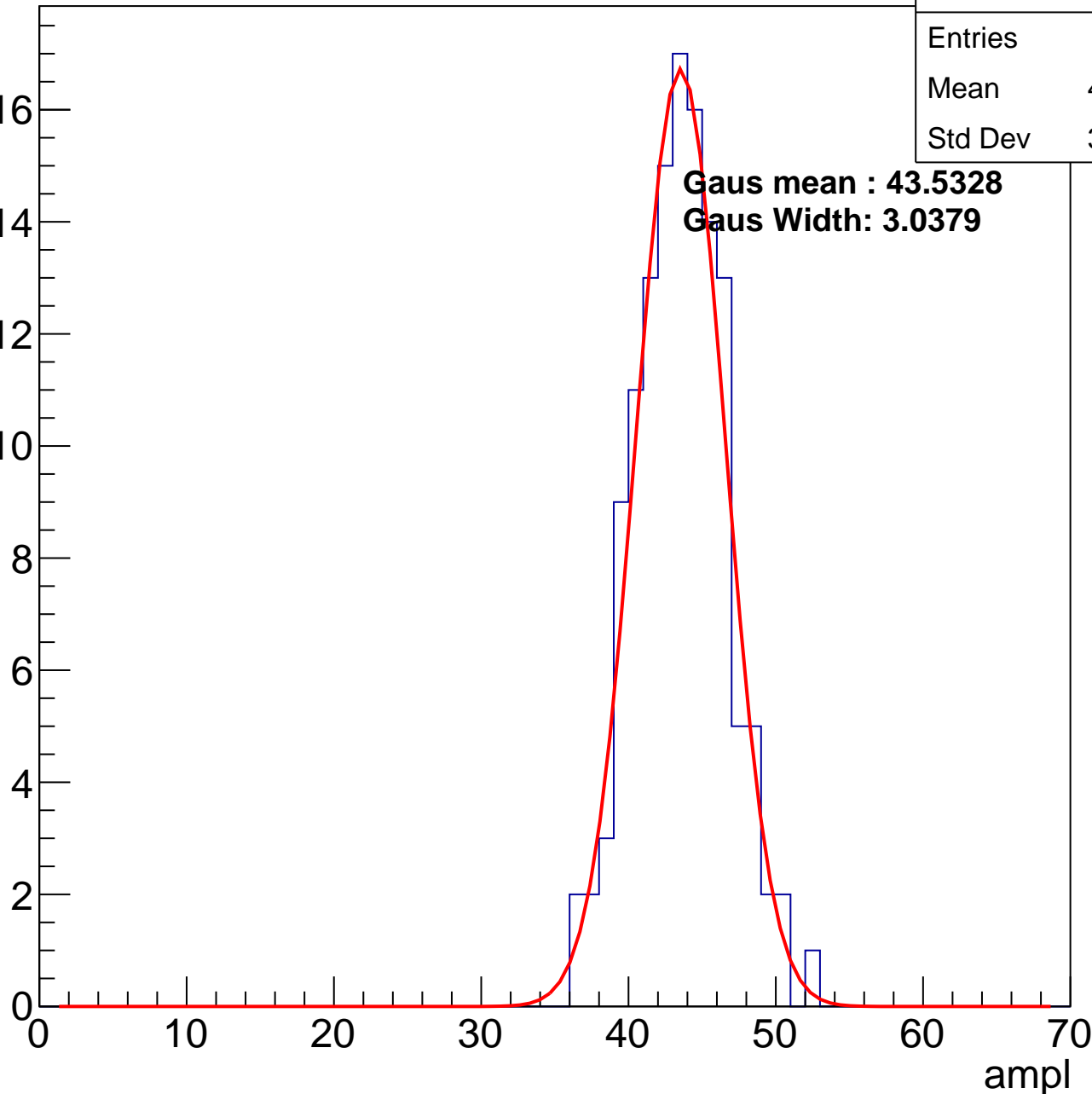
40

50

60

70

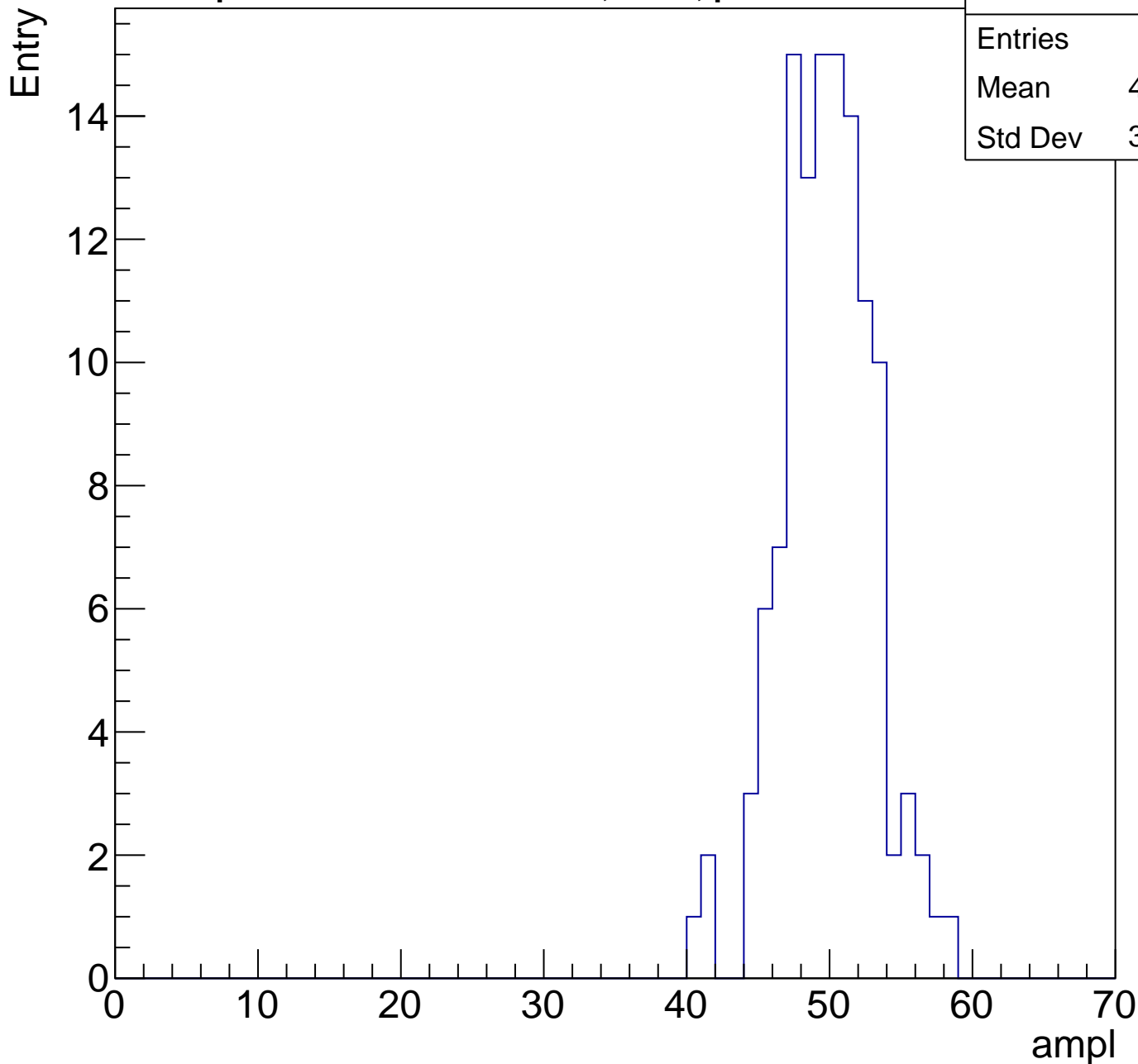
ampl



B1L001S, U22-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

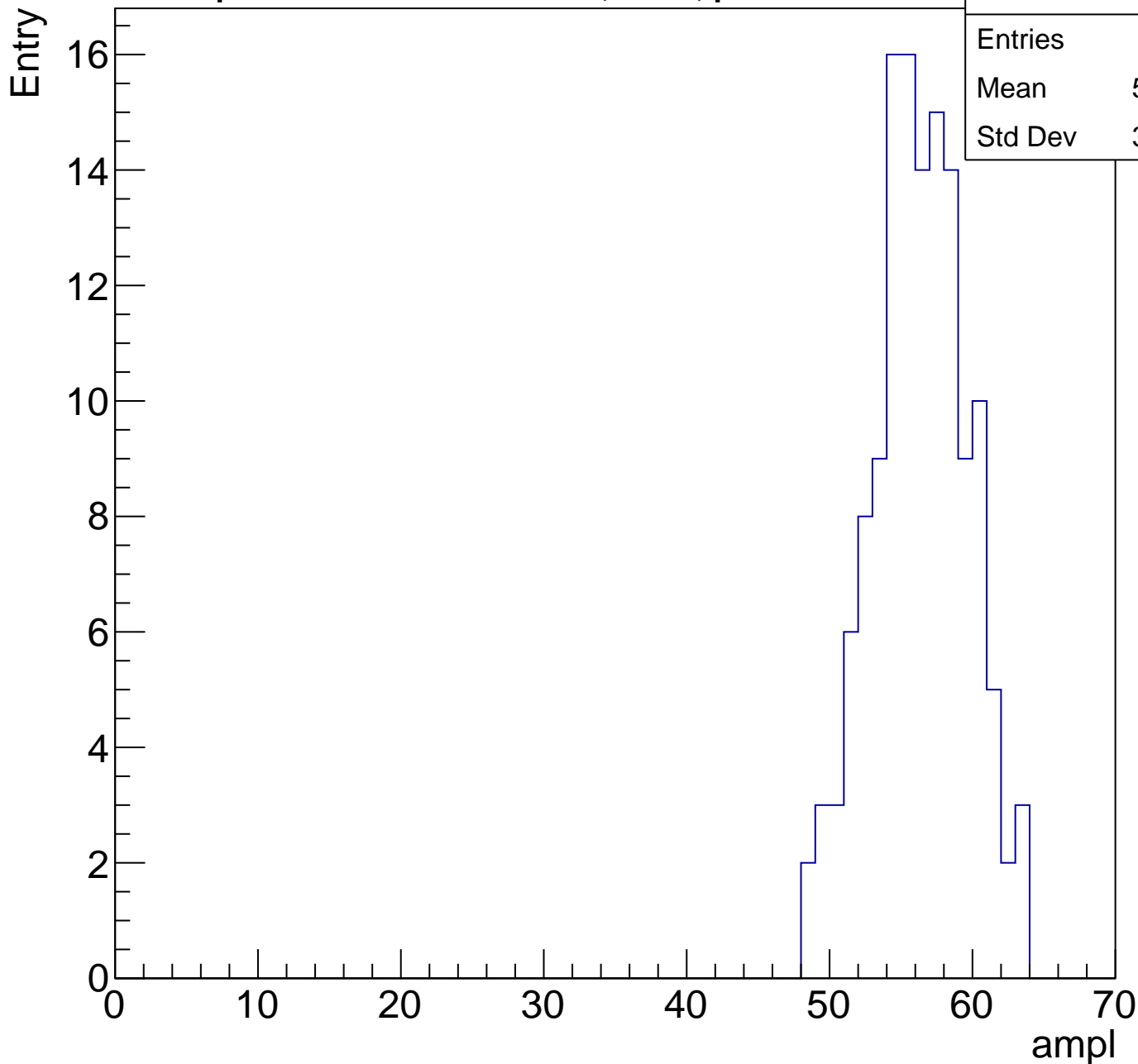
Entries	121
Mean	49.39
Std Dev	3.187



B1L001S, U22-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.82
Std Dev	3.304



B1L001S, U22-ch89, adc5

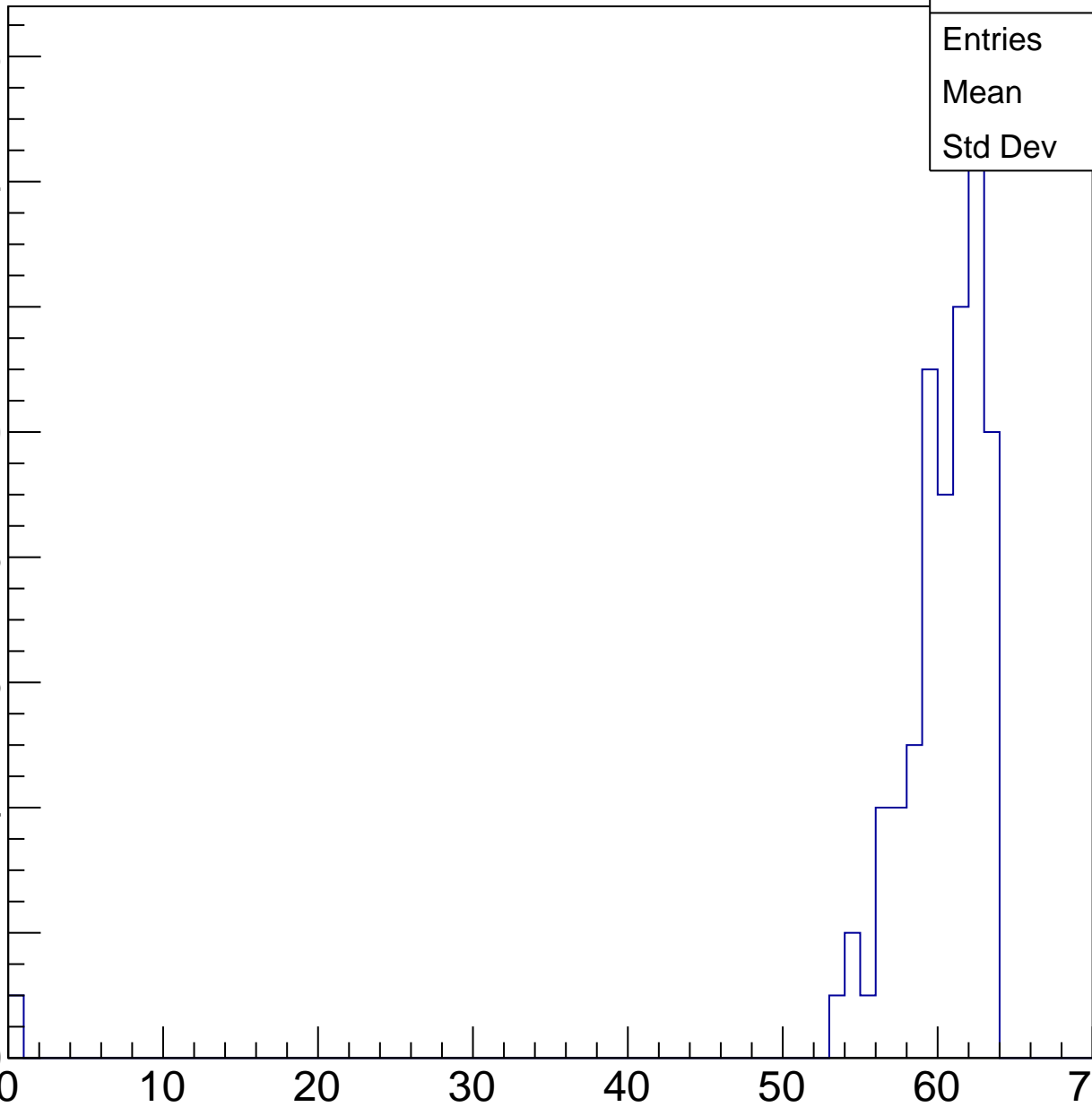
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.22
Std Dev	7.252

ampl

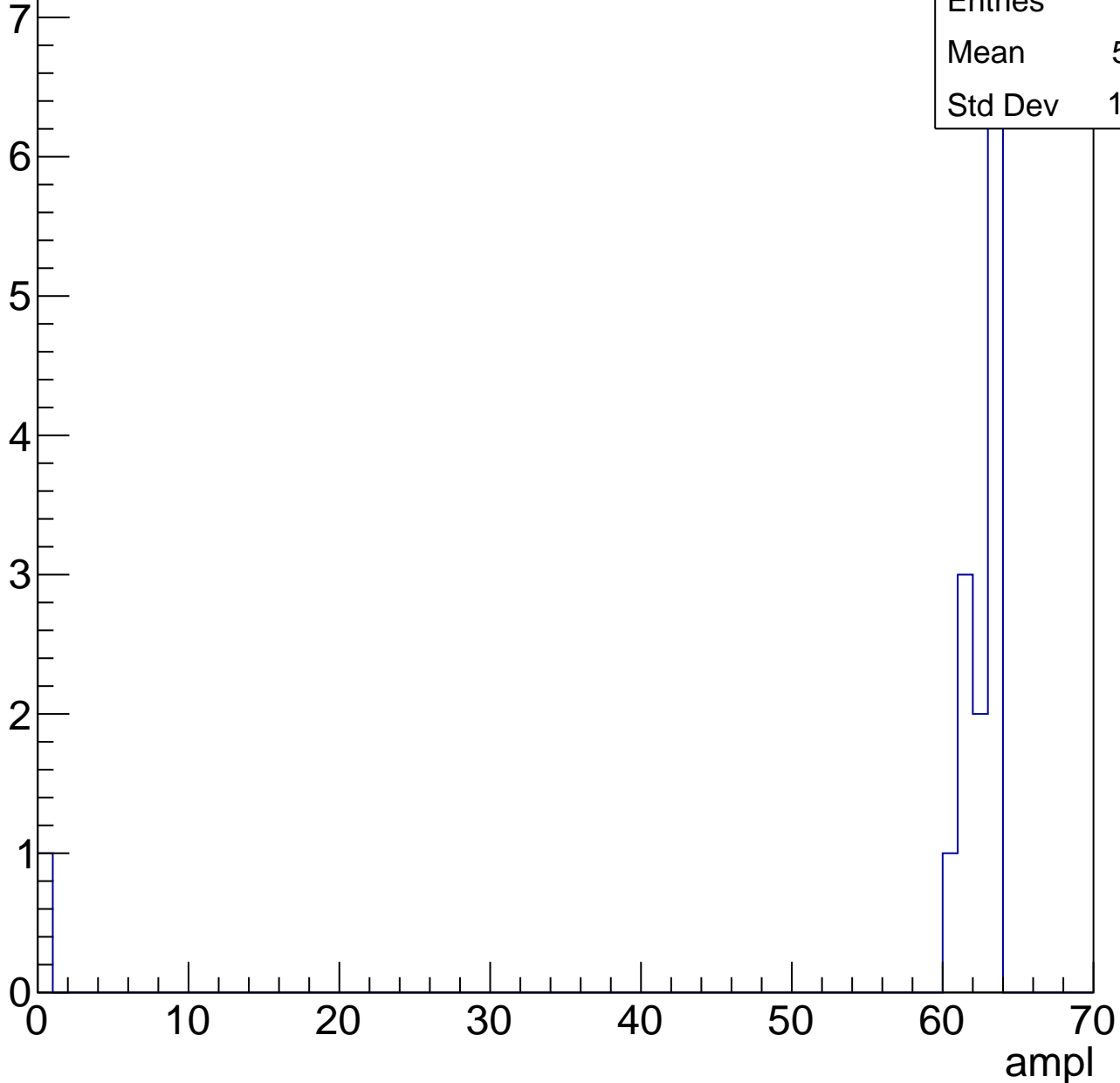


B1L001S, U22-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.71
Std Dev	16.04



B1L001S, U22-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L001S, U22-ch90, adc0

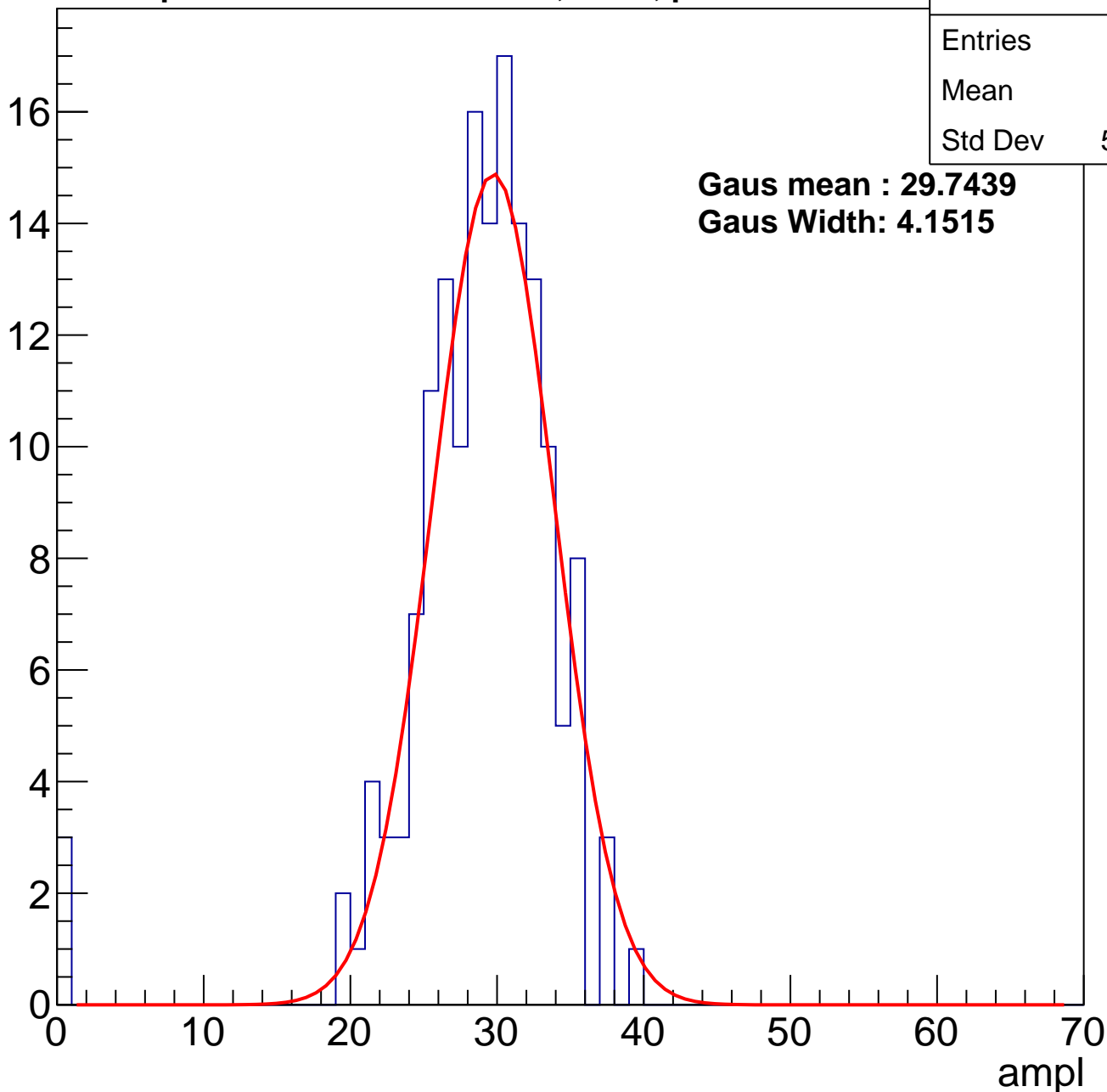
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	28.3
Std Dev	5.522

Gaus mean : 29.7439

Gaus Width: 4.1515

Entry



B1L001S, U22-ch90, adc1

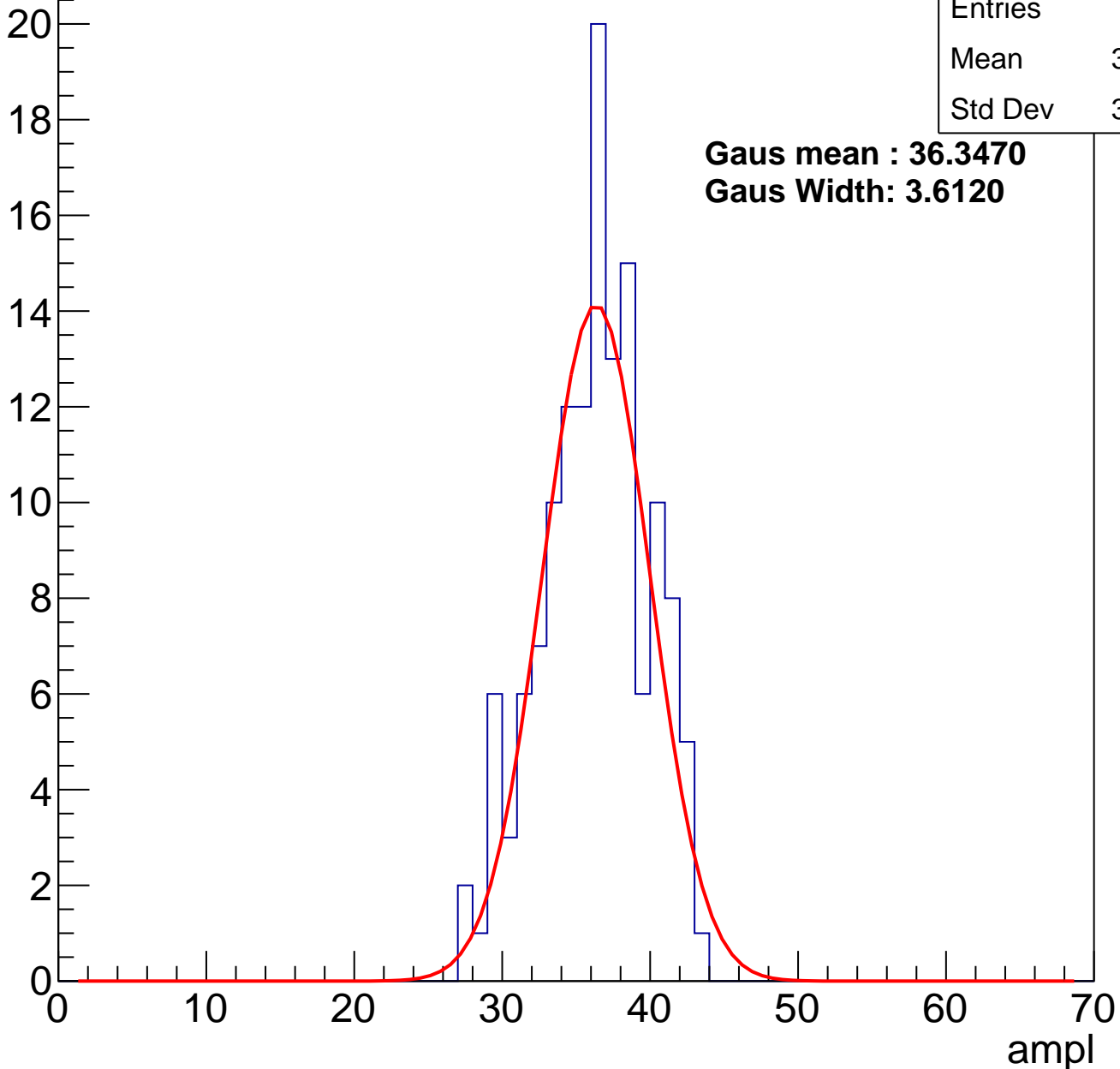
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.77
Std Dev	3.573

Gaus mean : 36.3470

Gaus Width: 3.6120

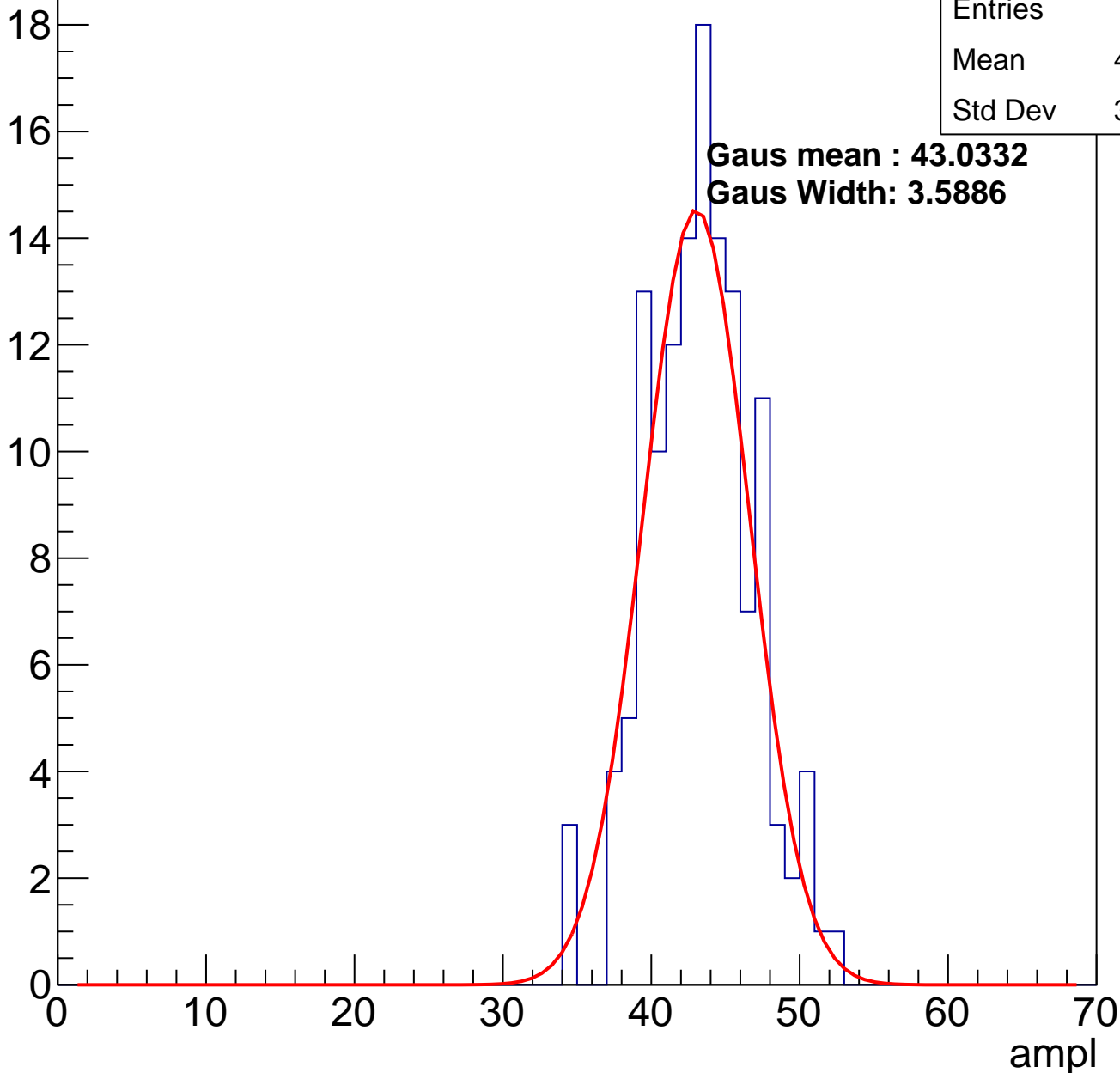
Entry



B1L001S, U22-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

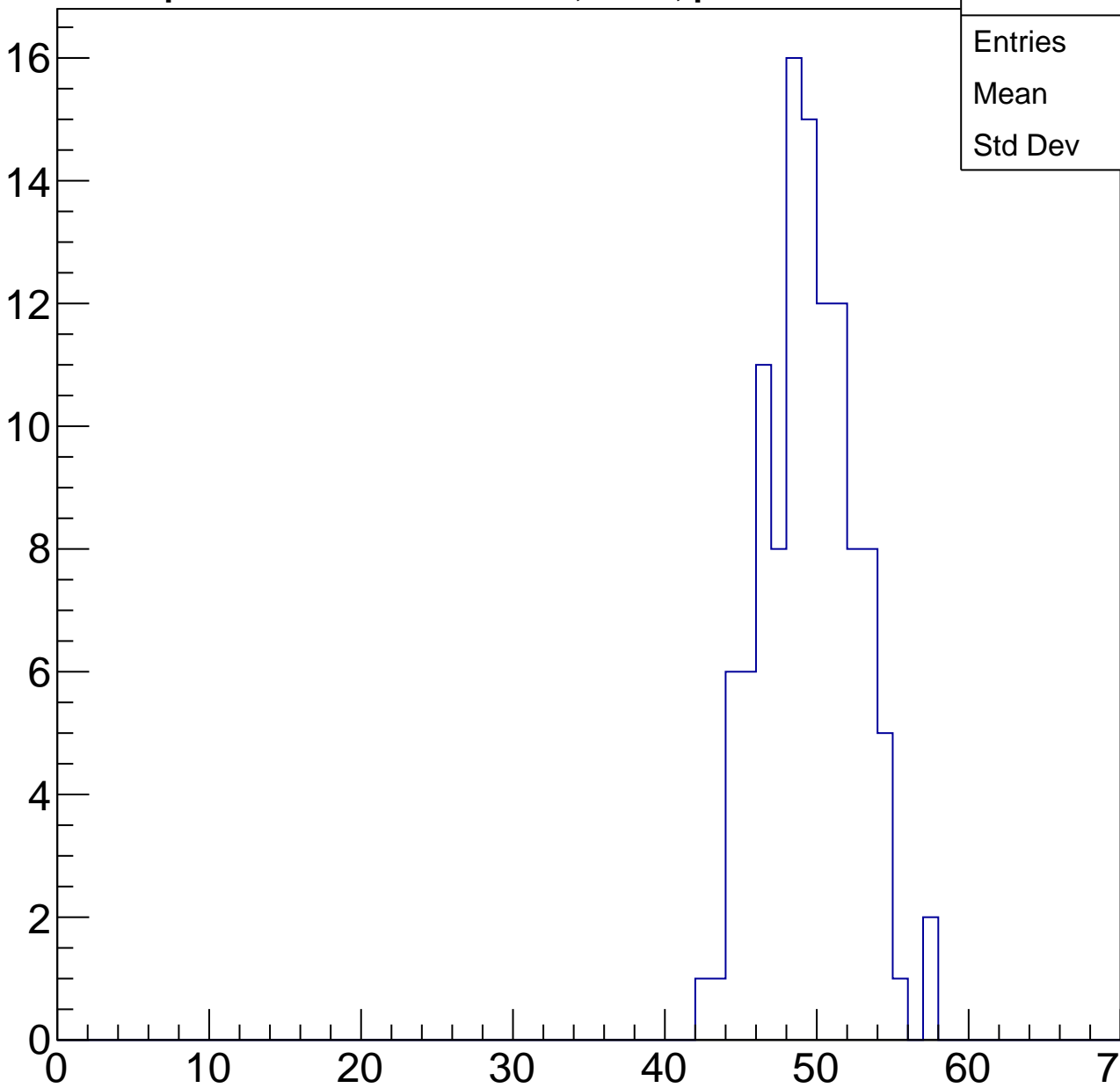
Entries	112
Mean	49.06
Std Dev	3.027

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch90, adc4

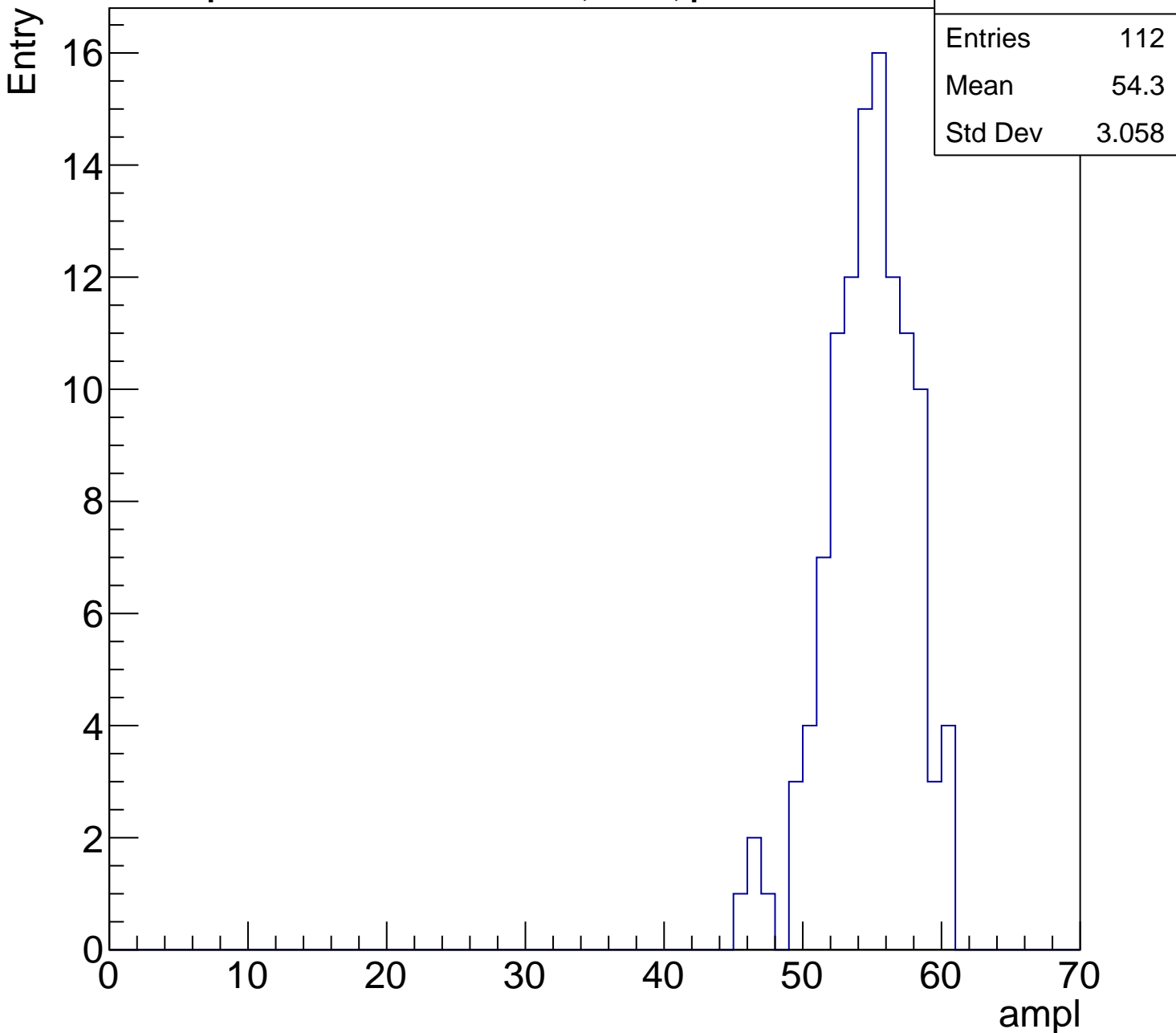
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	54.3
Std Dev	3.058

ampl



B1L001S, U22-ch90, adc5

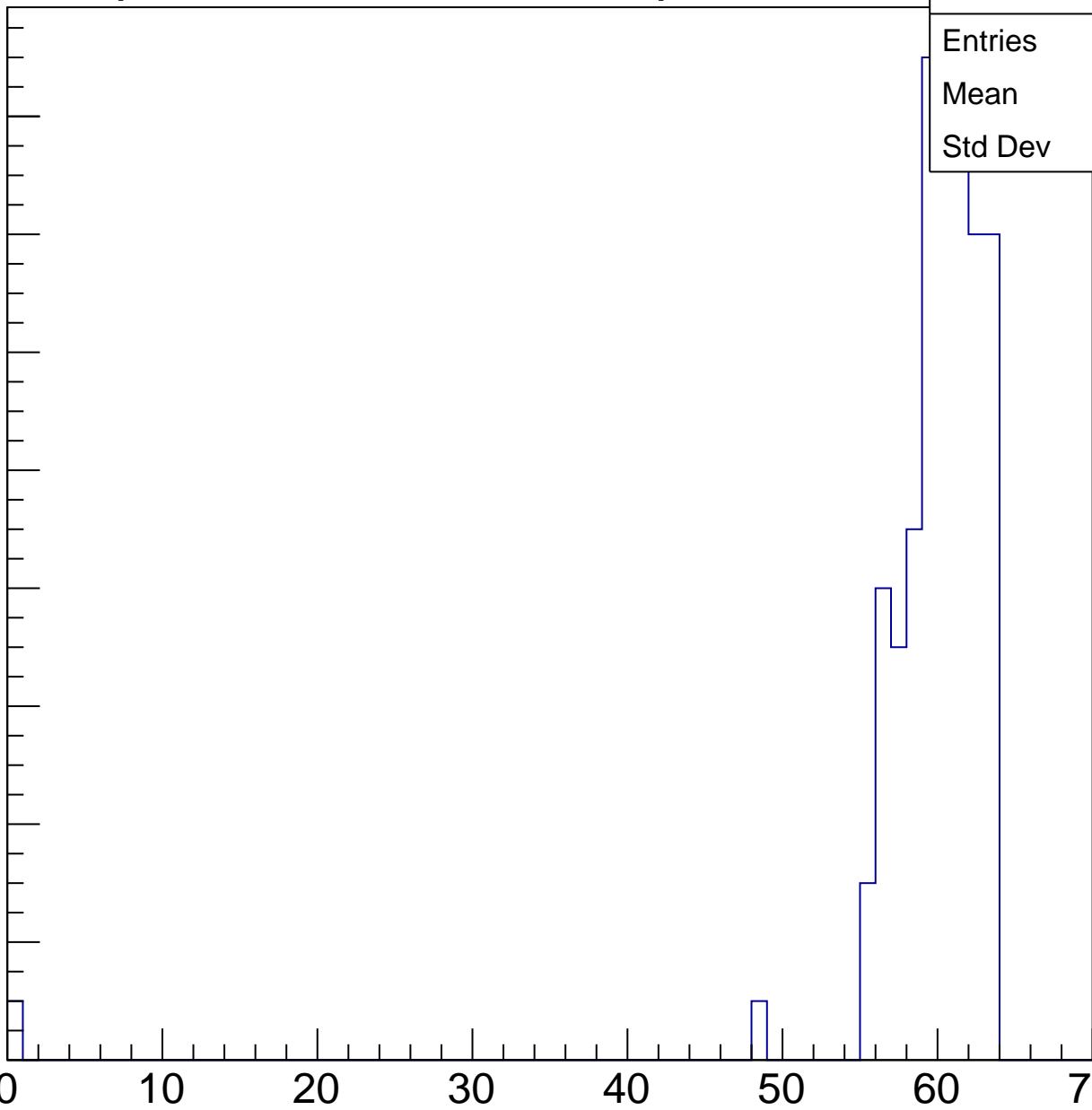
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	59.16
Std Dev	6.284

ampl

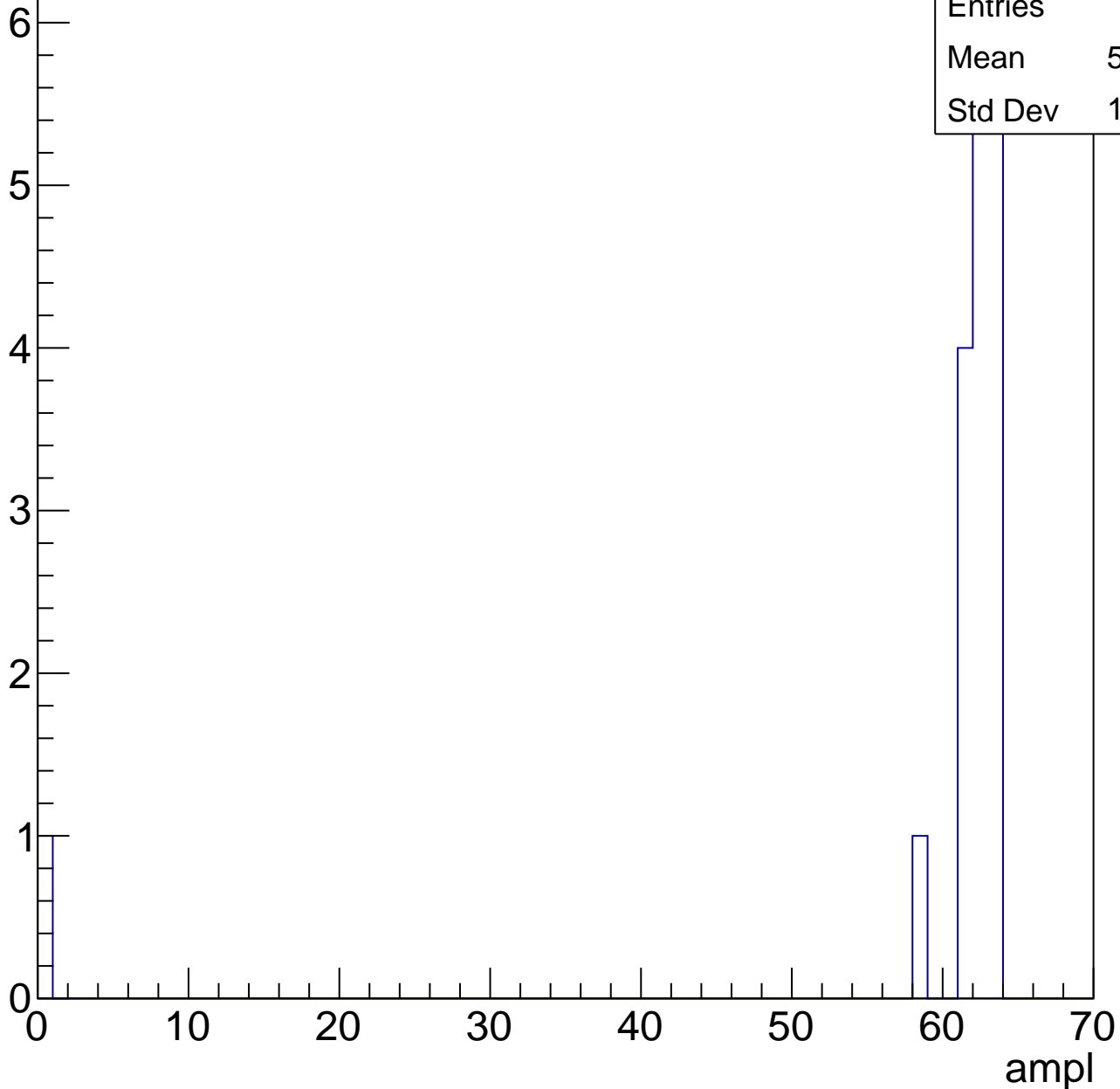


B1L001S, U22-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.44
Std Dev	14.23



B1L001S, U22-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch91, adc0

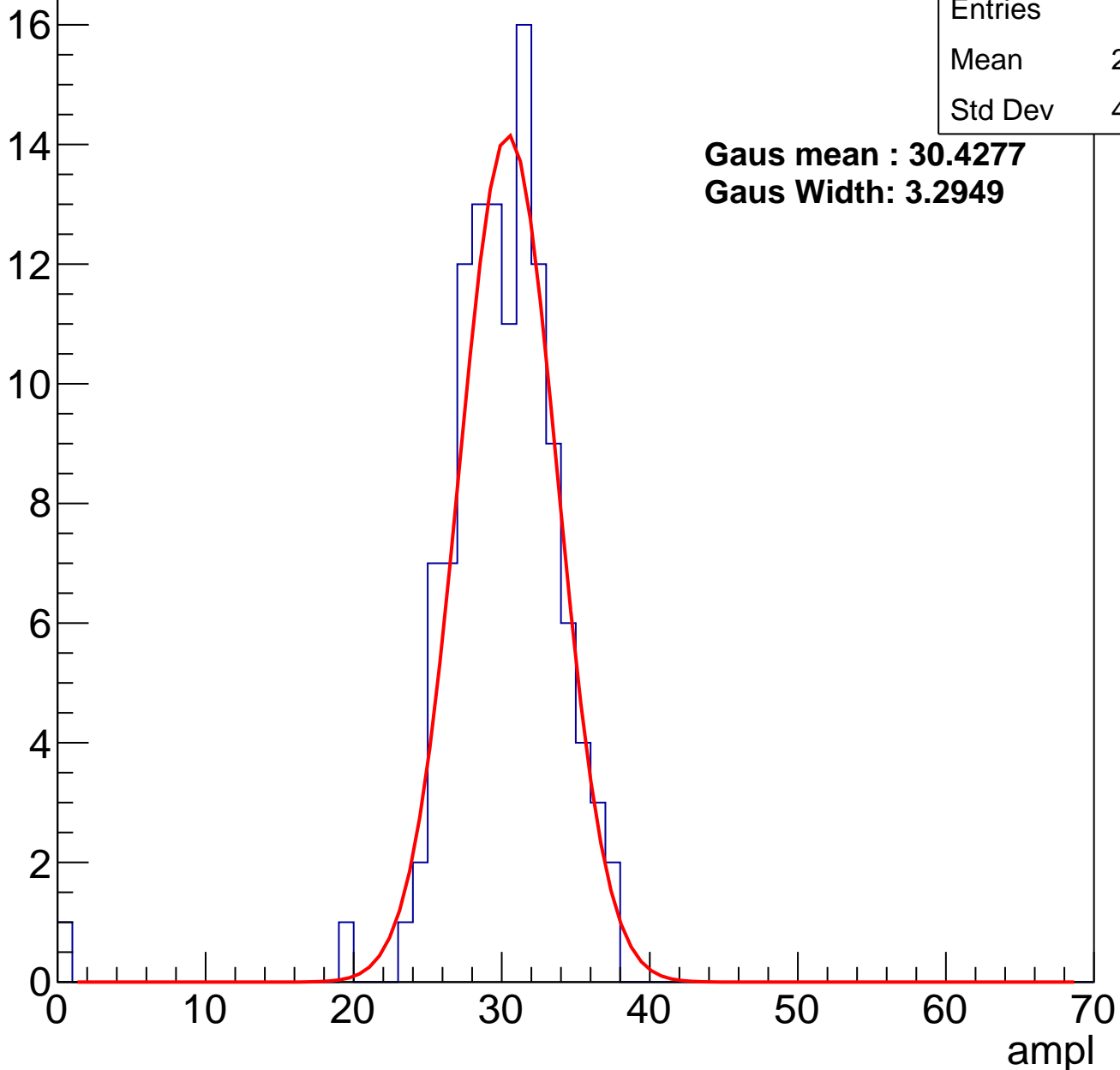
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	29.54
Std Dev	4.207

Gaus mean : 30.4277

Gaus Width: 3.2949

Entry



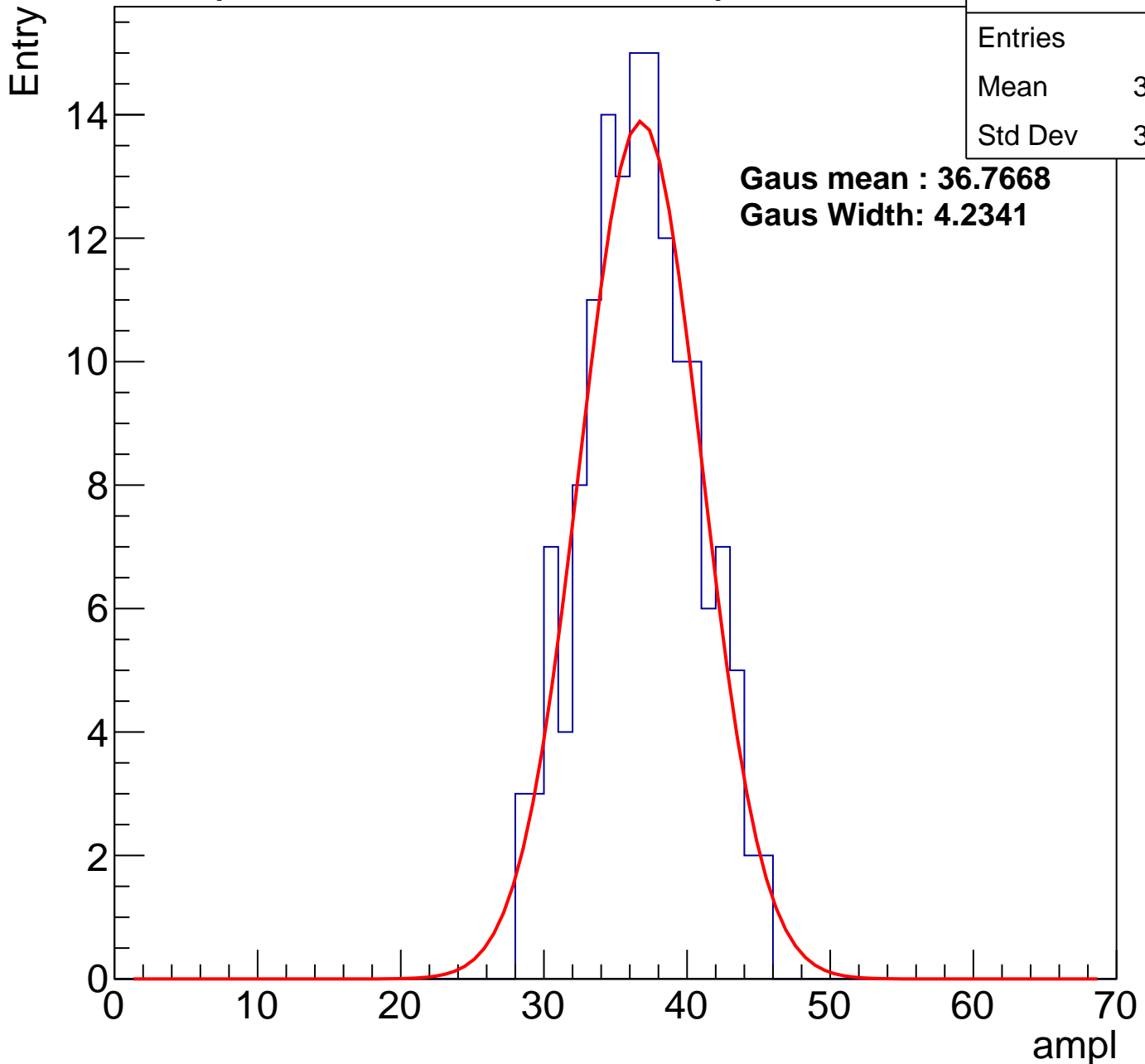
B1L001S, U22-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.25
Std Dev	3.893

Gaus mean : 36.7668

Gaus Width: 4.2341



B1L001S, U22-ch91, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

160

Mean

43.59

Std Dev

3.828

Gaus mean : 43.7359

Gaus Width: 3.6878

0

10

20

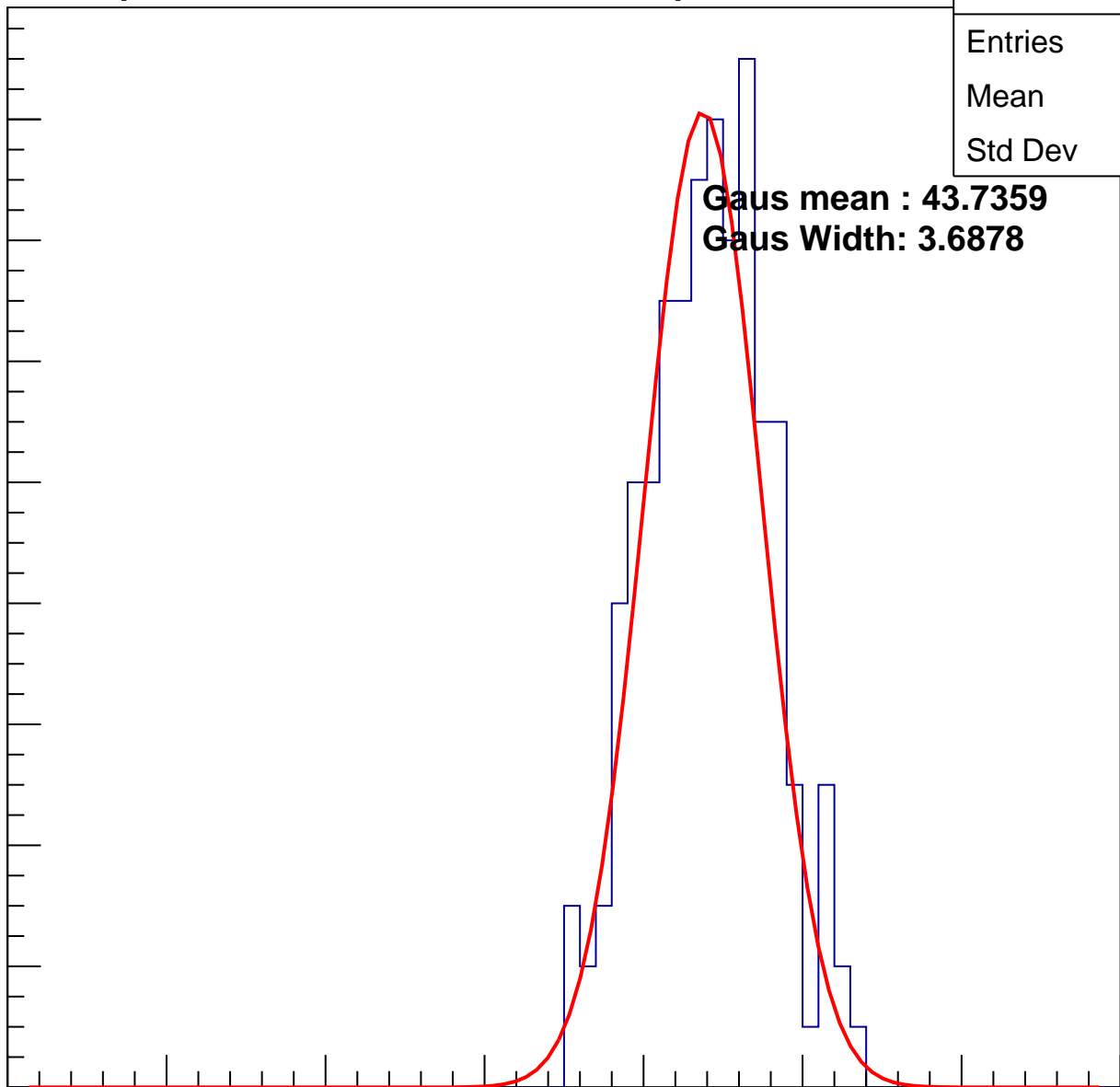
30

40

50

60

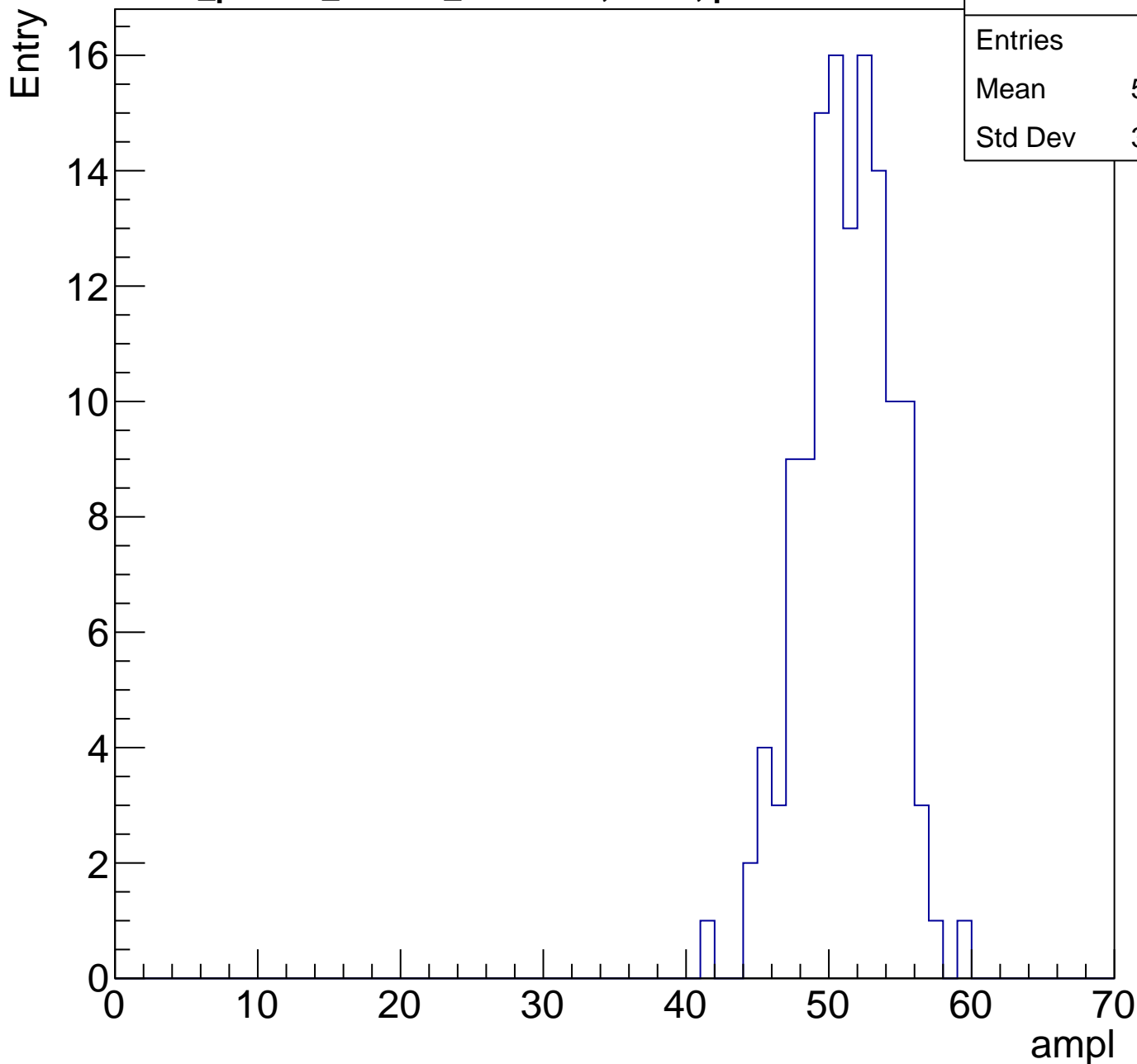
ampl



B1L001S, U22-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	50.77
Std Dev	3.085



B1L001S, U22-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

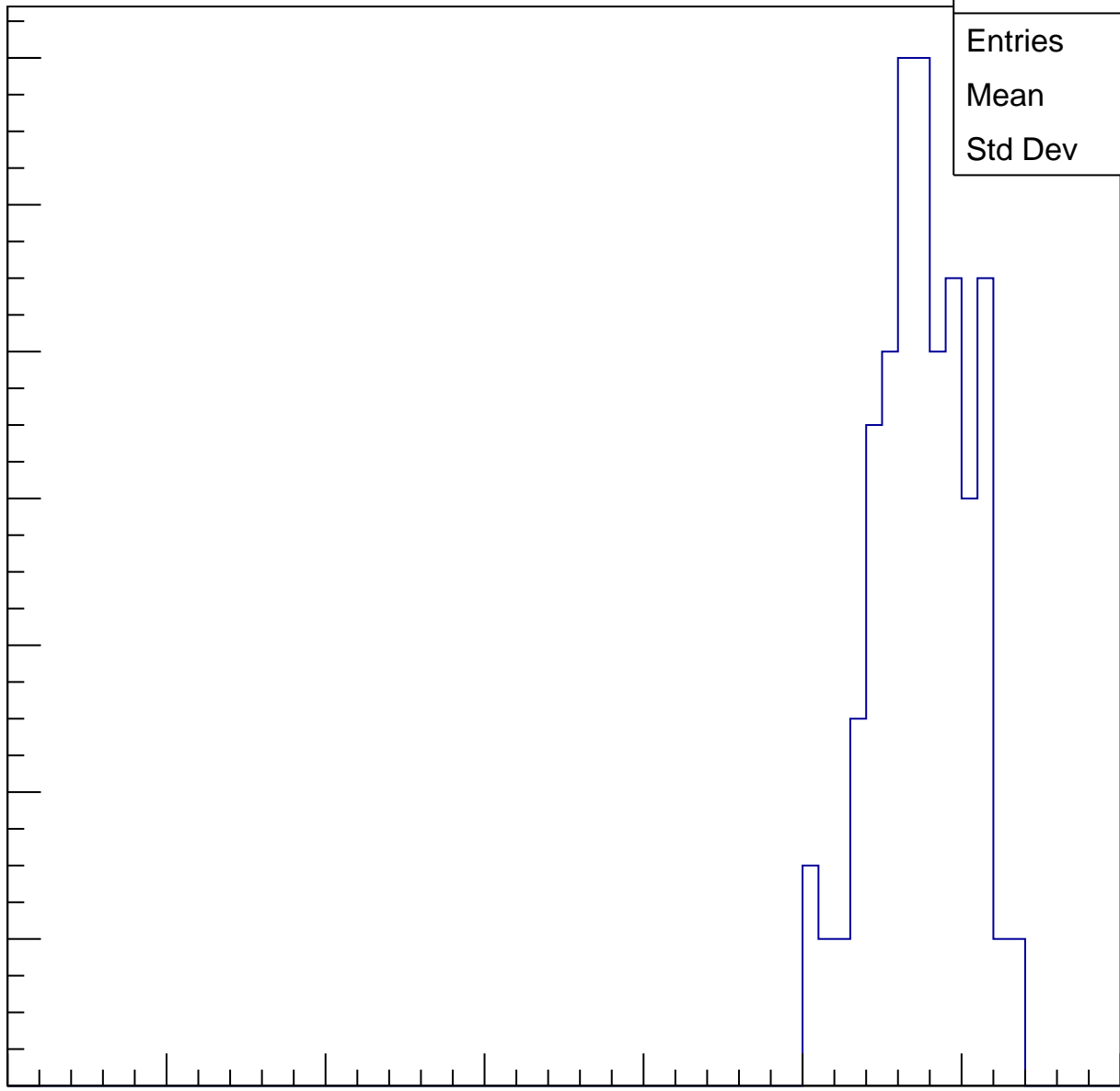
Entries	103
Mean	56.98
Std Dev	2.969

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch91, adc5

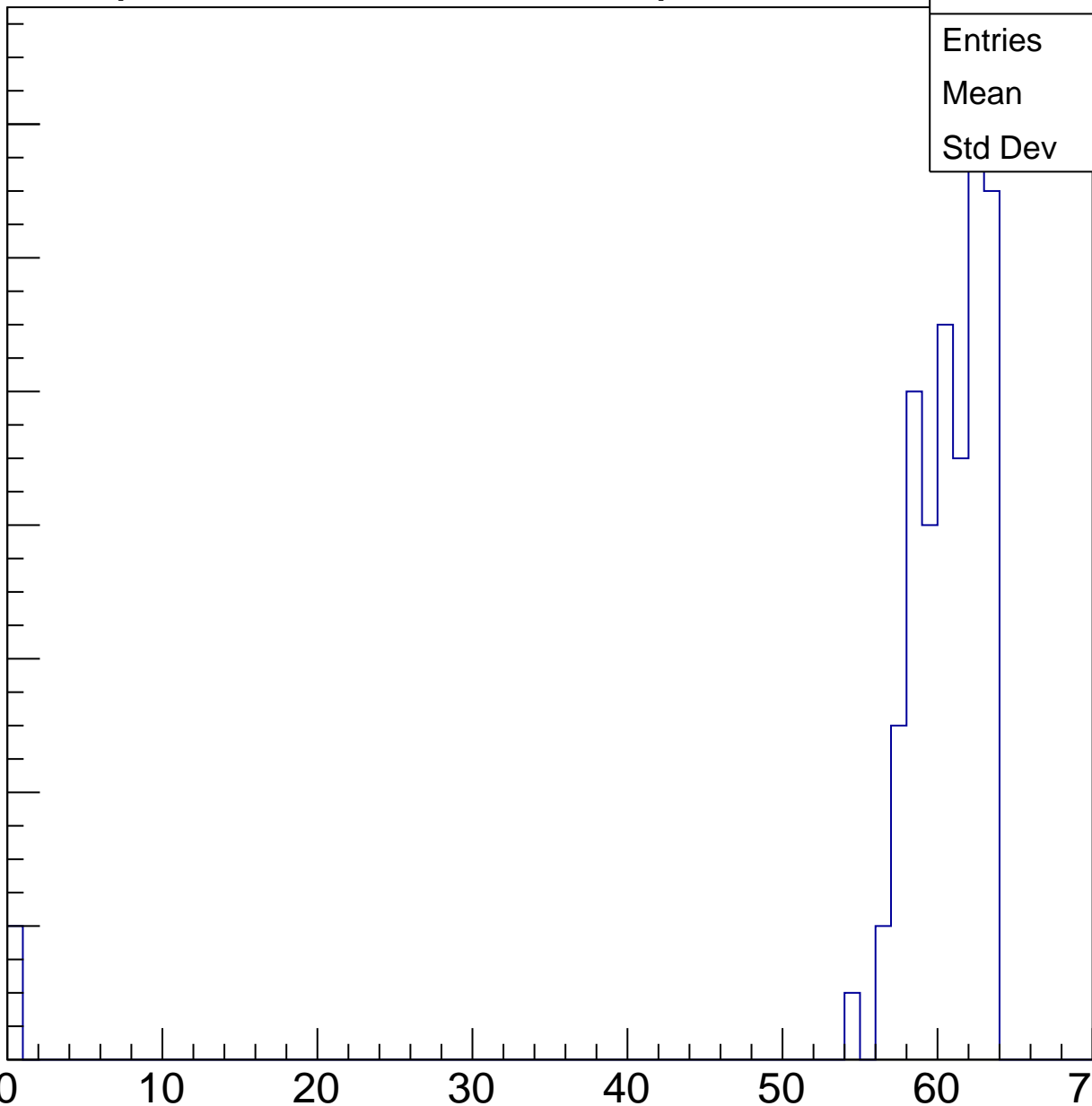
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.7
Std Dev	9.88

ampl

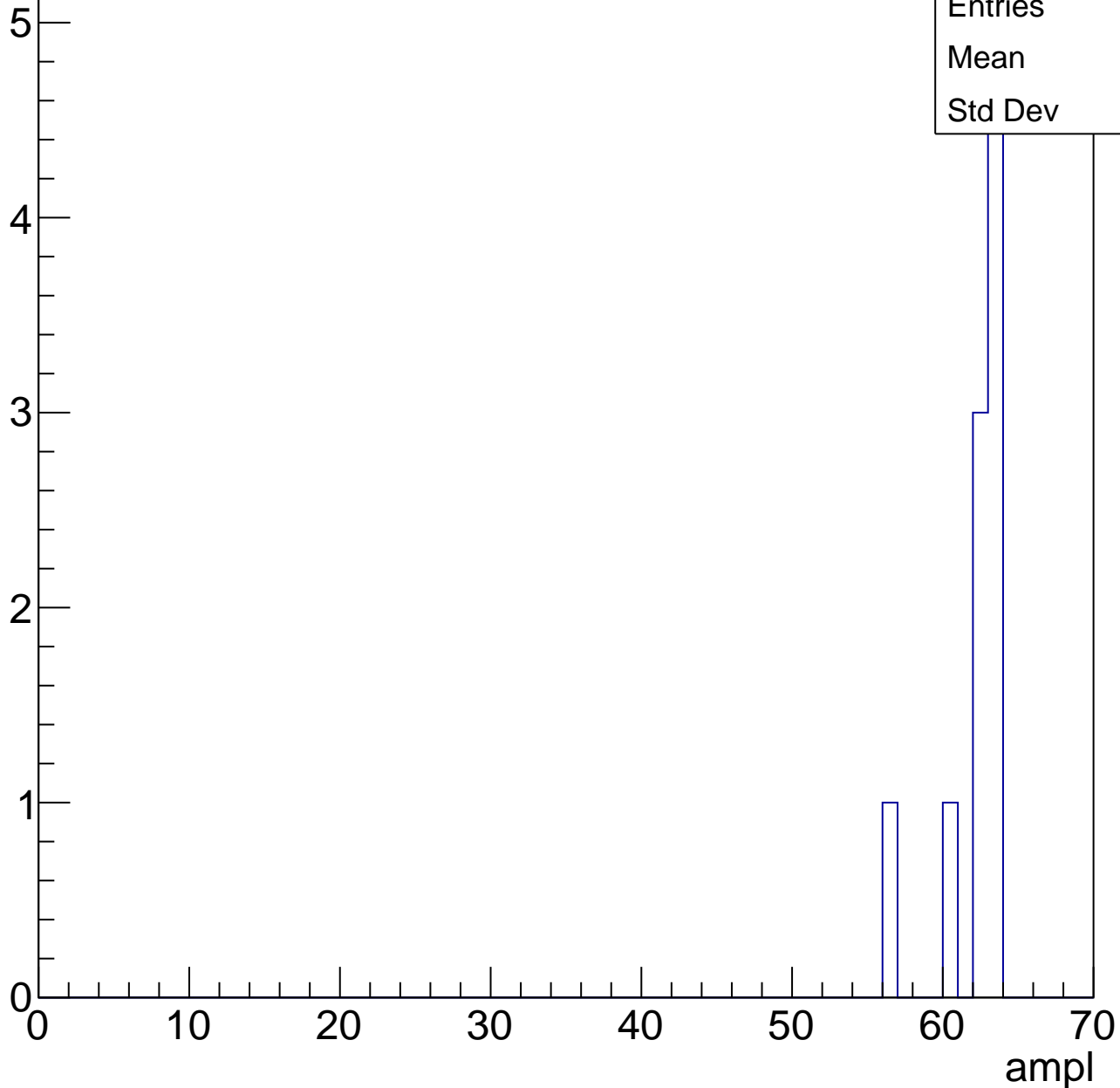


B1L001S, U22-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	61.7
Std Dev	2.1



B1L001S, U22-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch92, adc0

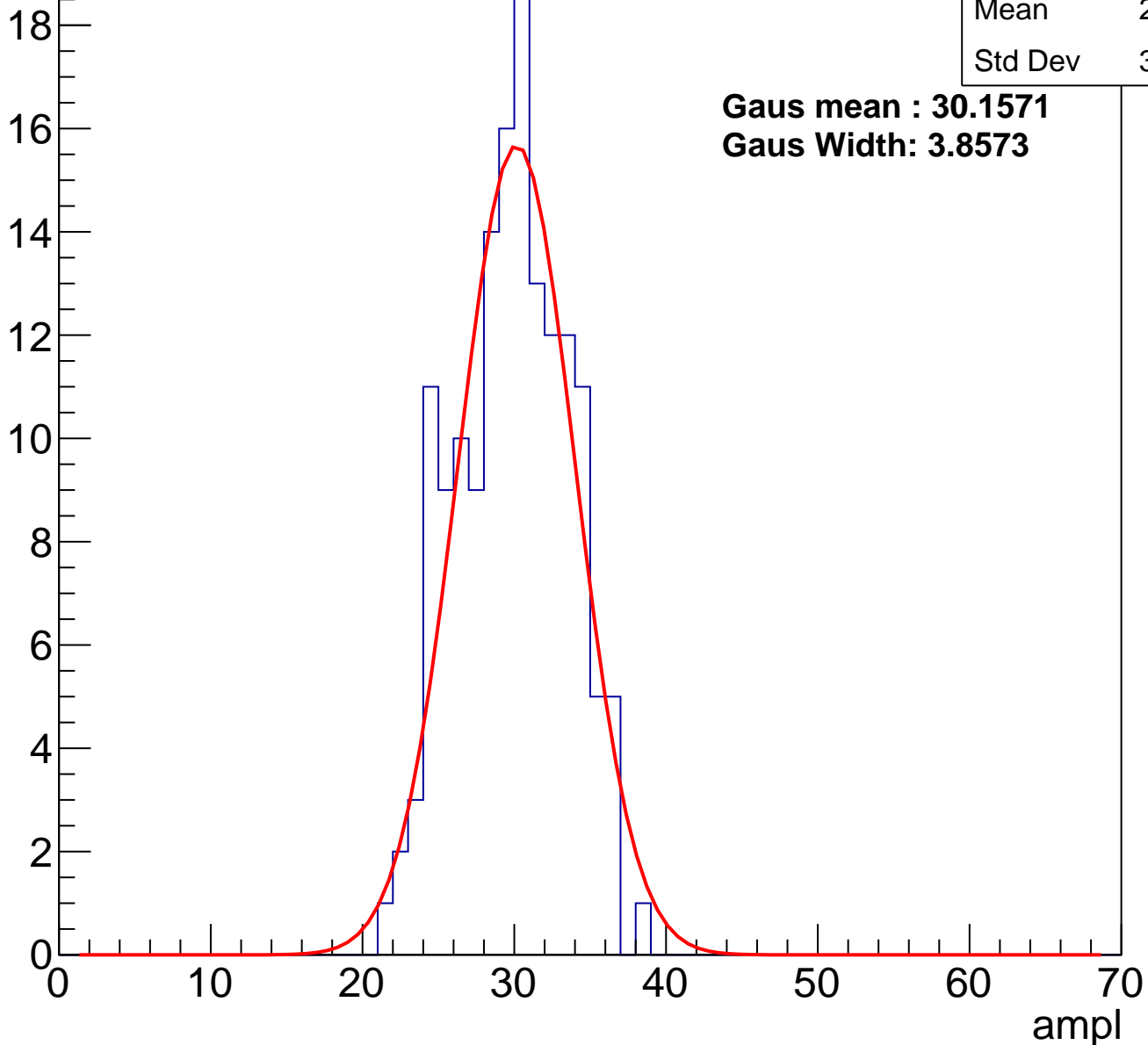
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.42
Std Dev	3.573

Gaus mean : 30.1571

Gaus Width: 3.8573

Entry



B1L001S, U22-ch92, adc1

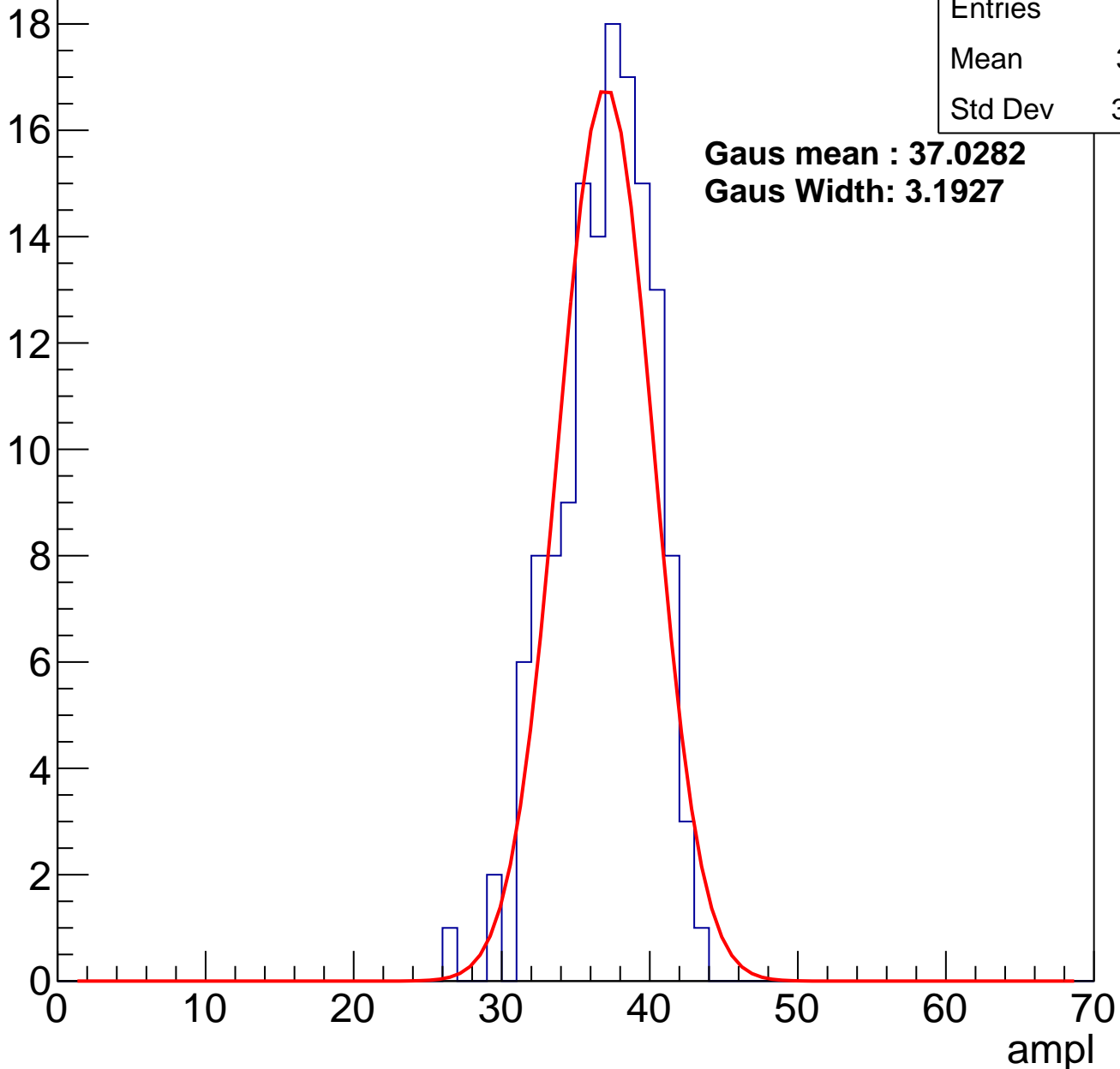
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.51
Std Dev	3.126

Gaus mean : 37.0282

Gaus Width: 3.1927

Entry



B1L001S, U22-ch92, adc2

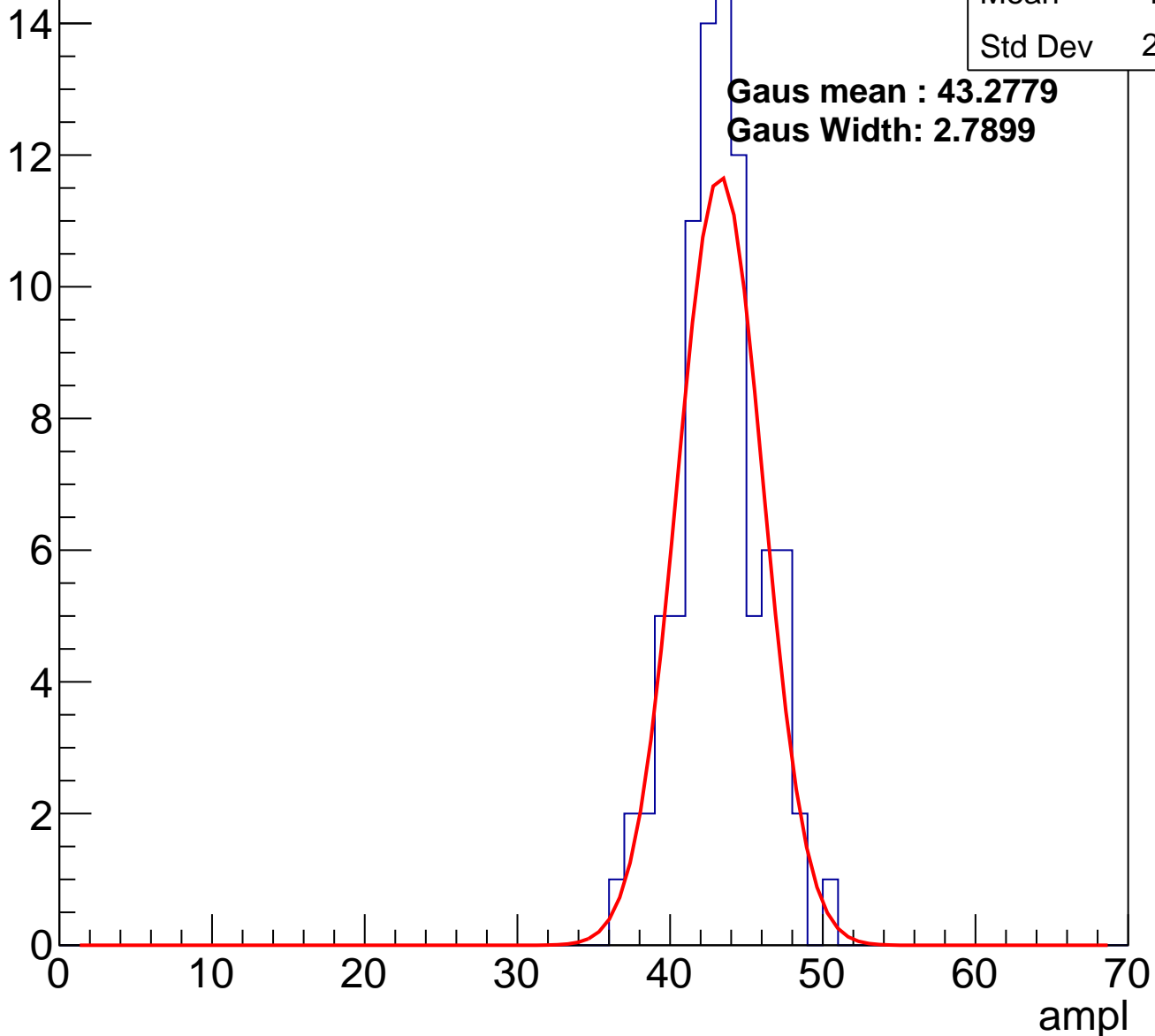
calib_packv5_042523_0143.root, FC#2, port C2

Entries	87
Mean	42.78
Std Dev	2.693

Gaus mean : 43.2779

Gaus Width: 2.7899

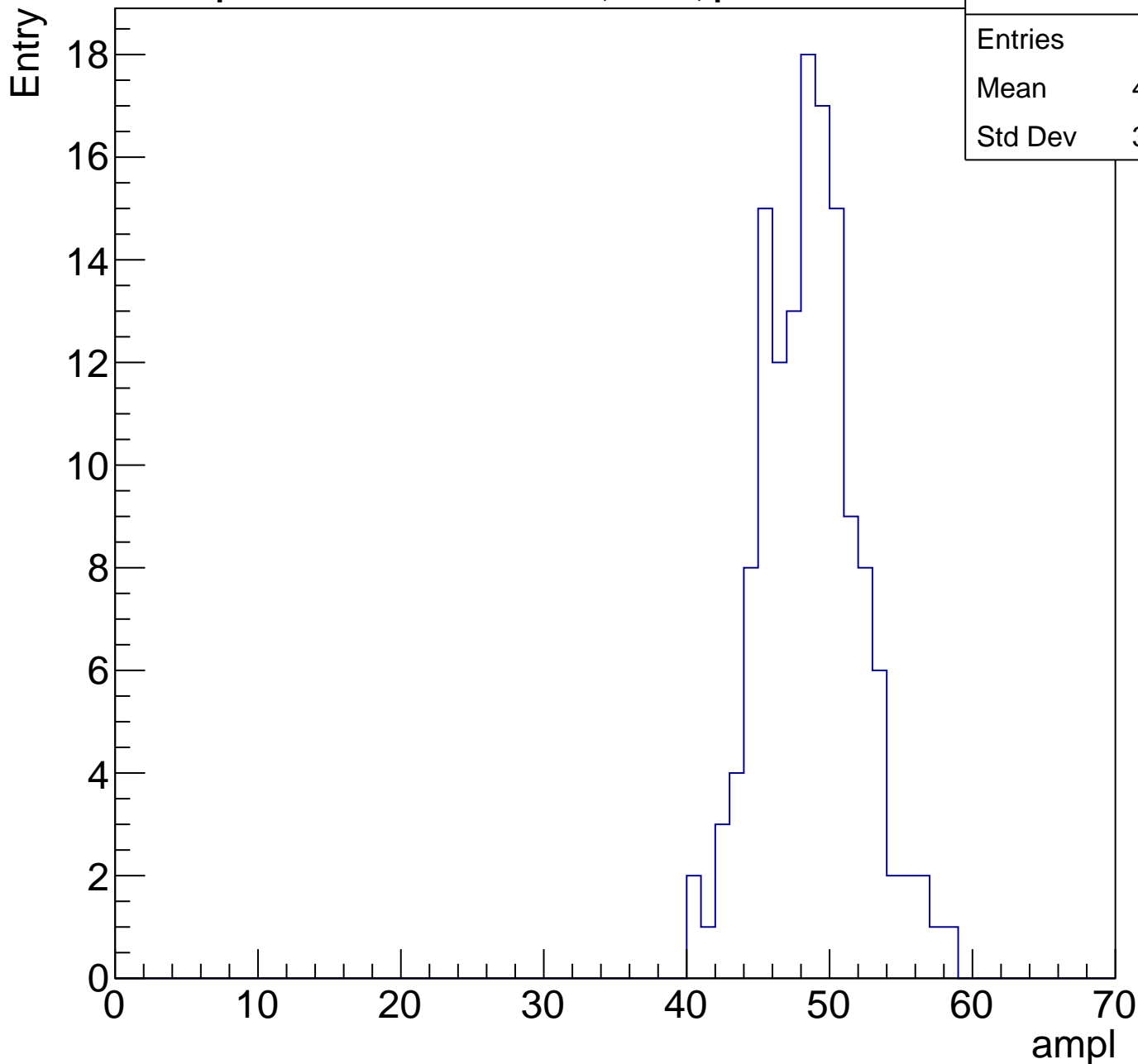
Entry



B1L001S, U22-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.16
Std Dev	3.414



B1L001S, U22-ch92, adc4

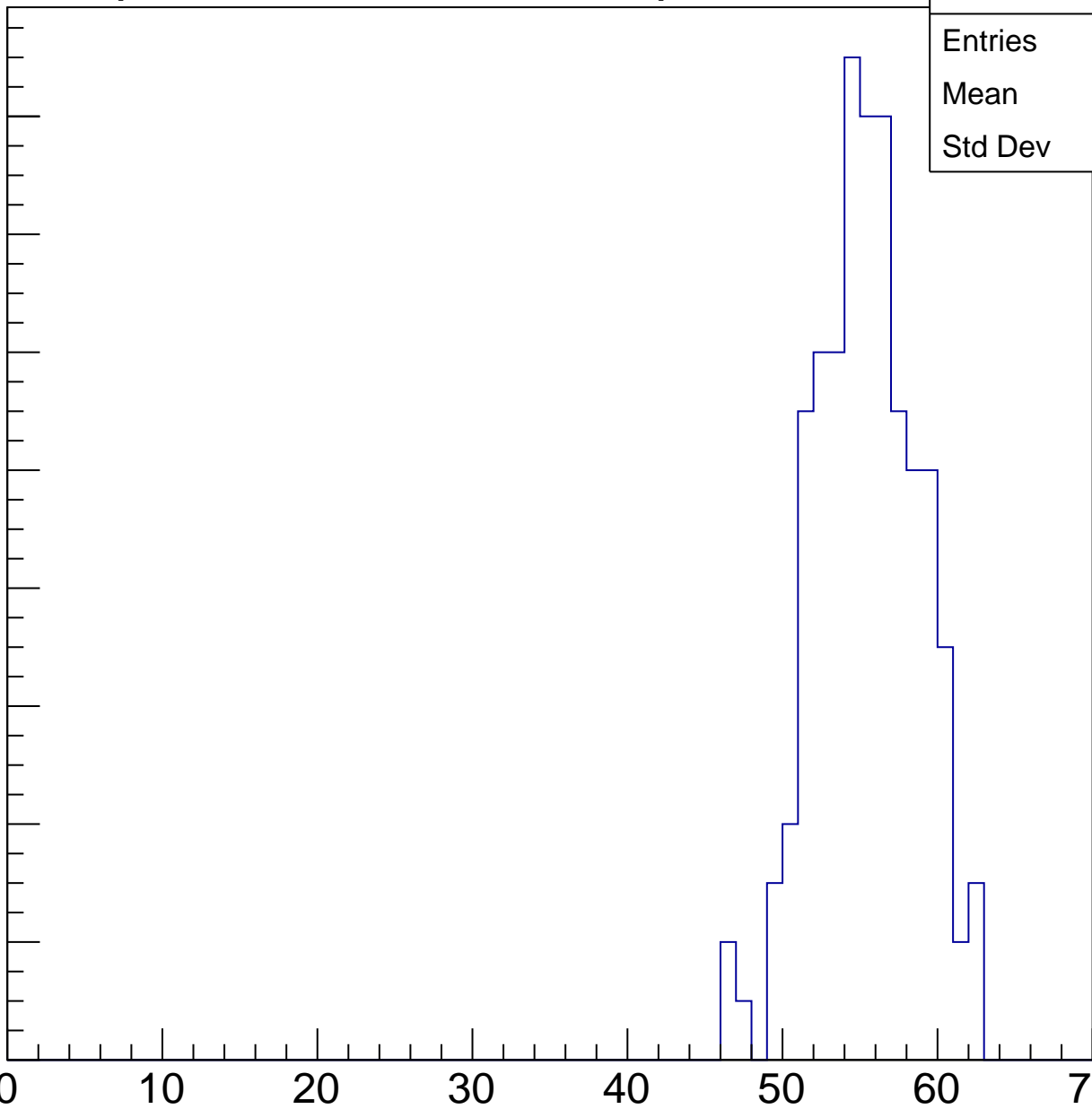
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	137
Mean	54.93
Std Dev	3.294

ampl



B1L001S, U22-ch92, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

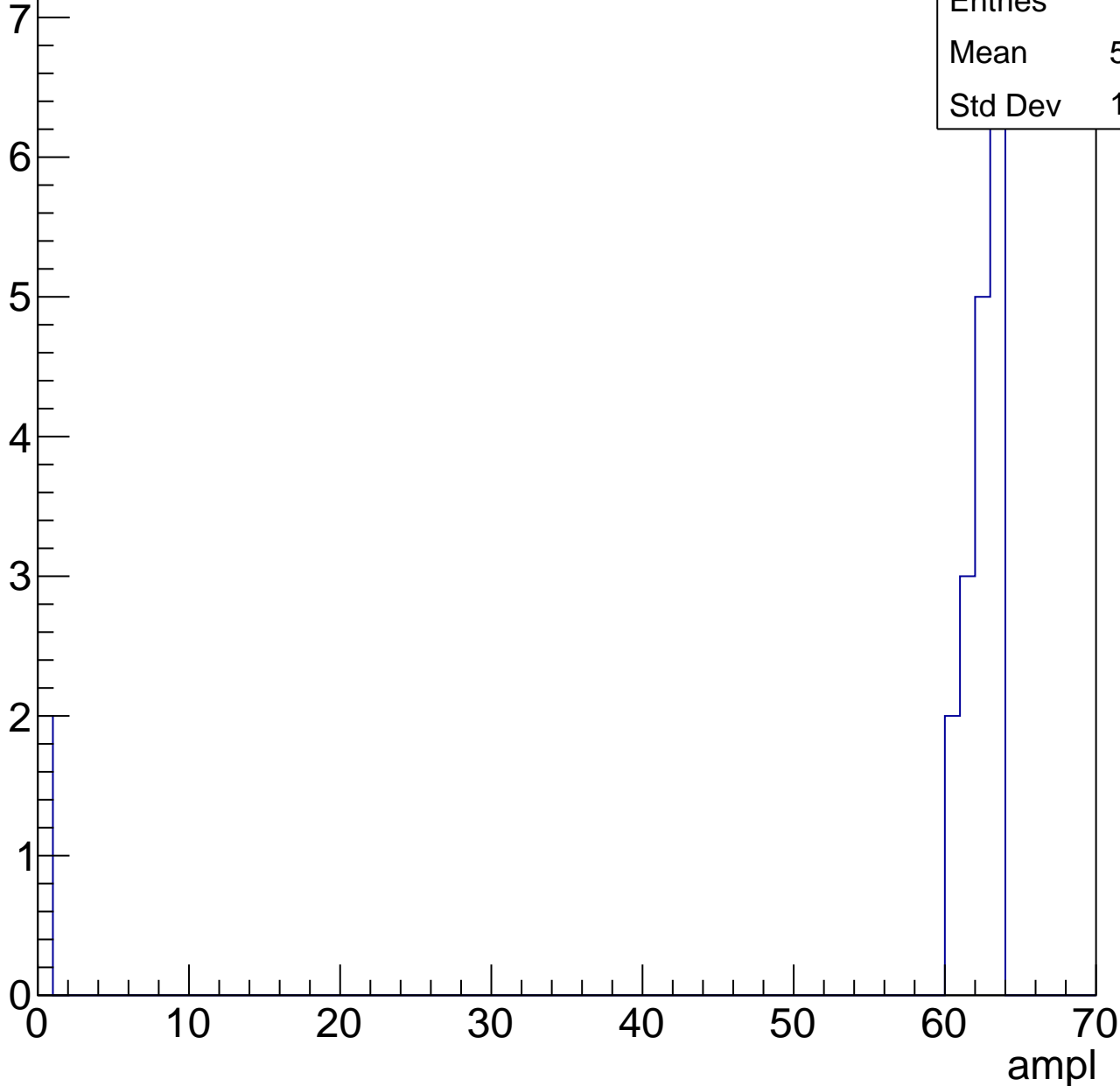
Entries	86
Mean	59.86
Std Dev	2.441

B1L001S, U22-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	55.47
Std Dev	19.05



B1L001S, U22-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch93, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.28
Std Dev	4.207

Gaus mean : 30.9176

Gaus Width: 3.2337

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

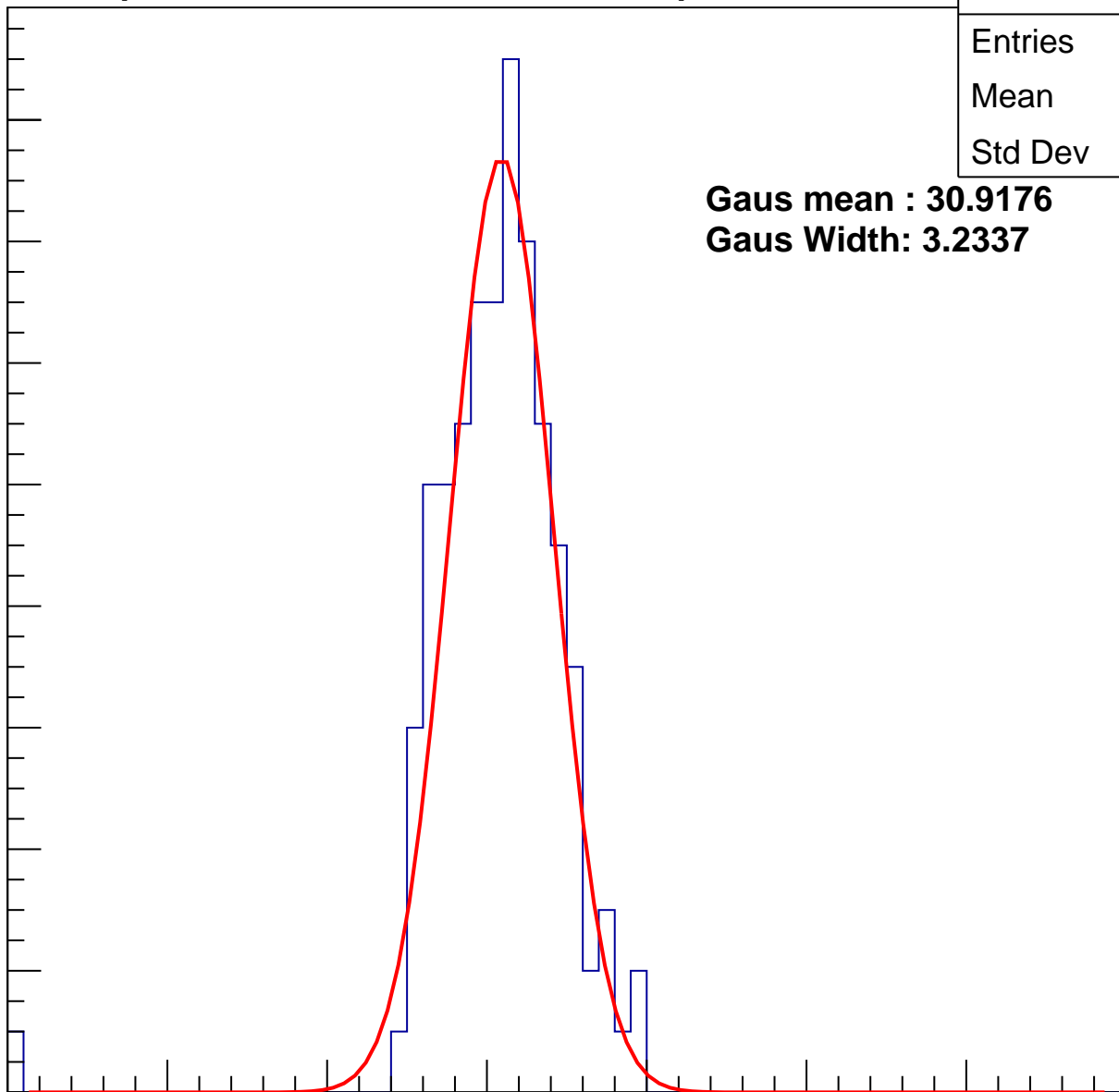
40

50

60

70

ampl



B1L001S, U22-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	36.77
Std Dev	3.044

Gaus mean : 37.1979

Gaus Width: 3.1829

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

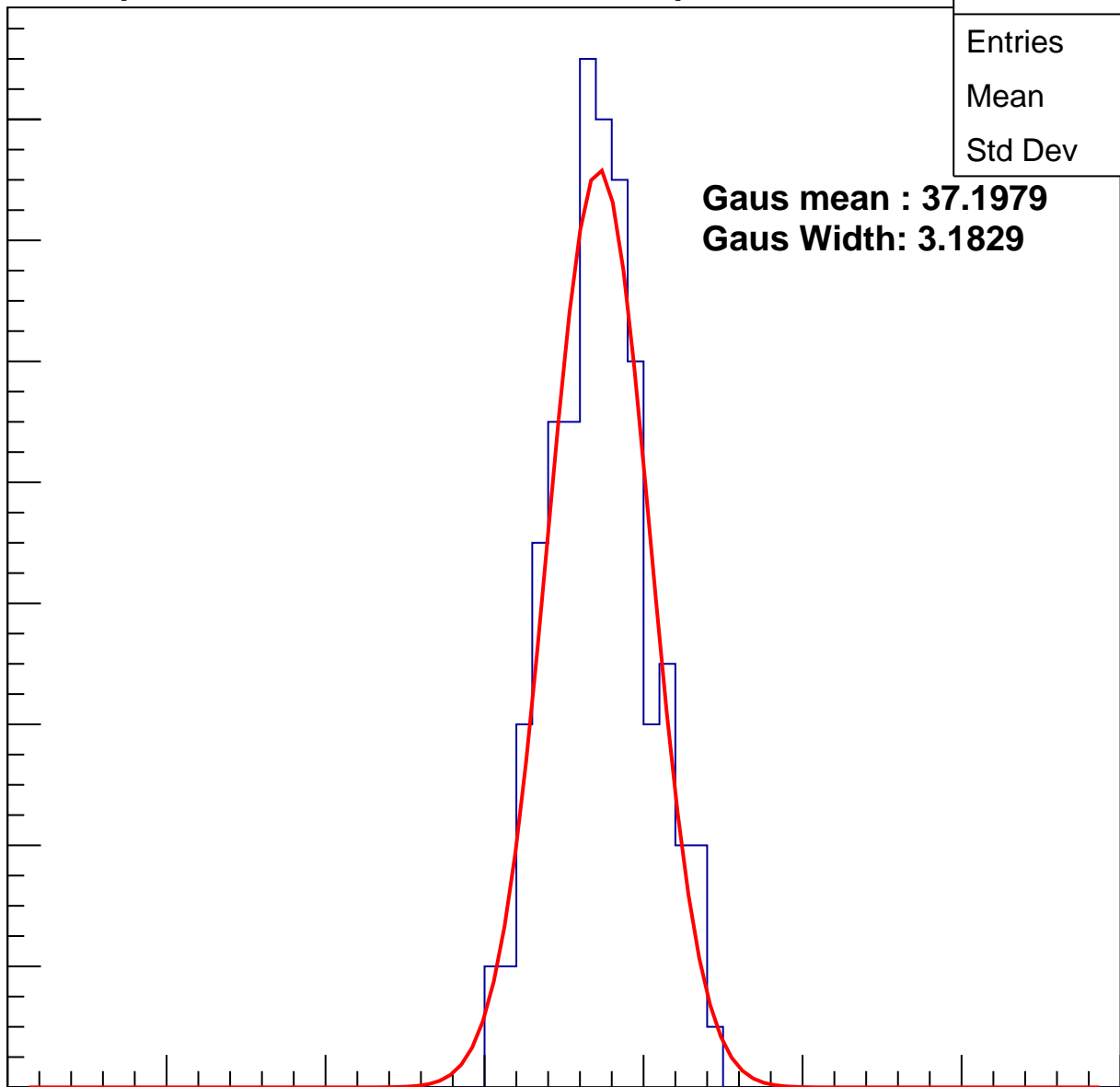
40

50

60

70

ampl



B1L001S, U22-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

156

Mean

44.01

Std Dev

3.595

Gaus mean : 44.4326

Gaus Width: 3.3561

0

2

4

6

8

10

12

14

16

B1L001S, U22-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

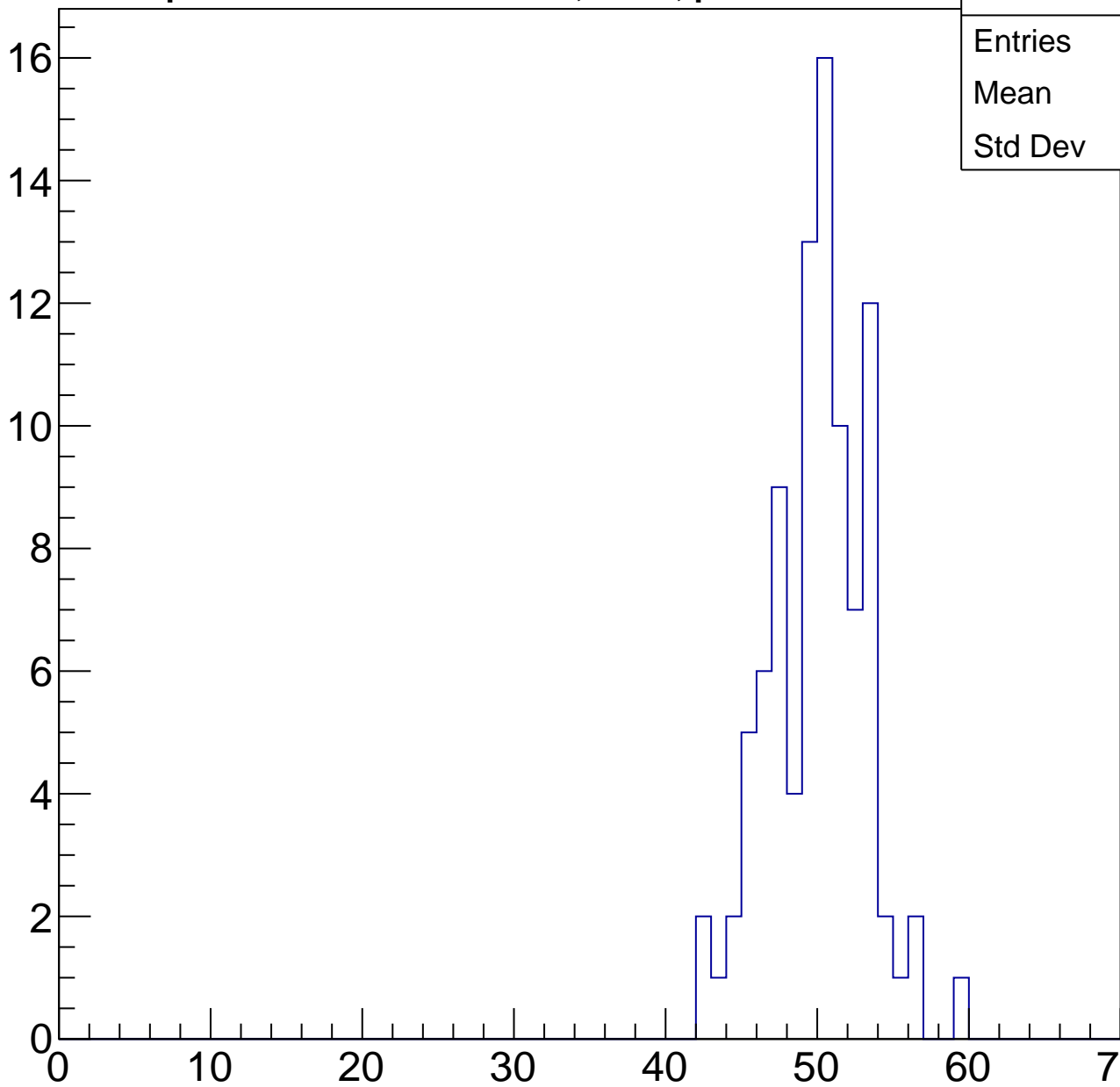
Entries	93
Mean	49.59
Std Dev	3.146

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

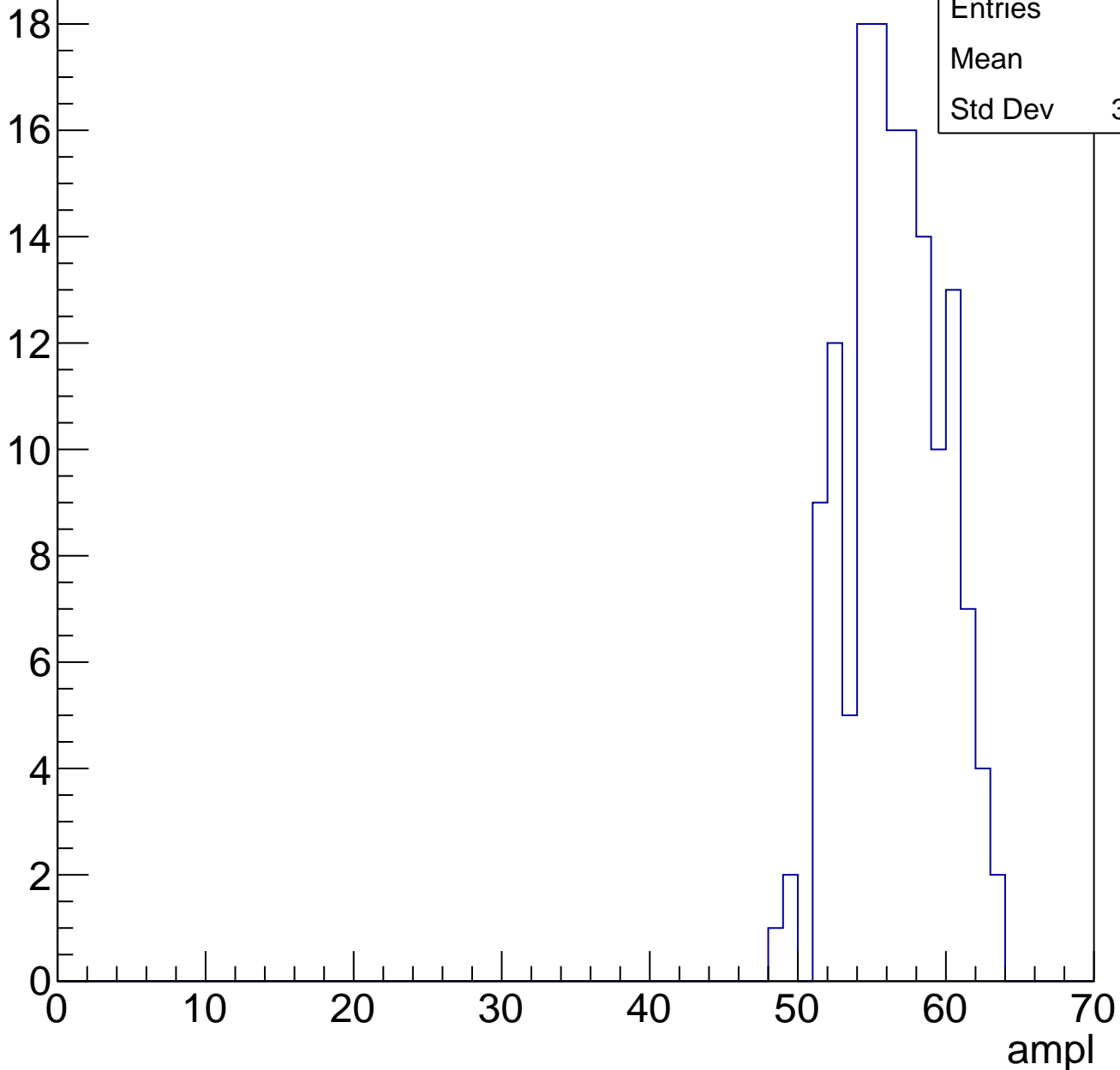


B1L001S, U22-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	56.1
Std Dev	3.194

Entry



B1L001S, U22-ch93, adc5

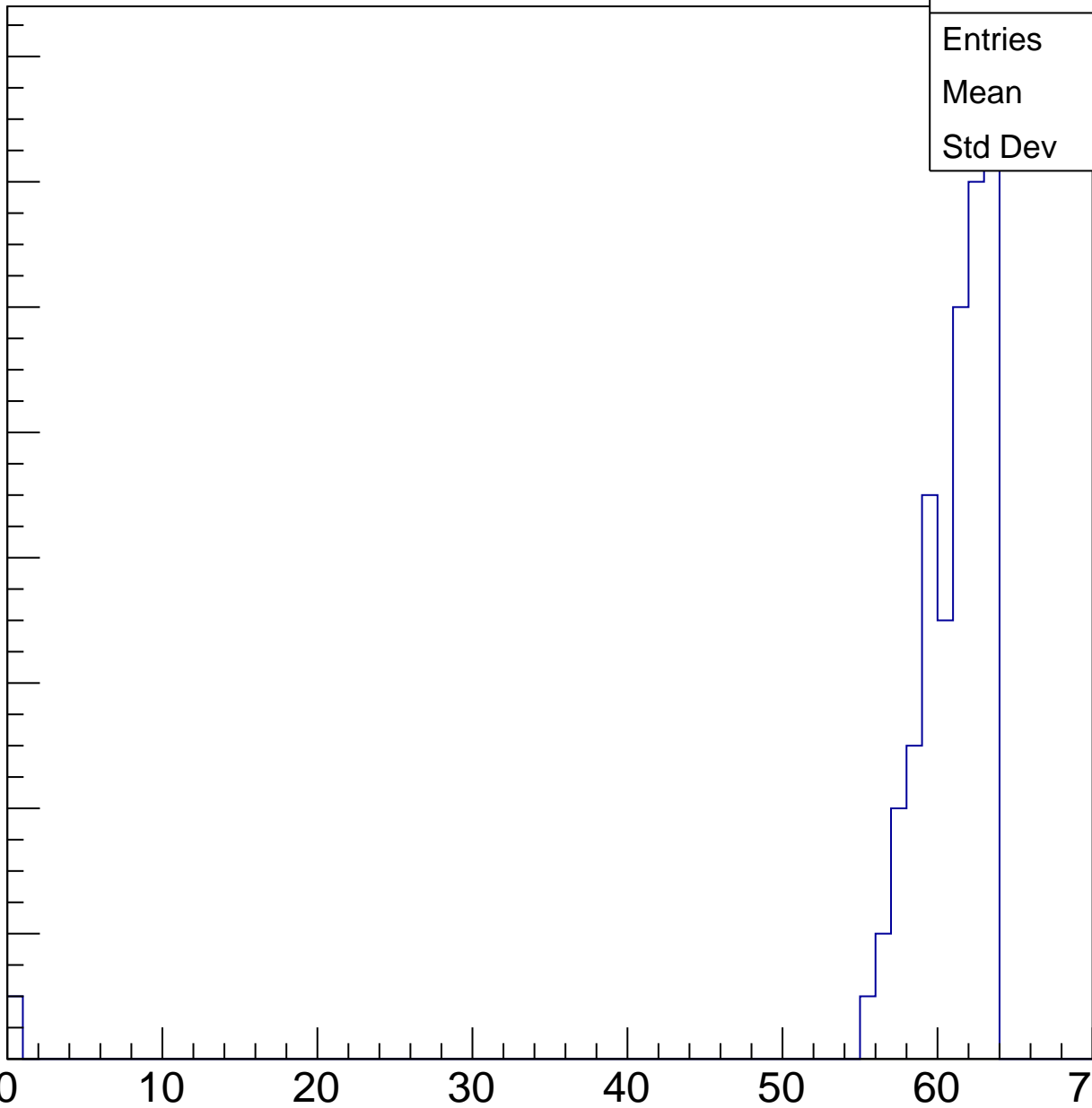
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.77
Std Dev	7.44

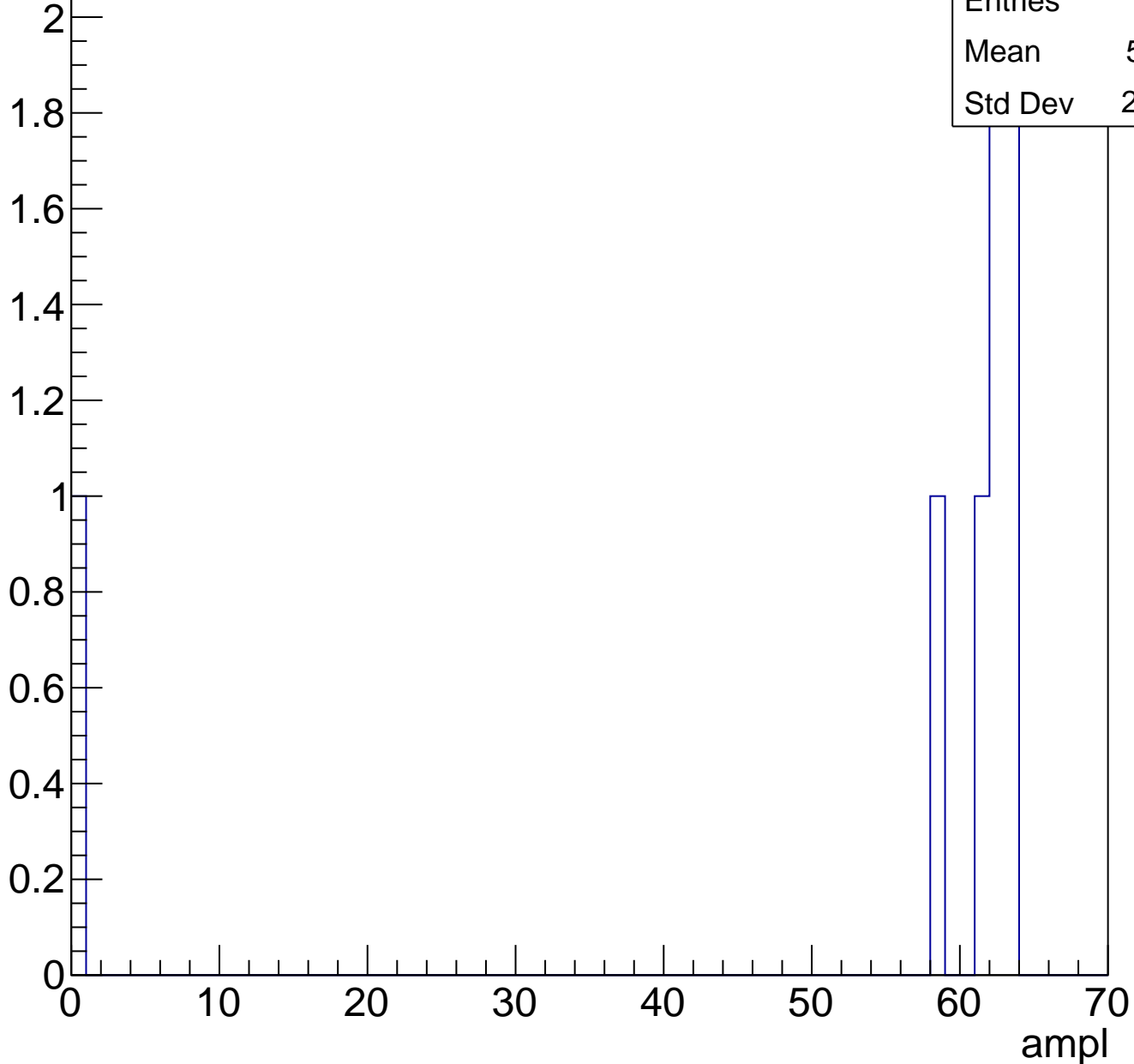
ampl



B1L001S, U22-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl

B1L001S, U22-ch94, adc0

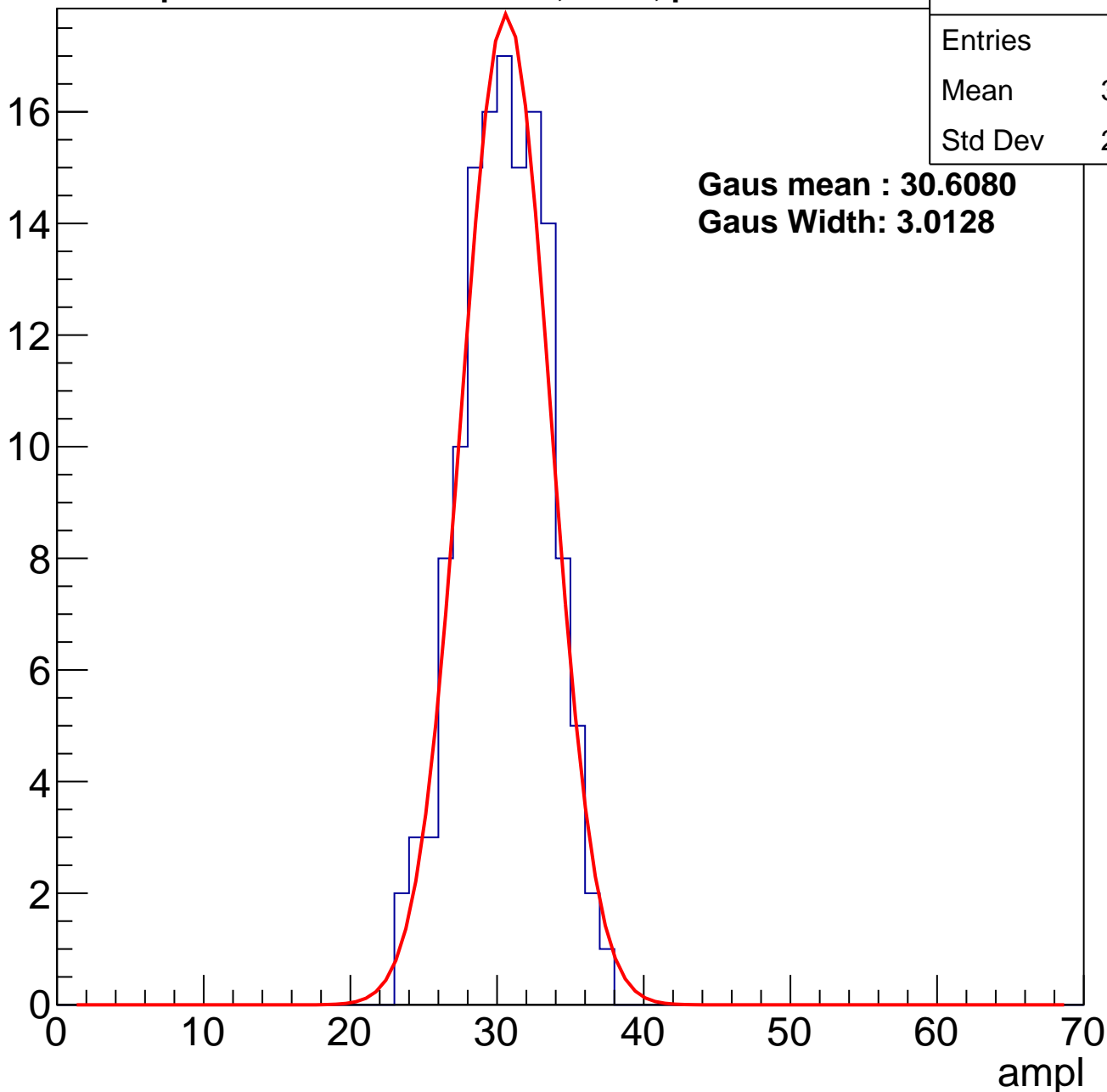
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.07
Std Dev	2.923

Gaus mean : 30.6080

Gaus Width: 3.0128

Entry



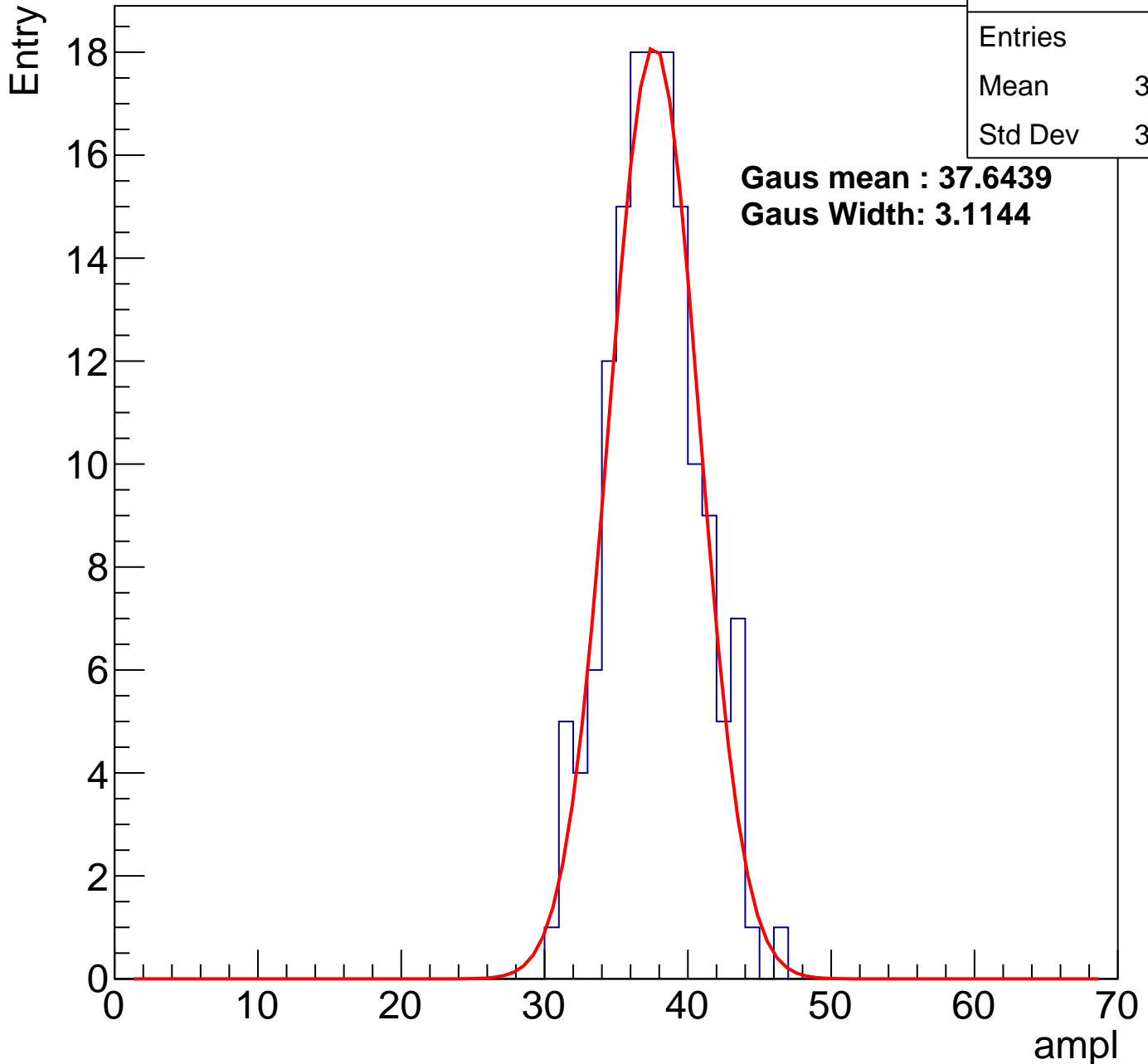
B1L001S, U22-ch94, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.22
Std Dev	3.133

Gaus mean : 37.6439

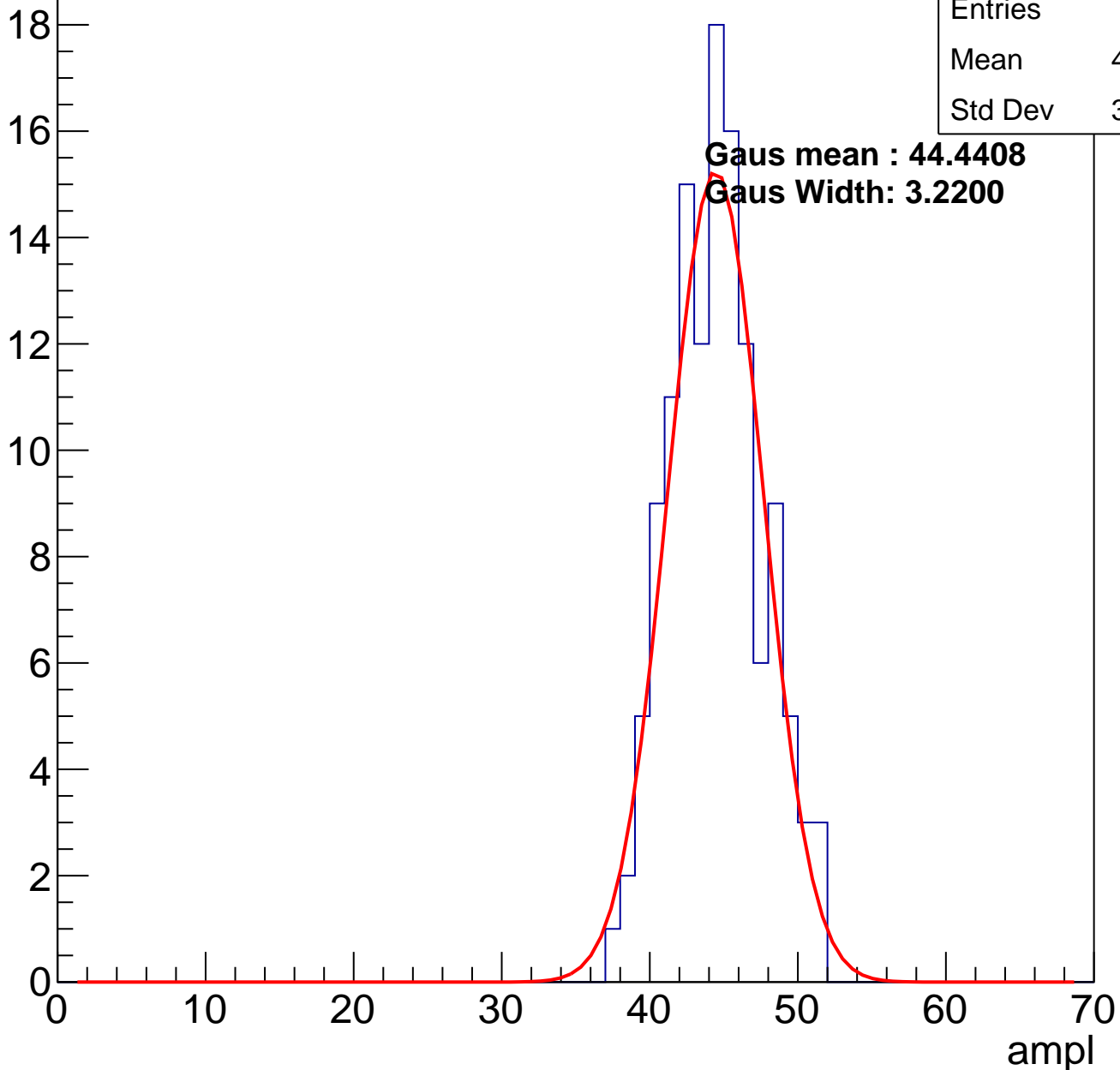
Gaus Width: 3.1144



B1L001S, U22-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

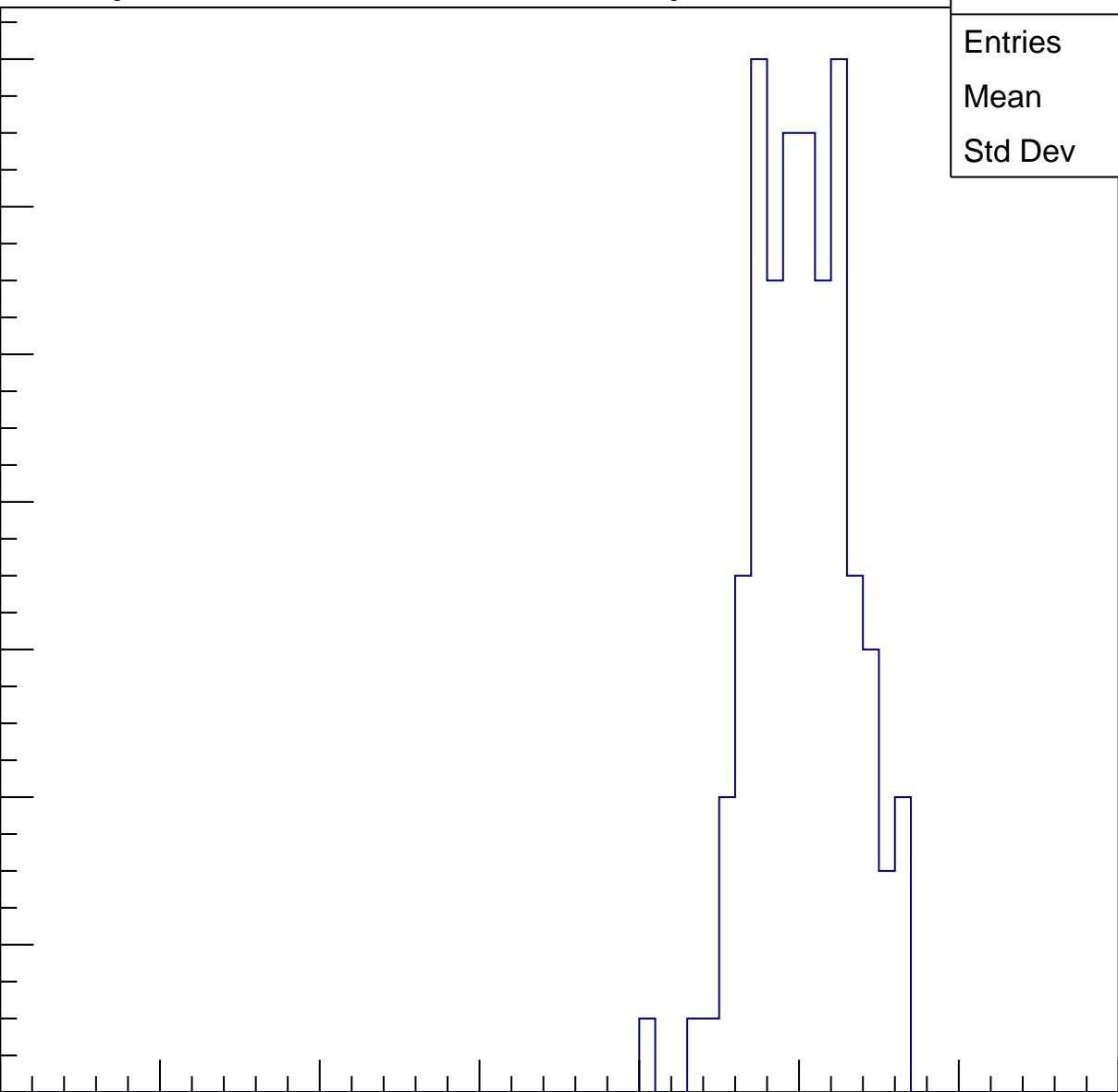
Entries	110
Mean	49.77
Std Dev	3.044

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

119

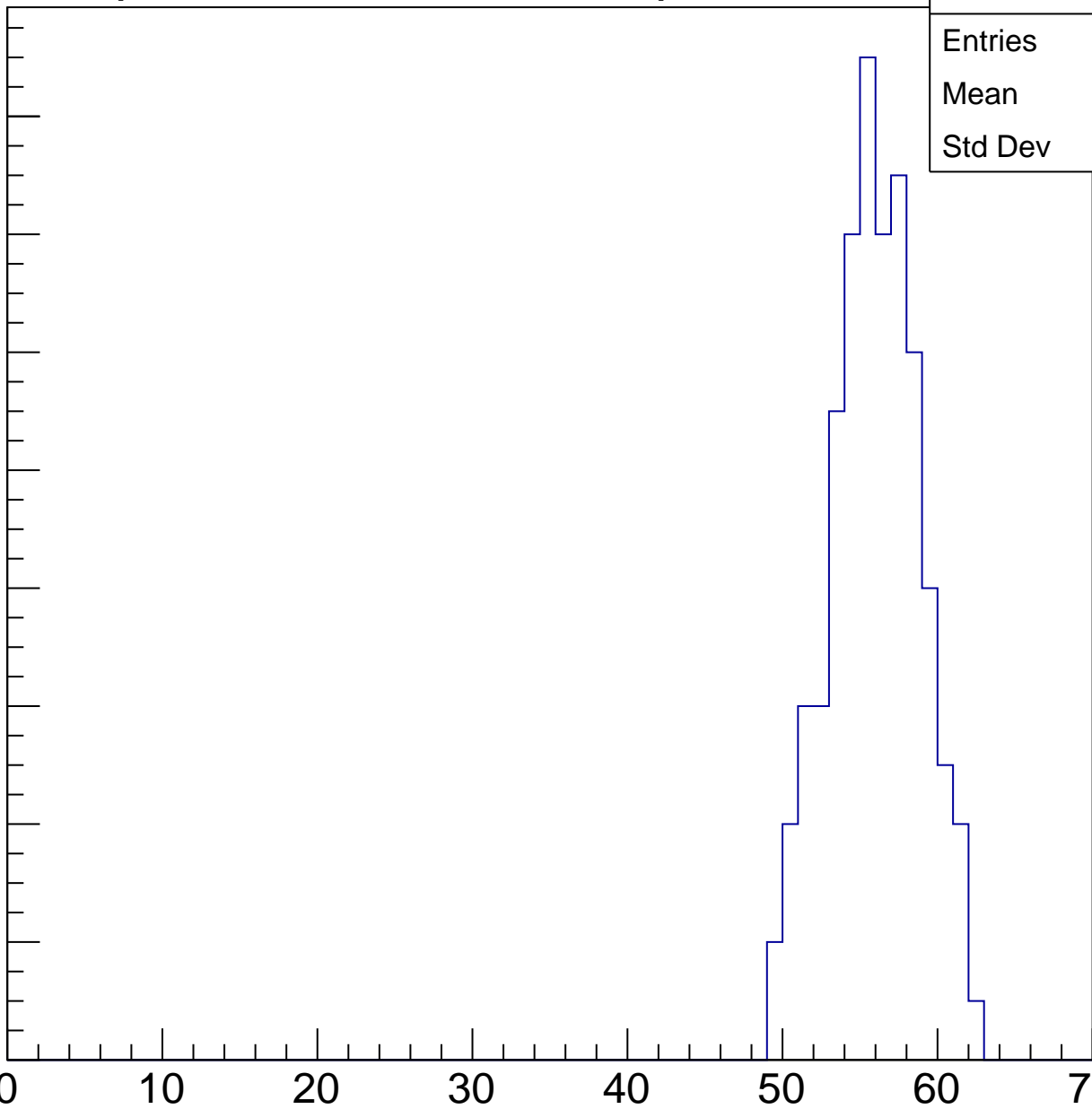
Mean

55.49

Std Dev

2.869

ampl



B1L001S, U22-ch94, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

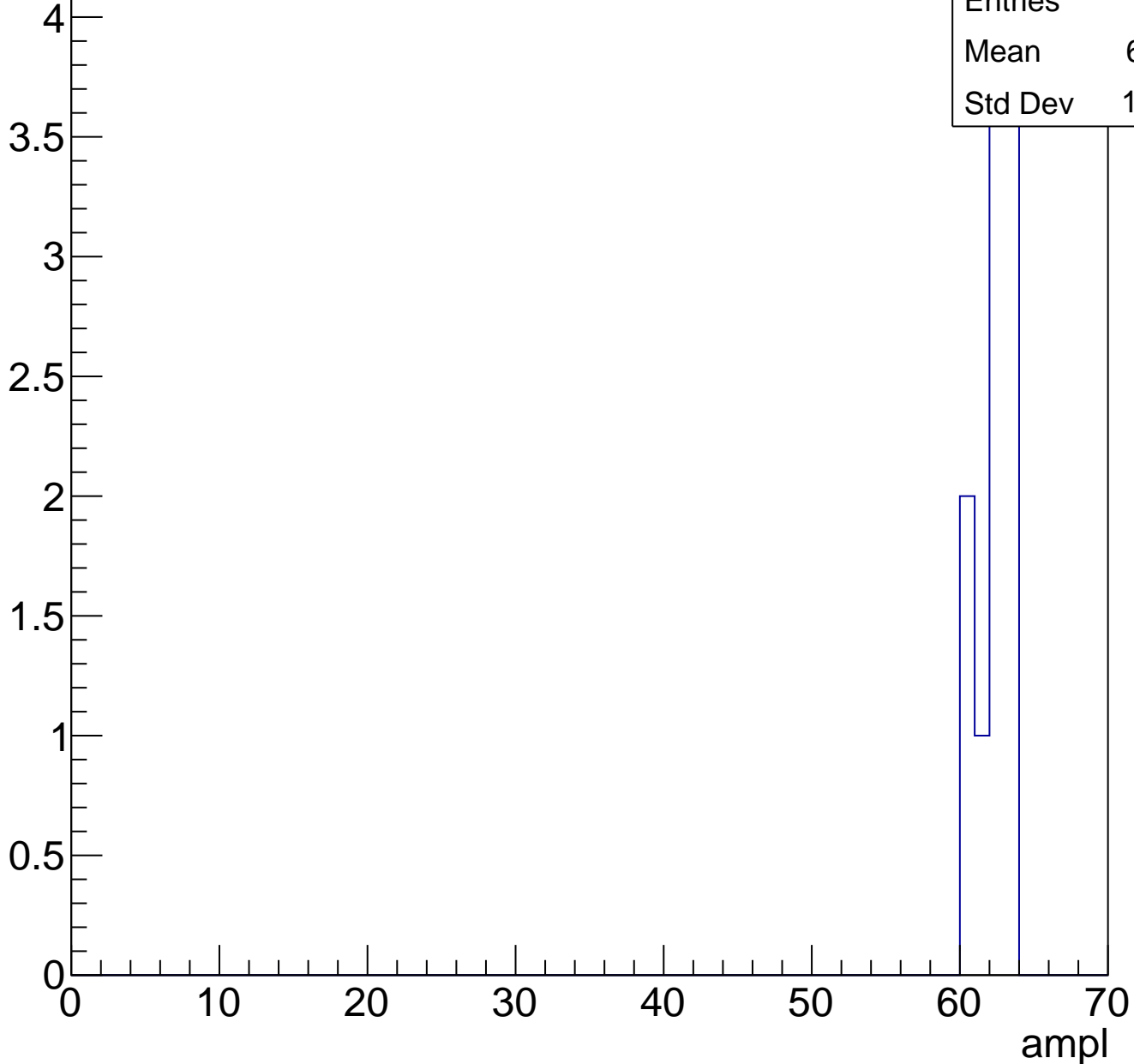
ampl

Entries	89
Mean	58.93
Std Dev	9.197

B1L001S, U22-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch95, adc0

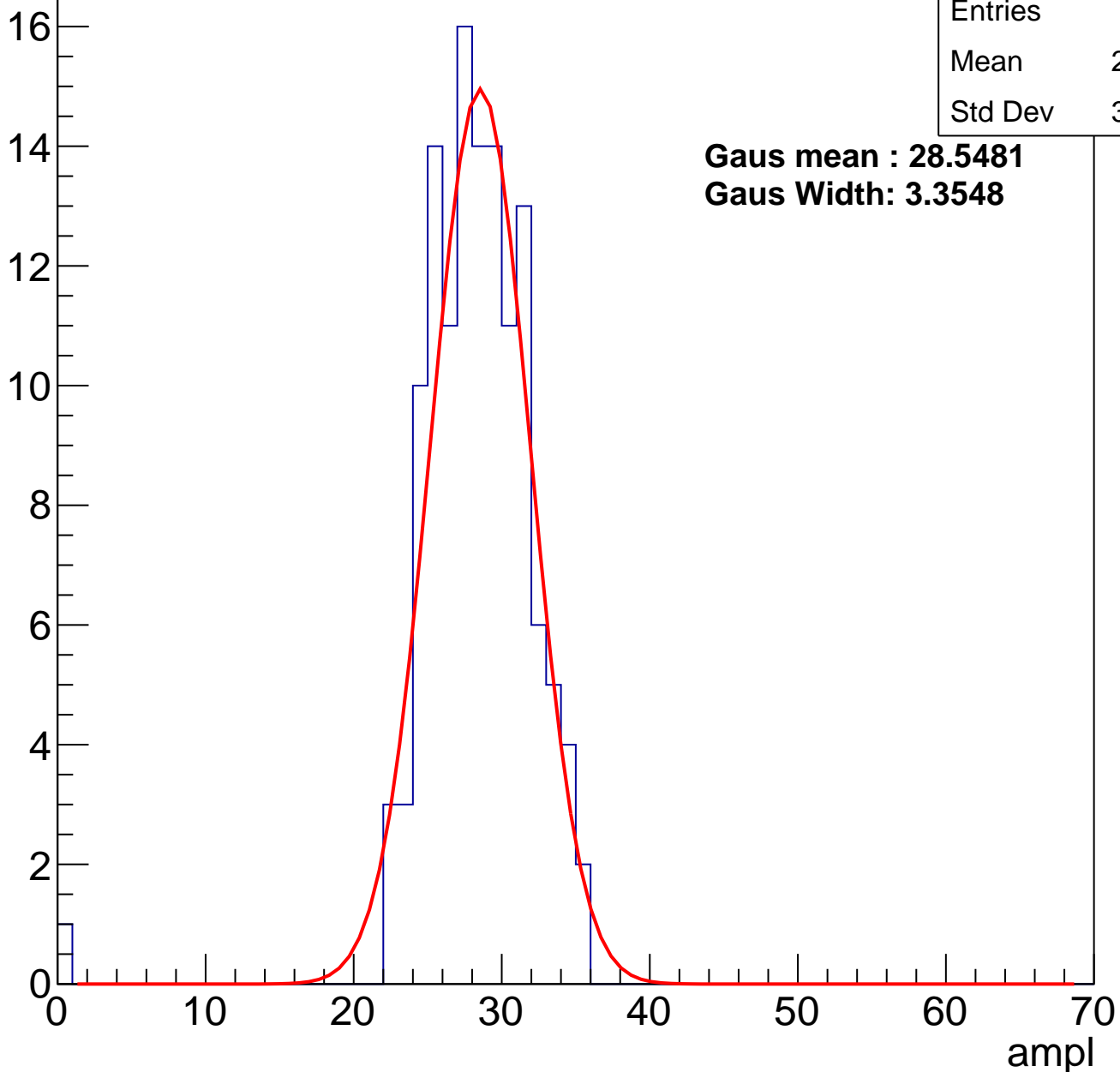
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	27.85
Std Dev	3.917

Gaus mean : 28.5481

Gaus Width: 3.3548

Entry



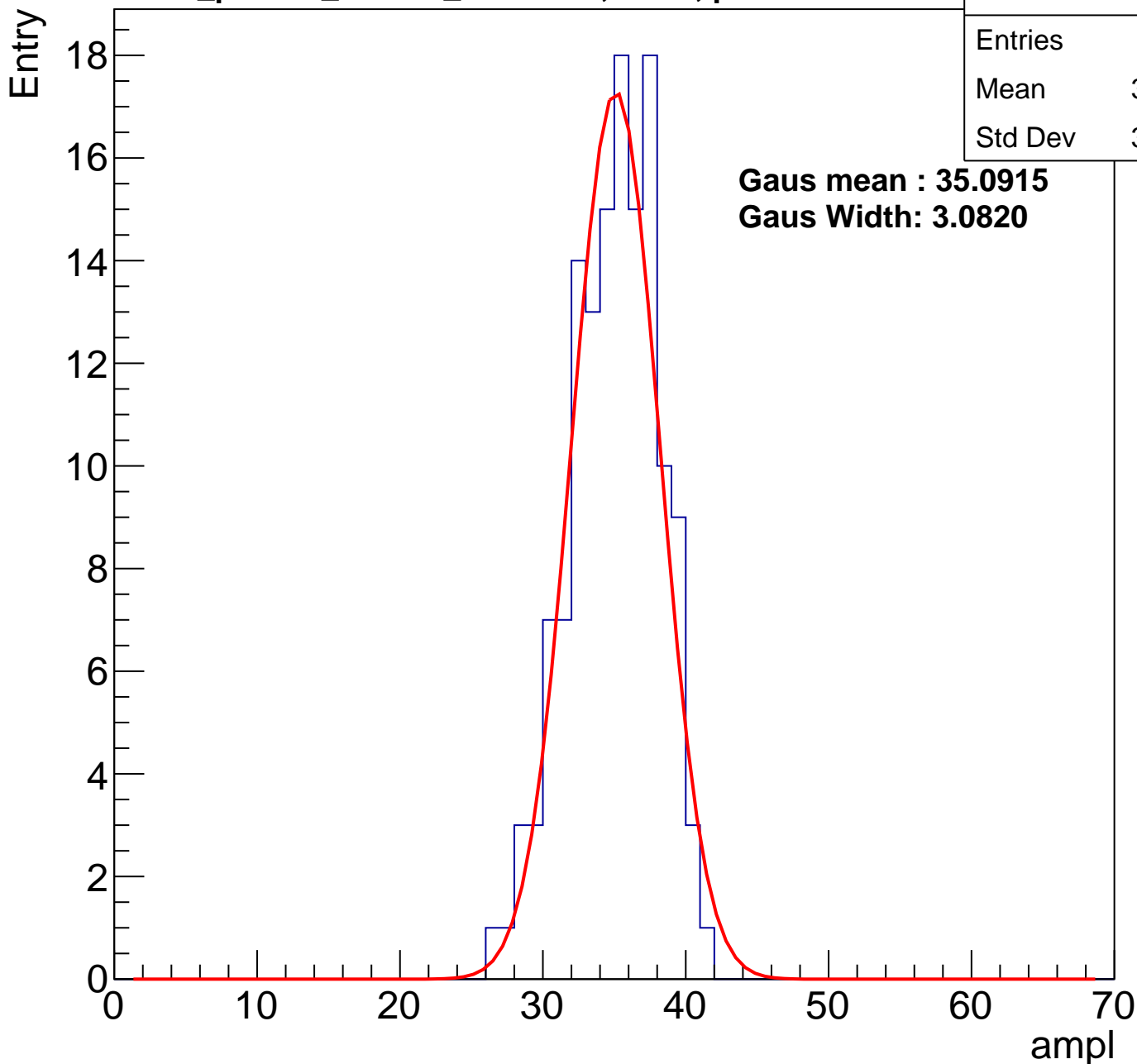
B1L001S, U22-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	34.54
Std Dev	3.048

Gaus mean : 35.0915

Gaus Width: 3.0820



B1L001S, U22-ch95, adc2

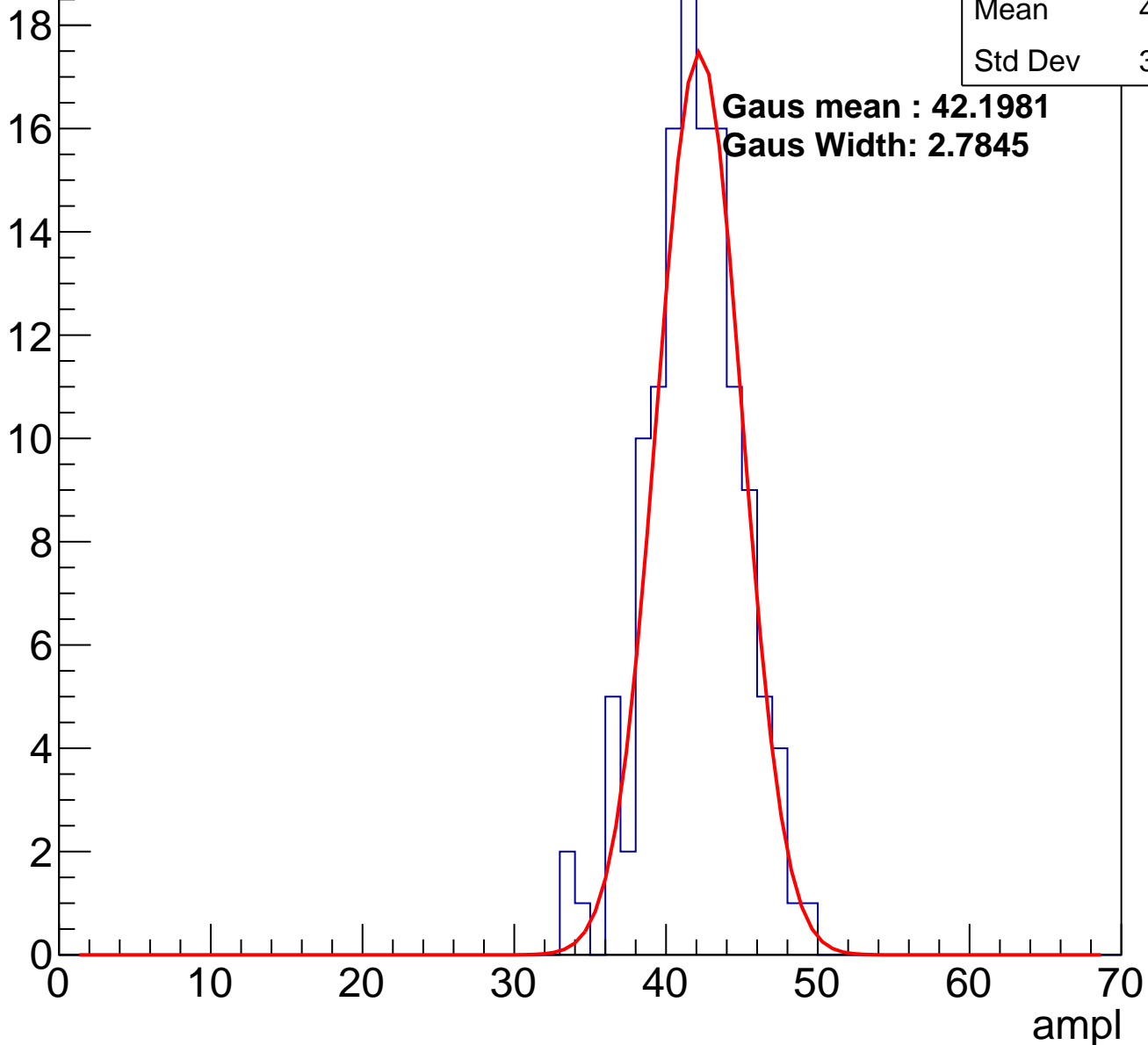
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	41.44
Std Dev	3.004

Gaus mean : 42.1981

Gaus Width: 2.7845

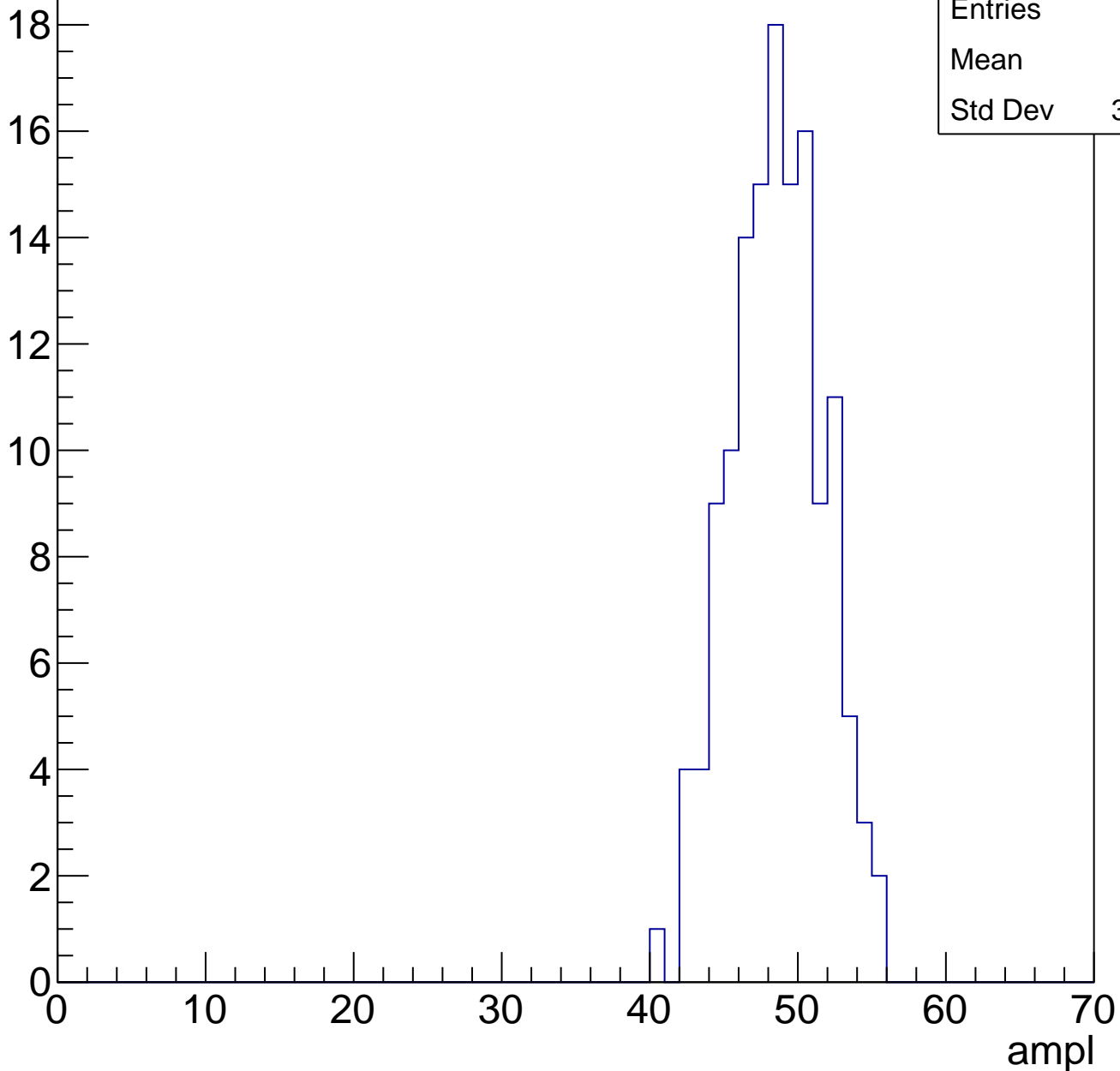
Entry



B1L001S, U22-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	136
Mean	48.1
Std Dev	3.066

B1L001S, U22-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

55.02

Std Dev

3.467

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch95, adc5

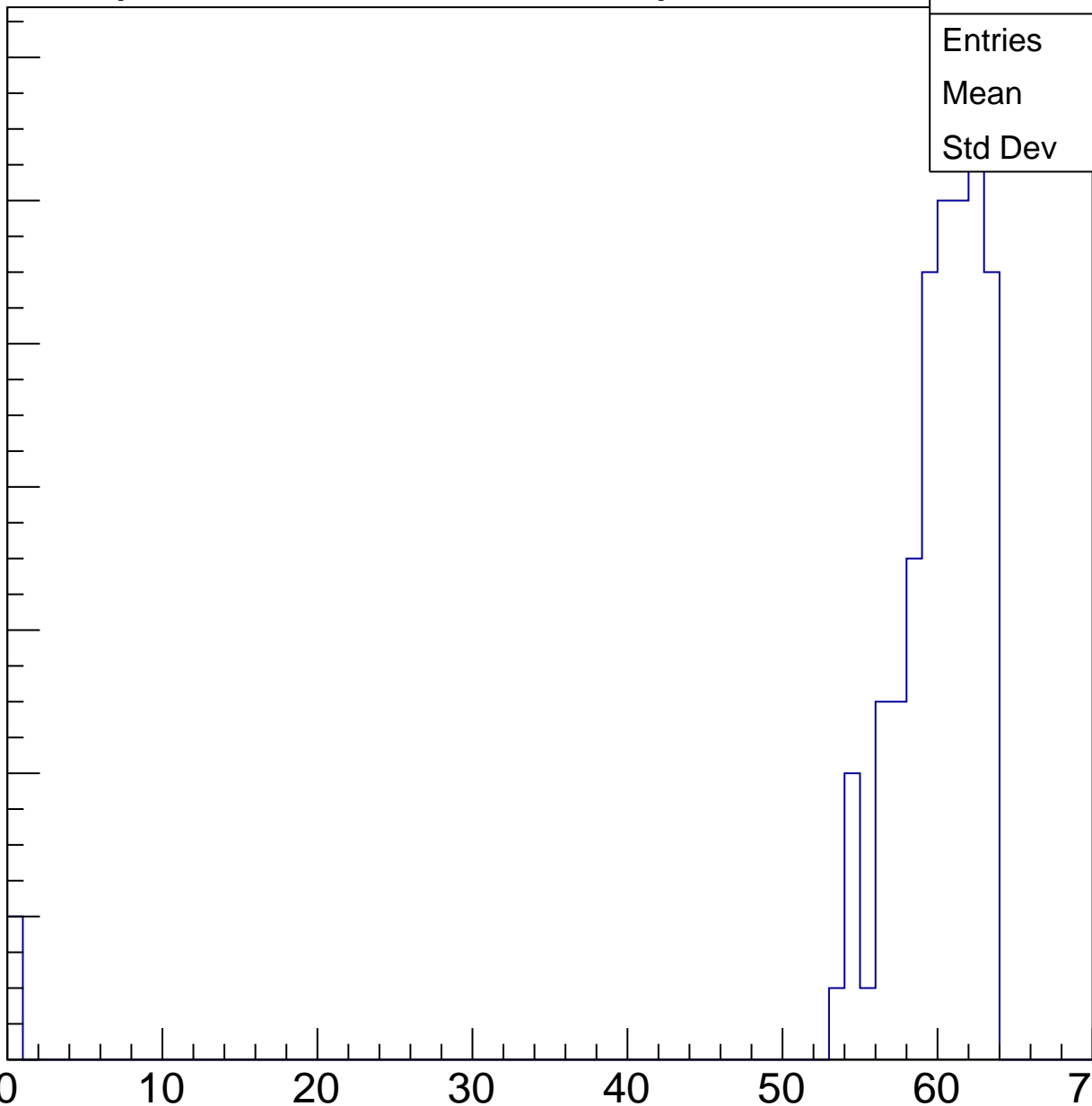
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	58.32
Std Dev	9.399

ampl

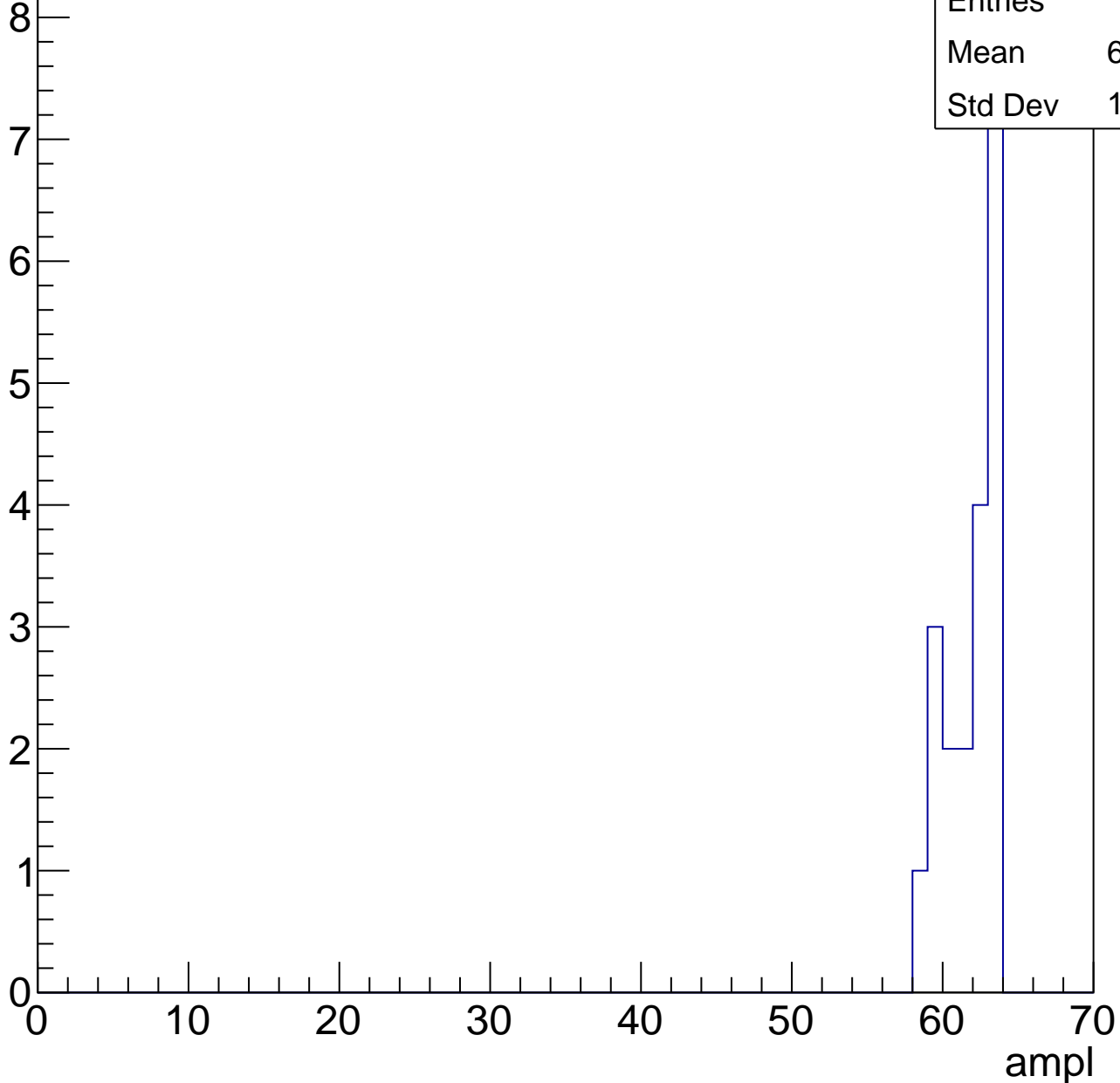


B1L001S, U22-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	61.45
Std Dev	1.658



B1L001S, U22-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U22-ch96, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.99
Std Dev	3.201

Gaus mean : 30.4978

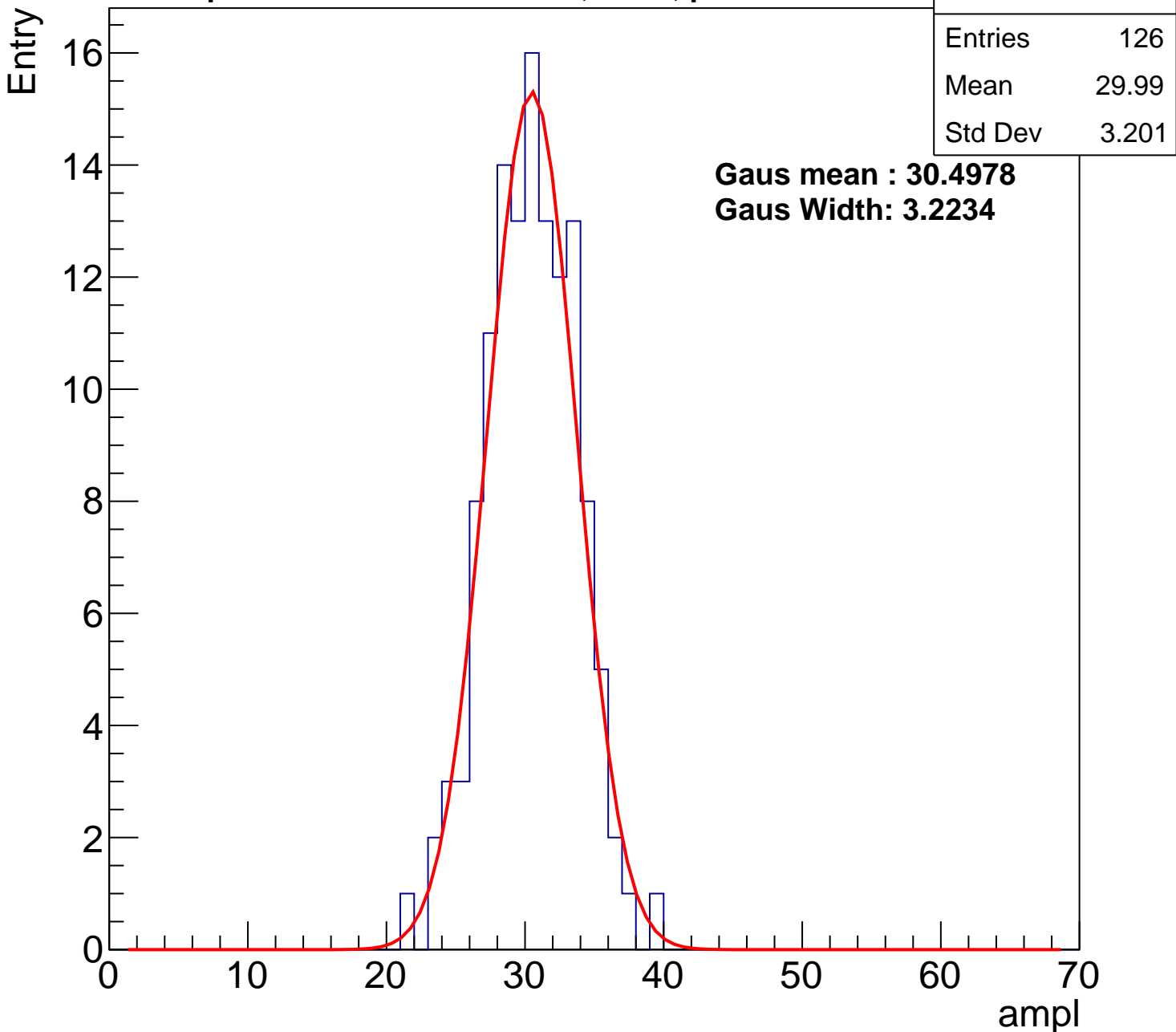
Gaus Width: 3.2234

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



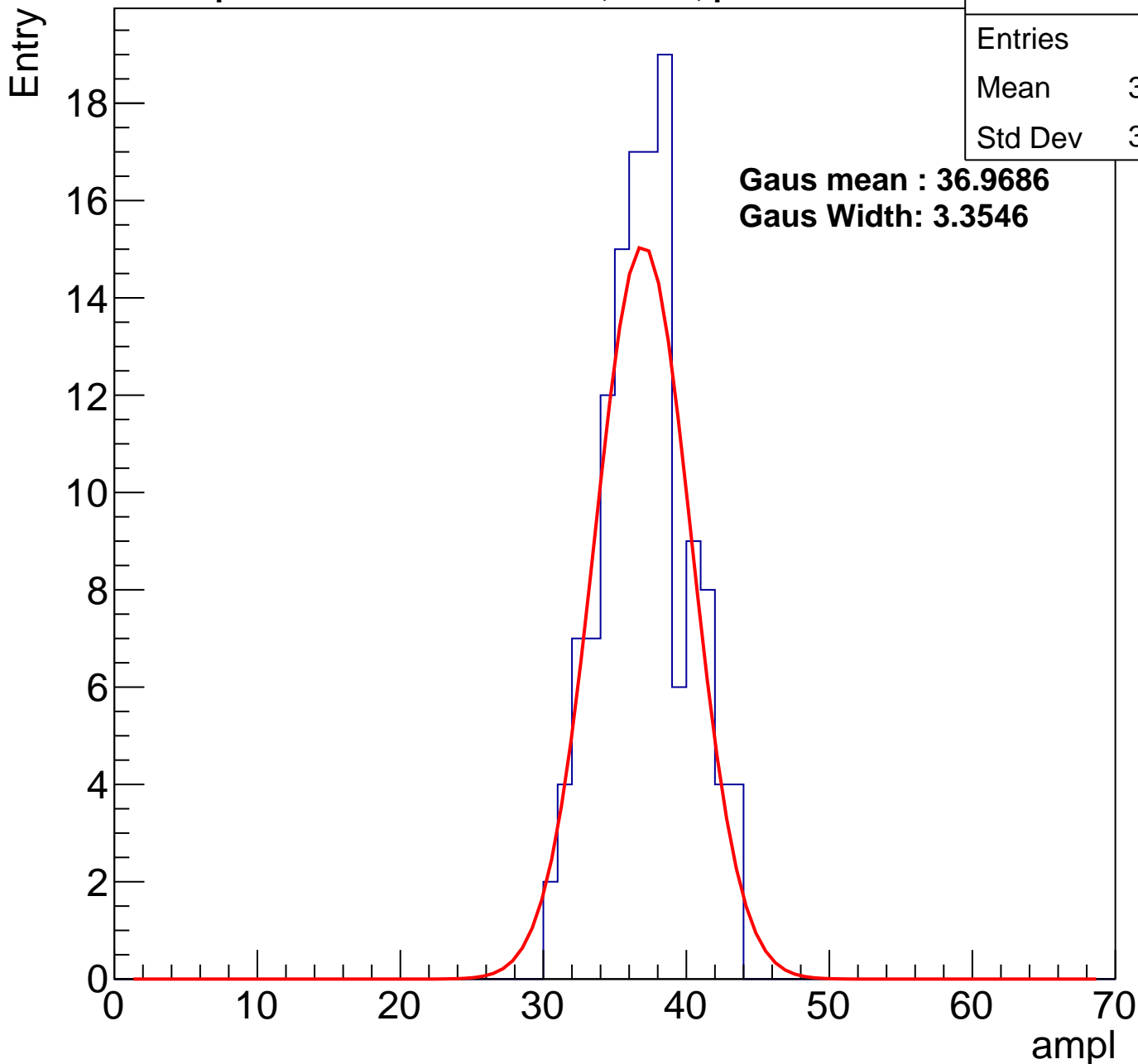
B1L001S, U22-ch96, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.62
Std Dev	3.015

Gaus mean : 36.9686

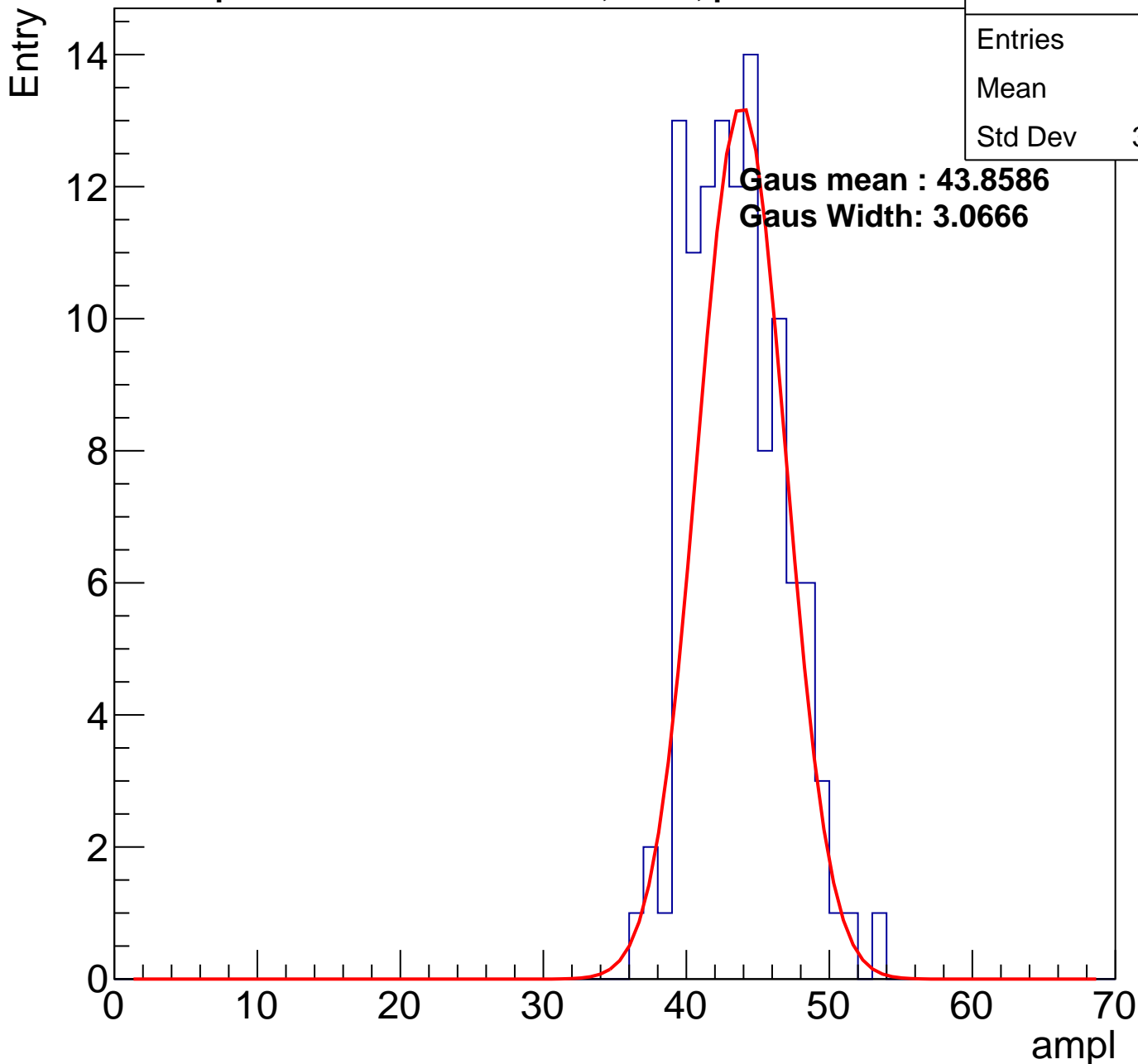
Gaus Width: 3.3546



B1L001S, U22-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	43.1
Std Dev	3.252

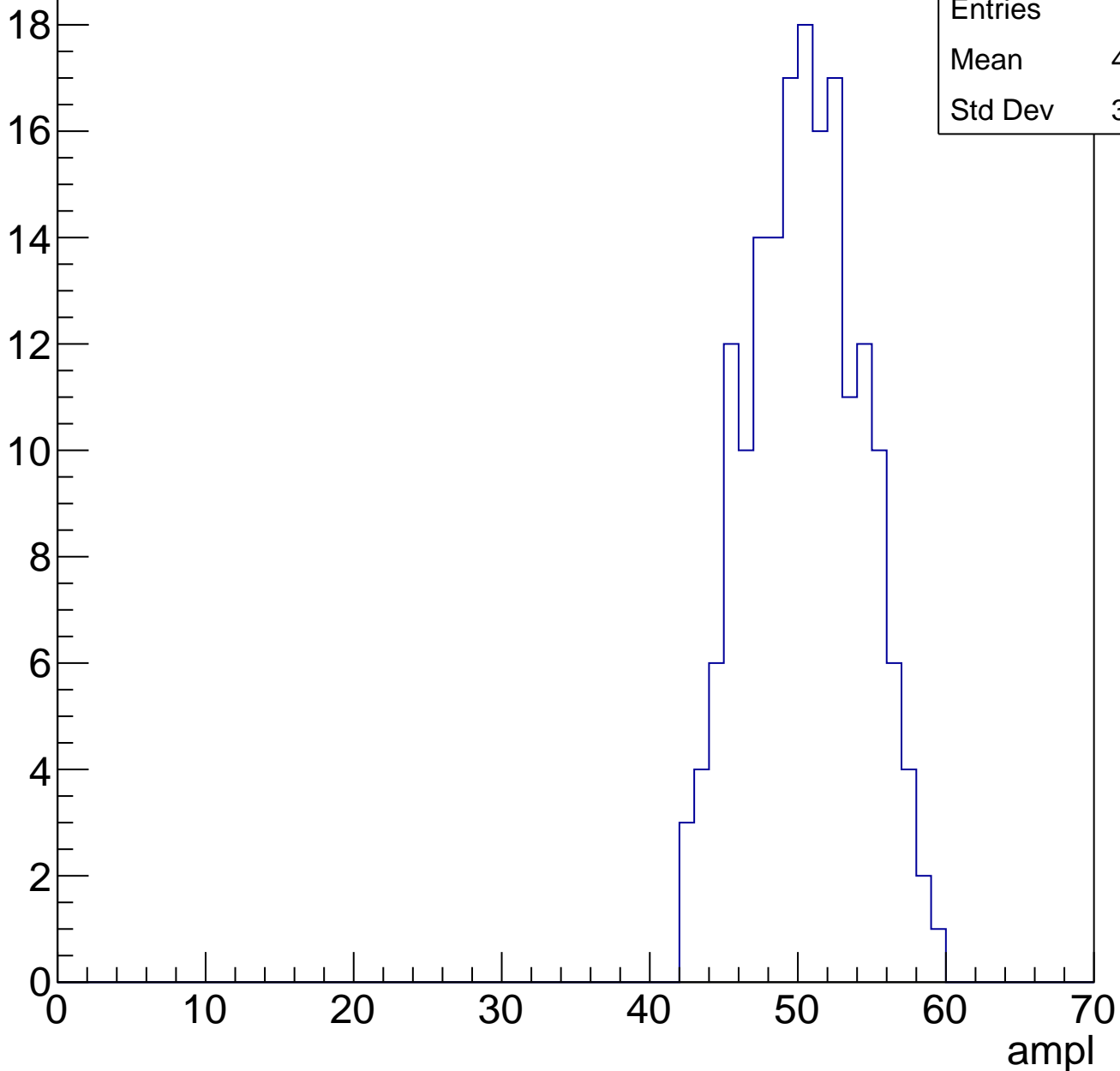


B1L001S, U22-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	177
Mean	49.97
Std Dev	3.765

Entry

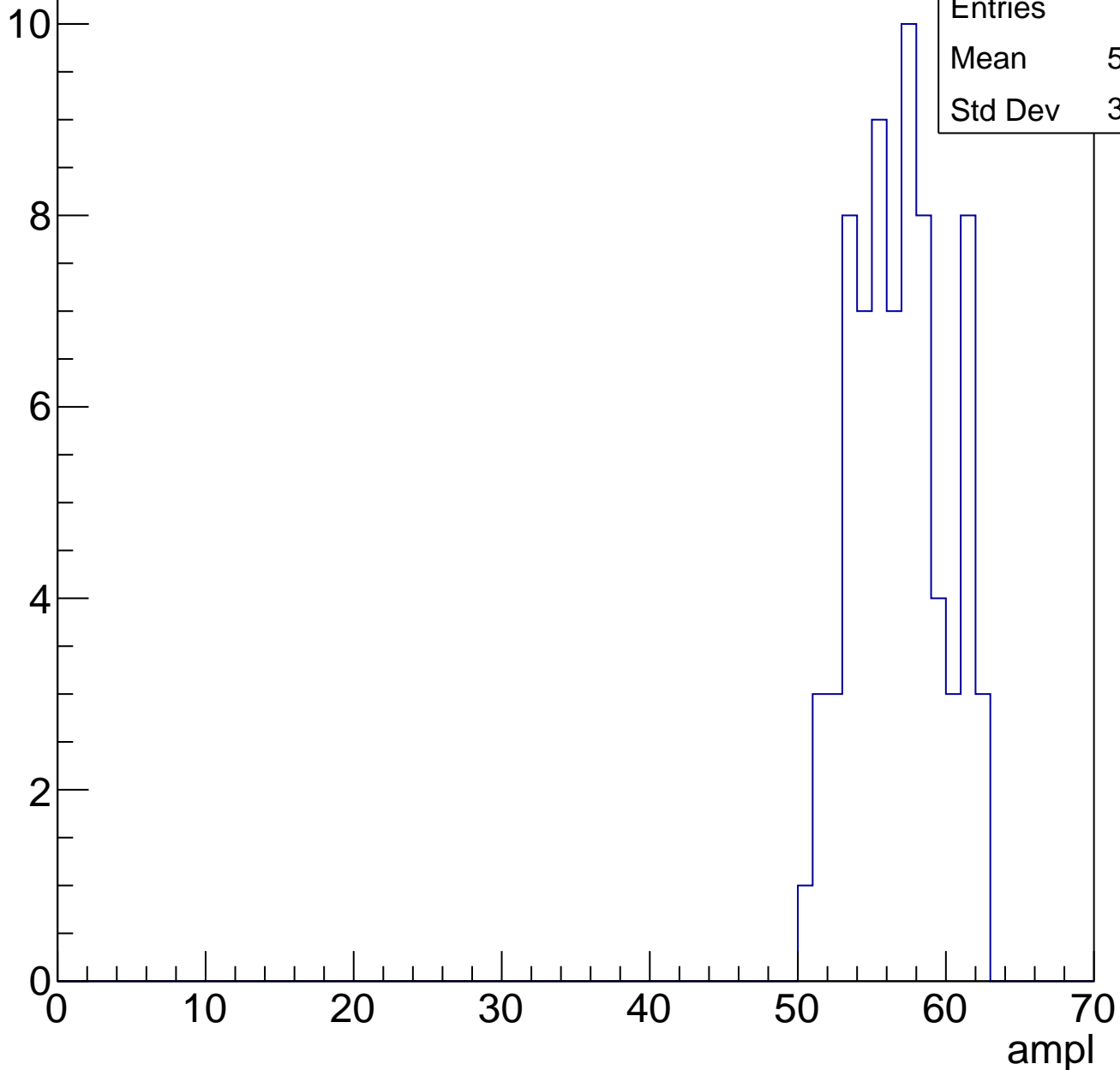


B1L001S, U22-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	74
Mean	56.38
Std Dev	3.057

Entry



B1L001S, U22-ch96, adc5

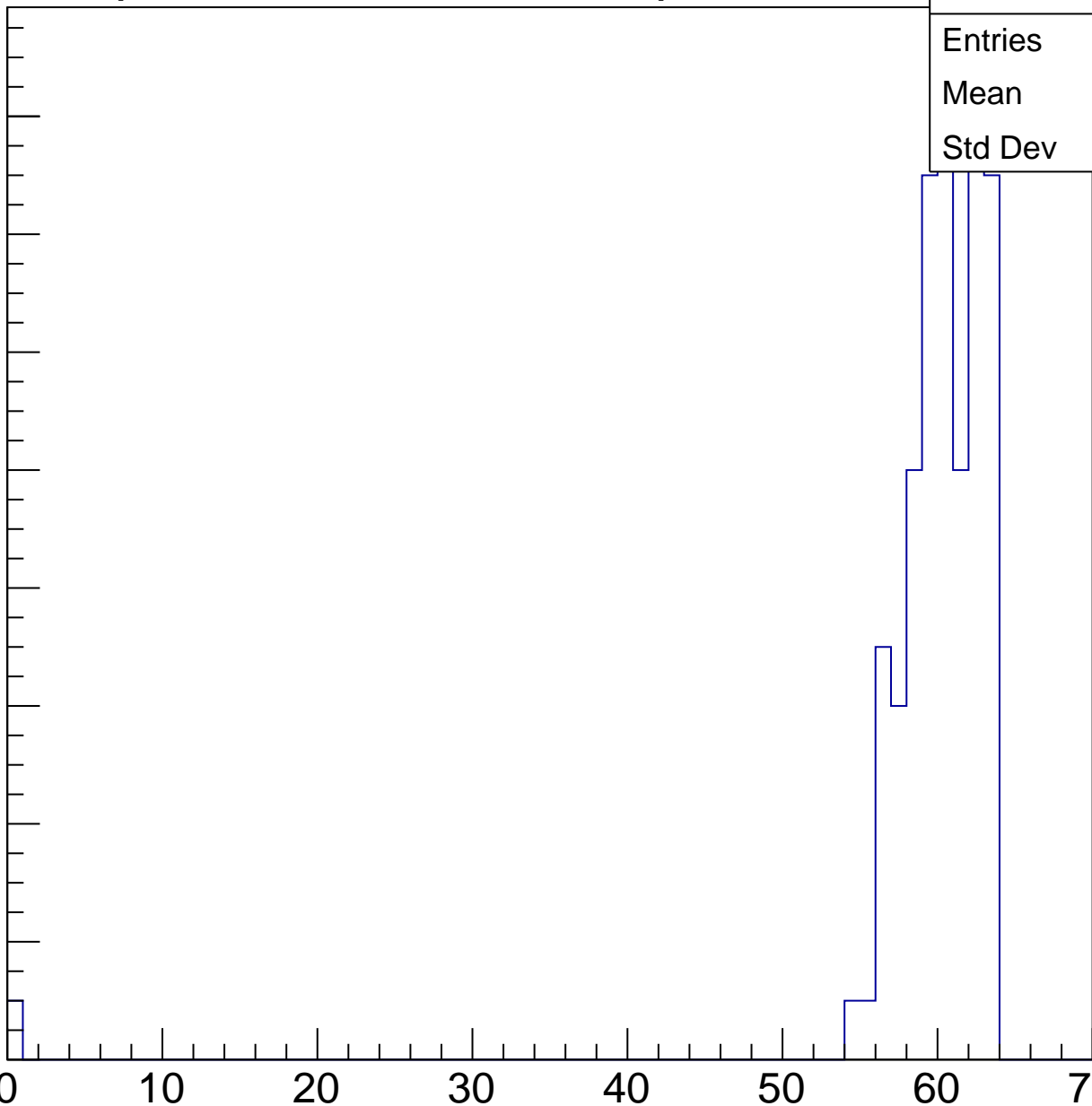
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.34
Std Dev	6.395

ampl

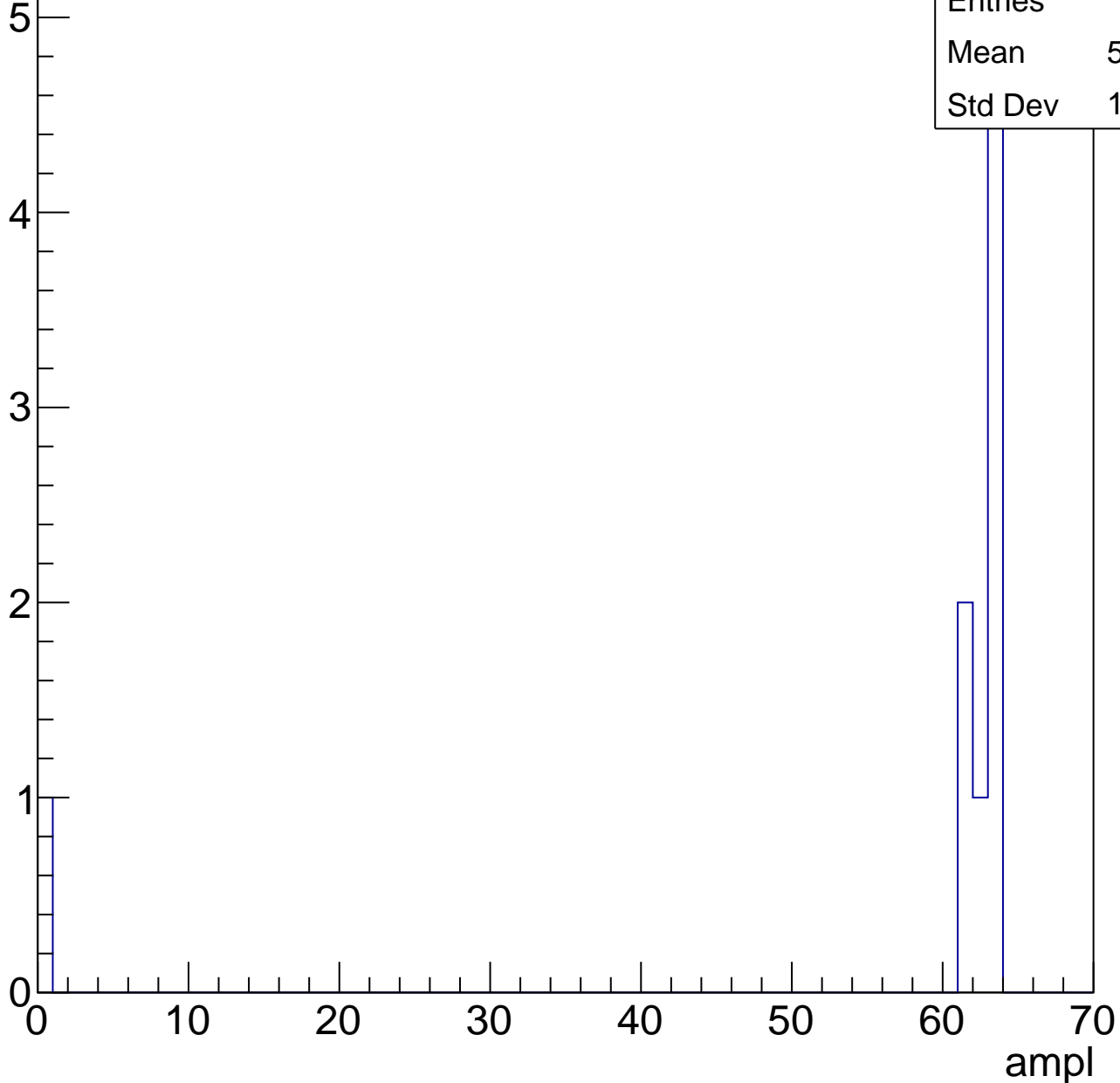


B1L001S, U22-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	55.44
Std Dev	19.62



B1L001S, U22-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	30.66
Std Dev	3.47

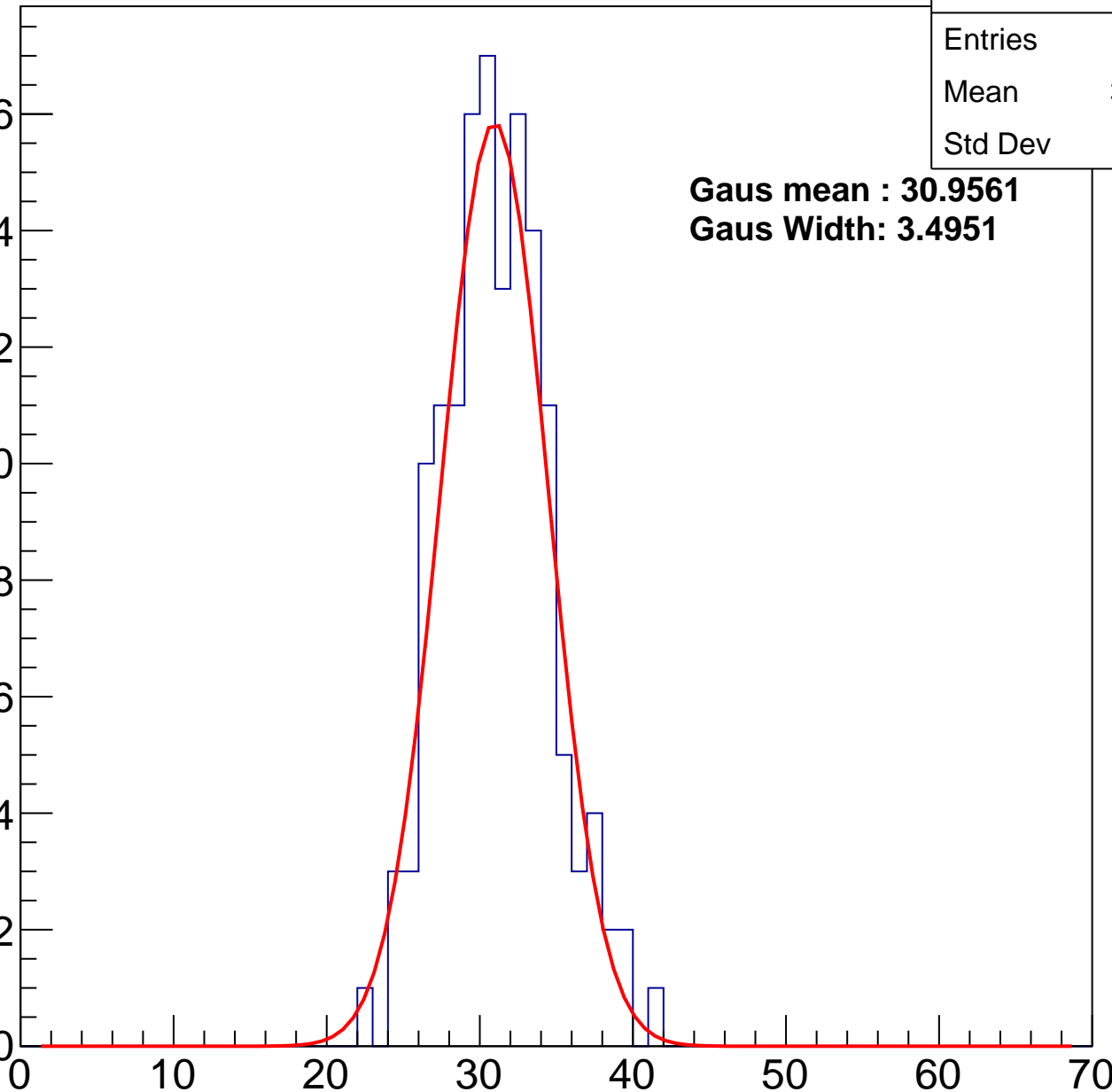
Gaus mean : 30.9561

Gaus Width: 3.4951

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.64
Std Dev	3.133

Gaus mean : 38.0326

Gaus Width: 3.3440

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

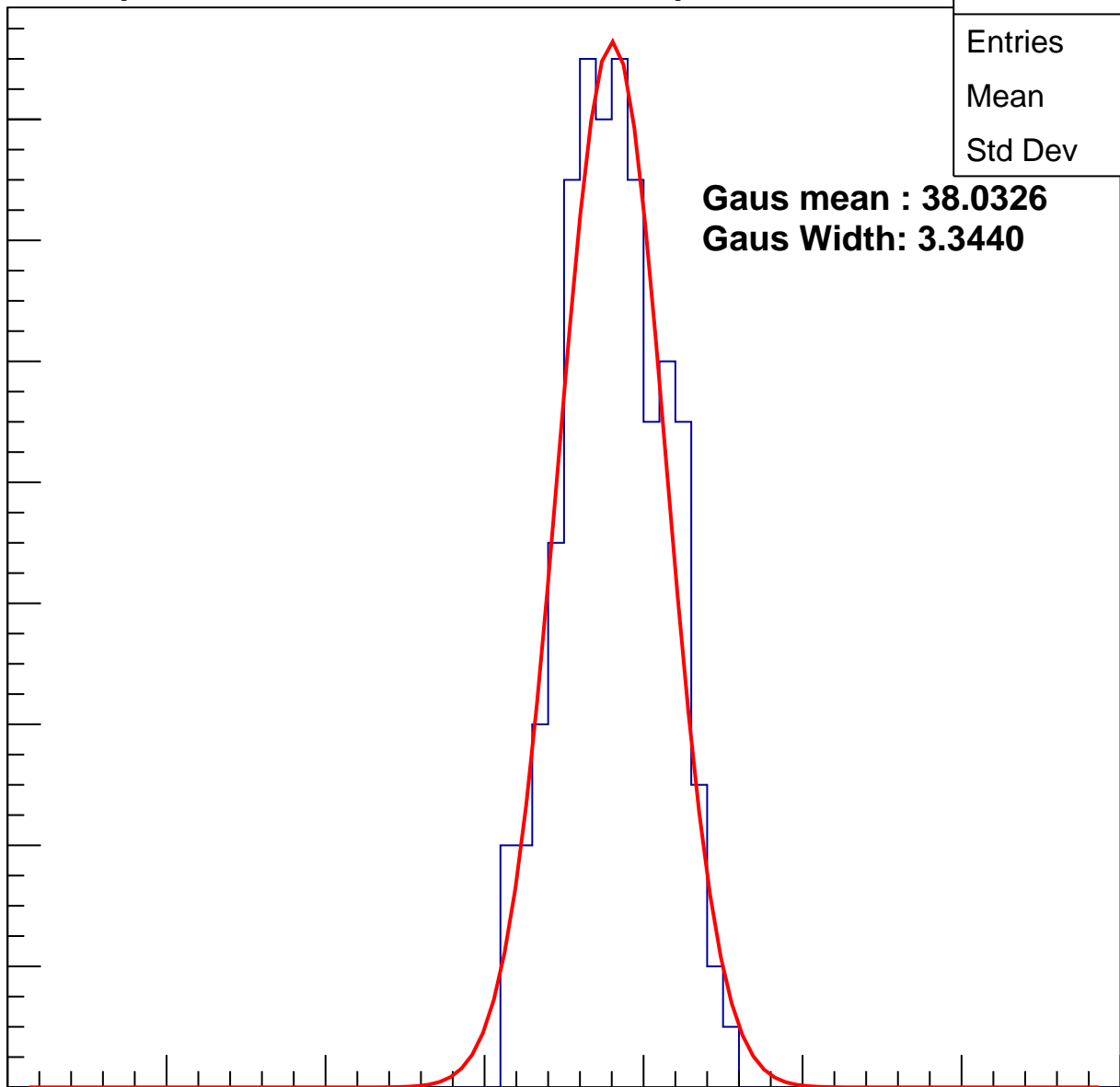
40

50

60

70

ampl



B1L001S, U22-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

44.93

Std Dev

3.067

Gaus mean : 45.5296

Gaus Width: 3.1107

0

10

20

30

40

50

60

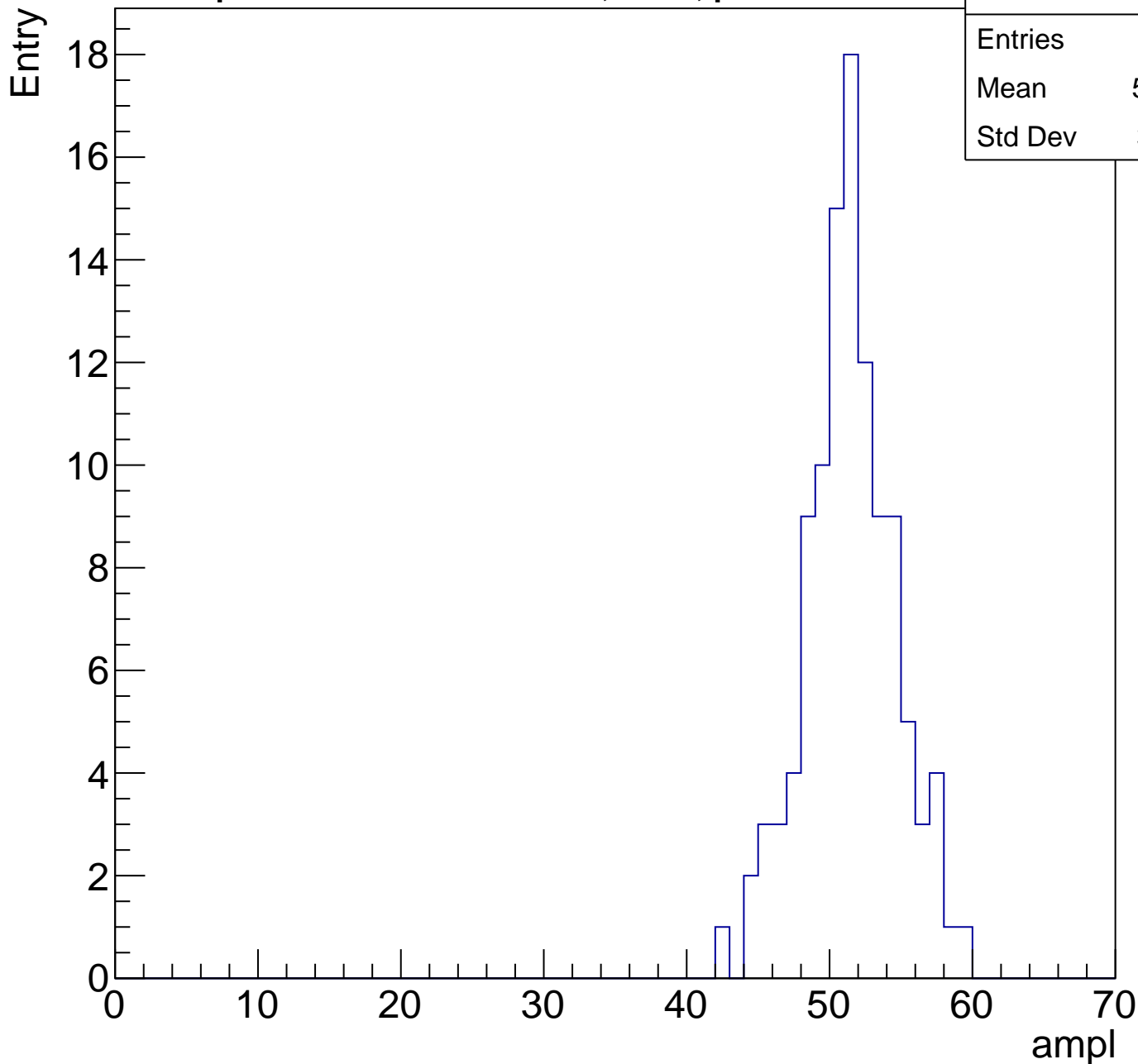
ampl

70

B1L001S, U22-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

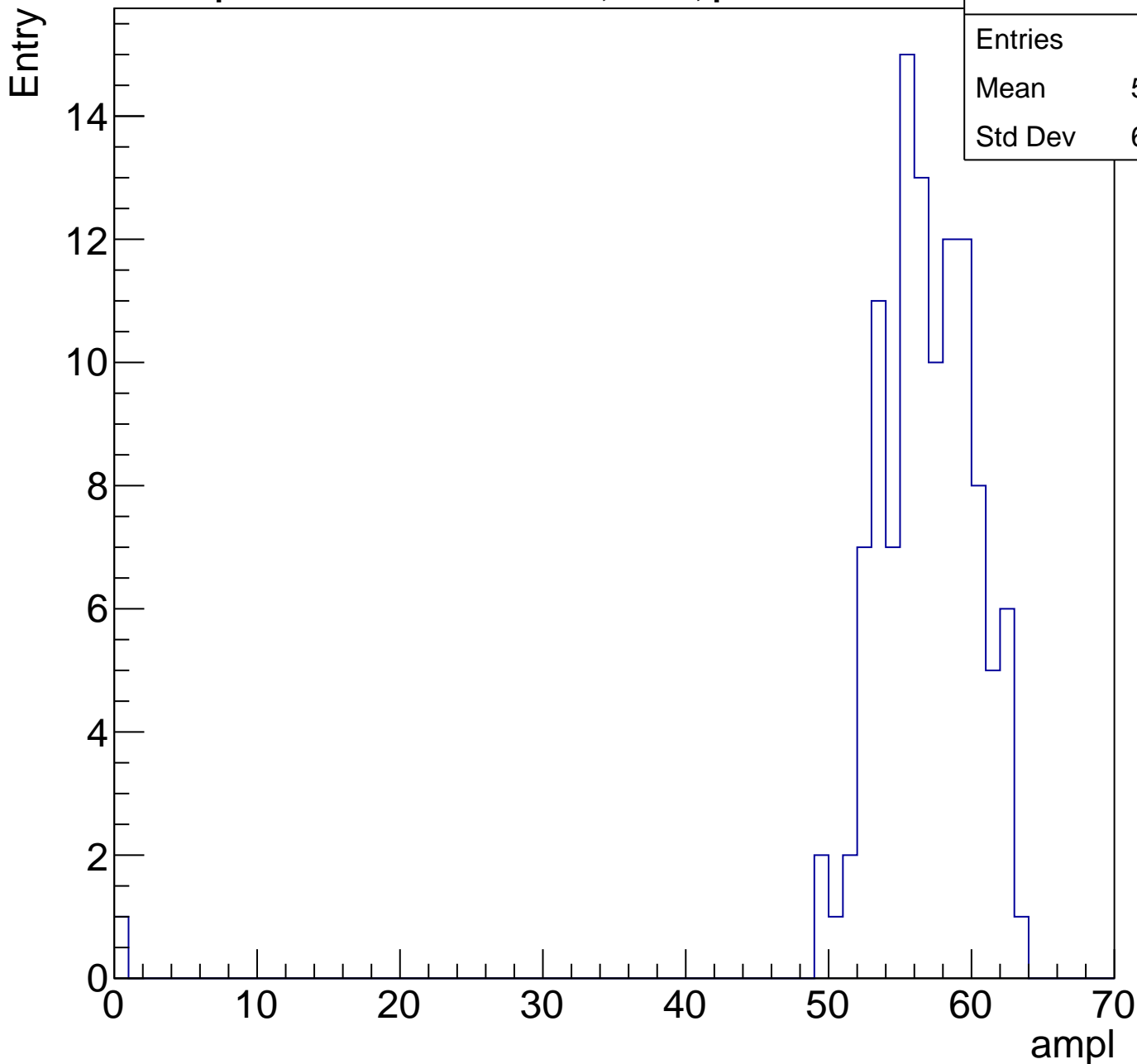
Entries	109
Mean	50.97
Std Dev	3.181



B1L001S, U22-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	55.95
Std Dev	6.138



B1L001S, U22-ch97, adc5

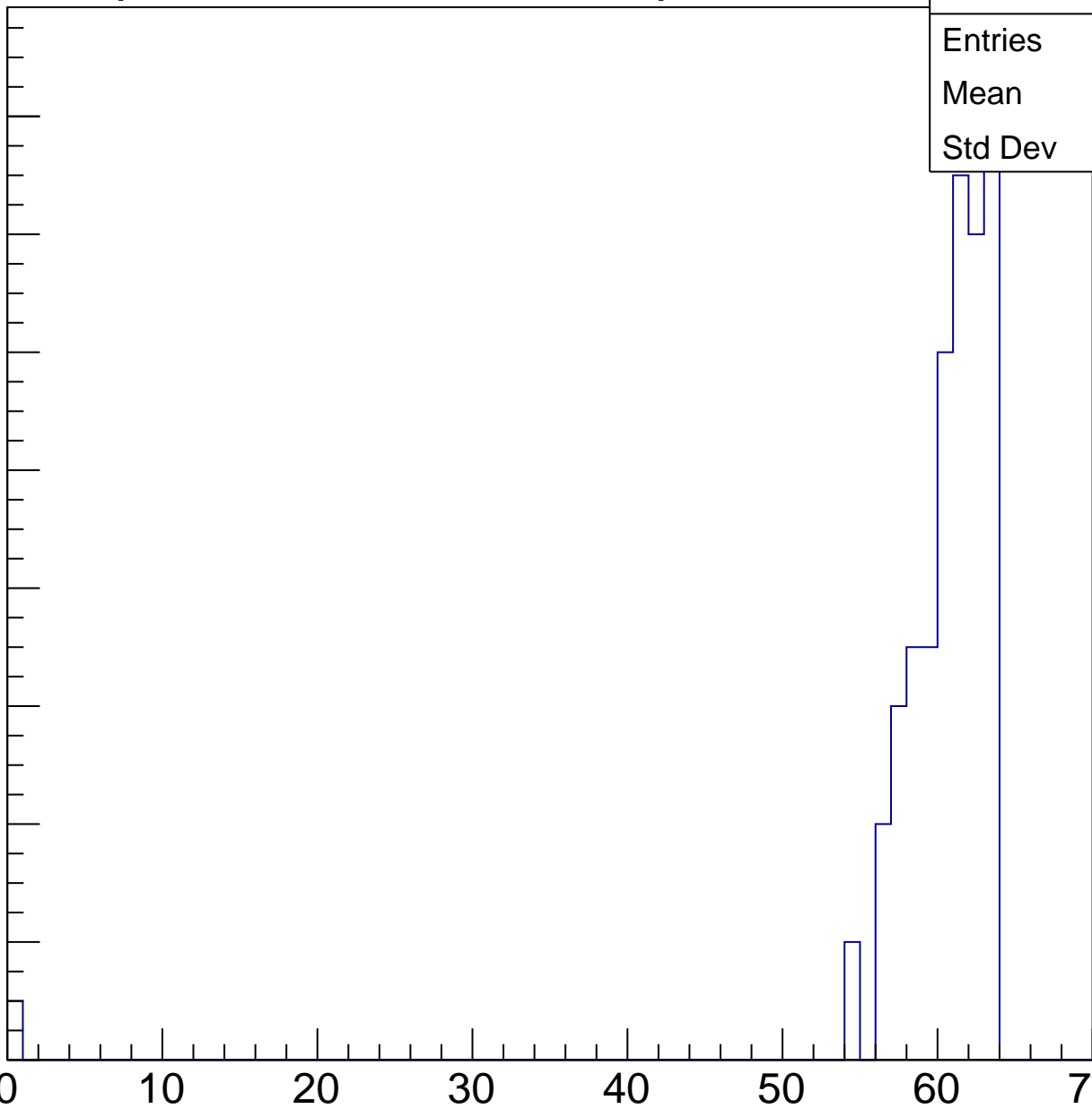
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.61
Std Dev	6.891

ampl



B1L001S, U22-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U22-ch98, adc0

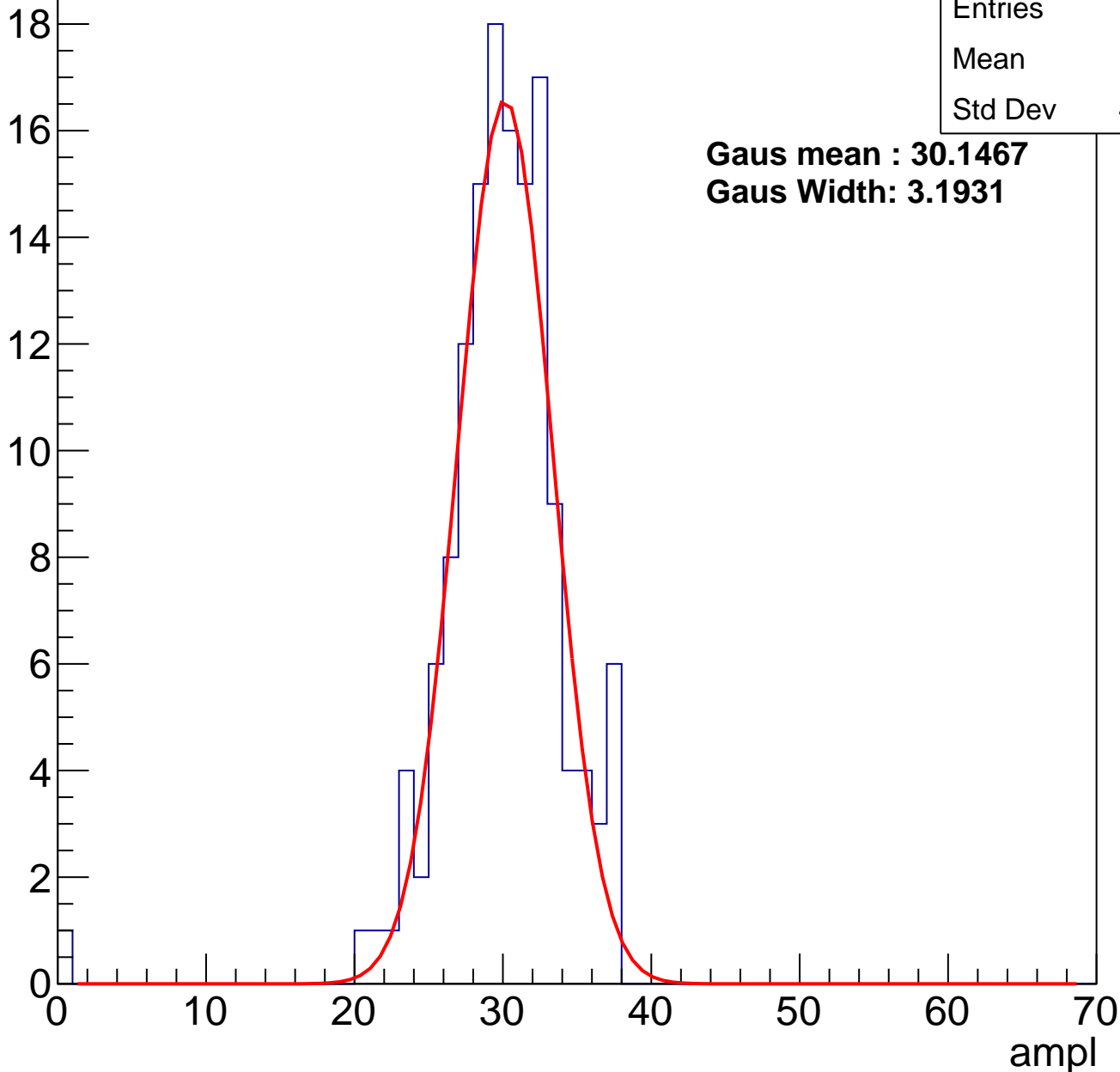
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.5
Std Dev	4.241

Gaus mean : 30.1467

Gaus Width: 3.1931

Entry



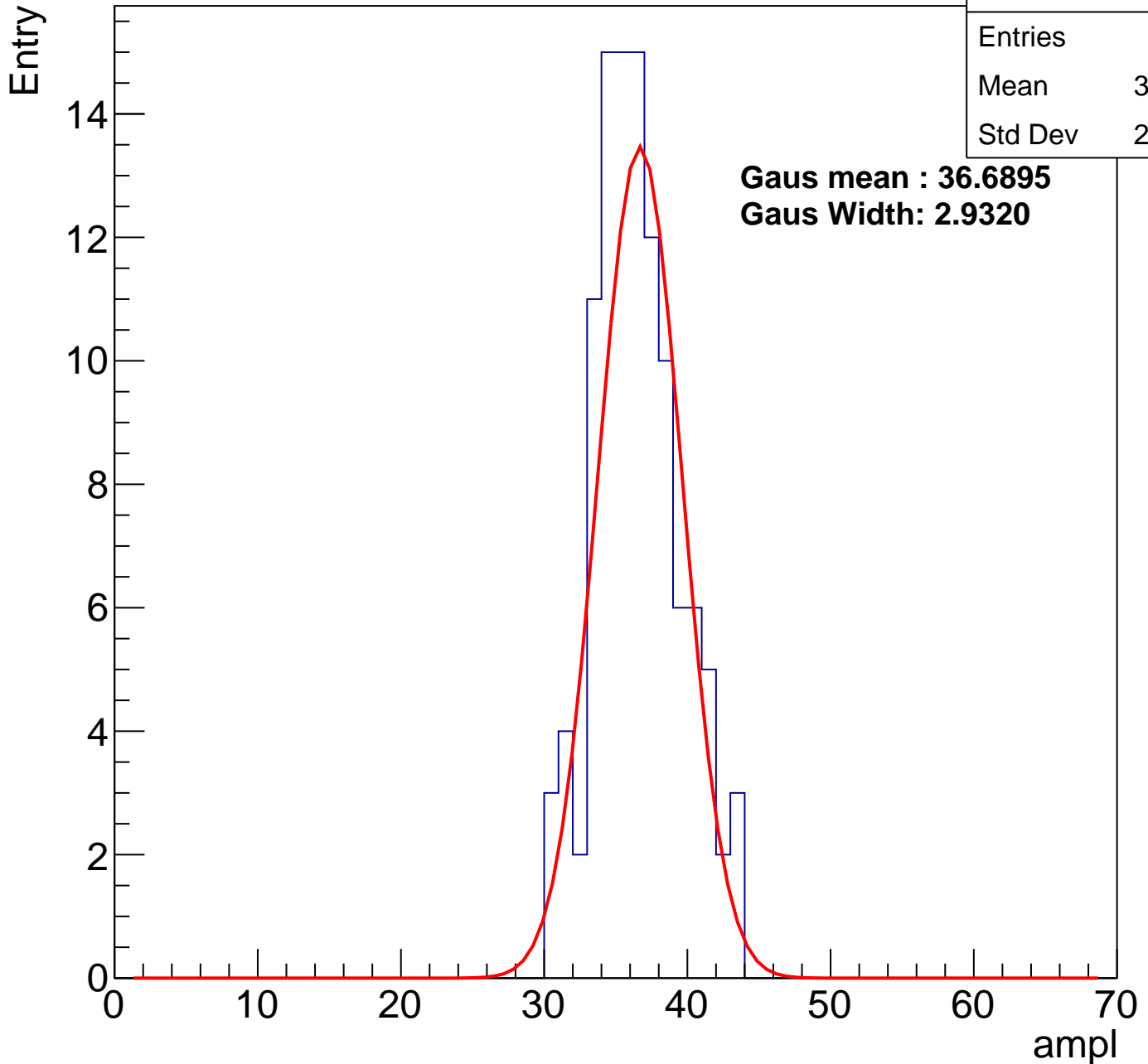
B1L001S, U22-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	36.07
Std Dev	2.967

Gaus mean : 36.6895

Gaus Width: 2.9320



B1L001S, U22-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

42.66

Std Dev

3.288

Gaus mean : 43.1058

Gaus Width: 3.1267

0

10

20

30

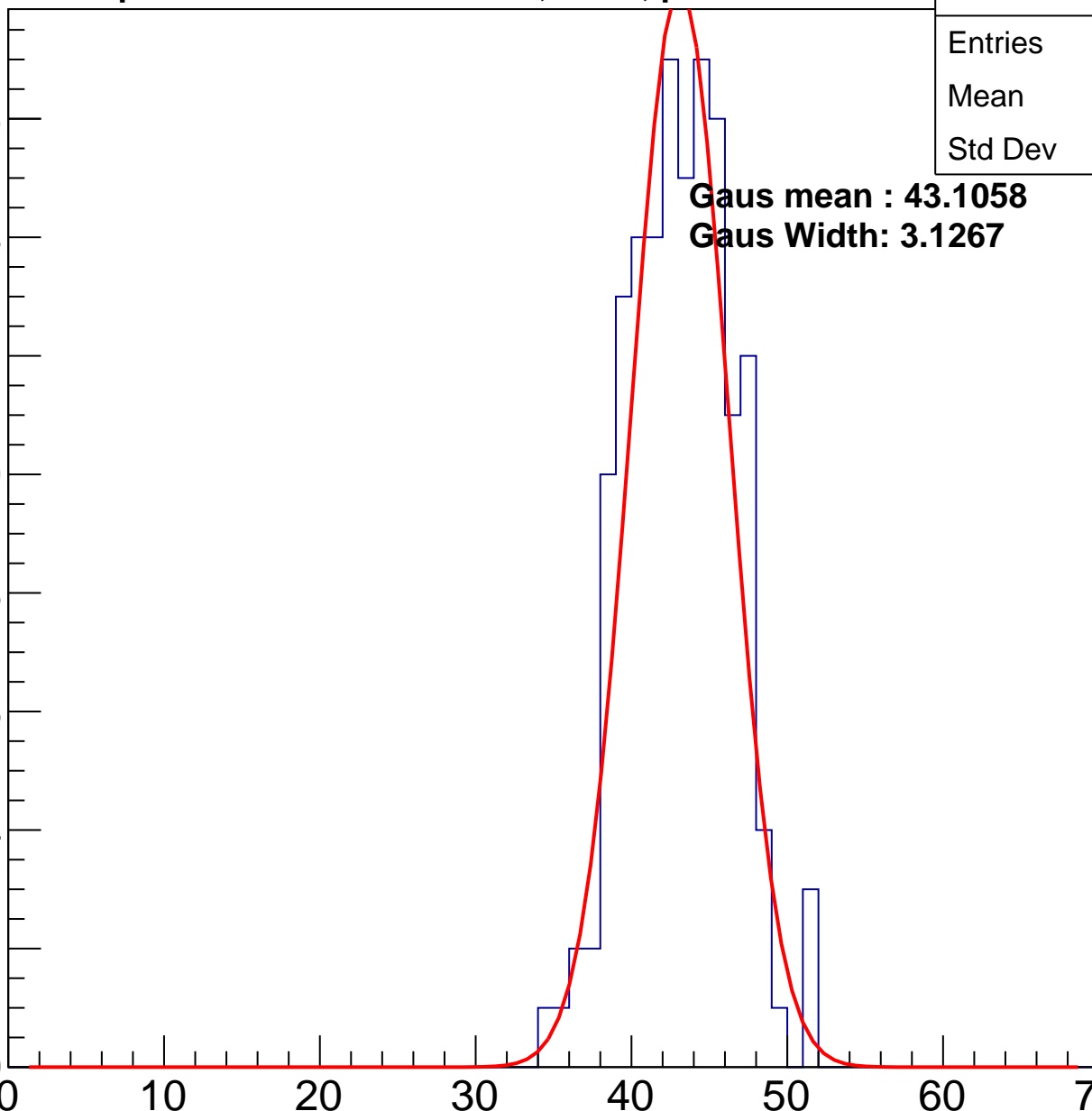
40

50

60

70

ampl



B1L001S, U22-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	48.81
Std Dev	3.039

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

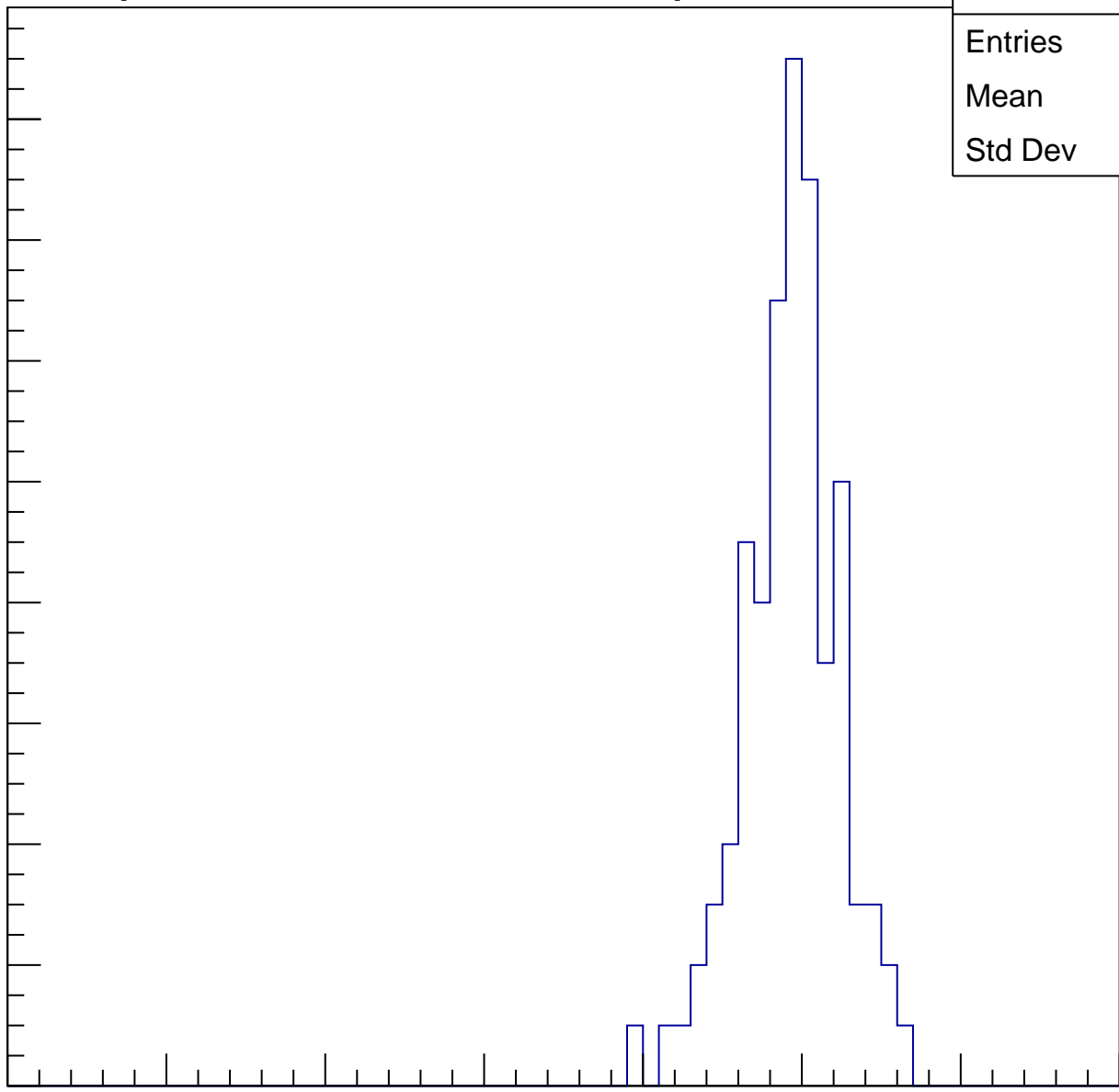
40

50

60

70

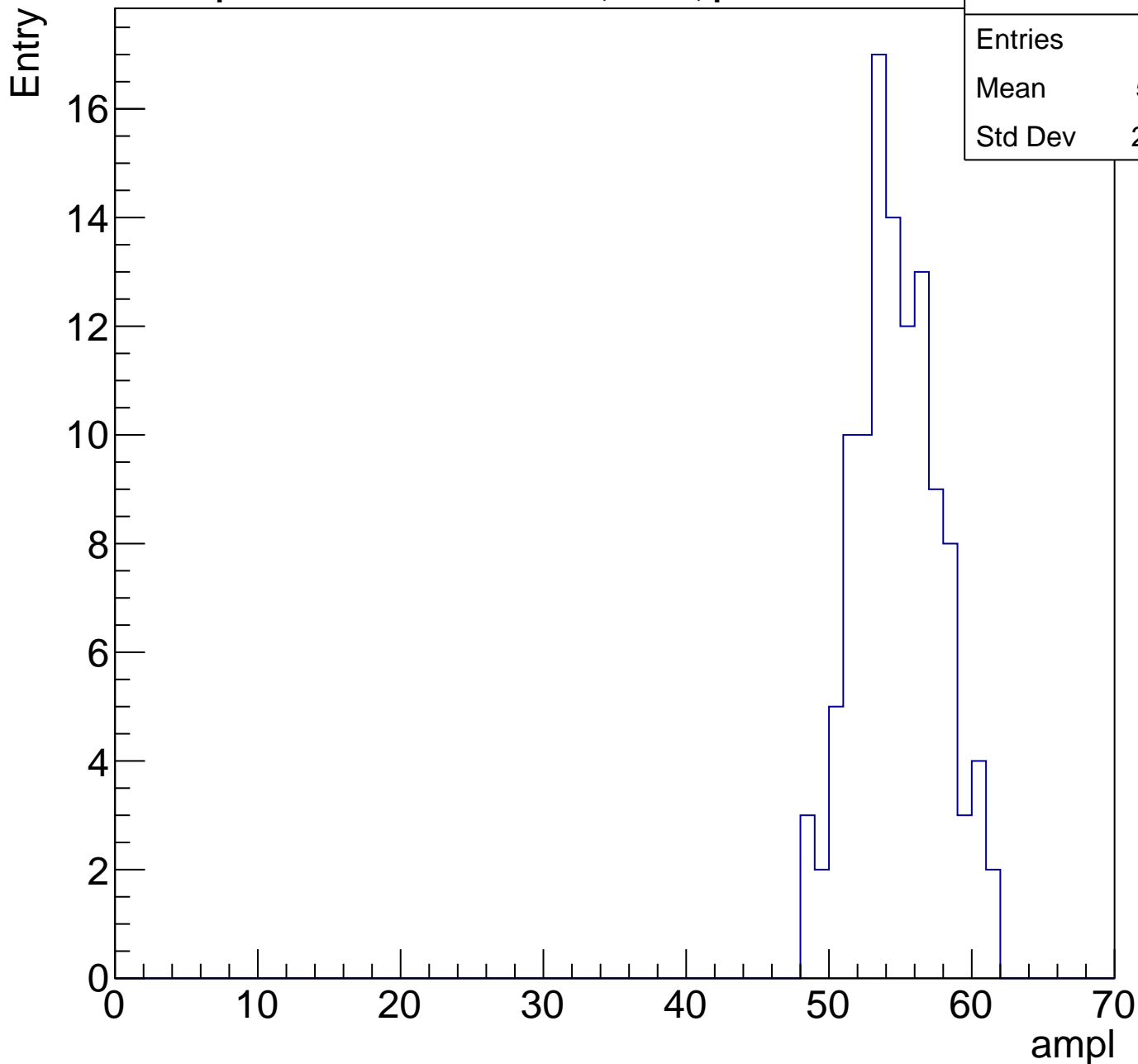
ampl



B1L001S, U22-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	54.31
Std Dev	2.934

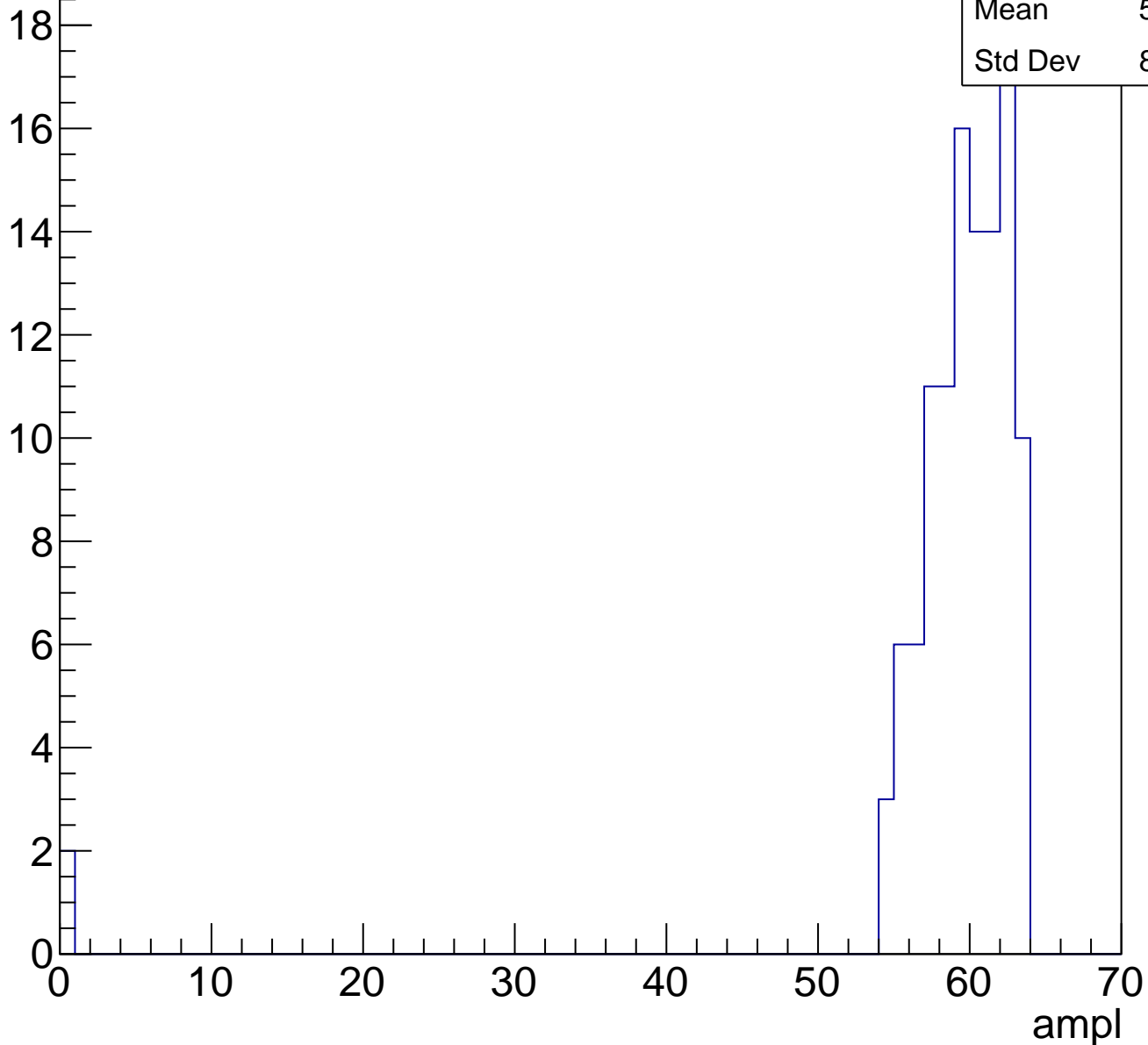


B1L001S, U22-ch98, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	58.38
Std Dev	8.238

Entry

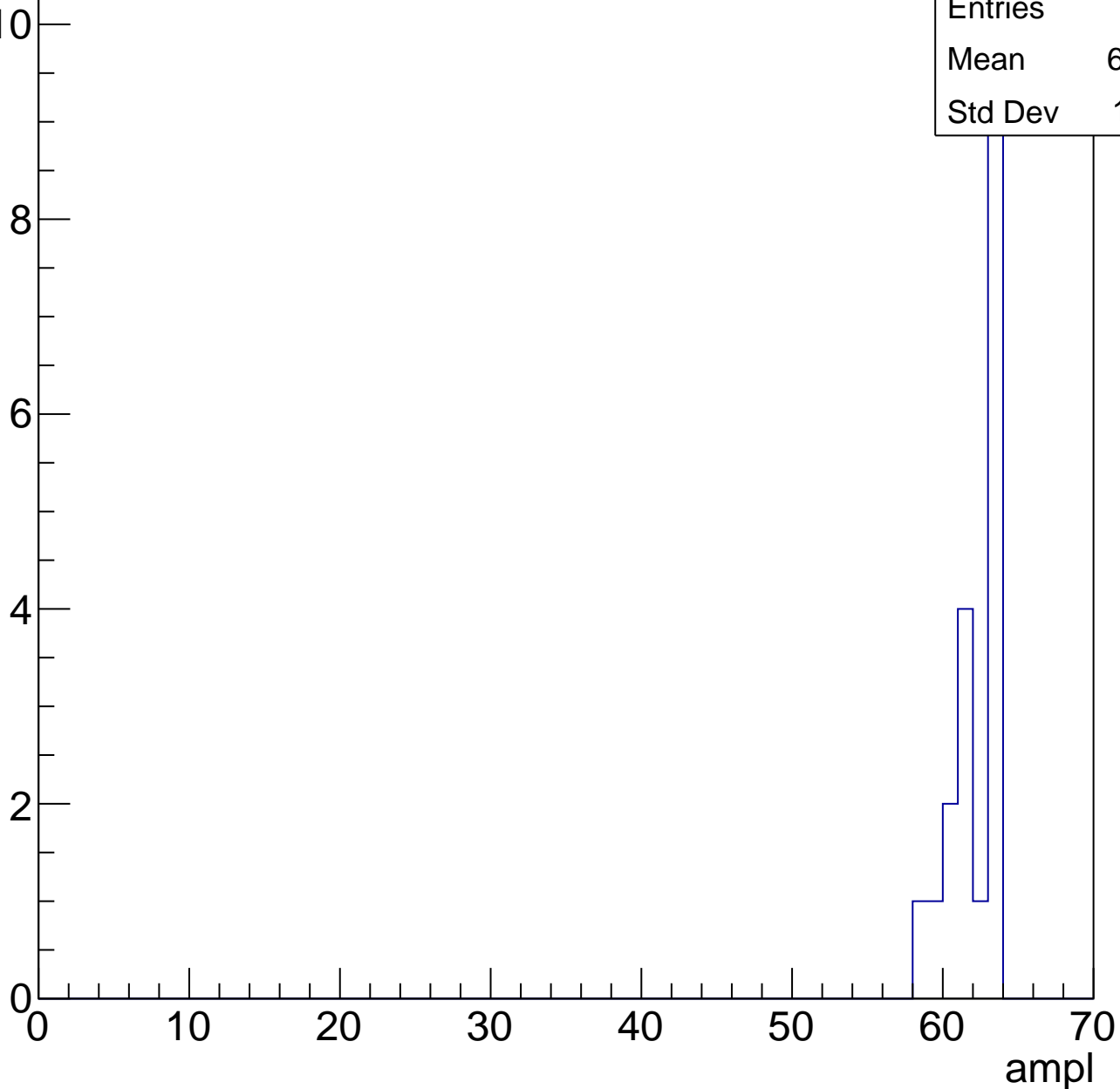


B1L001S, U22-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

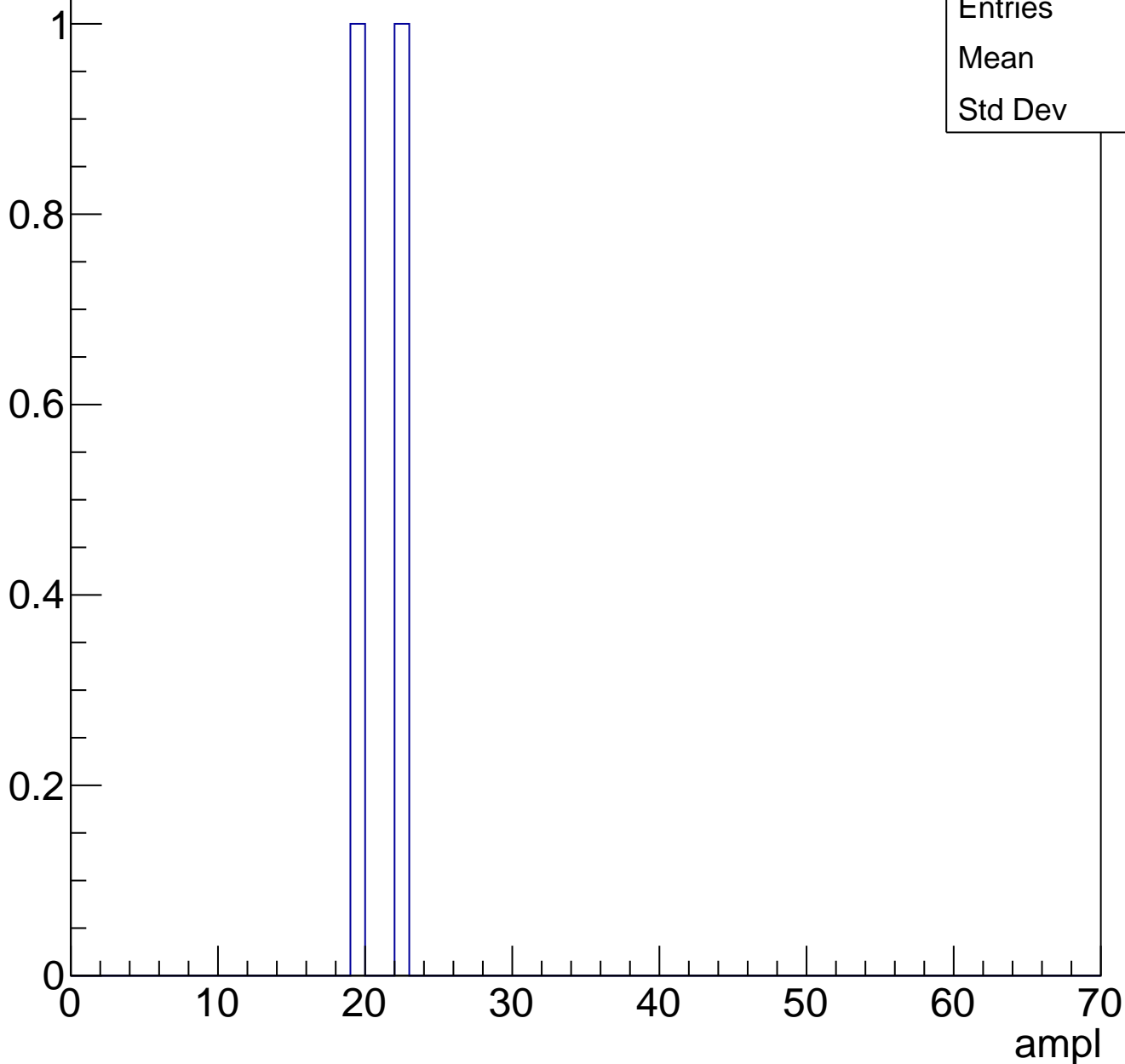
Entries	19
Mean	61.74
Std Dev	1.551



B1L001S, U22-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch99, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	31.63
Std Dev	3.365

Gaus mean : 32.0786

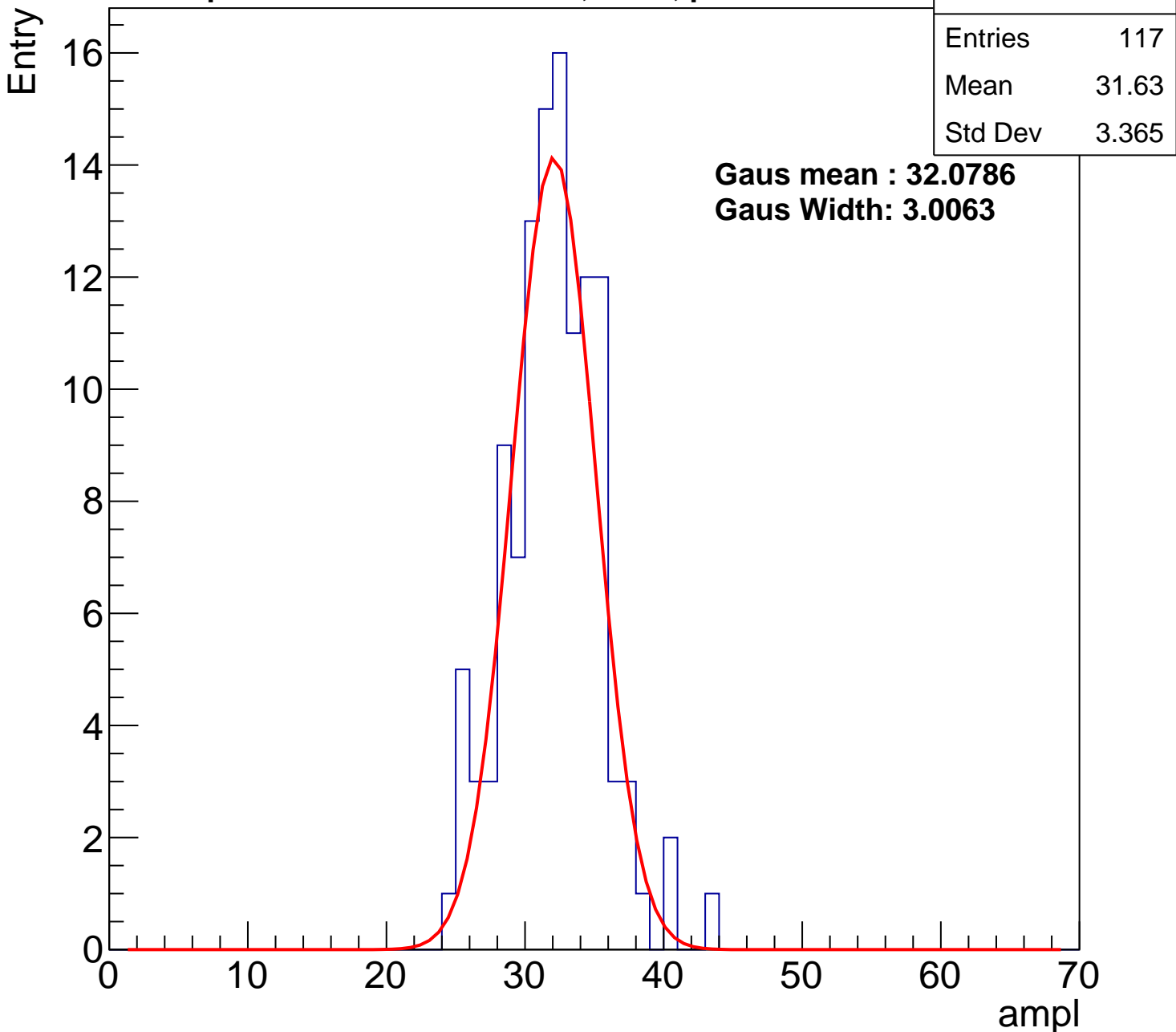
Gaus Width: 3.0063

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.94
Std Dev	3.447

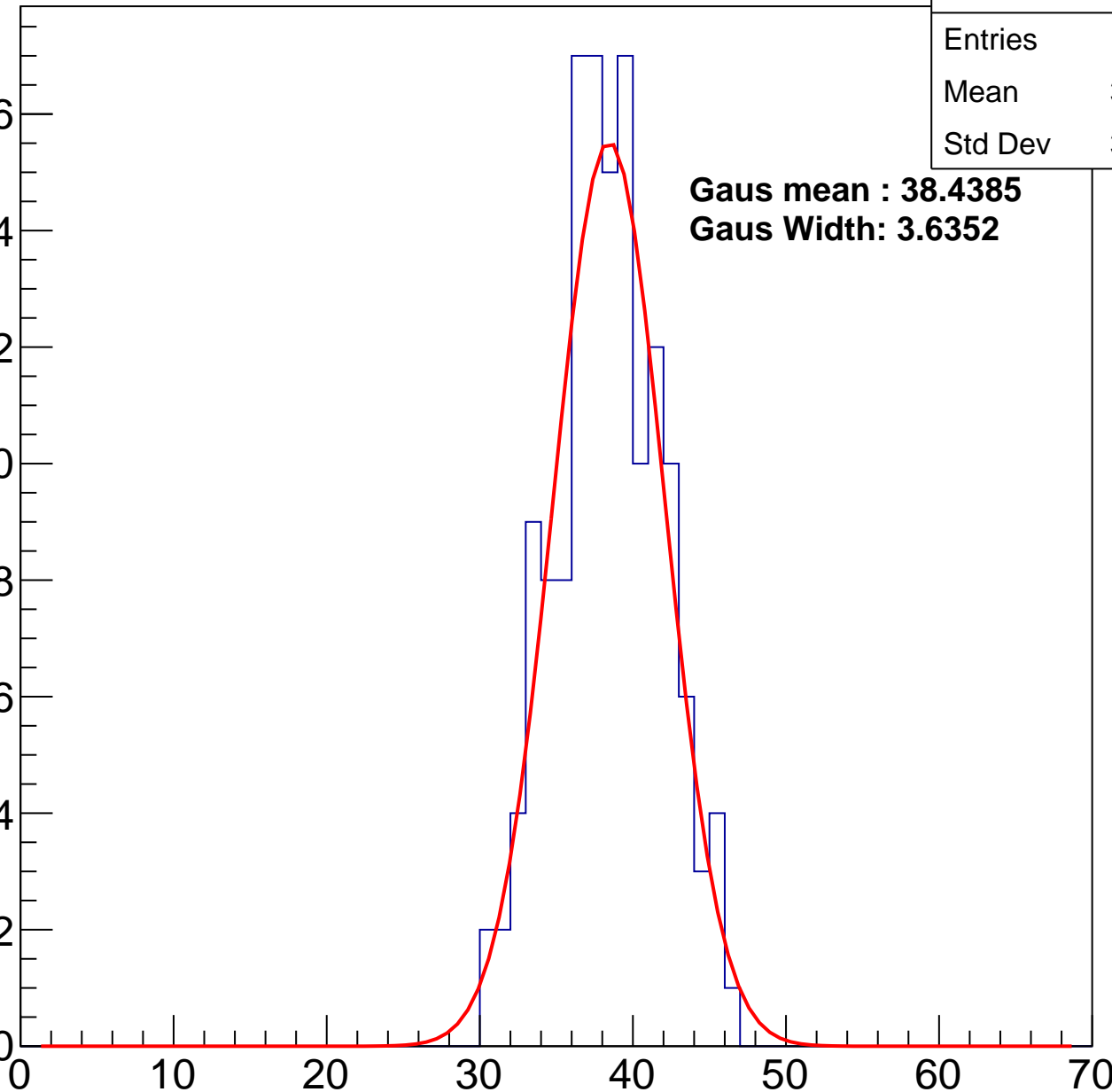
Gaus mean : 38.4385

Gaus Width: 3.6352

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch99, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

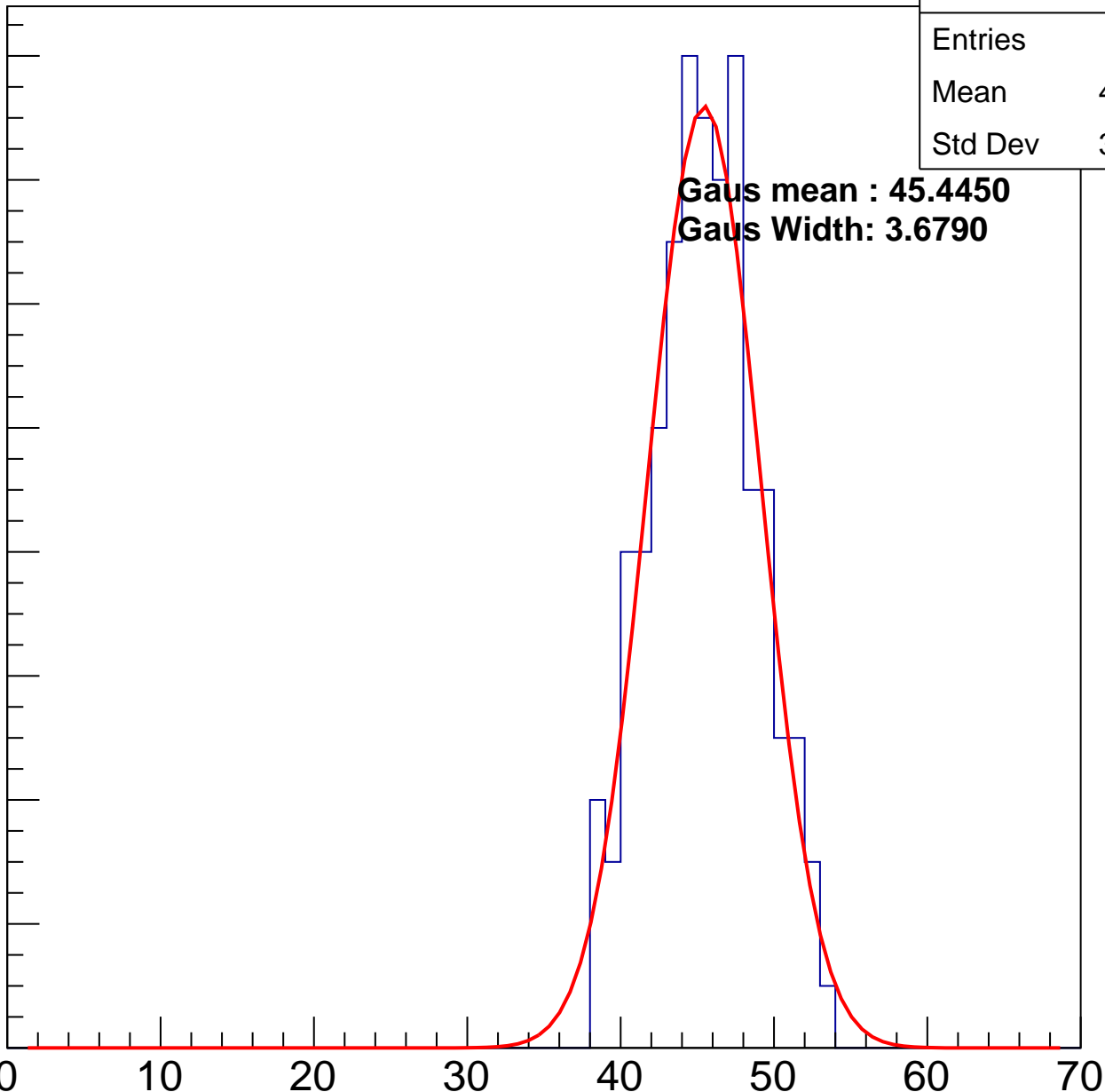
16
14
12
10
8
6
4
2
0

Entries	139
Mean	45.02
Std Dev	3.402

Gaus mean : 45.4450

Gaus Width: 3.6790

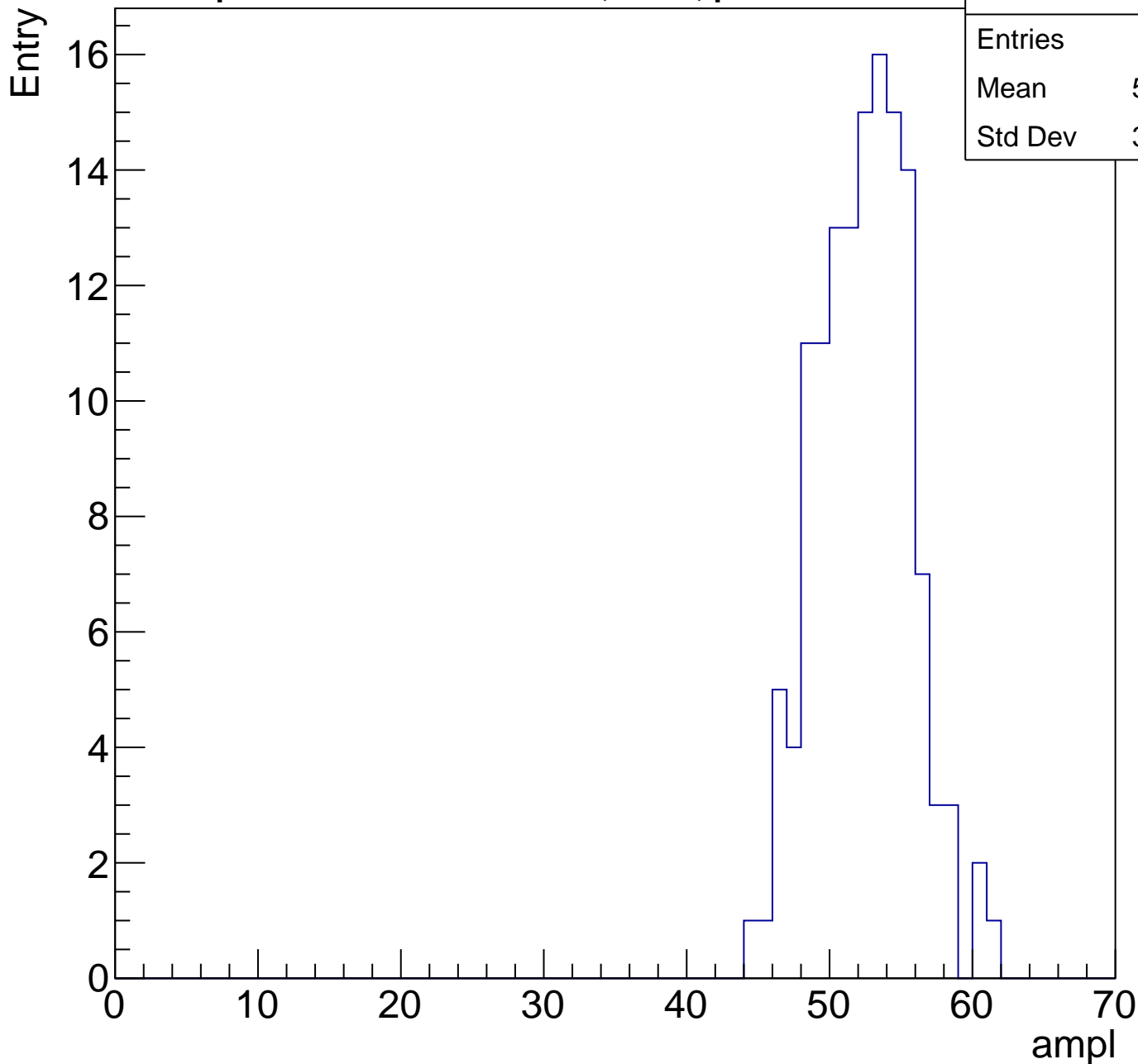
ampl



B1L001S, U22-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

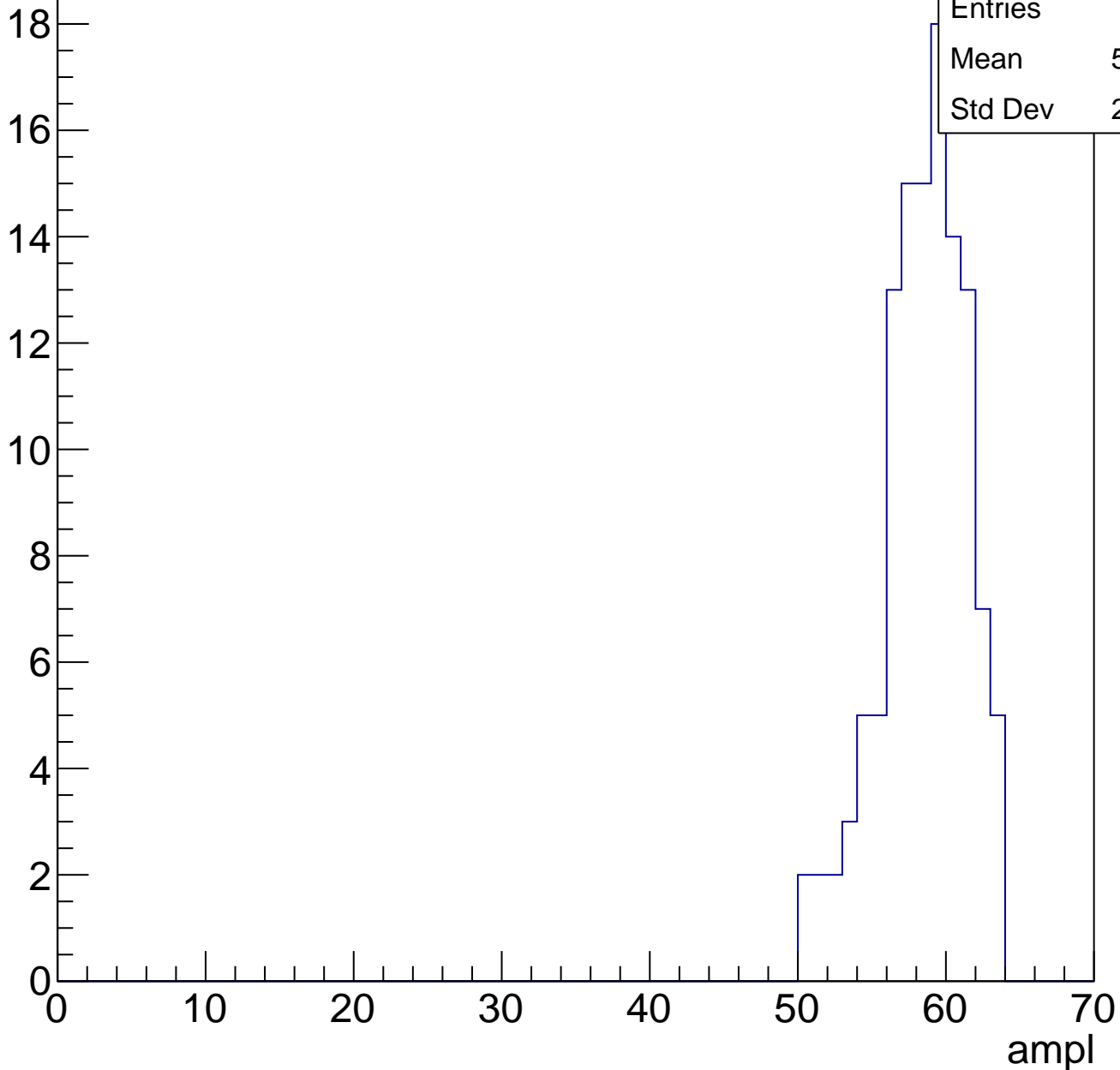
Entries	135
Mean	51.95
Std Dev	3.264



B1L001S, U22-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch99, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

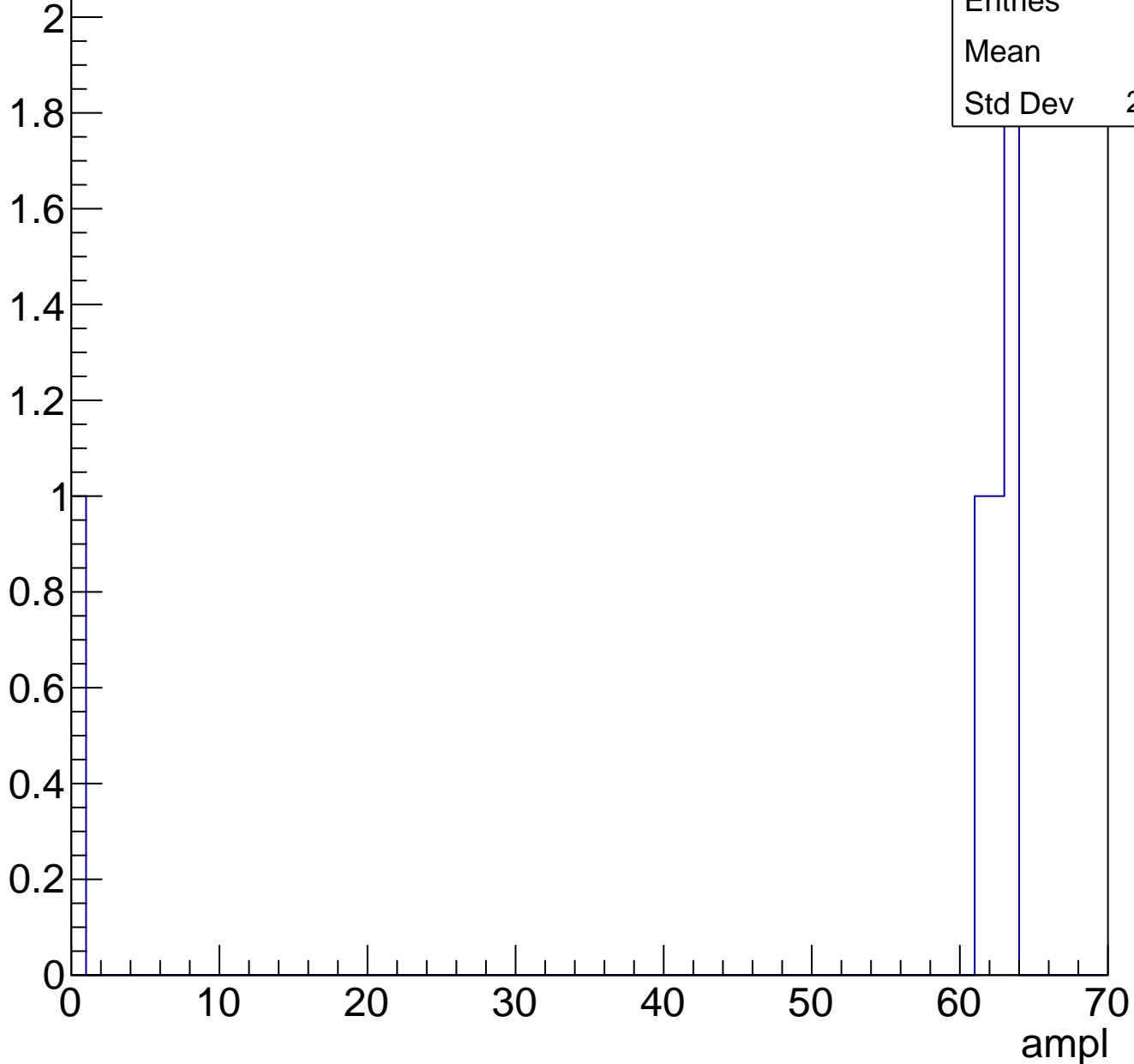
ampl

Entries	42
Mean	59.79
Std Dev	9.538

B1L001S, U22-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch99, adc7

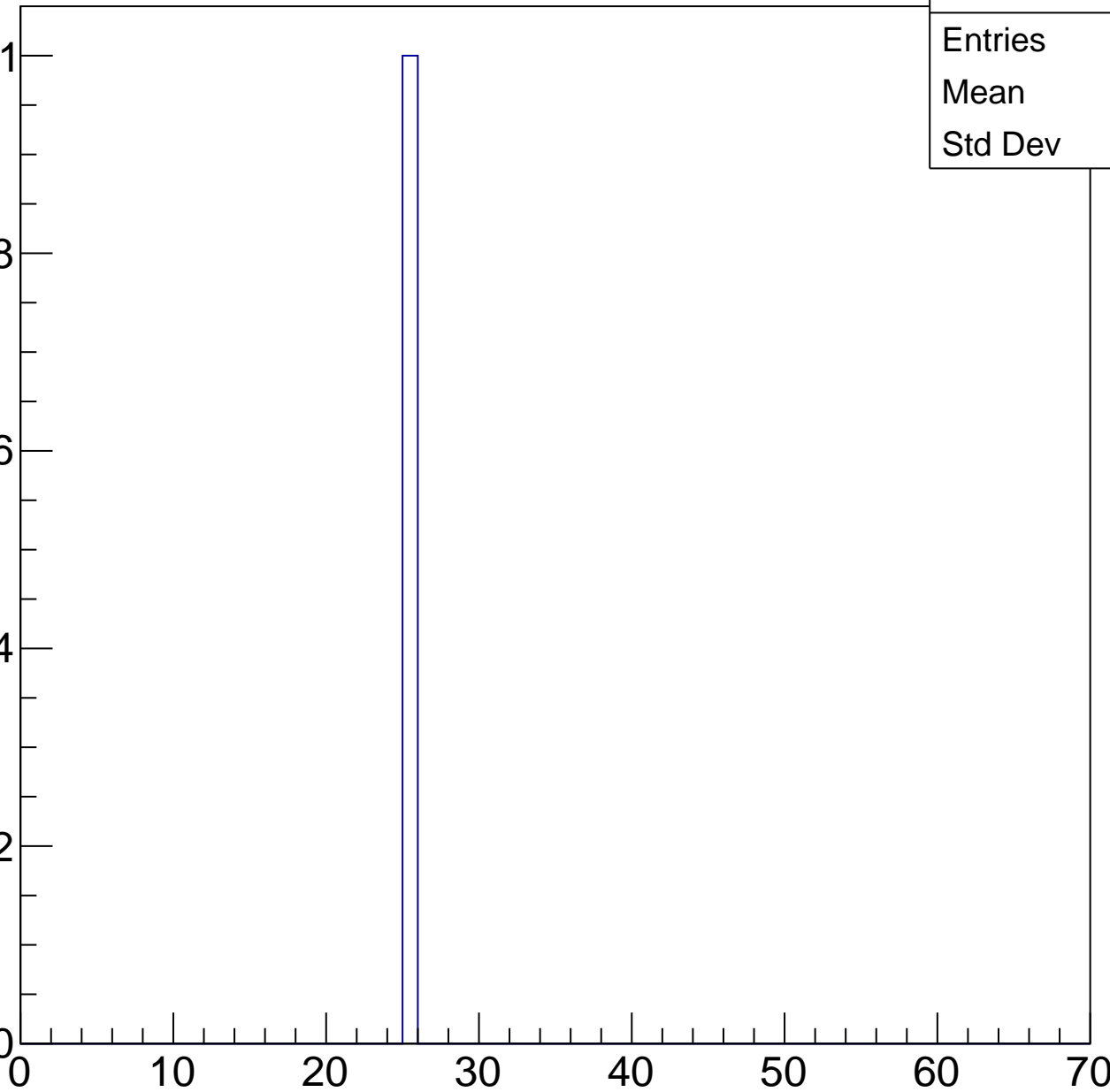
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L001S, U22-ch100, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	30.32
Std Dev	3.37

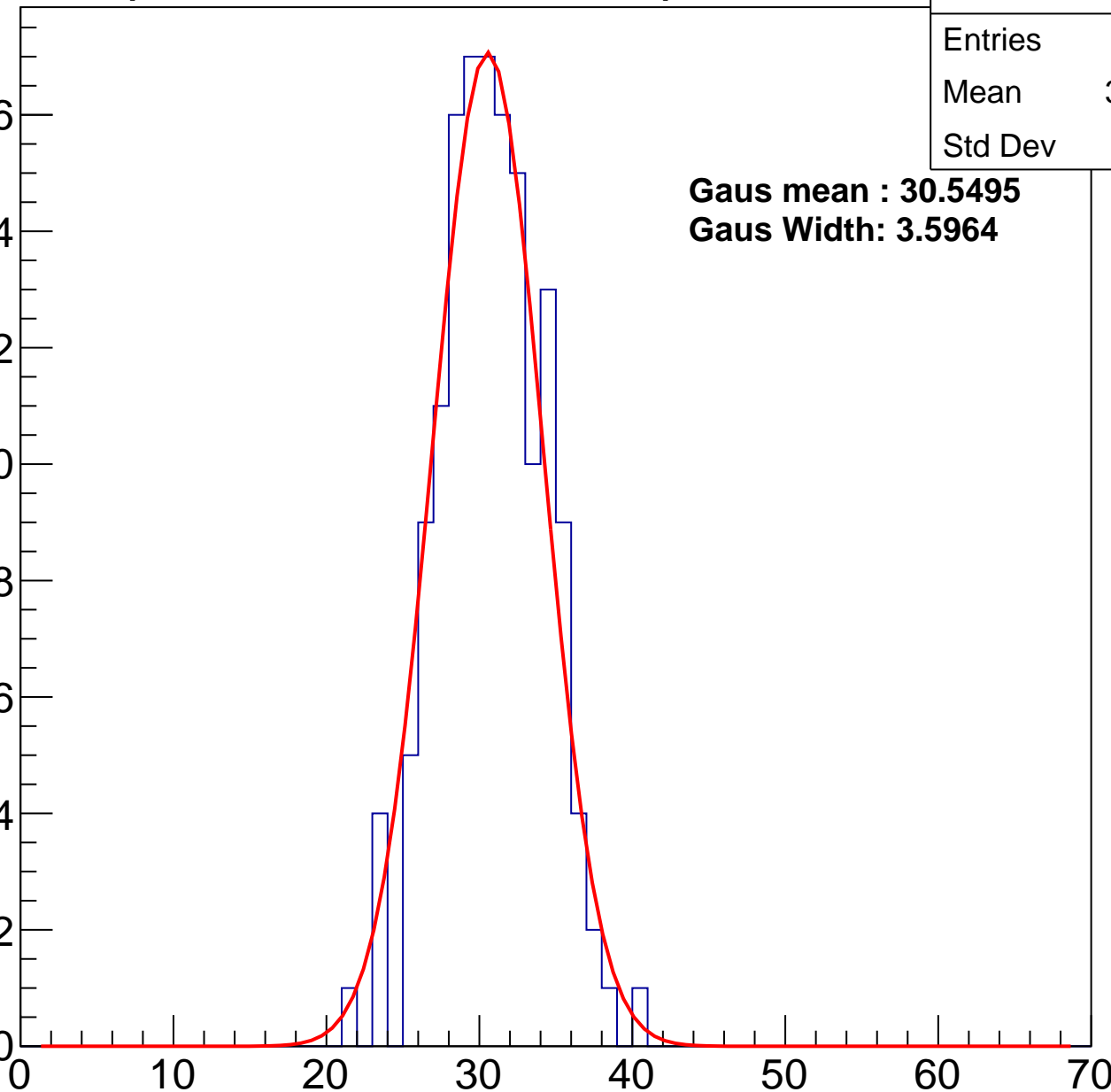
Gaus mean : 30.5495

Gaus Width: 3.5964

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch100, adc1

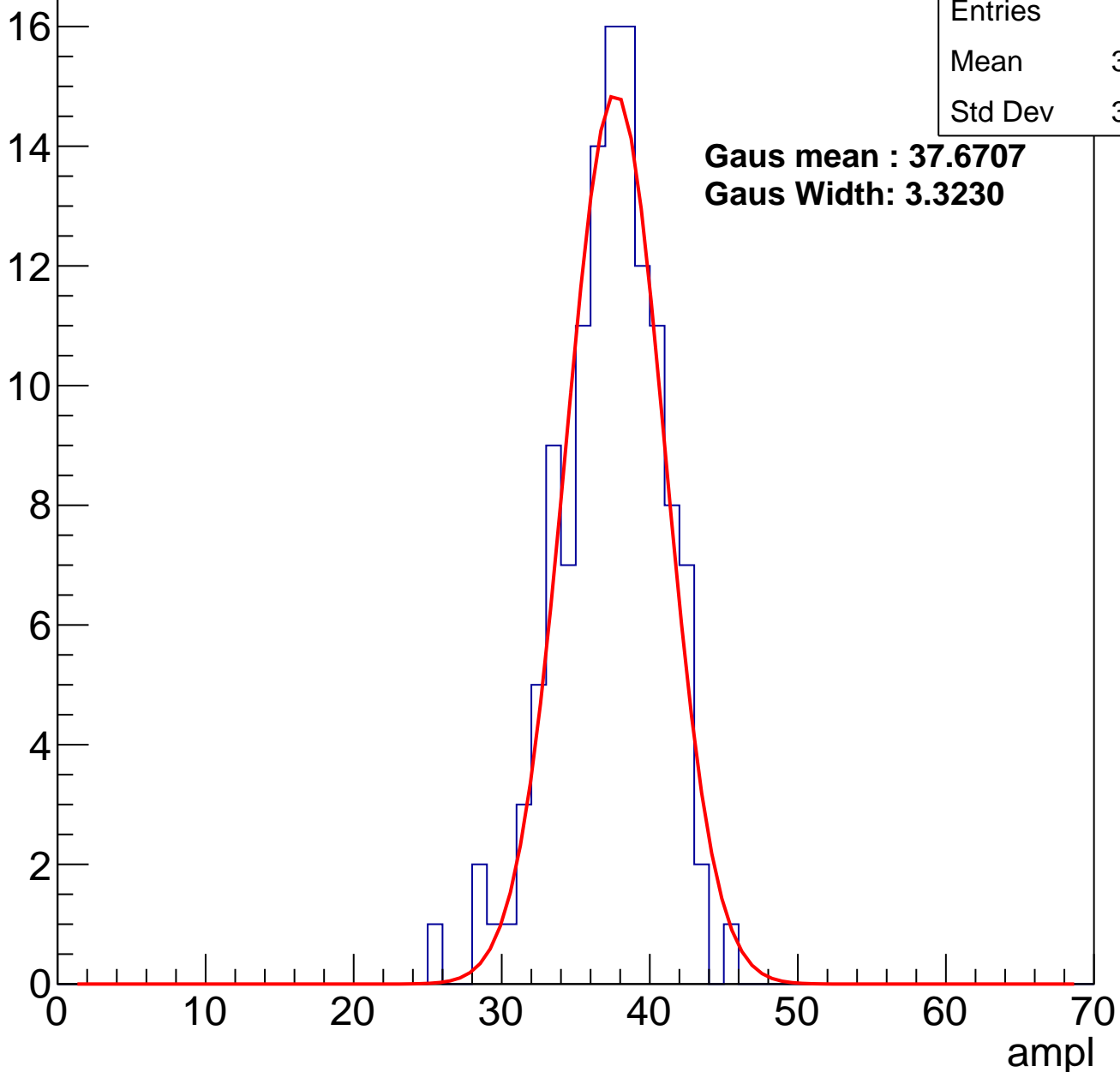
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.83
Std Dev	3.454

Gaus mean : 37.6707

Gaus Width: 3.3230

Entry



B1L001S, U22-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	43.16
Std Dev	3.293

Gaus mean : 43.6470

Gaus Width: 3.3790

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

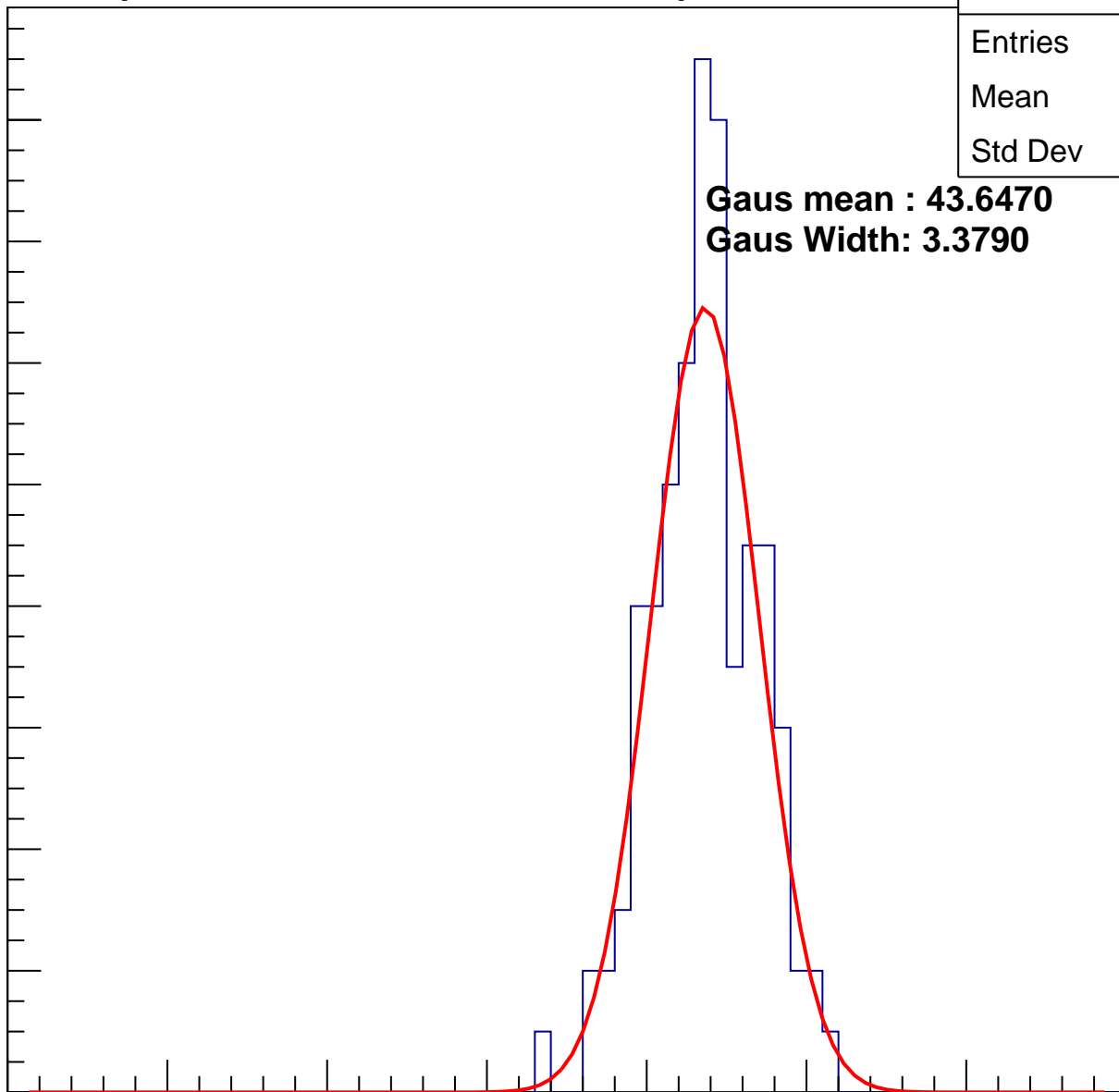
40

50

60

70

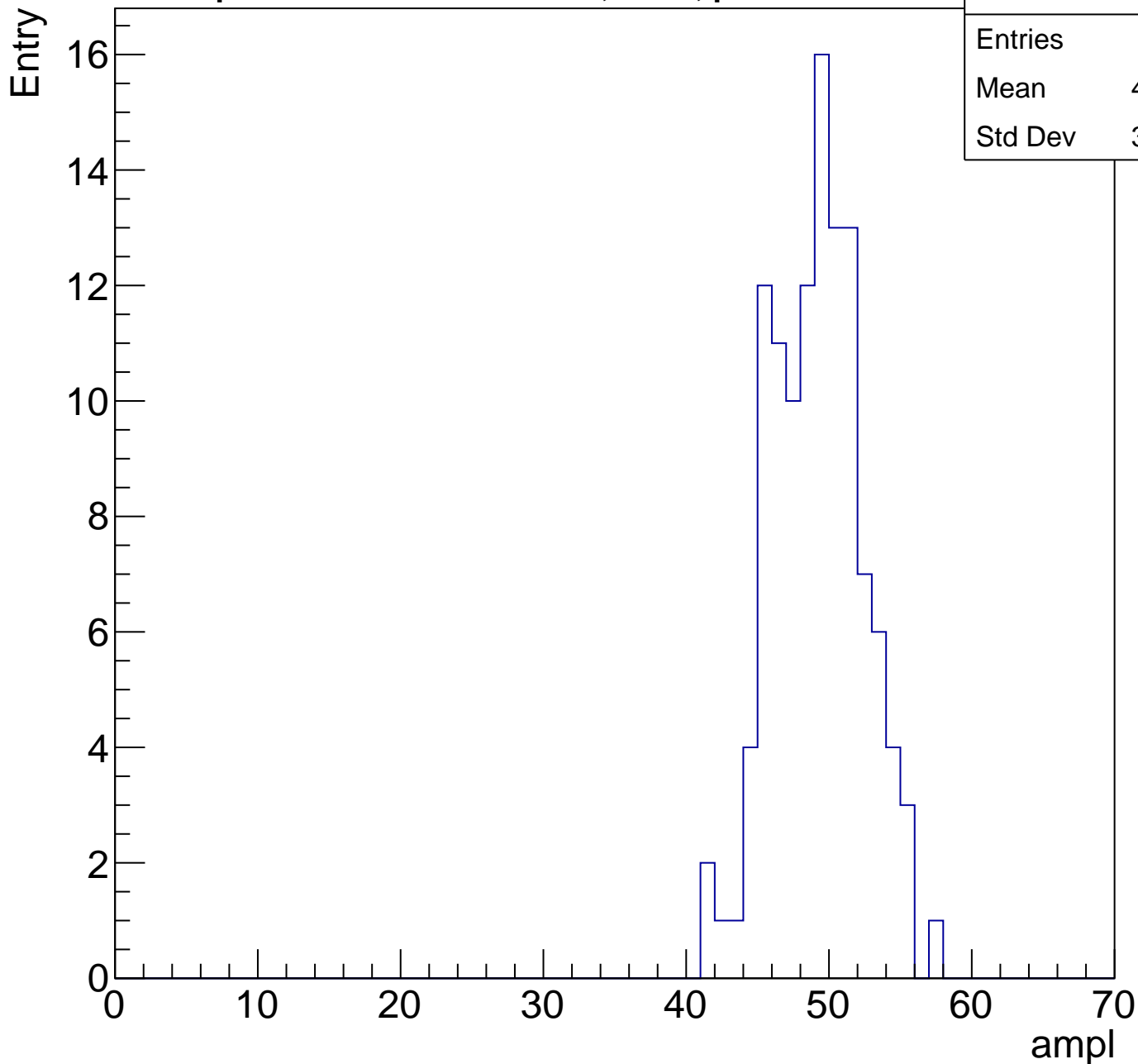
ampl



B1L001S, U22-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

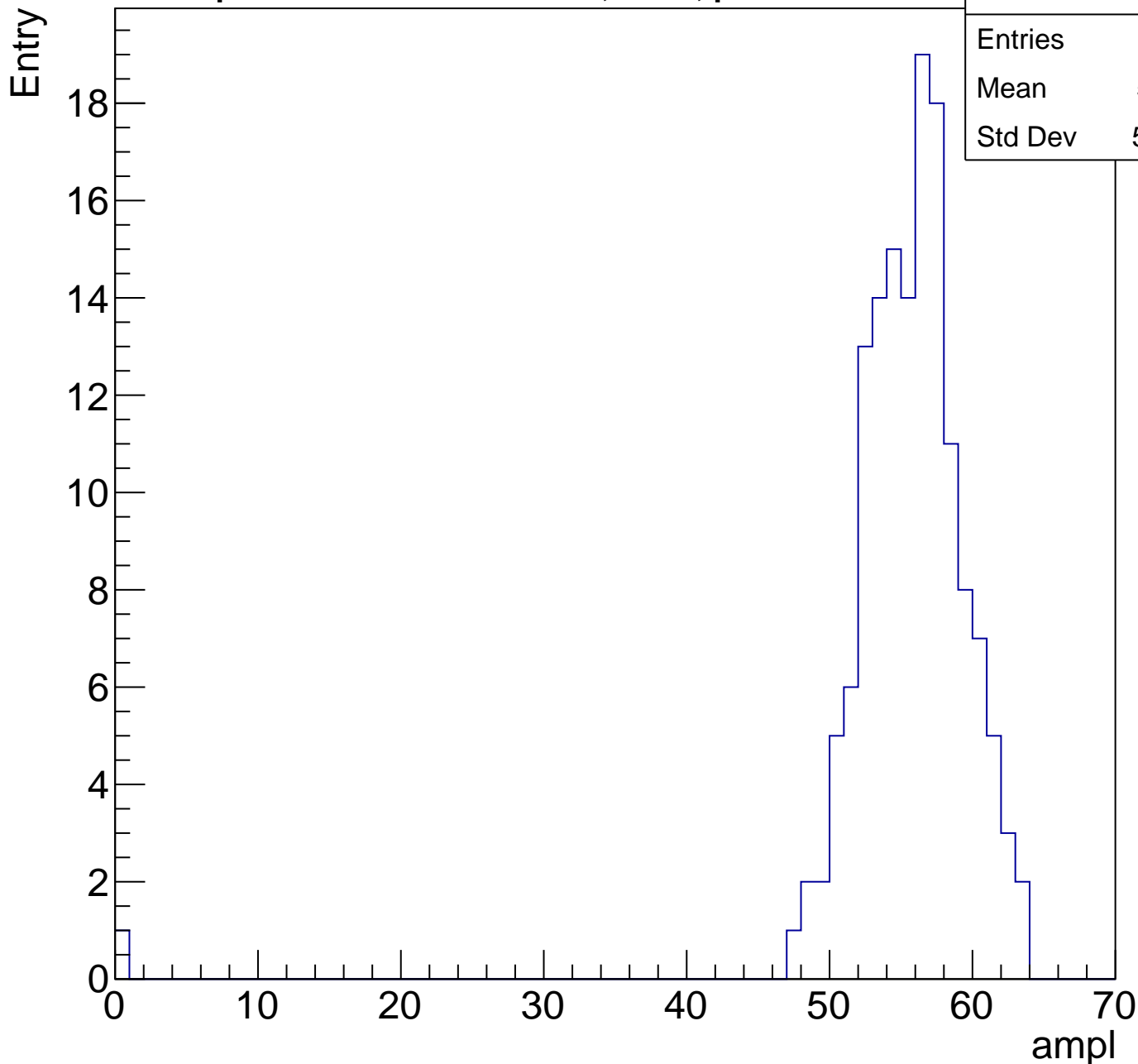
Entries	116
Mean	48.72
Std Dev	3.139



B1L001S, U22-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	55.01
Std Dev	5.623



B1L001S, U22-ch100, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

78

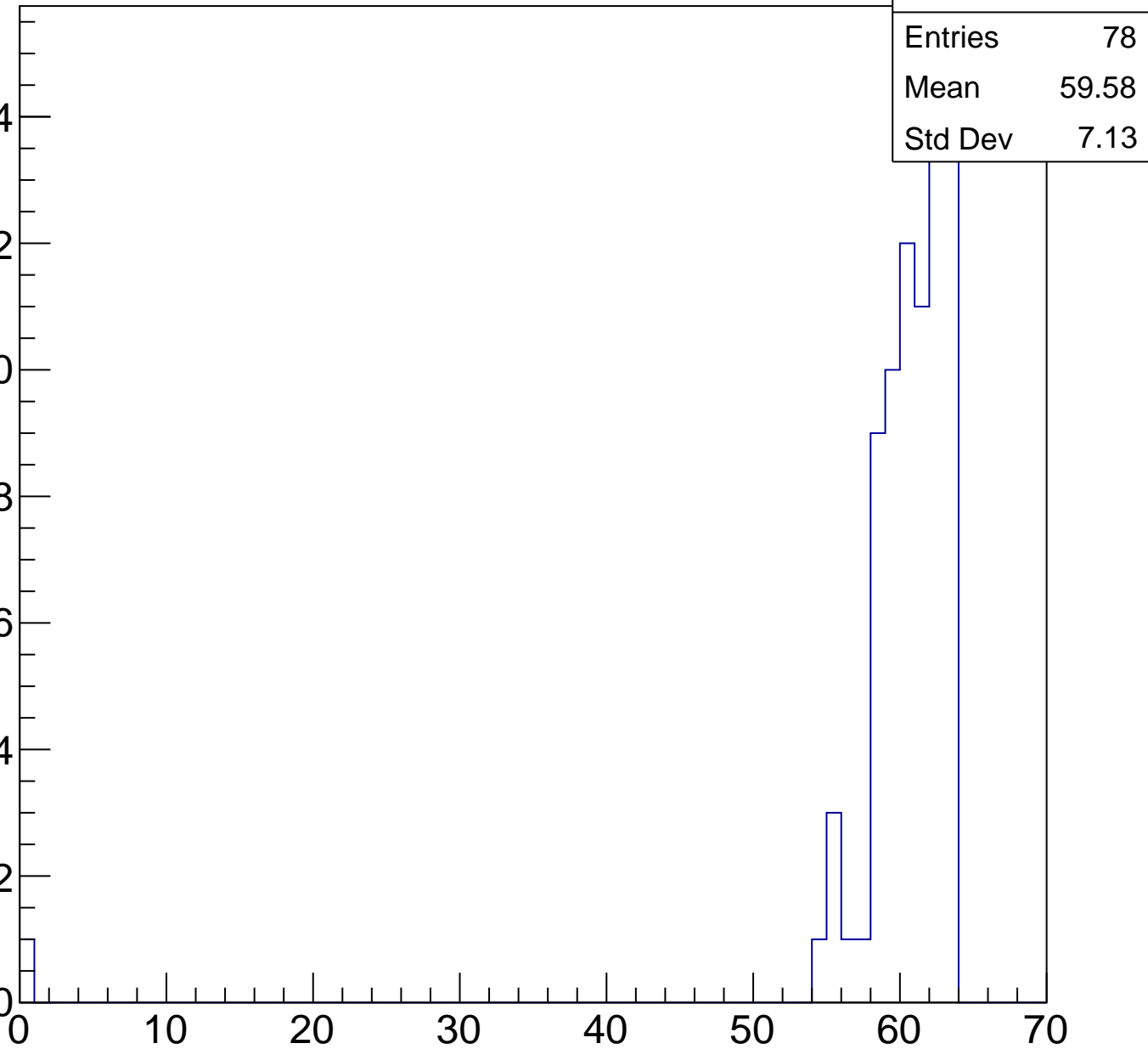
Mean

59.58

Std Dev

7.13

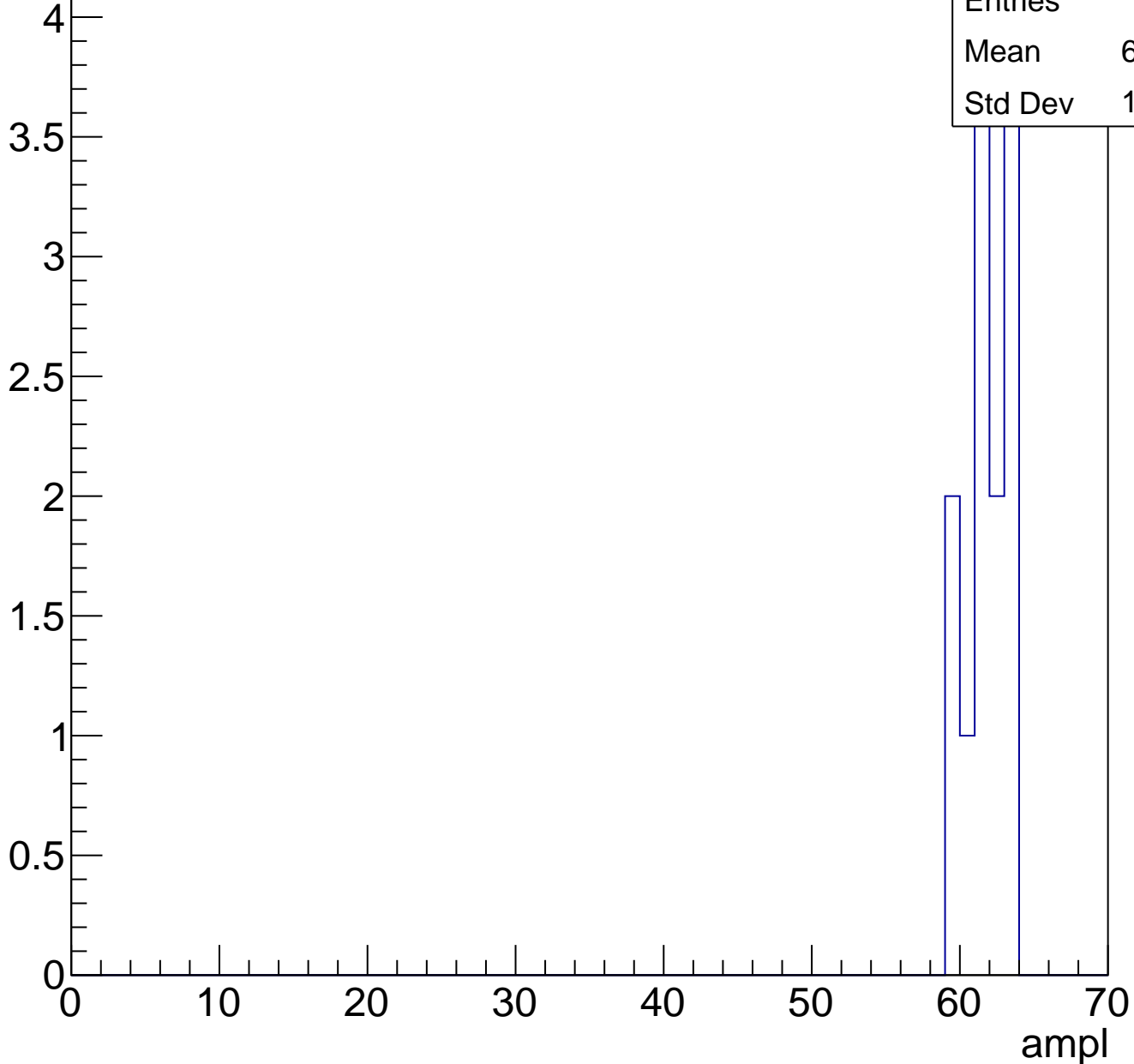
ampl



B1L001S, U22-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U22-ch101, adc0

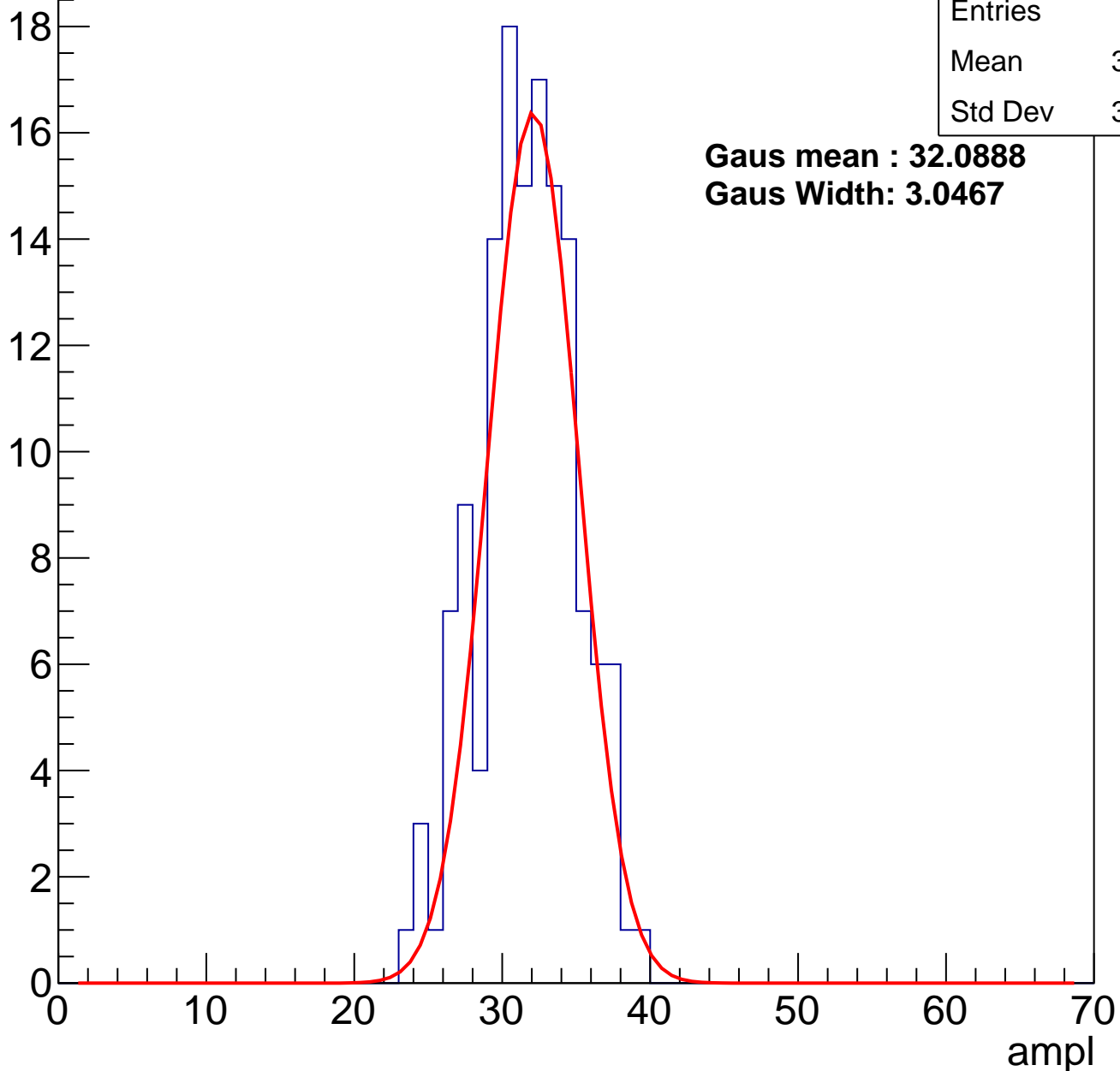
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	31.24
Std Dev	3.243

Gaus mean : 32.0888

Gaus Width: 3.0467

Entry



B1L001S, U22-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	38.13
Std Dev	3.142

Gaus mean : 38.6937

Gaus Width: 3.2745

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

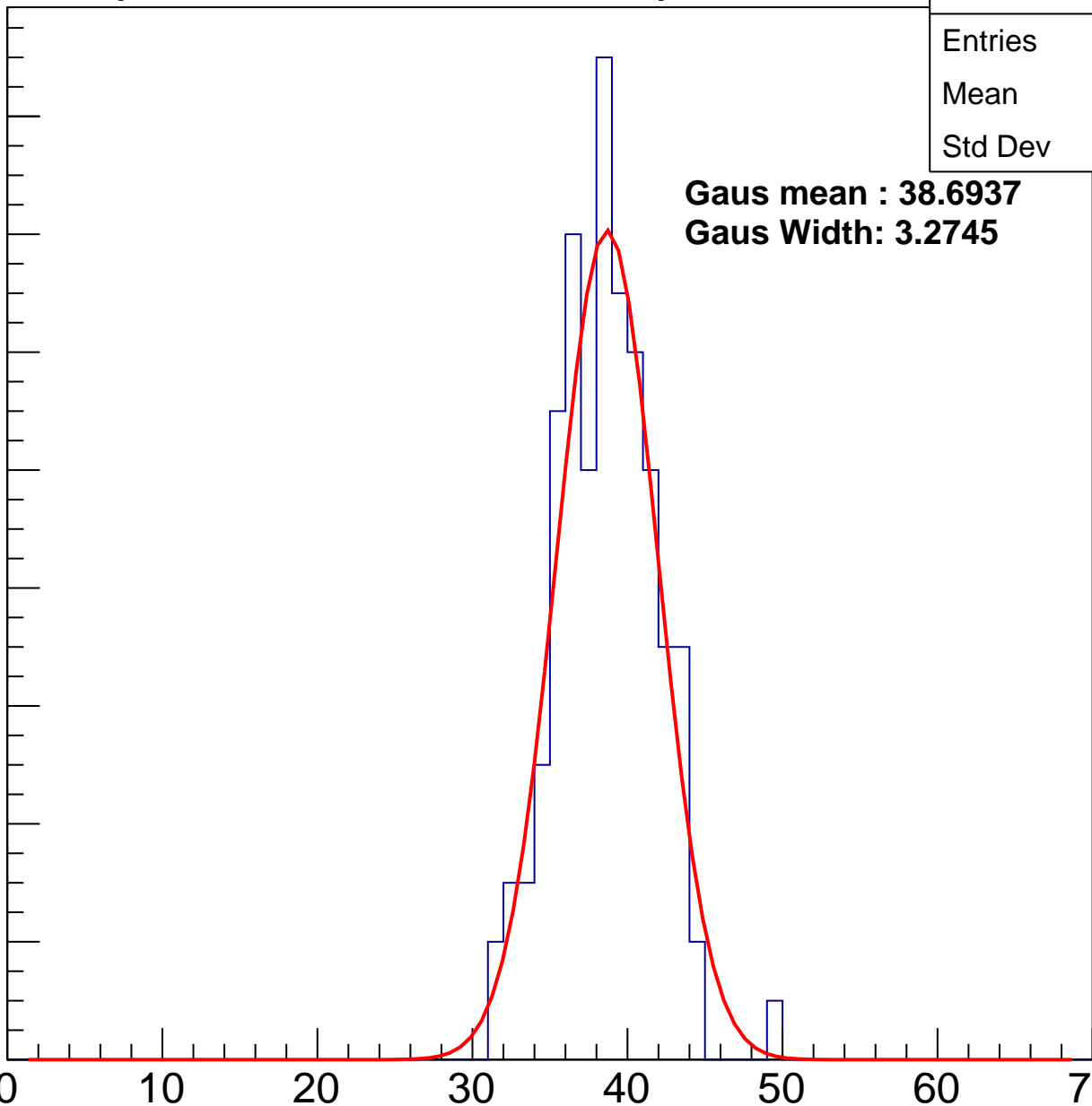
40

50

60

70

ampl



B1L001S, U22-ch101, adc2

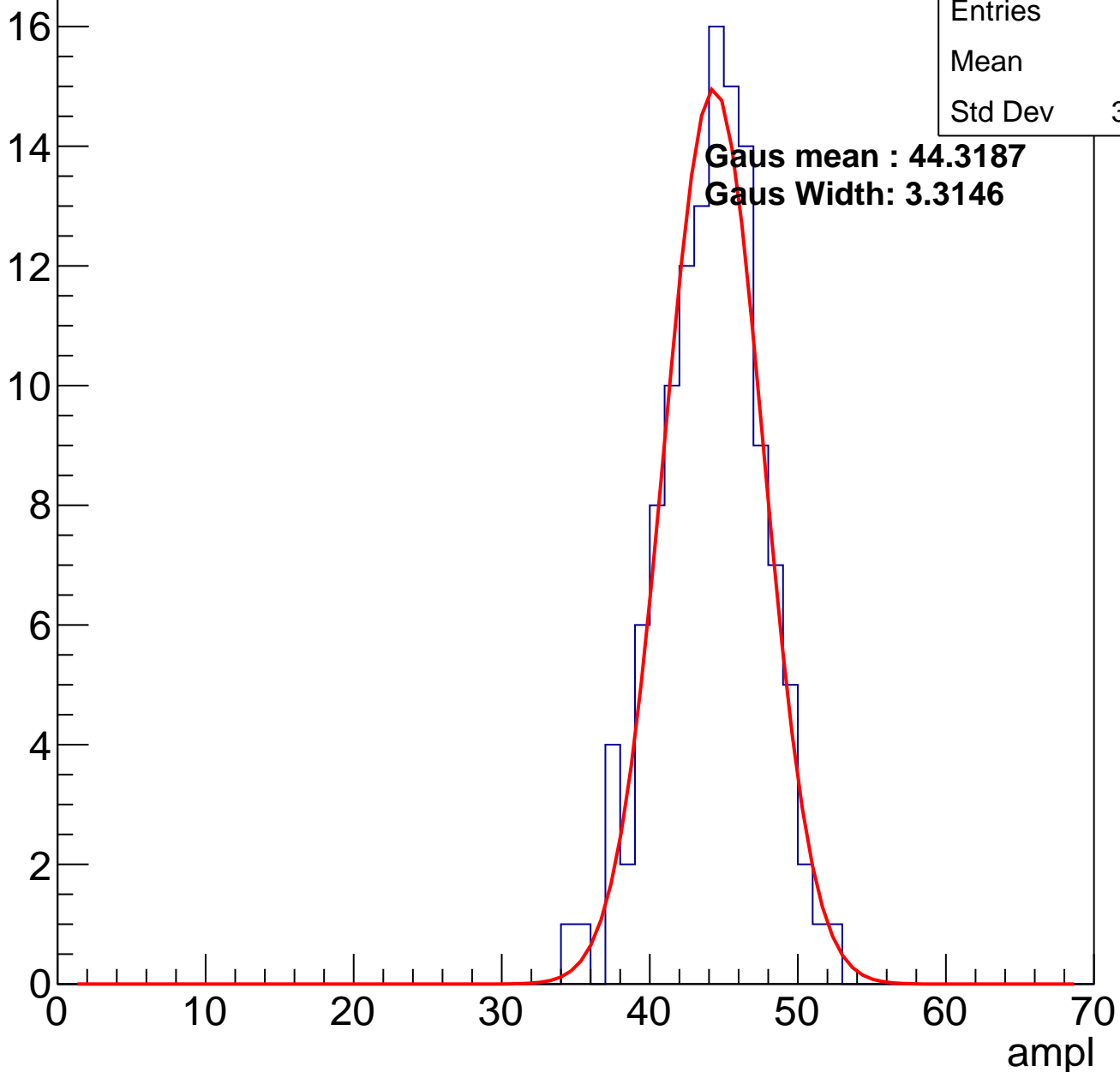
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	43.7
Std Dev	3.368

Gaus mean : 44.3187

Gaus Width: 3.3146

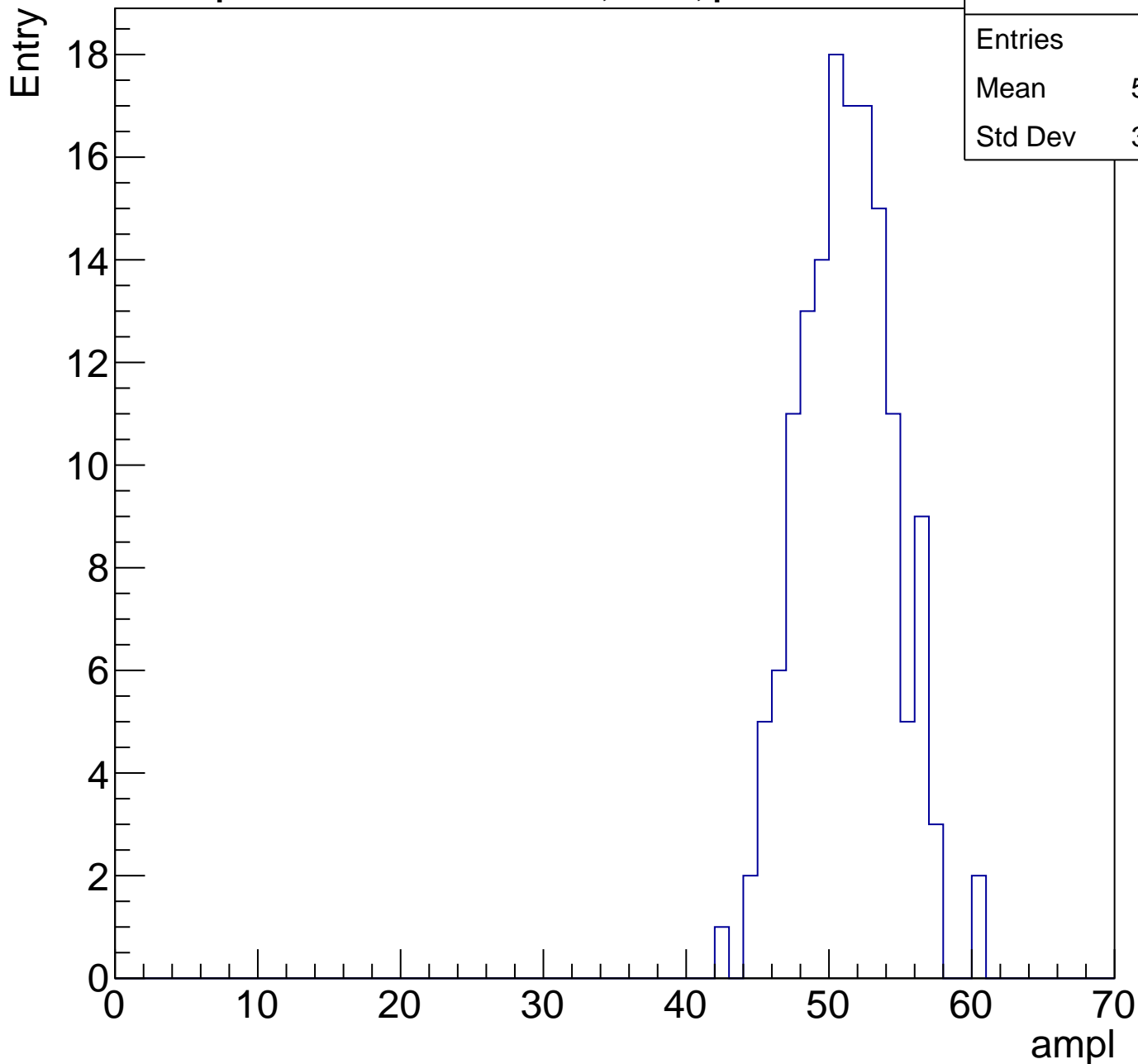
Entry



B1L001S, U22-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	50.79
Std Dev	3.298



B1L001S, U22-ch101, adc4

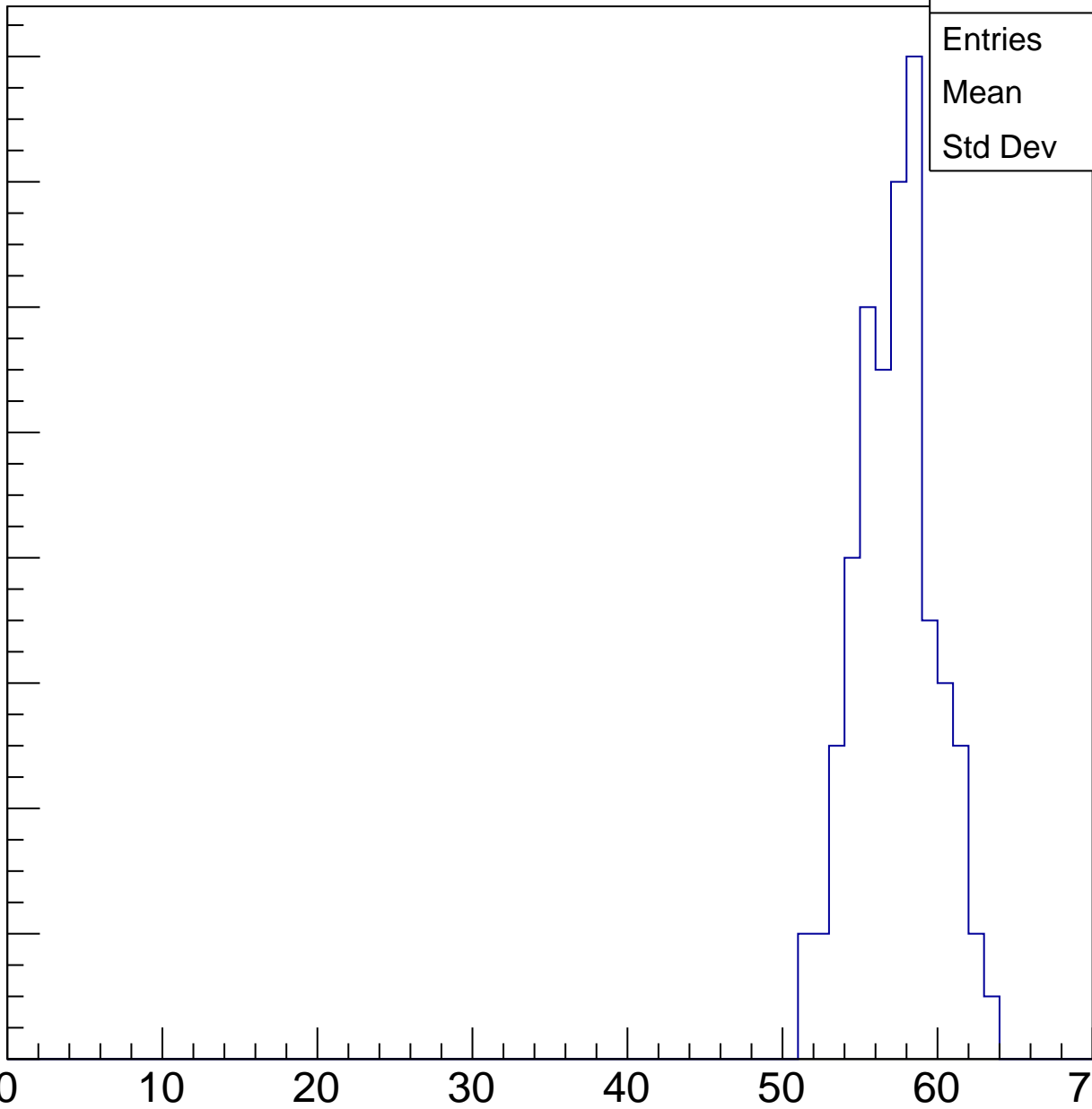
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	56.81
Std Dev	2.555

ampl



B1L001S, U22-ch101, adc5

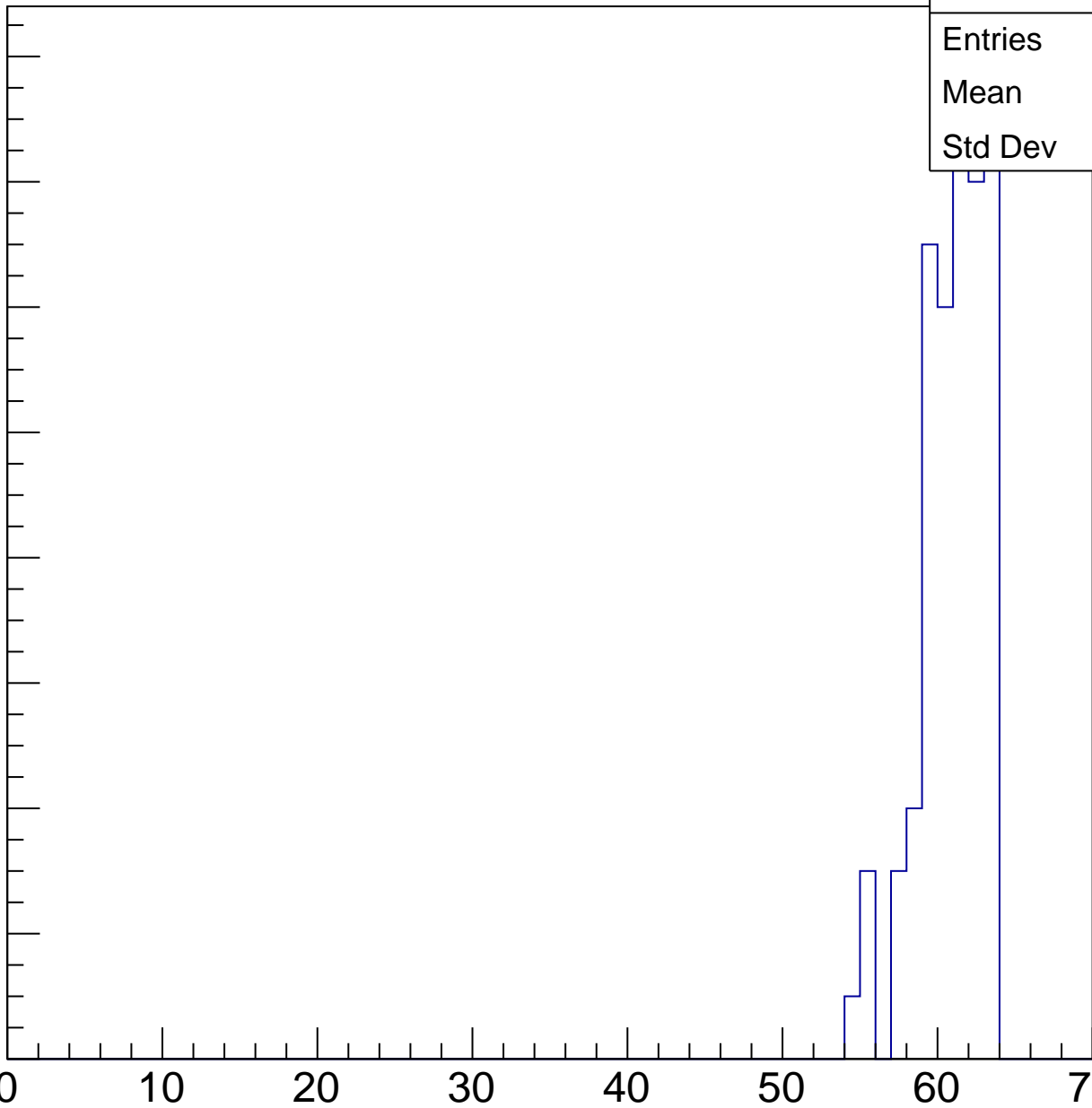
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	60.49
Std Dev	2.115

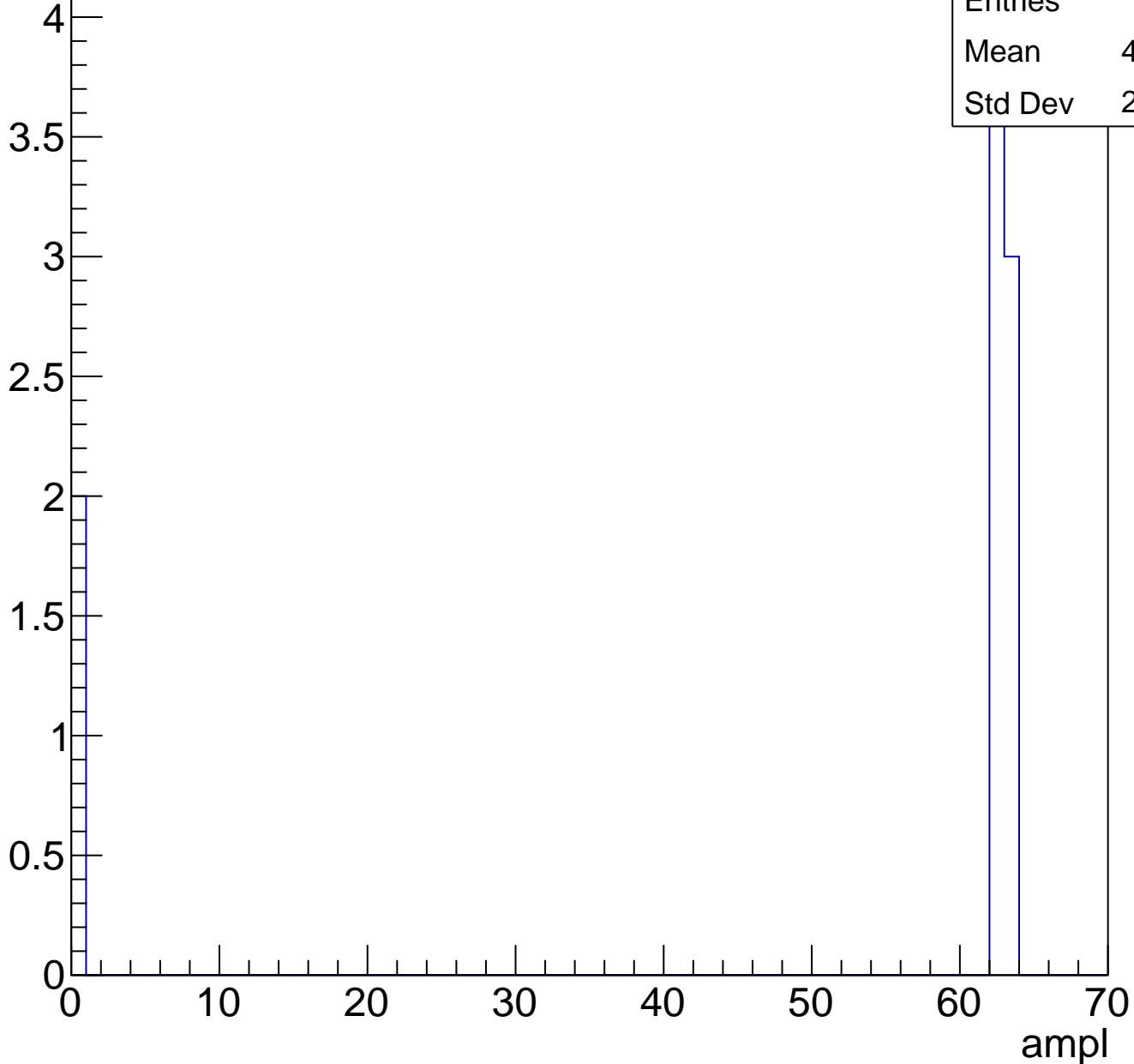
ampl



B1L001S, U22-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

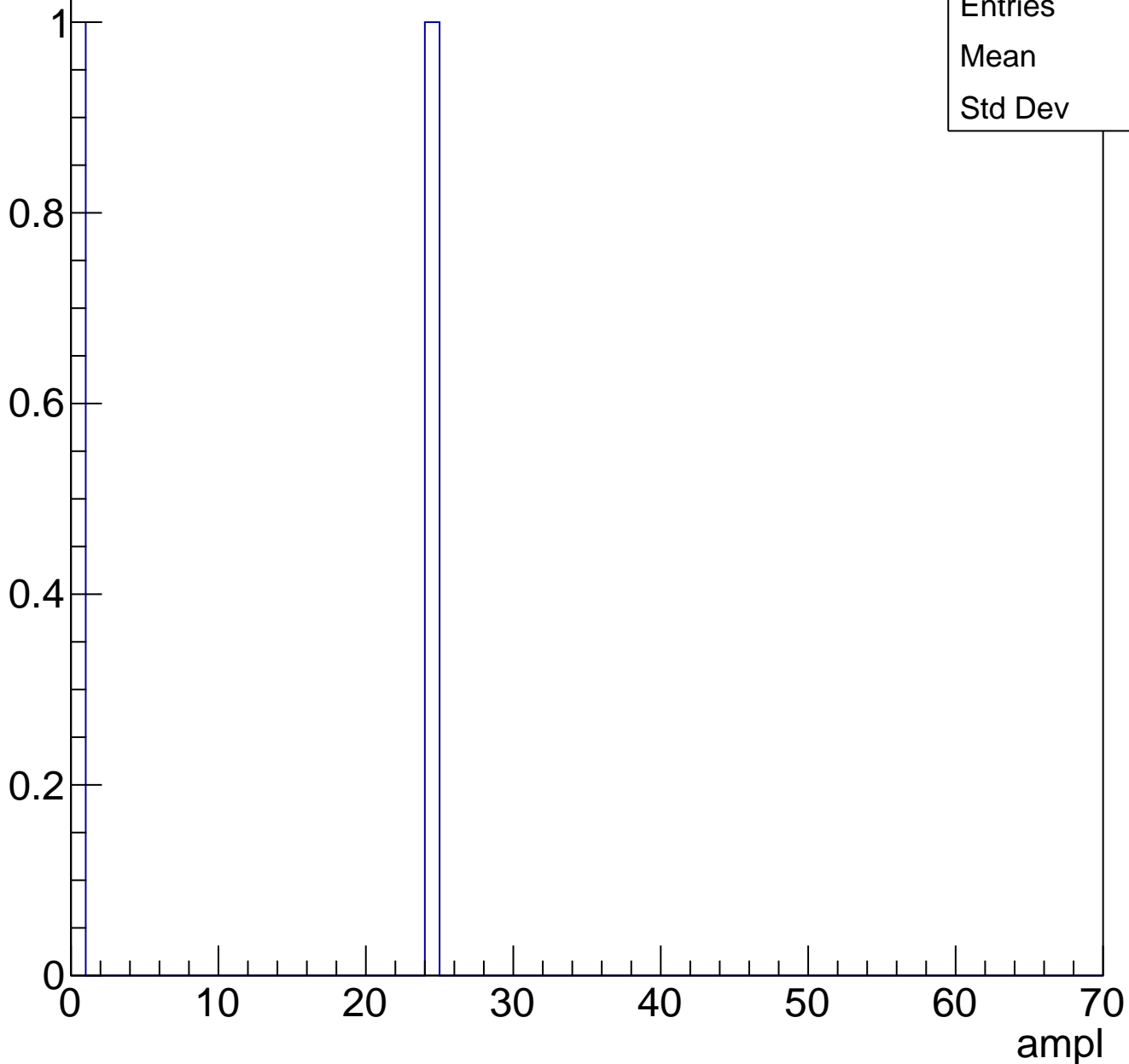


Entries	9
Mean	48.56
Std Dev	25.96

B1L001S, U22-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



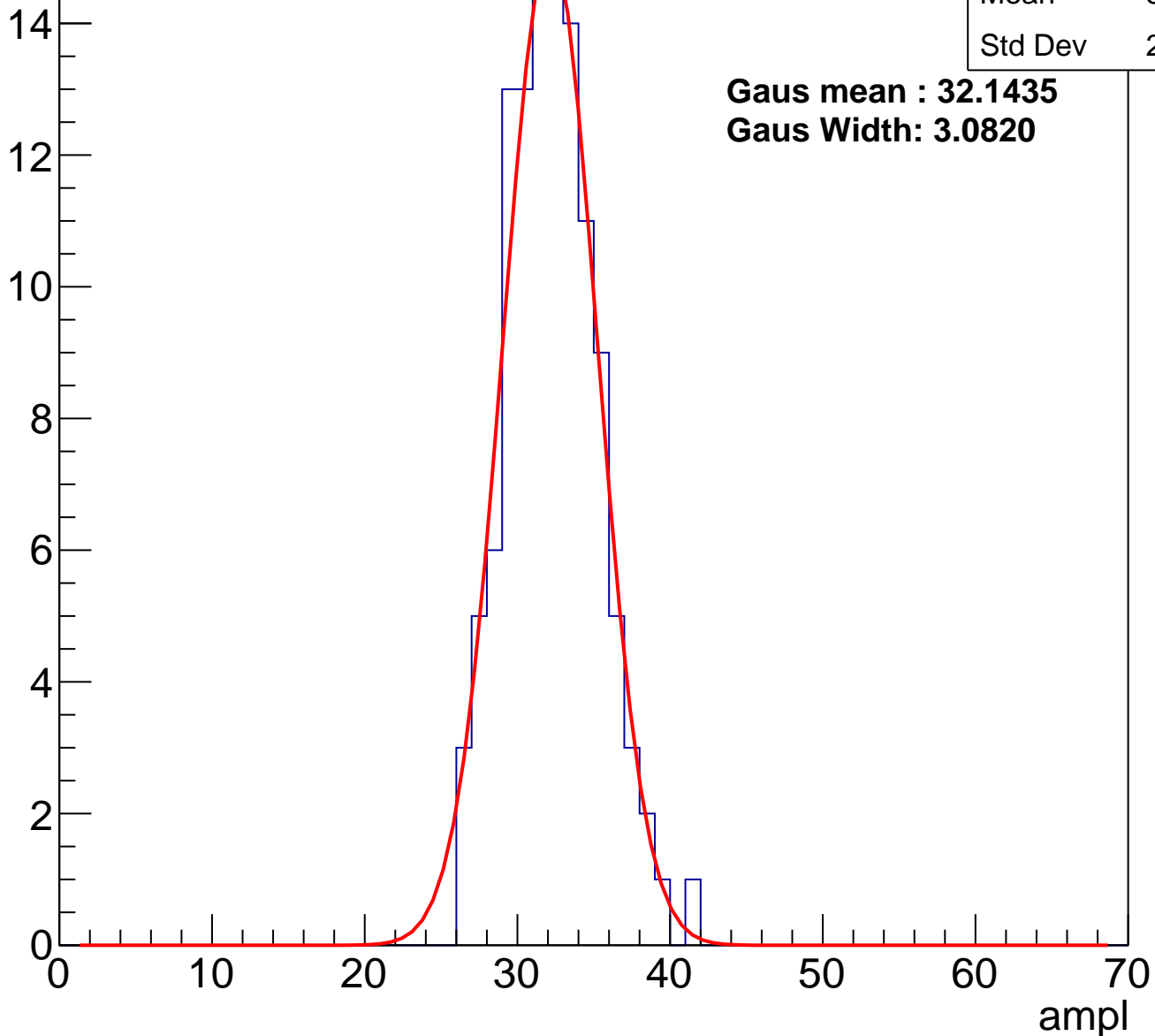
B1L001S, U22-ch102, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	31.82
Std Dev	2.944

Gaus mean : 32.1435
Gaus Width: 3.0820

Entry



B1L001S, U22-ch102, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	37.76
Std Dev	3.341

Gaus mean : 38.2558

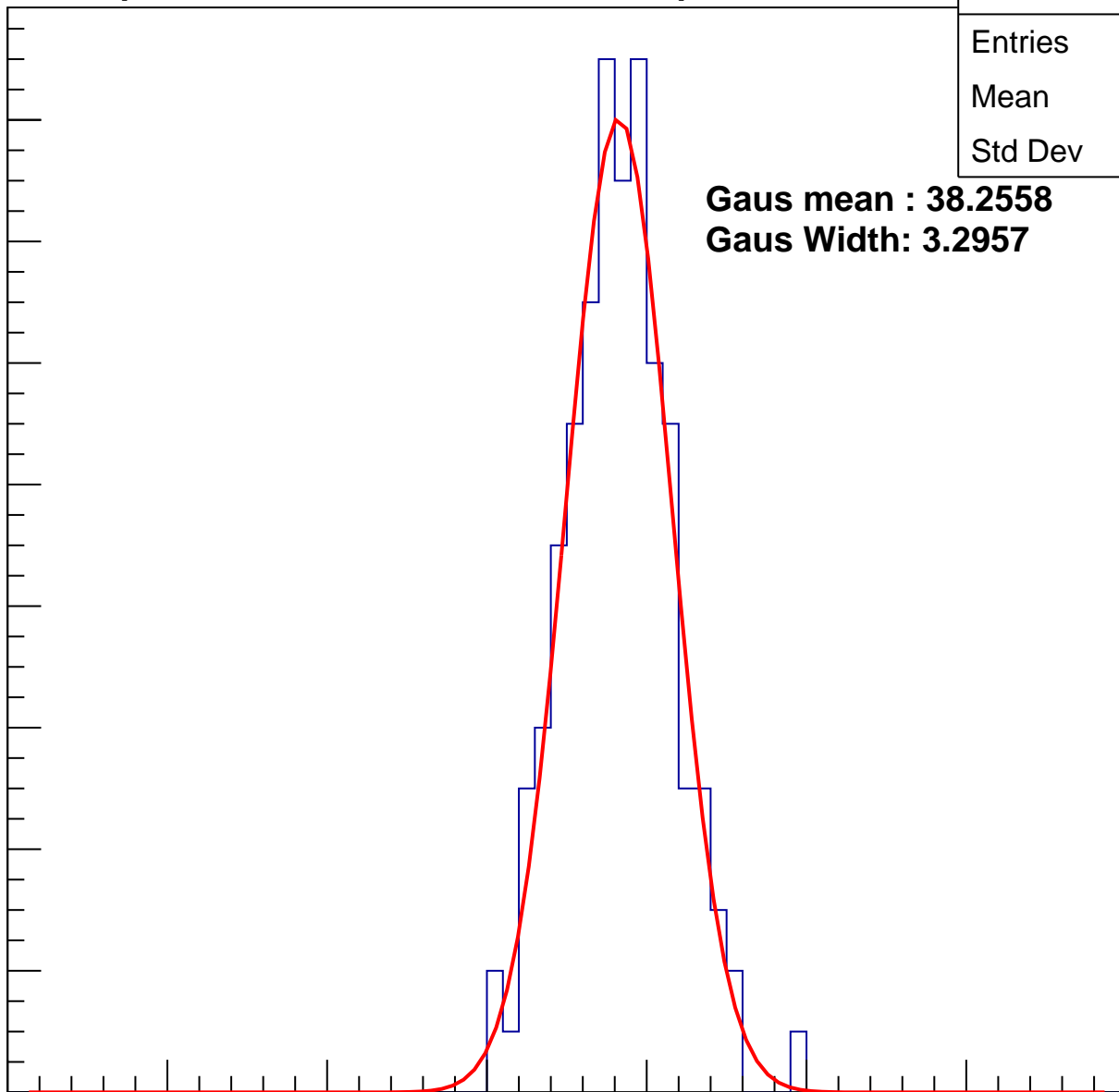
Gaus Width: 3.2957

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

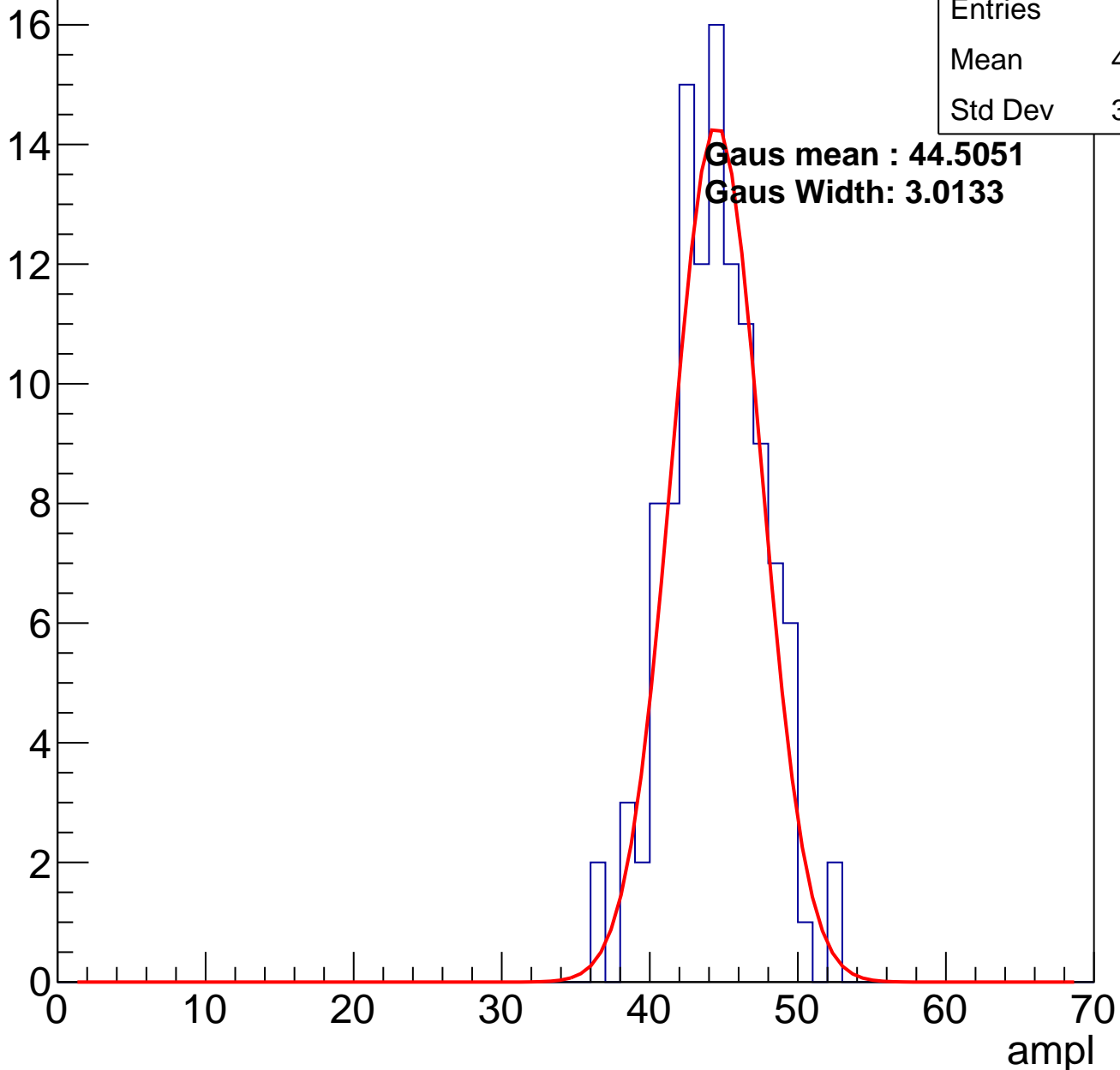
ampl



B1L001S, U22-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

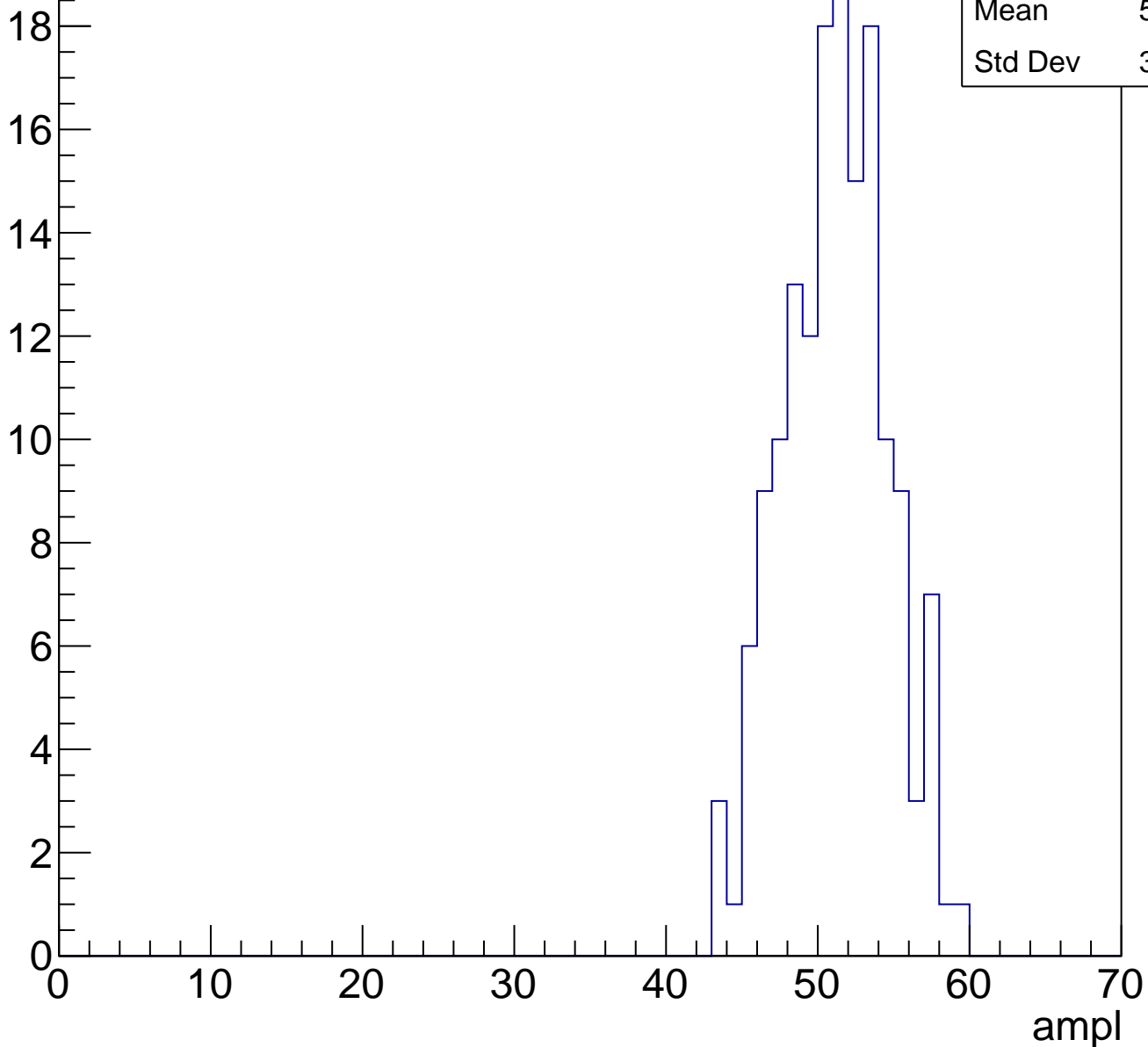
Entry



B1L001S, U22-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

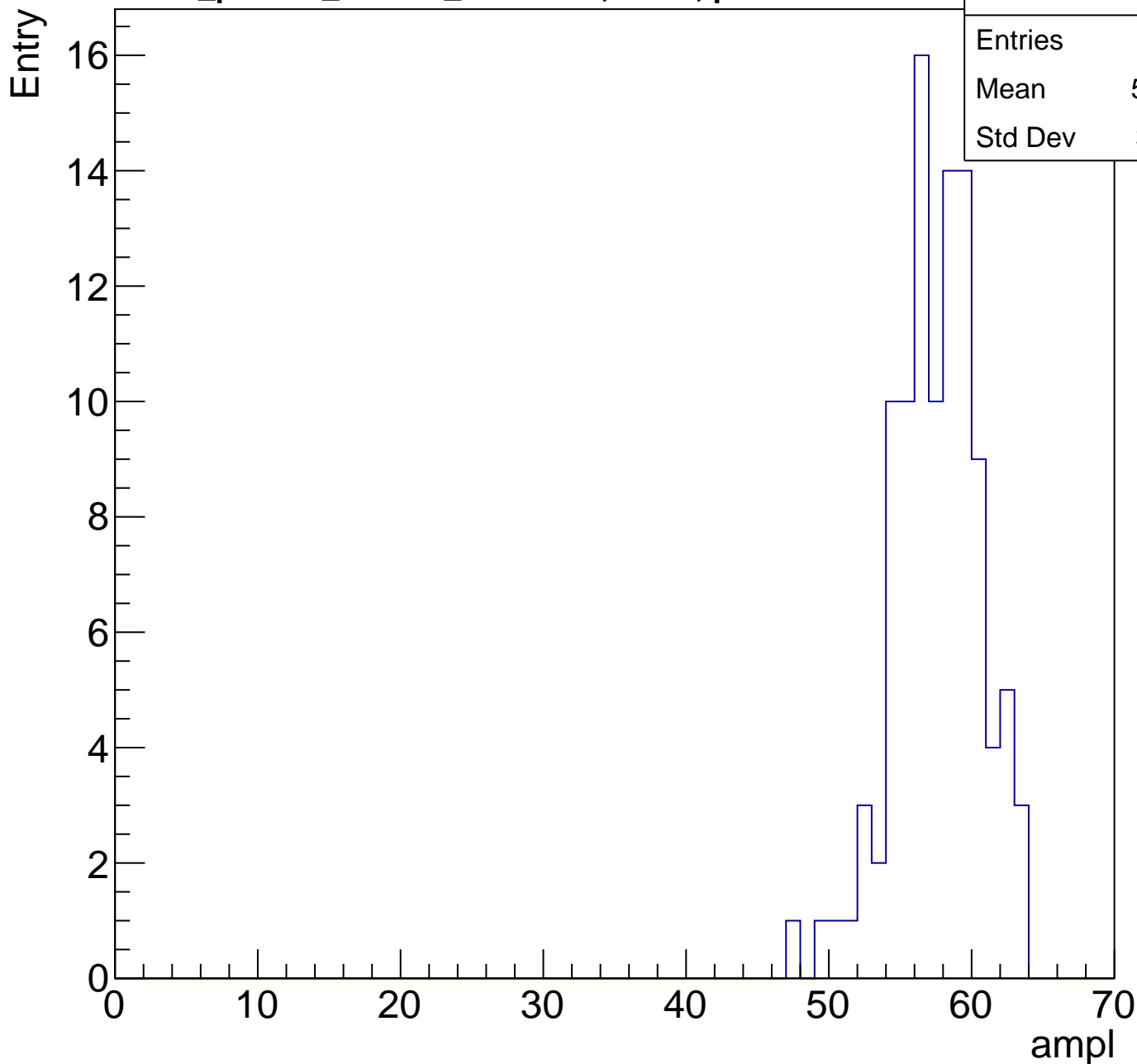


Entries	155
Mean	50.72
Std Dev	3.383

B1L001S, U22-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	57.08
Std Dev	3.021



B1L001S, U22-ch102, adc5

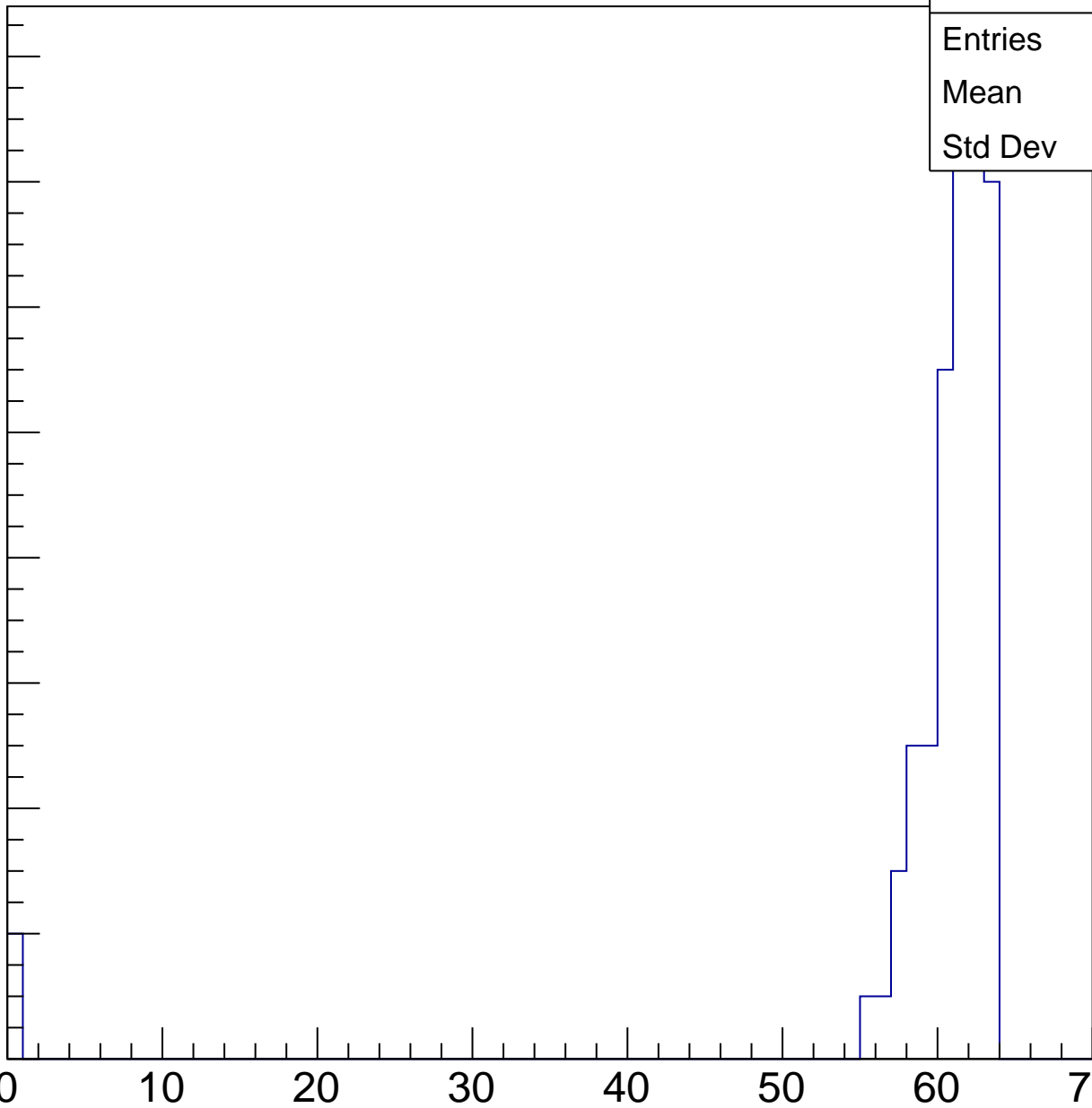
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.11
Std Dev	10.09

ampl



B1L001S, U22-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

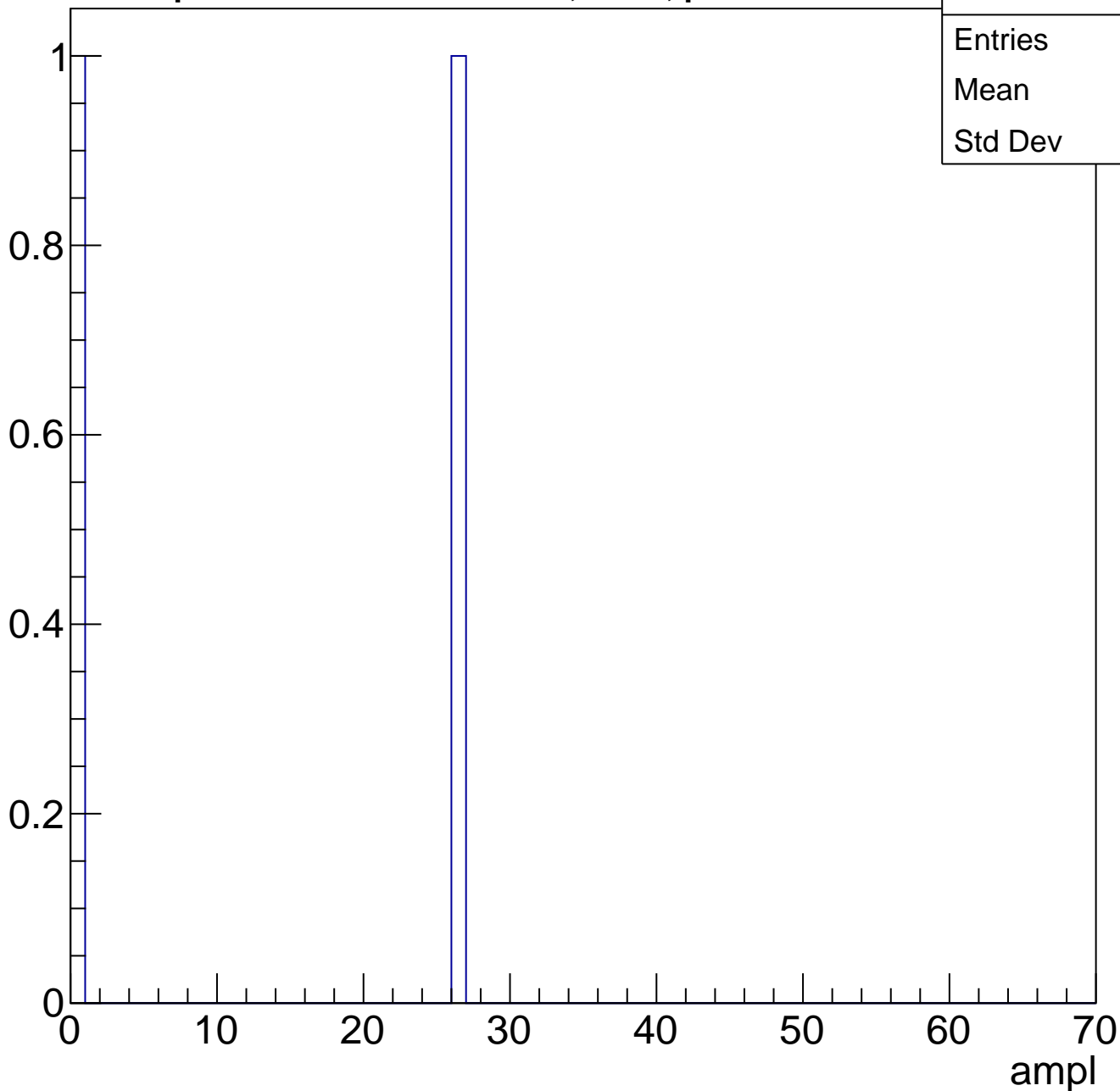
Entry



B1L001S, U22-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch103, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	29.99
Std Dev	5.175

Gaus mean : 30.9416

Gaus Width: 2.6731

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

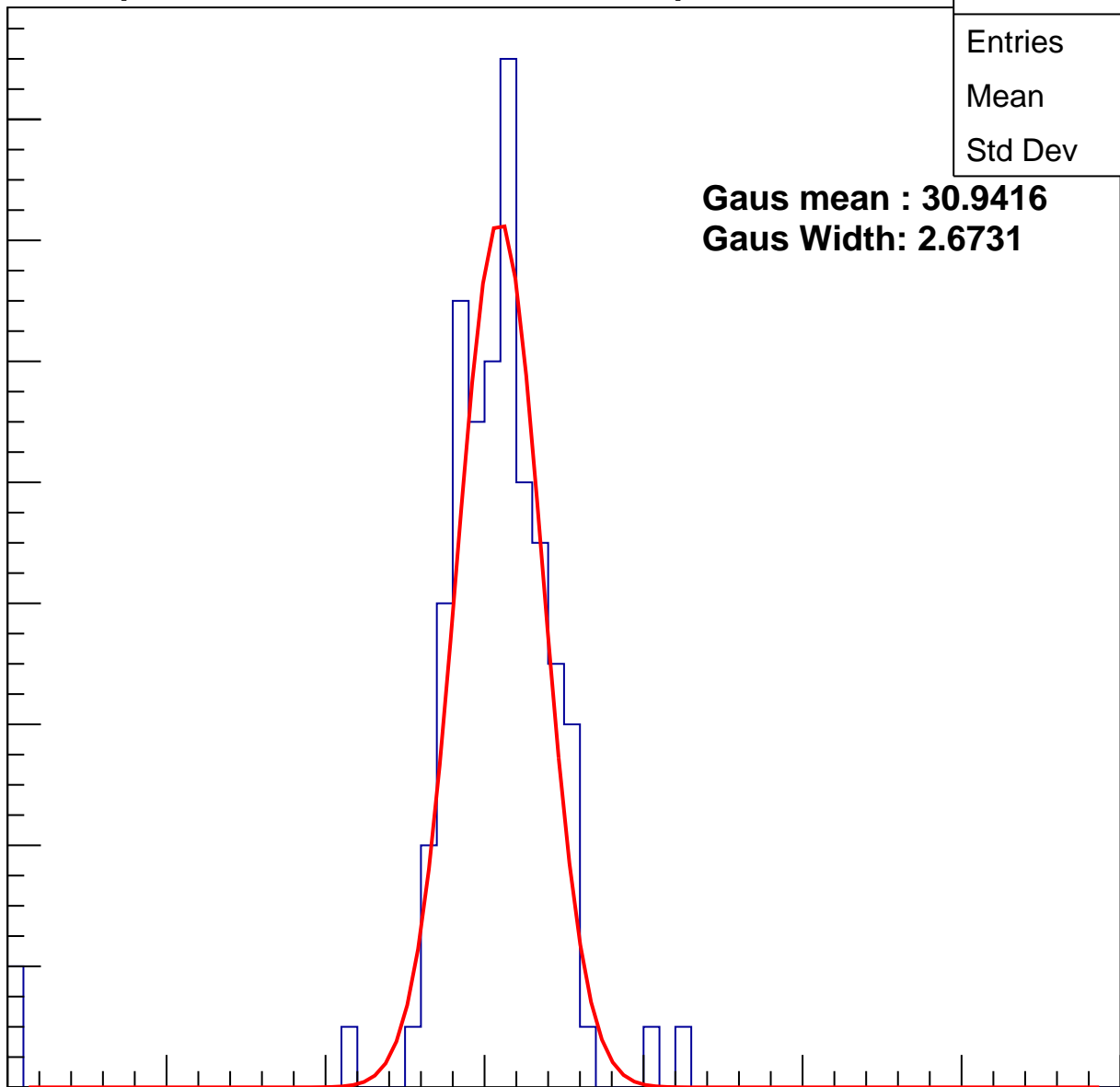
40

50

60

70

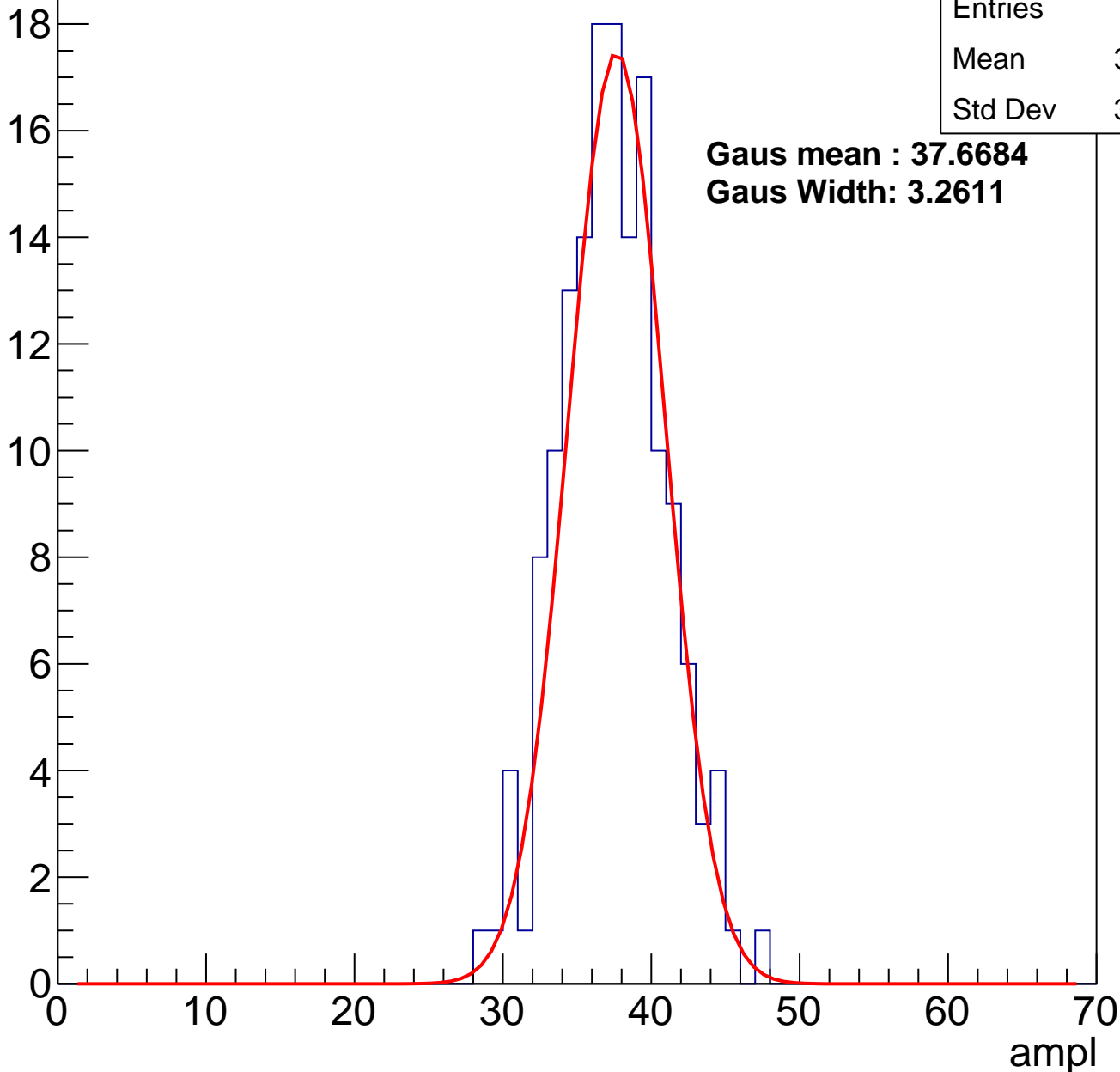
ampl



B1L001S, U22-ch103, adc1

calib_packv5_042523_0143.root, FC#2, port C2

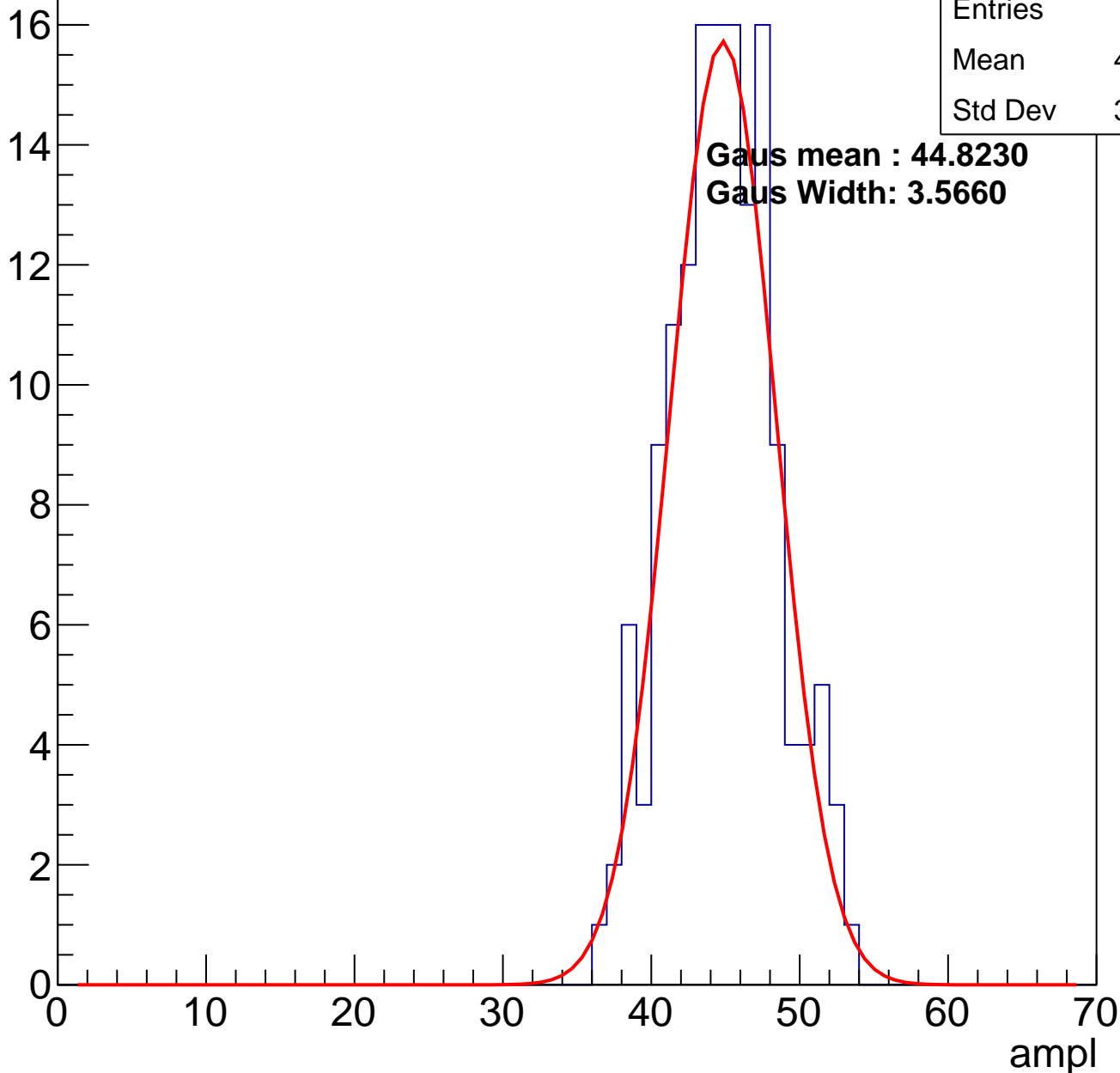
Entry



B1L001S, U22-ch103, adc2

calib_packv5_042523_0143.root, FC#2, port C2

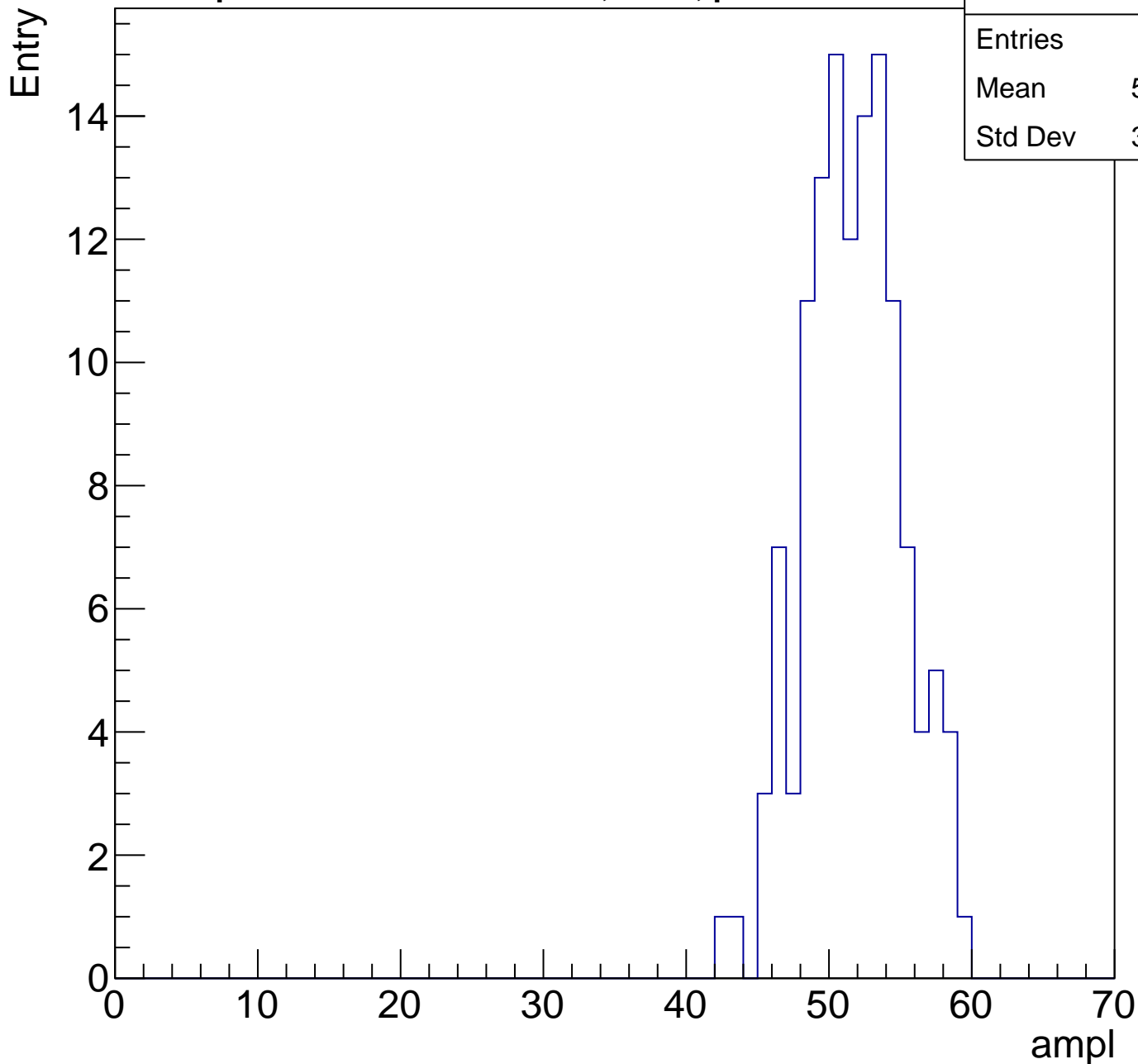
Entry



B1L001S, U22-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

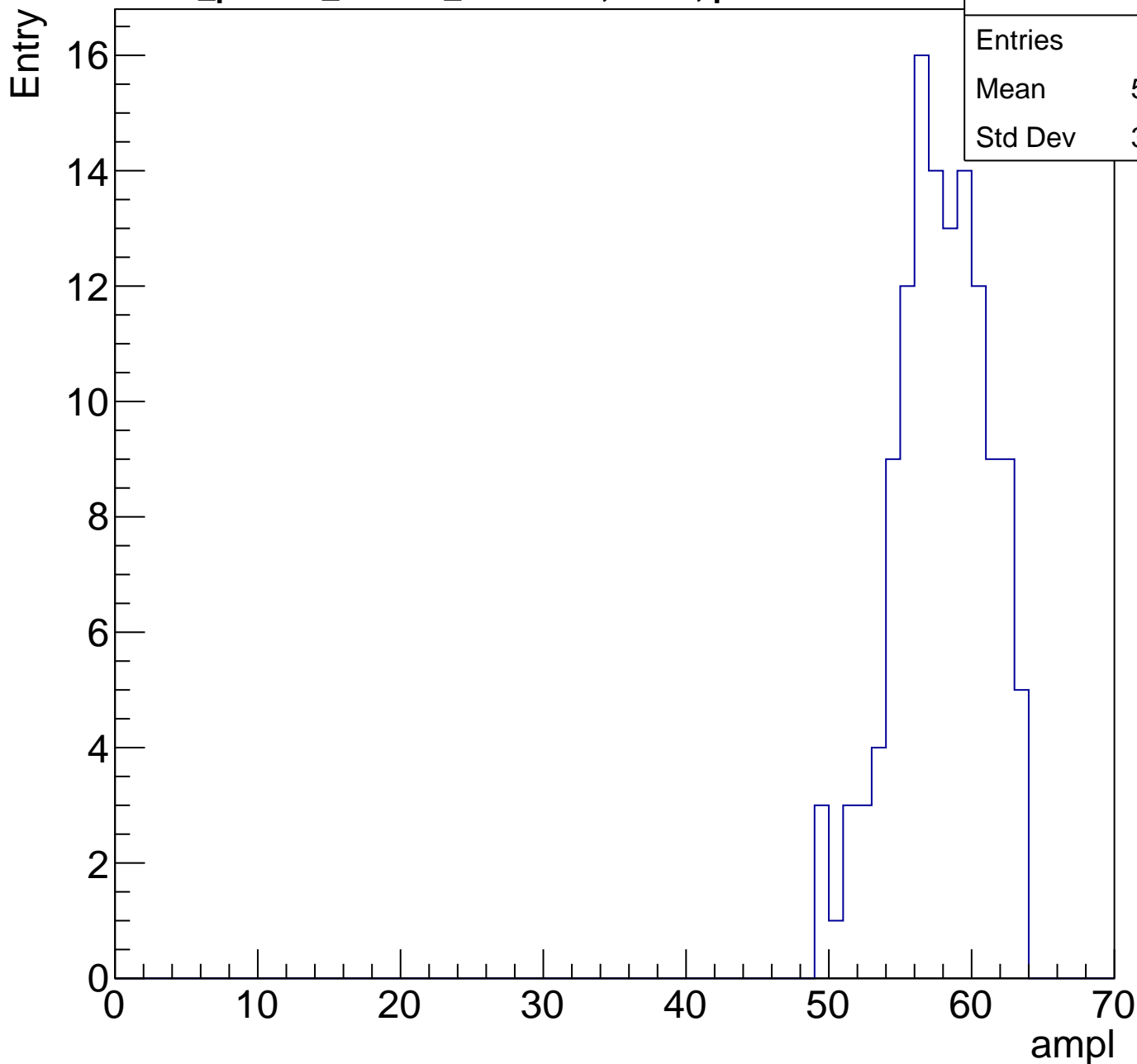
Entries	127
Mean	51.28
Std Dev	3.388



B1L001S, U22-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	57.32
Std Dev	3.255



B1L001S, U22-ch103, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

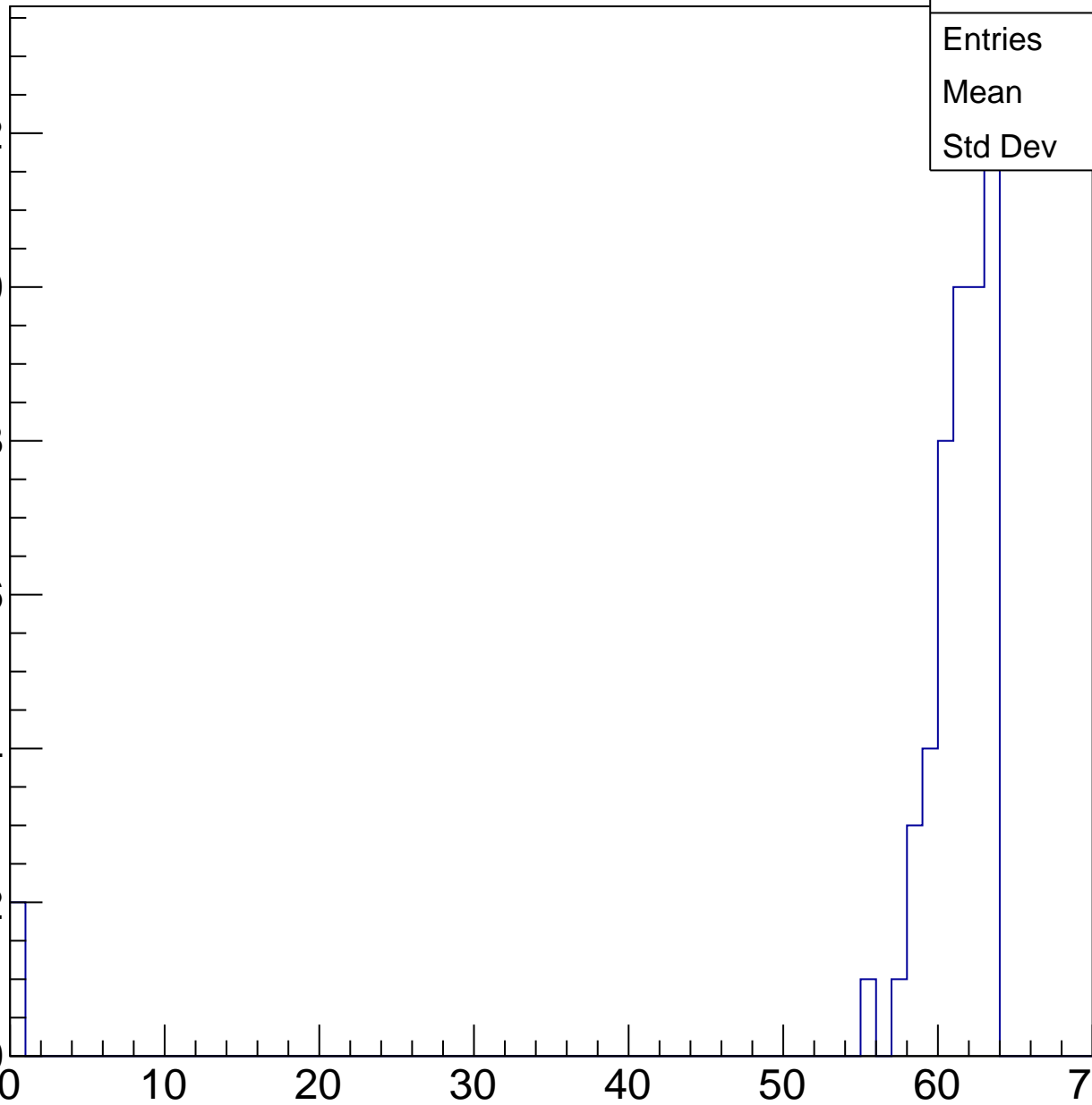
4

2

0

Entries	52
Mean	58.67
Std Dev	11.87

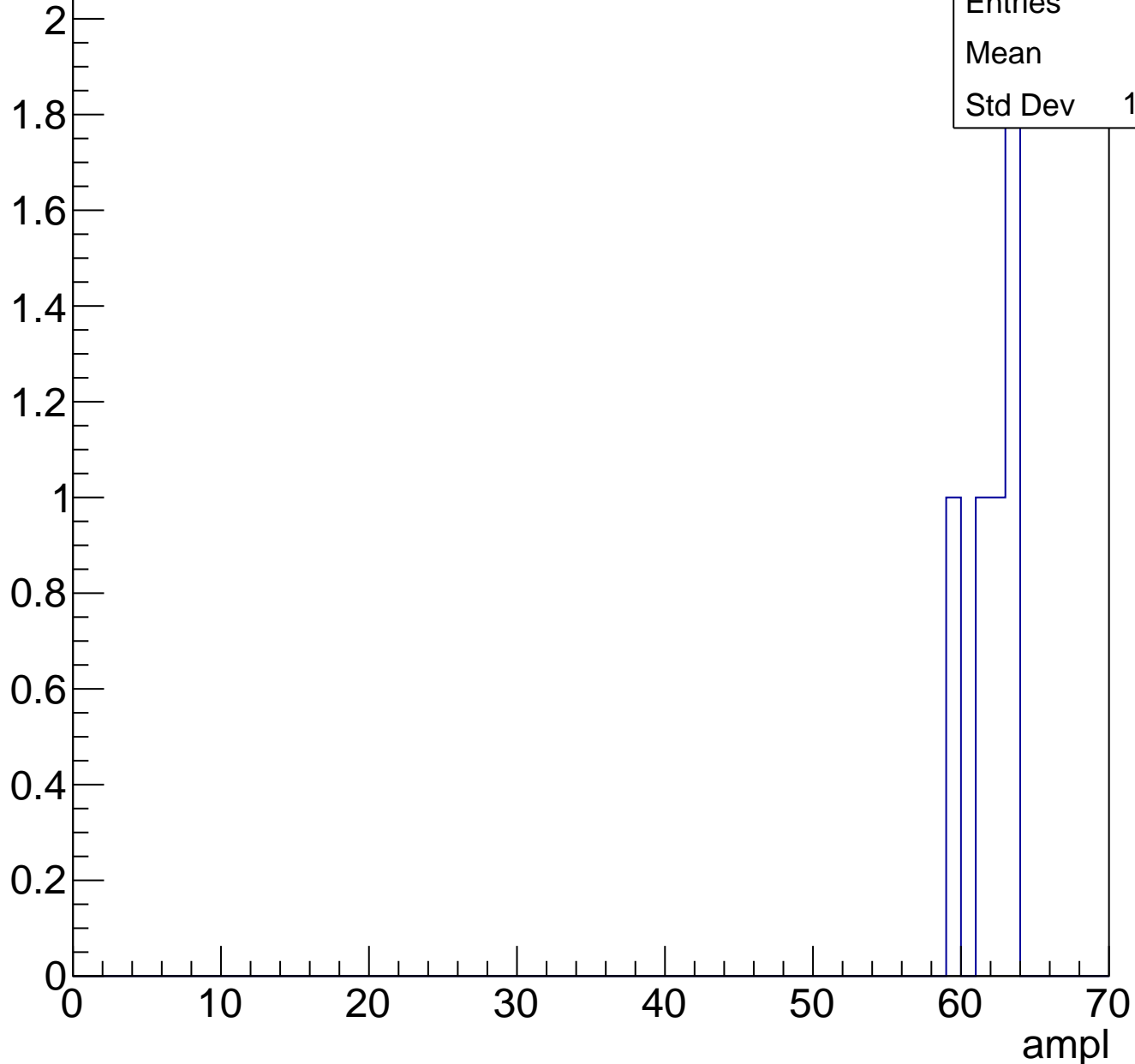
ampl



B1L001S, U22-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U22-ch104, adc0

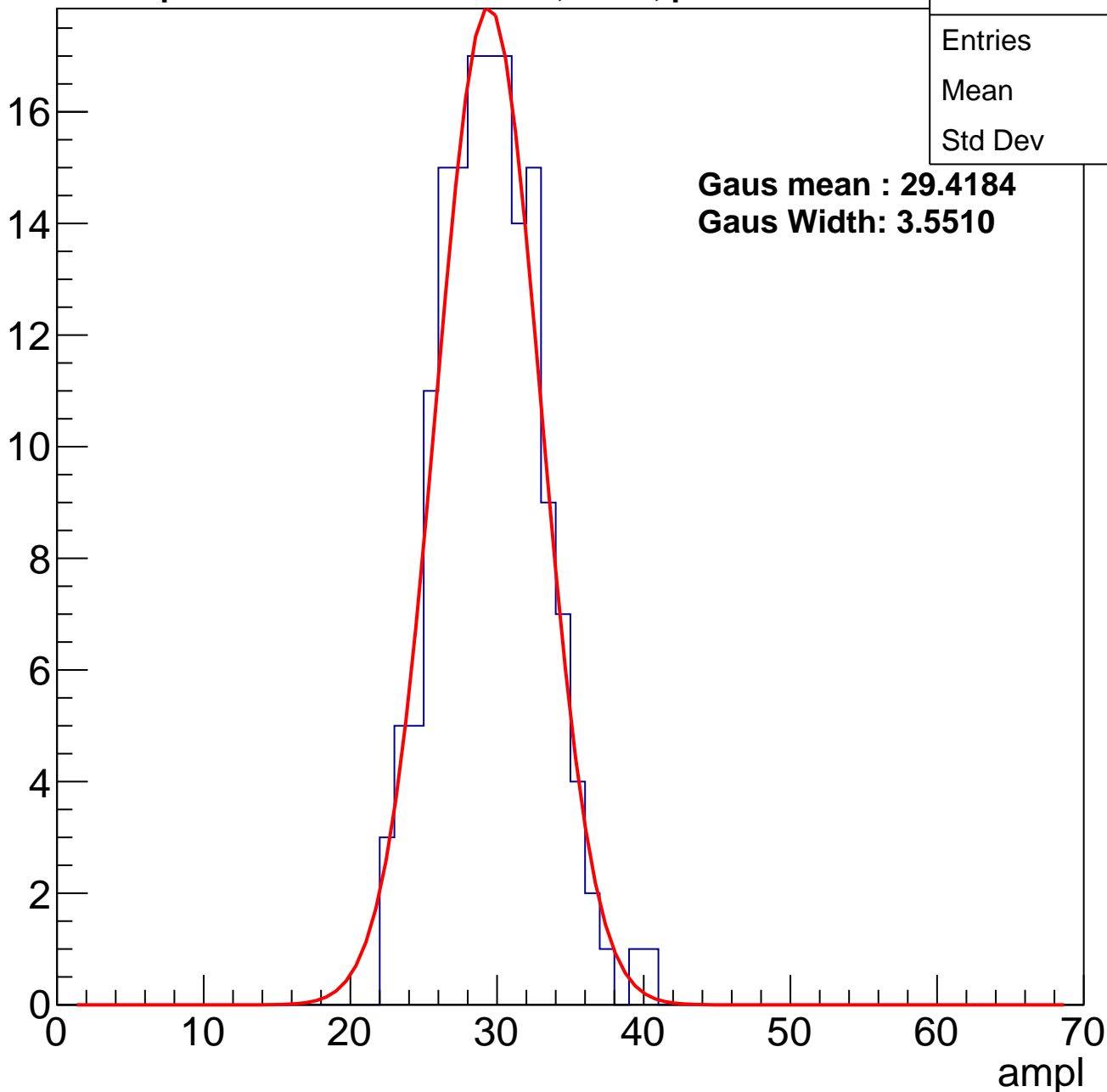
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	29.1
Std Dev	3.45

Gaus mean : 29.4184

Gaus Width: 3.5510

Entry



B1L001S, U22-ch104, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	35.46
Std Dev	2.828

Gaus mean : 35.9930

Gaus Width: 2.7810

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

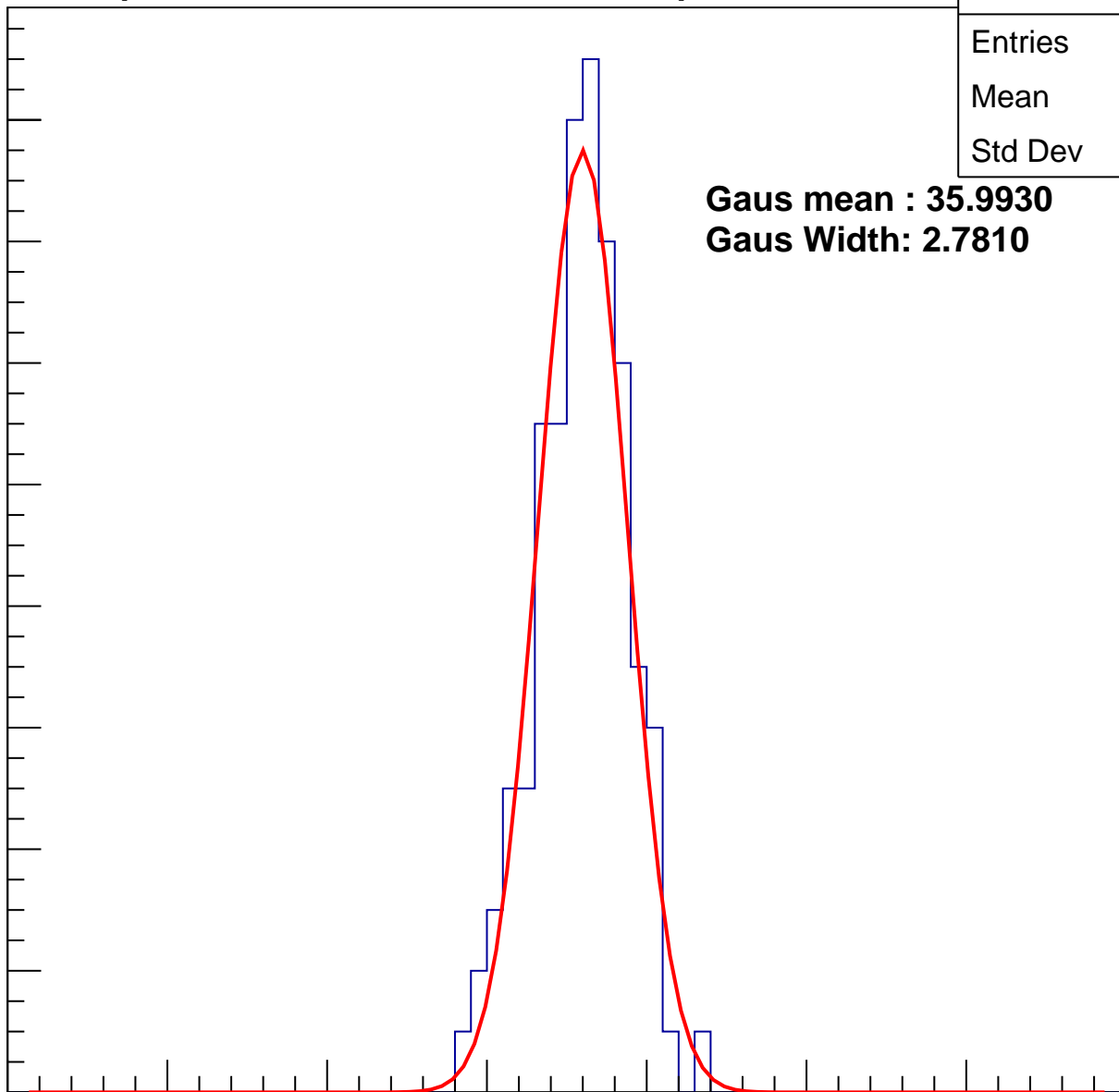
40

50

60

70

ampl



B1L001S, U22-ch104, adc2

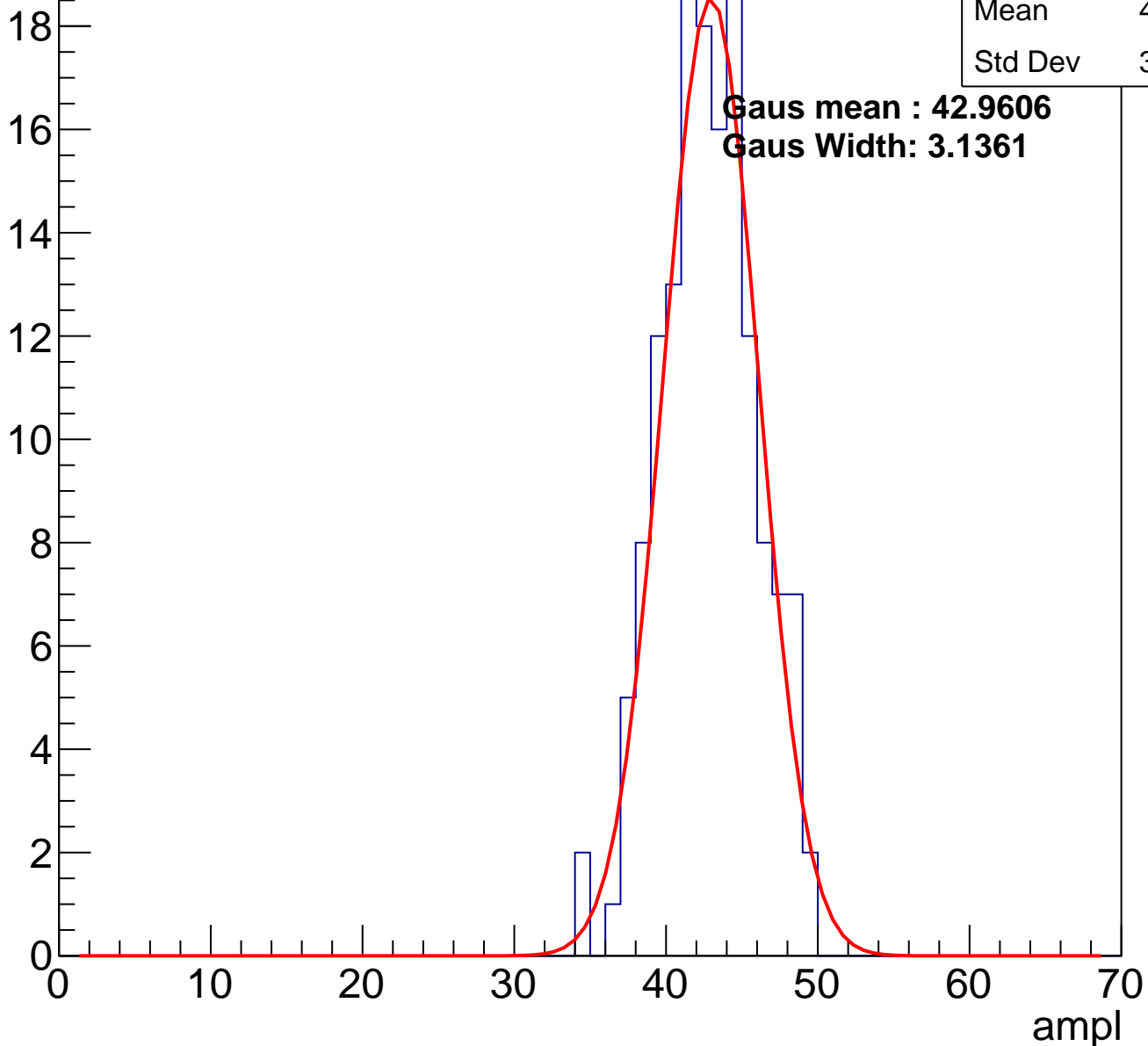
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	42.36
Std Dev	3.098

Gaus mean : 42.9606

Gaus Width: 3.1361

Entry



B1L001S, U22-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

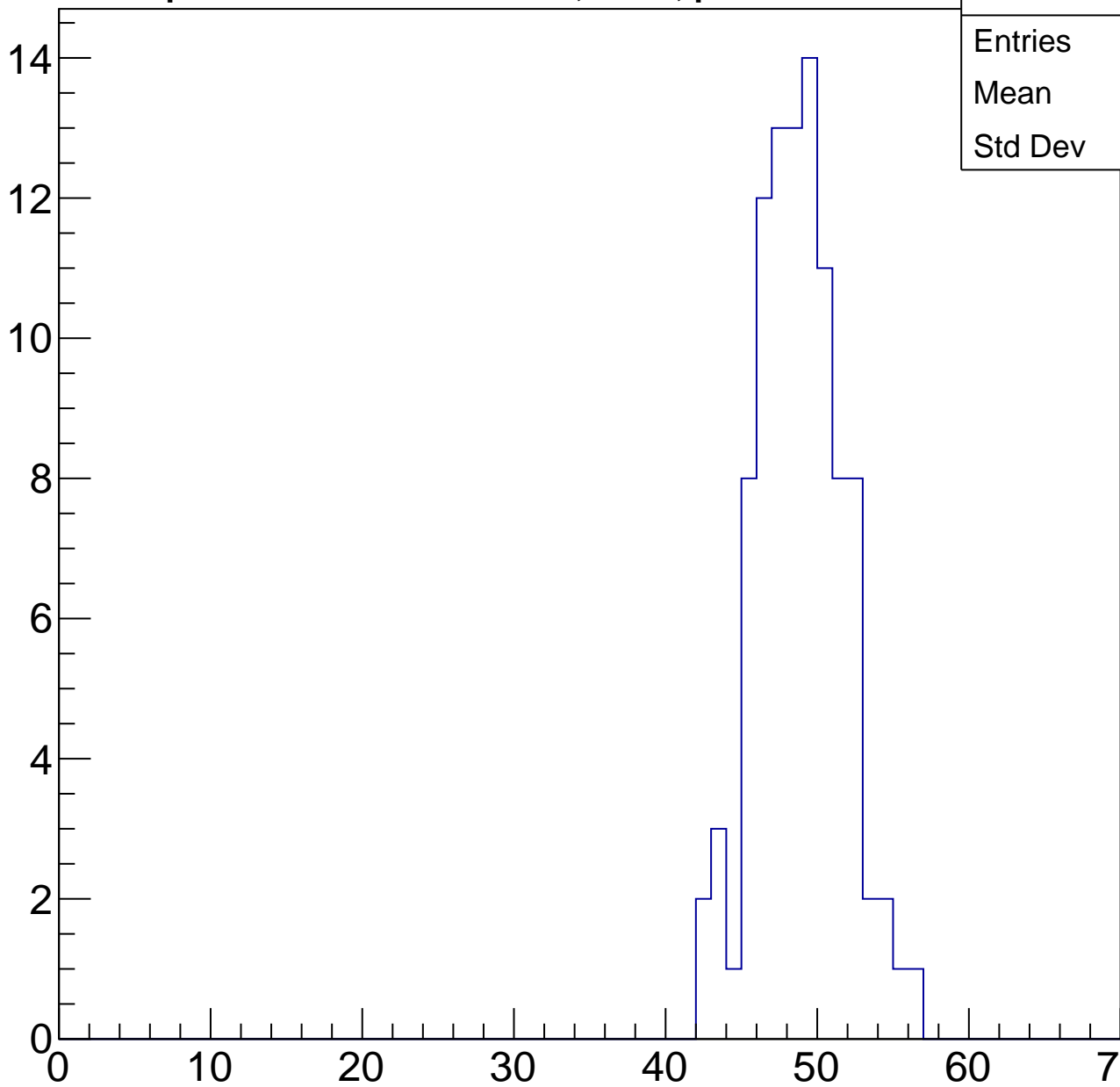
Entries	99
Mean	48.37
Std Dev	2.798

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

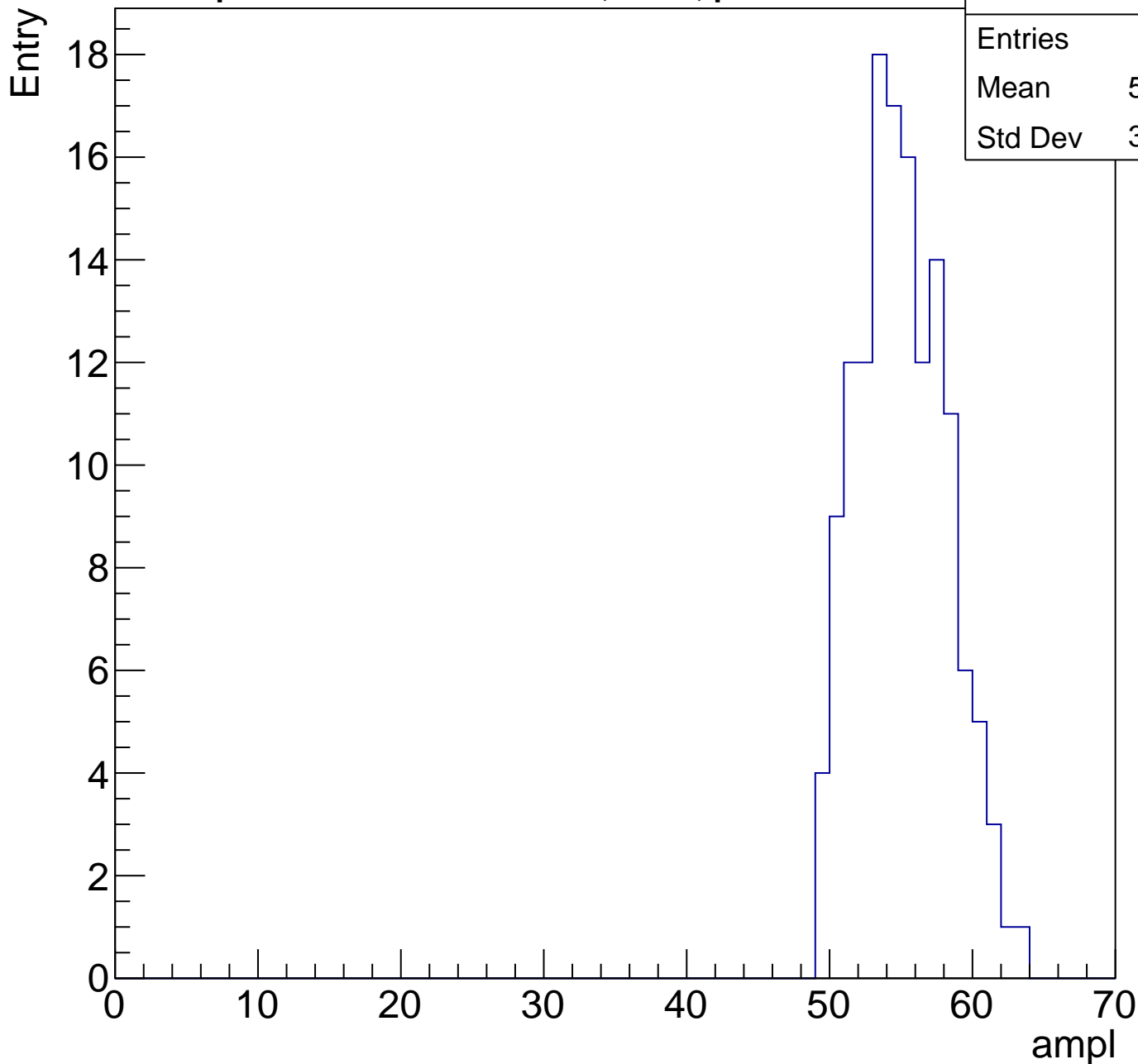
ampl



B1L001S, U22-ch104, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	54.64
Std Dev	3.093



B1L001S, U22-ch104, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

92

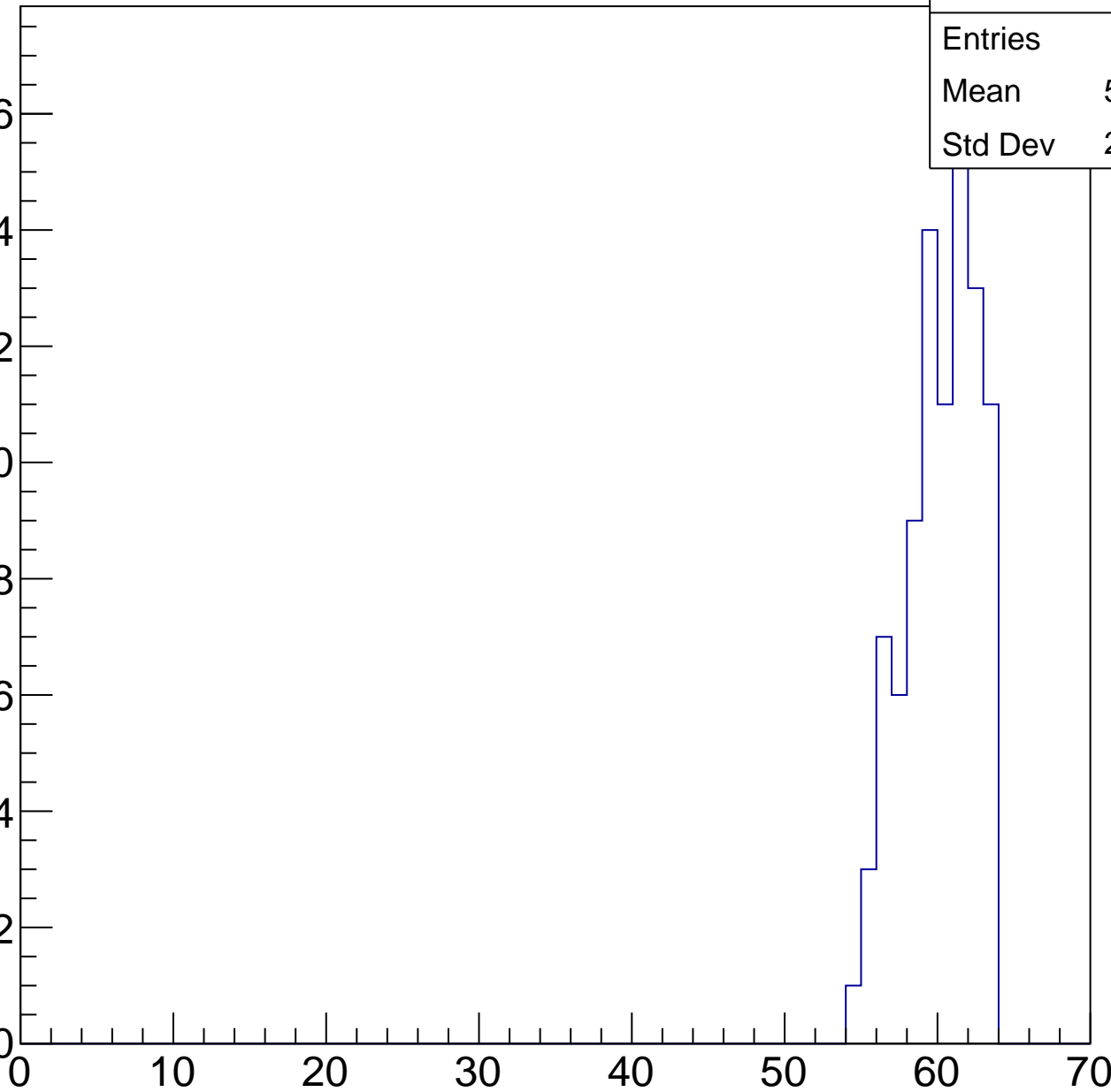
Mean

59.75

Std Dev

2.306

ampl

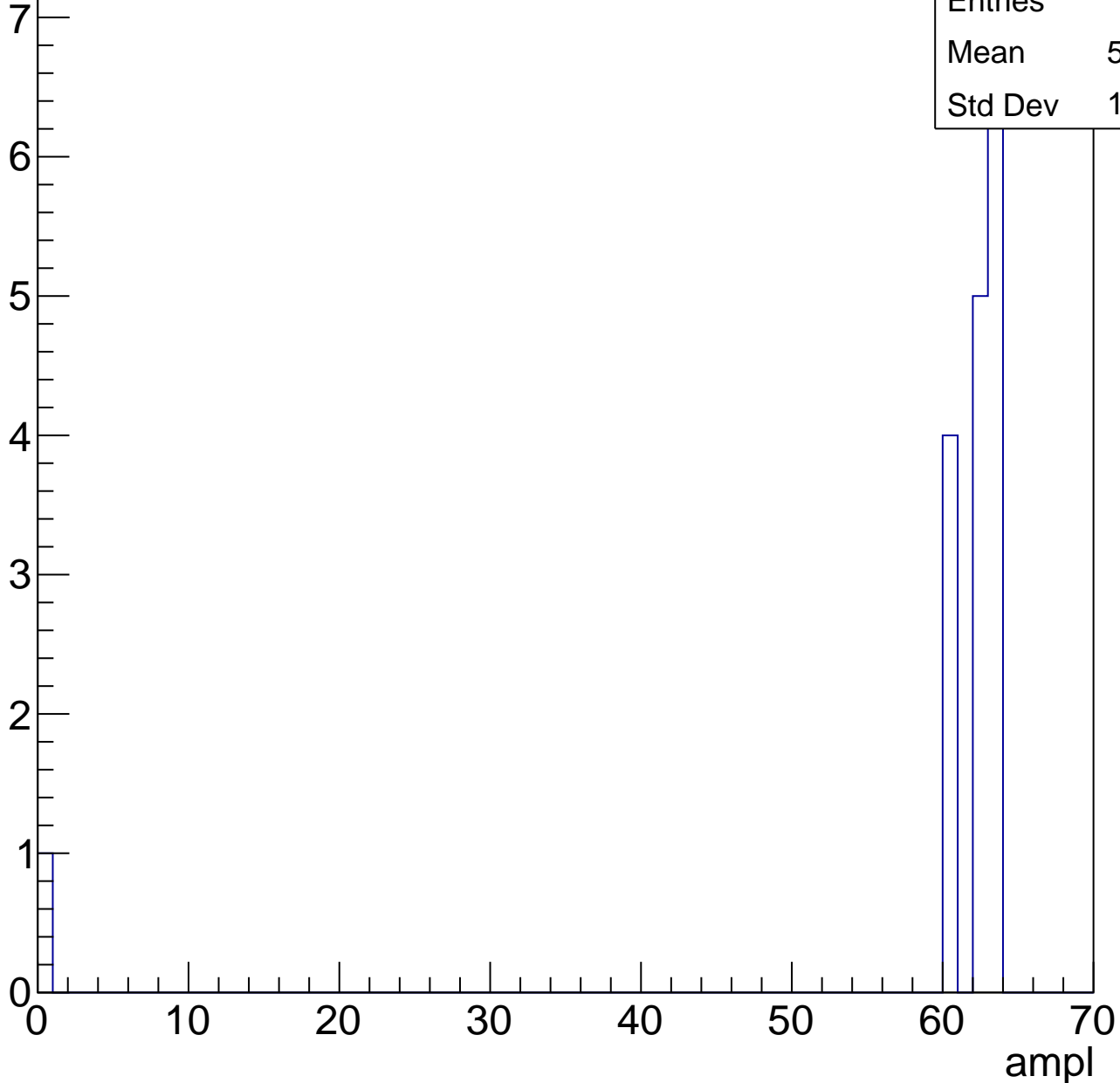


B1L001S, U22-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

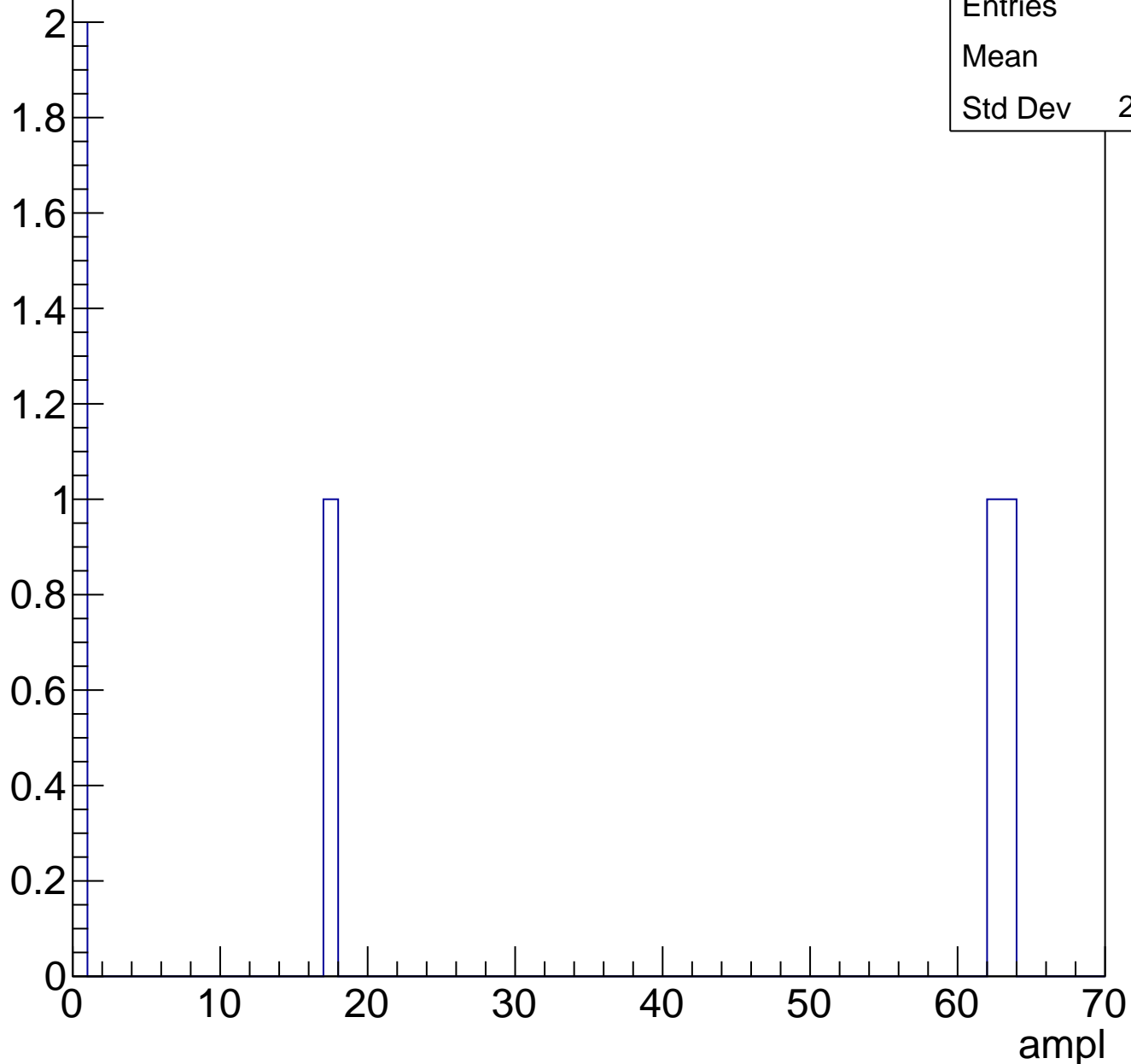
Entries	17
Mean	58.29
Std Dev	14.62



B1L001S, U22-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	28.4
Std Dev	28.53

B1L001S, U22-ch105, adc0

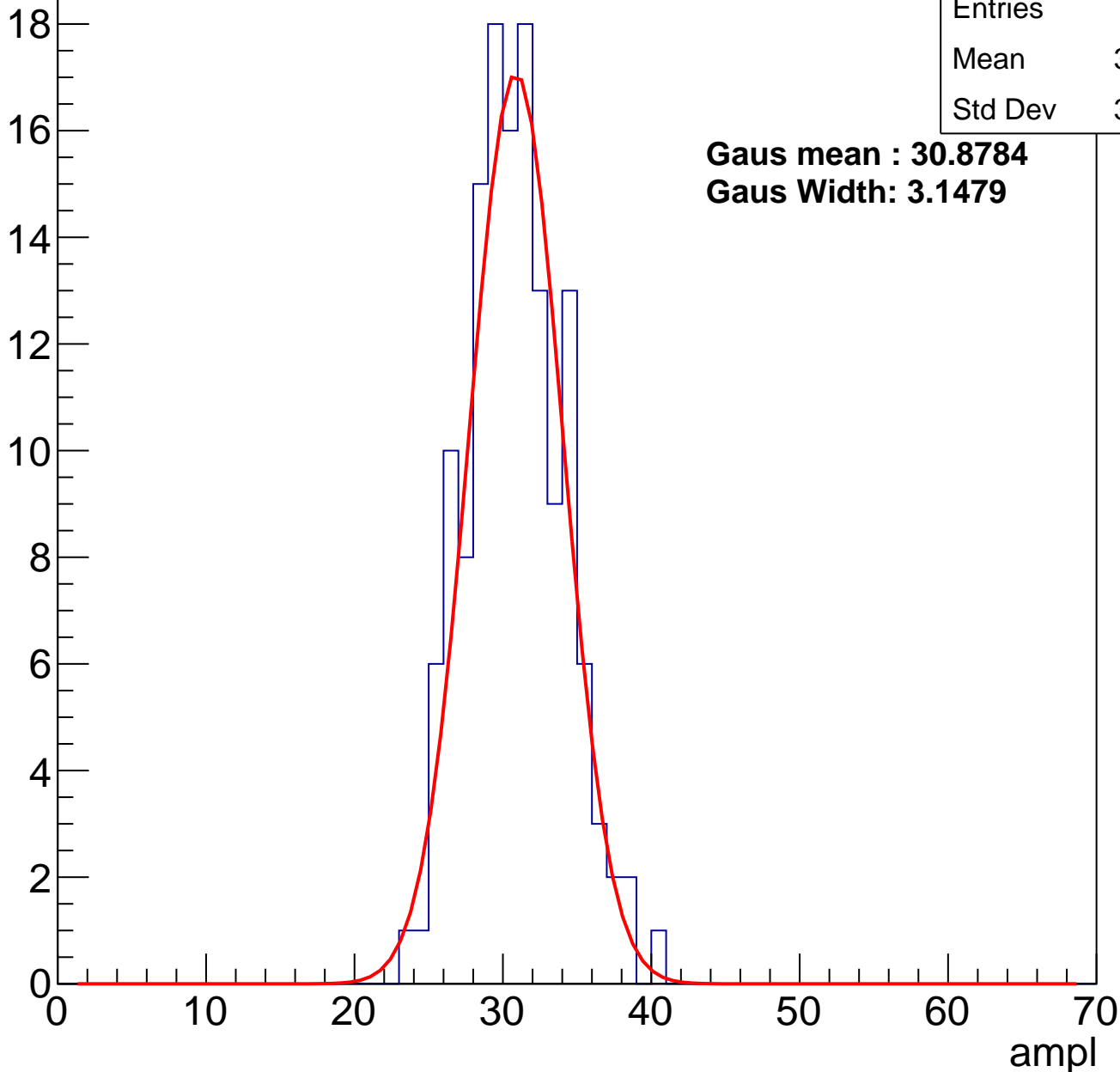
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.39
Std Dev	3.213

Gaus mean : 30.8784

Gaus Width: 3.1479

Entry

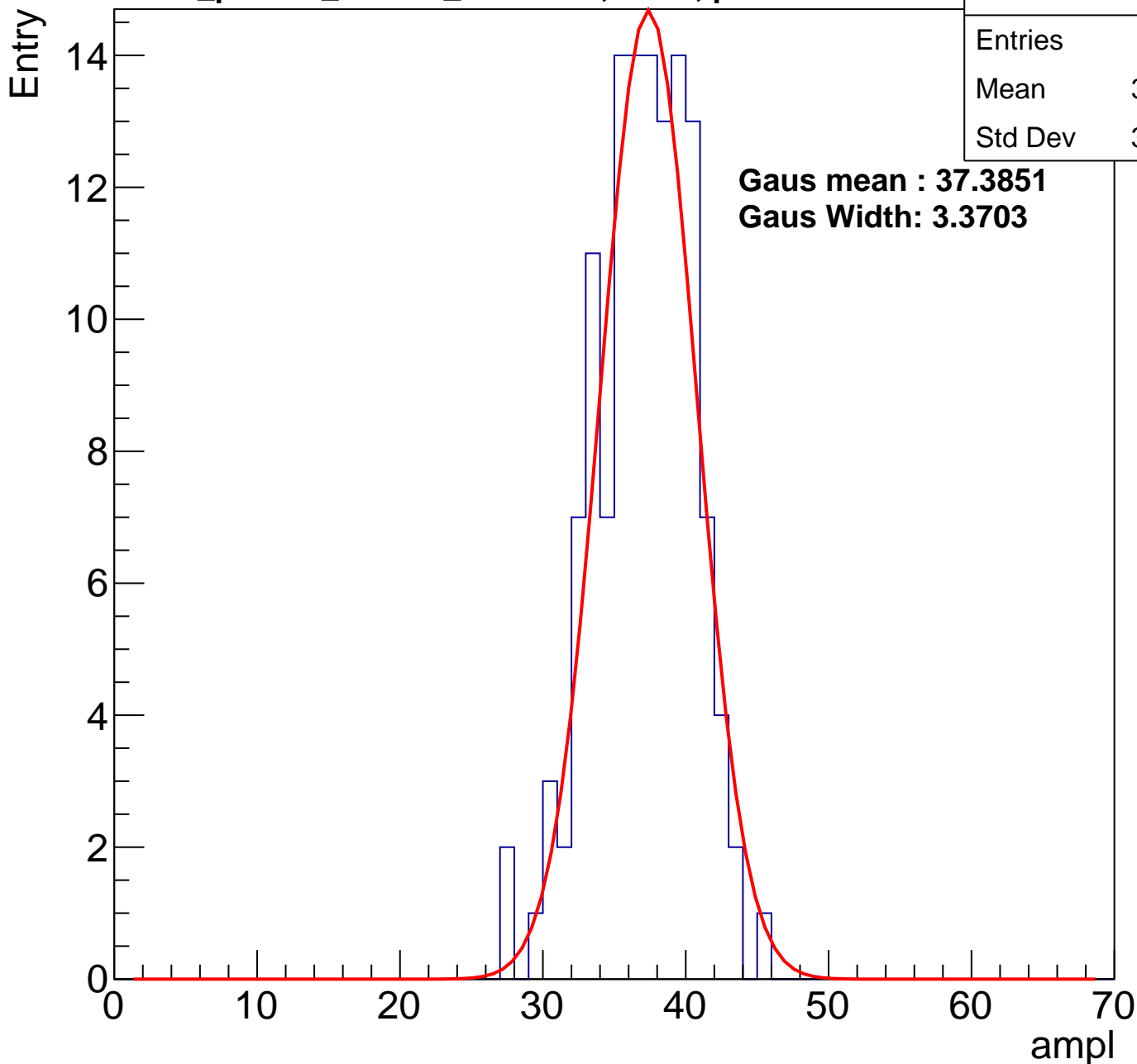


B1L001S, U22-ch105, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.57
Std Dev	3.384

Gaus mean : 37.3851
Gaus Width: 3.3703



B1L001S, U22-ch105, adc2

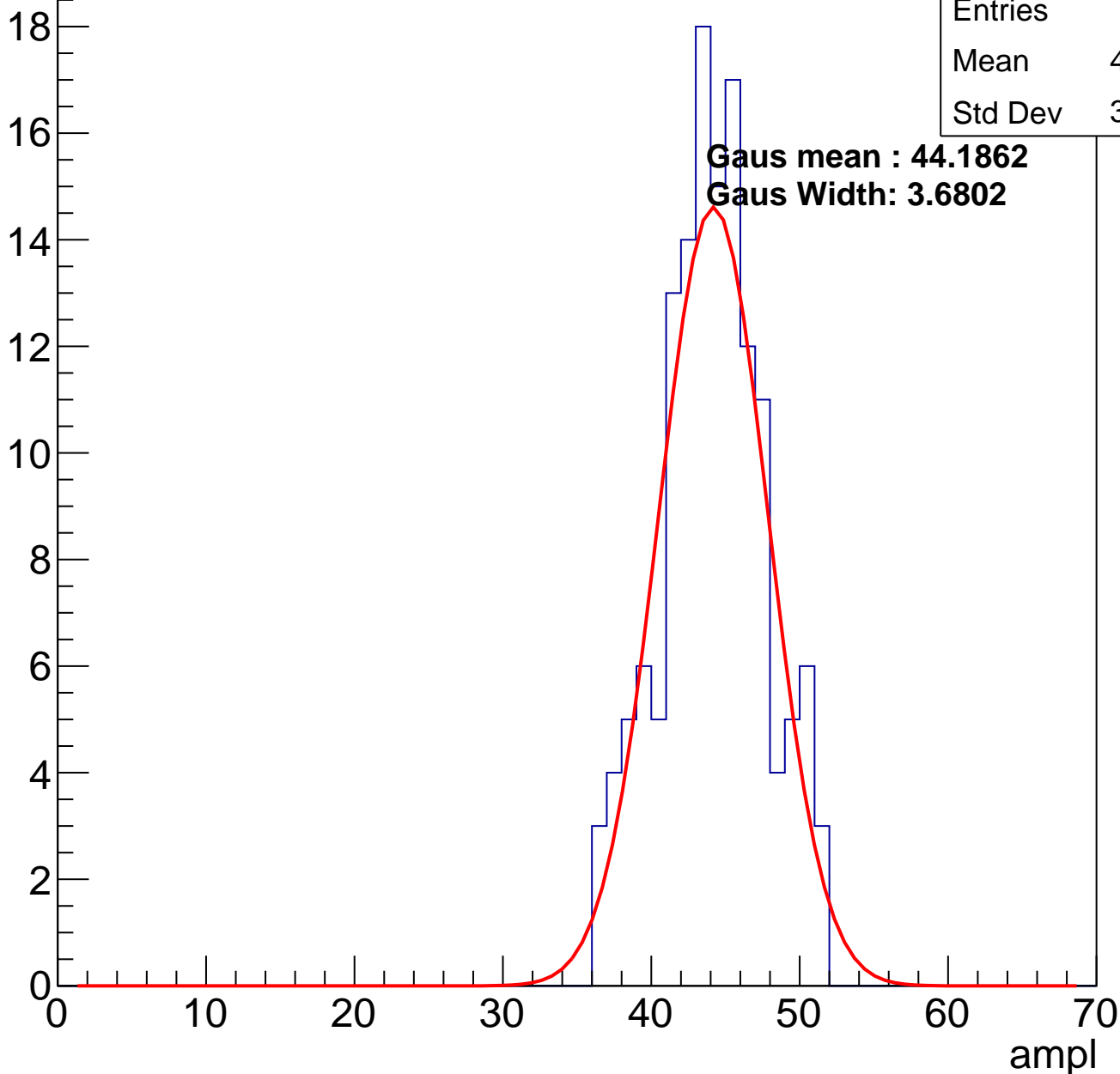
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	141
Mean	43.68
Std Dev	3.463

Gaus mean : 44.1862

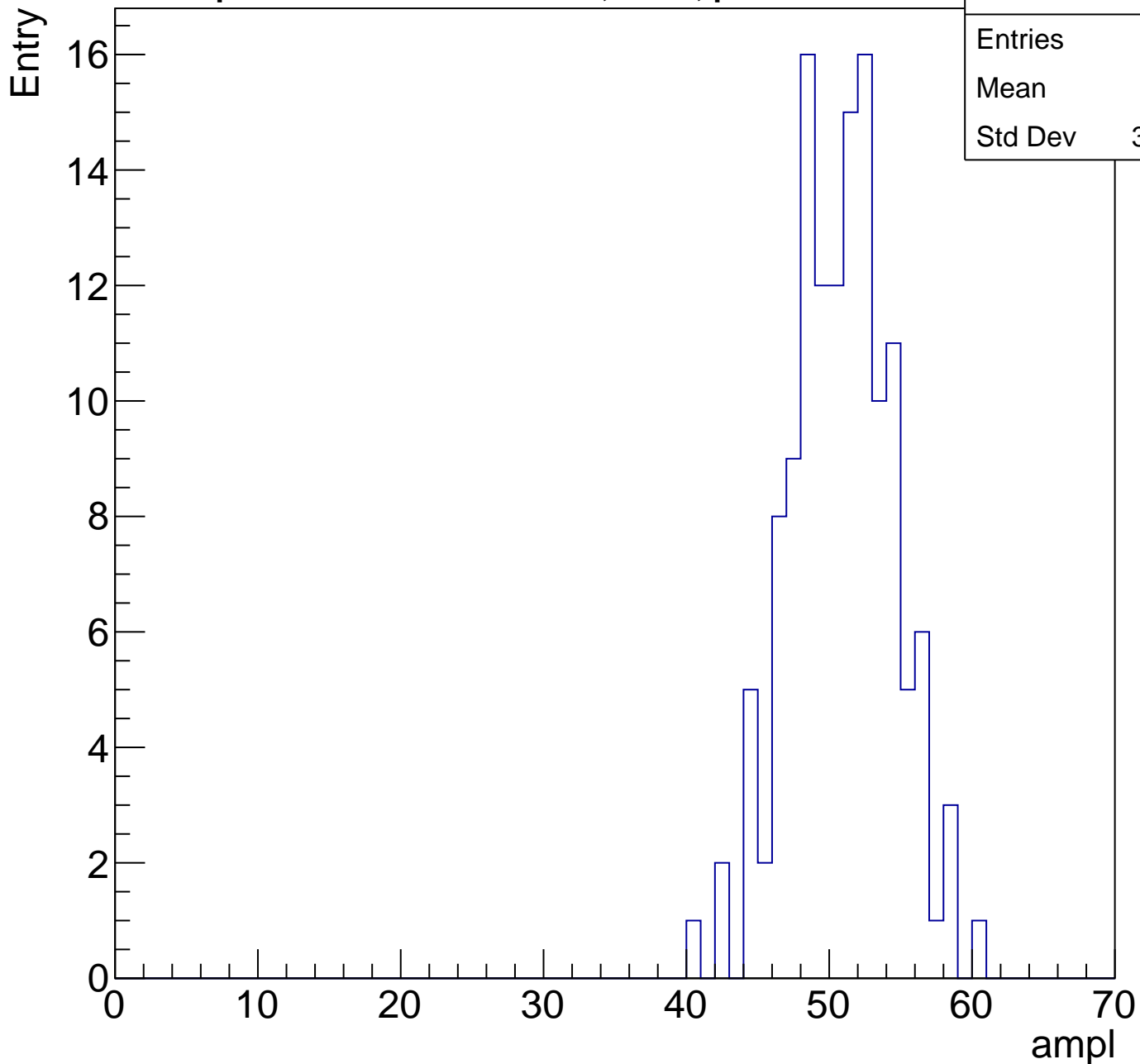
Gaus Width: 3.6802



B1L001S, U22-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	50.4
Std Dev	3.599



B1L001S, U22-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	56.71
Std Dev	3.185

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

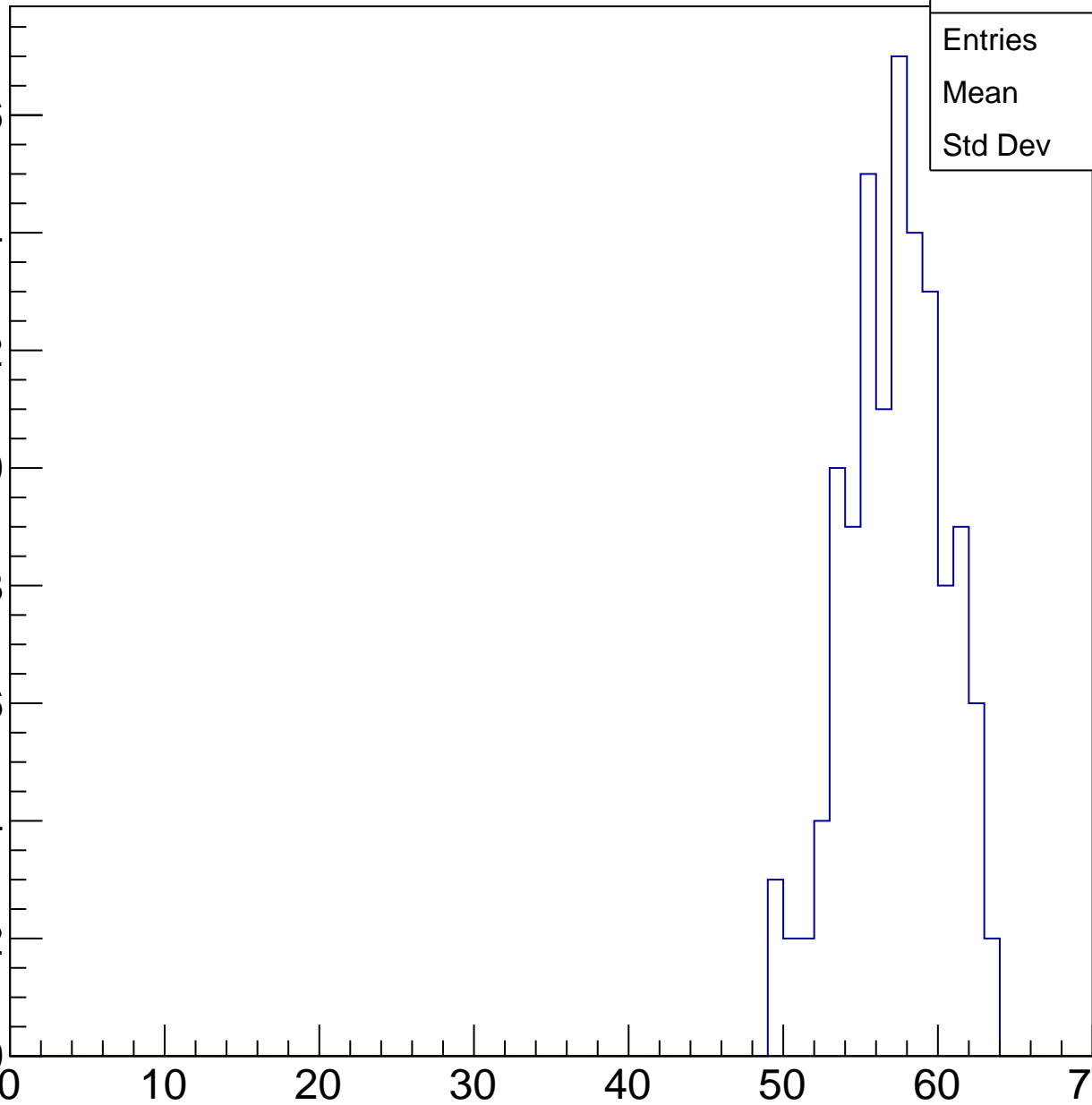
40

50

60

70

ampl



B1L001S, U22-ch105, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	63
Mean	58.78
Std Dev	10.81

Entry

12

10

8

6

4

2

0

0

10

20

30

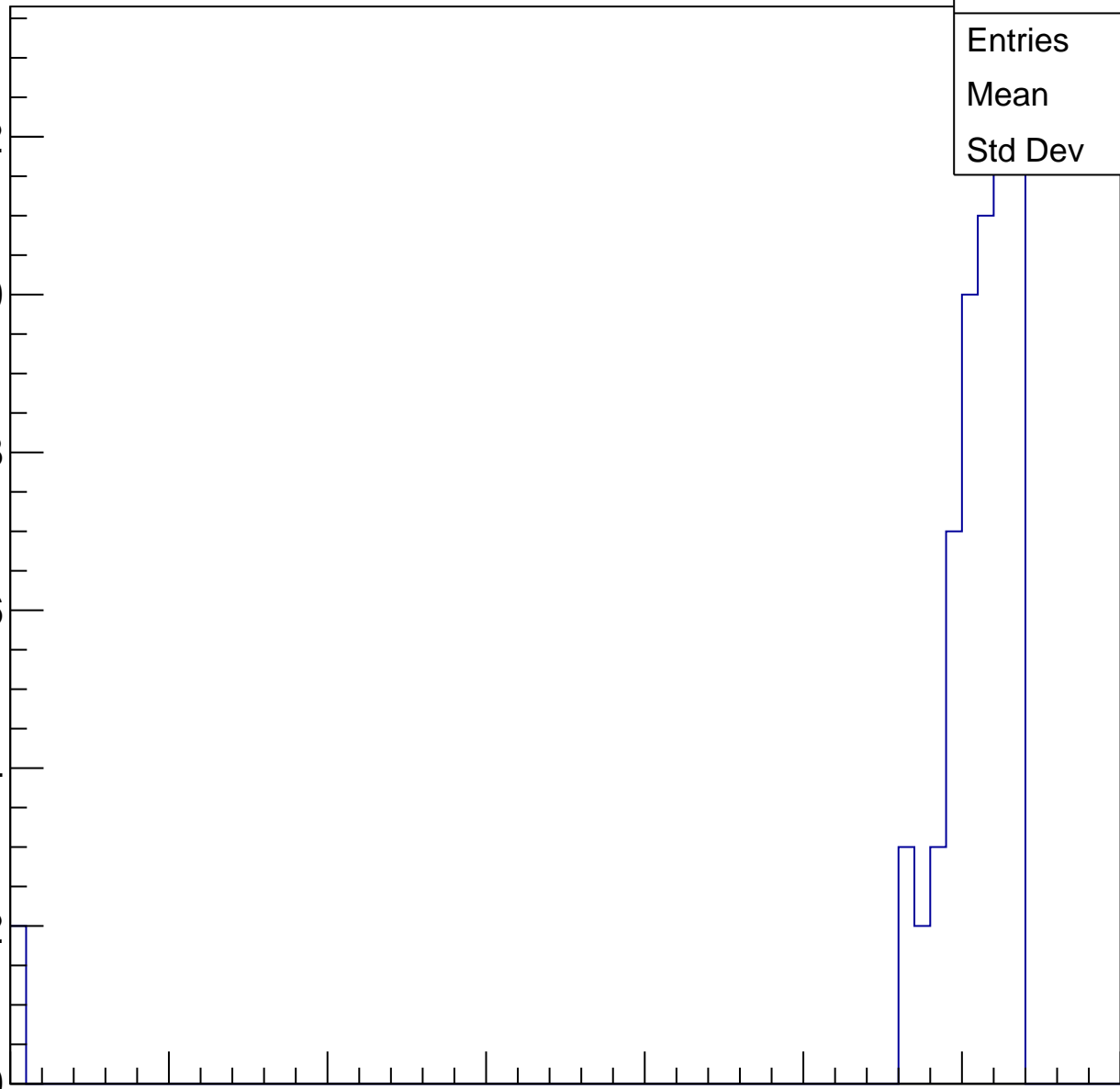
40

50

60

70

ampl

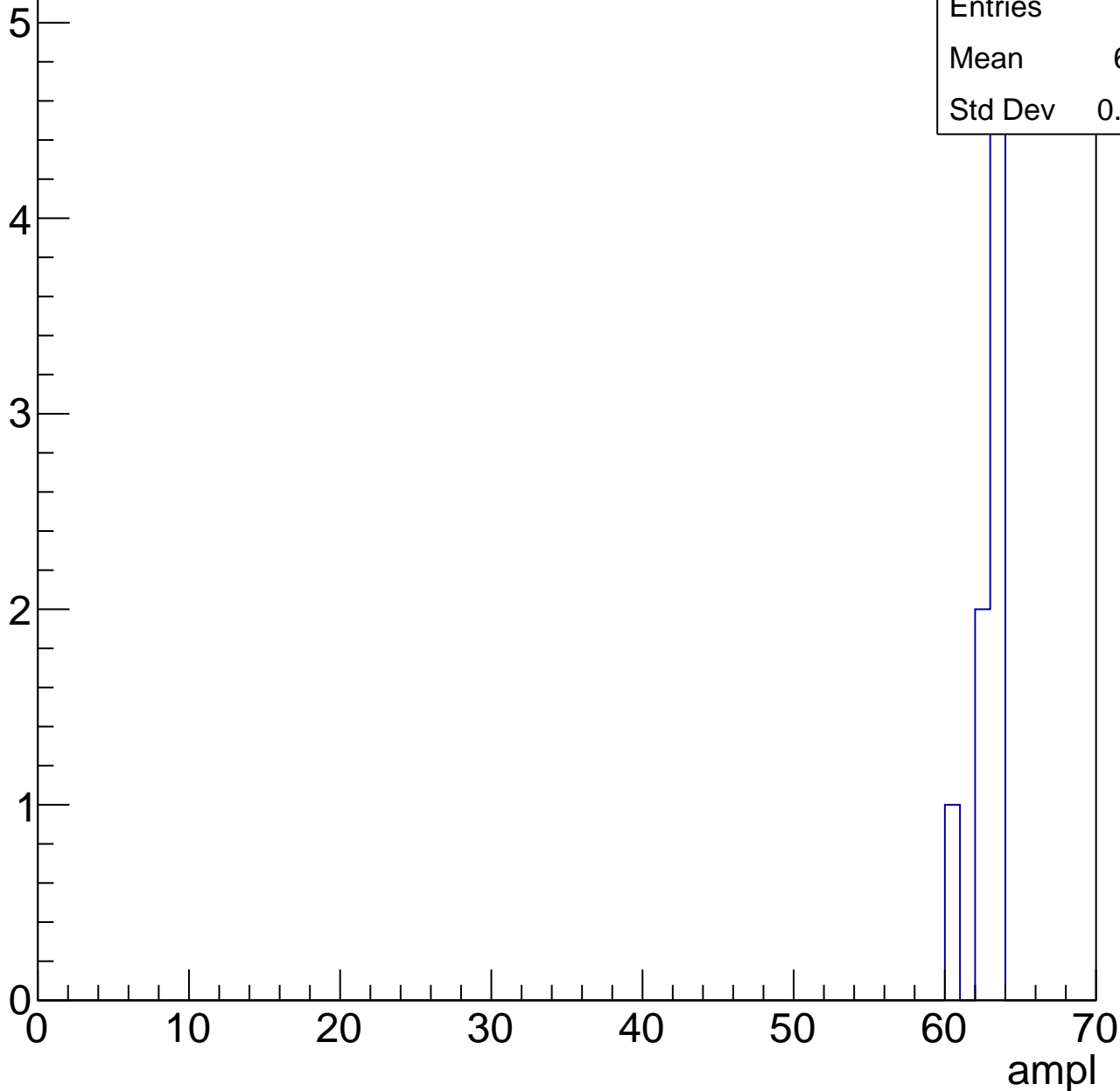


B1L001S, U22-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L001S, U22-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch106, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	28.42
Std Dev	3.931

Gaus mean : 28.8707

Gaus Width: 2.9490

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

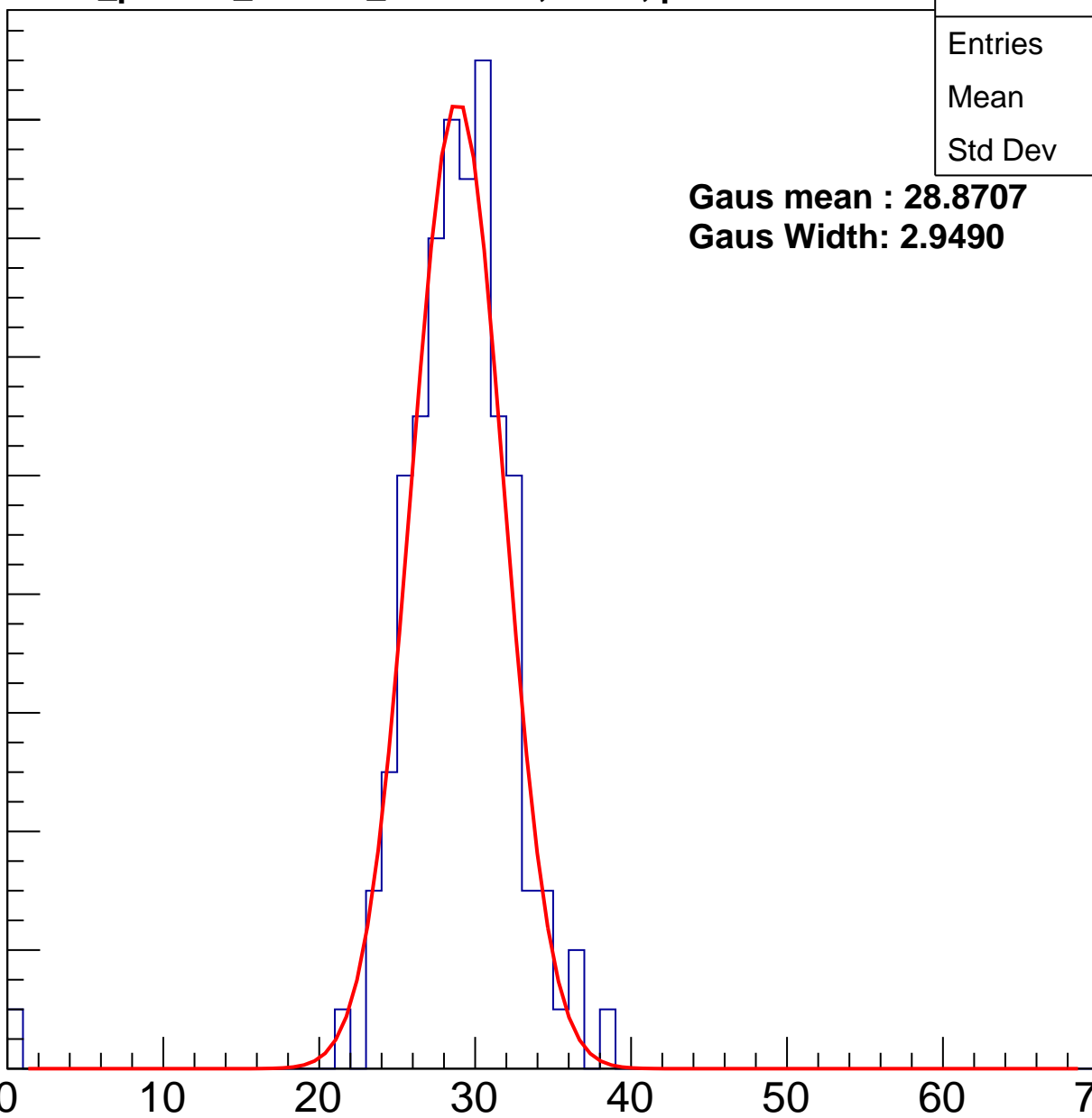
40

50

60

70

ampl



B1L001S, U22-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	34.73
Std Dev	3.194

Gaus mean : 35.3396

Gaus Width: 3.3005

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

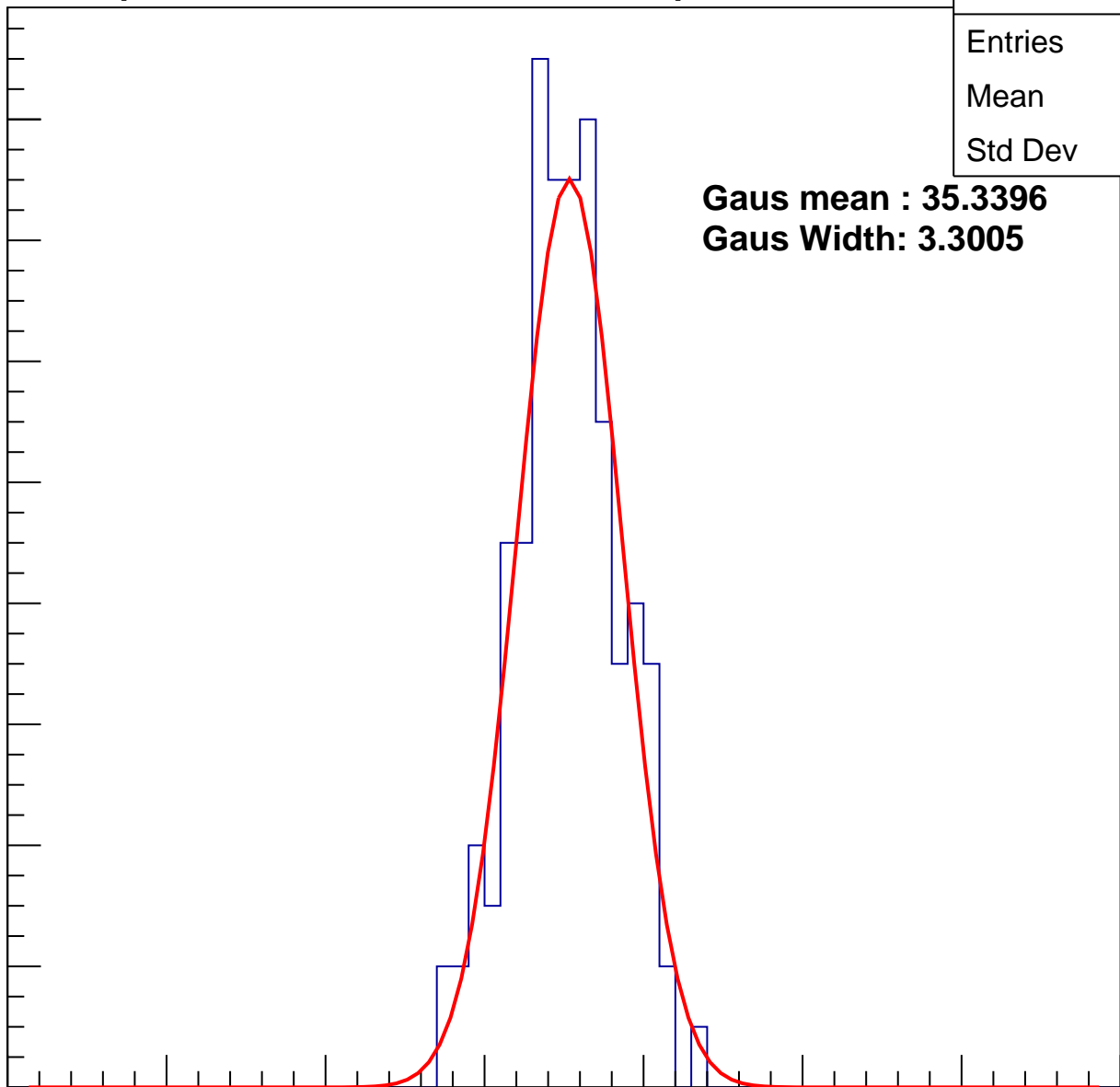
40

50

60

70

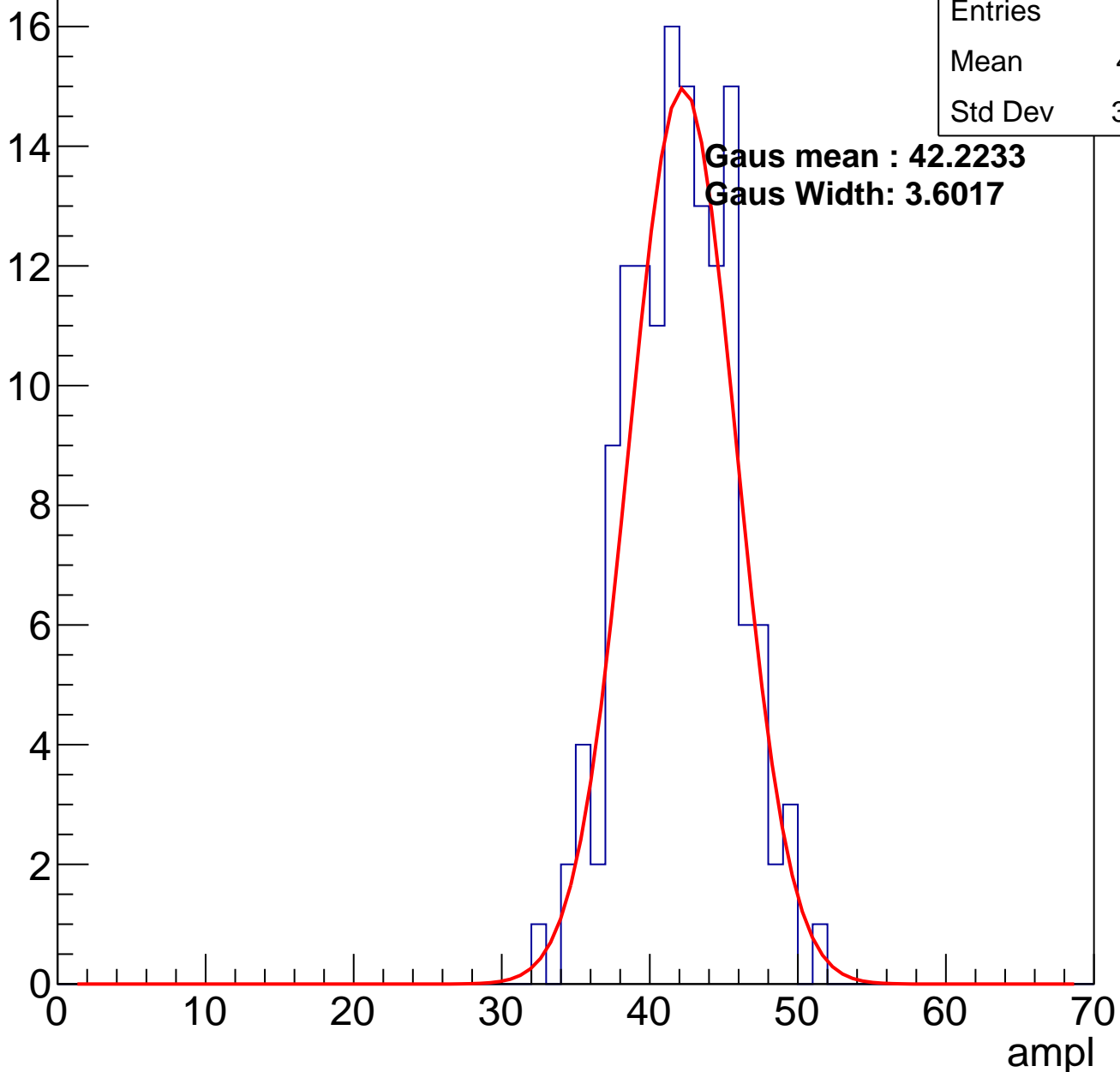
ampl



B1L001S, U22-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

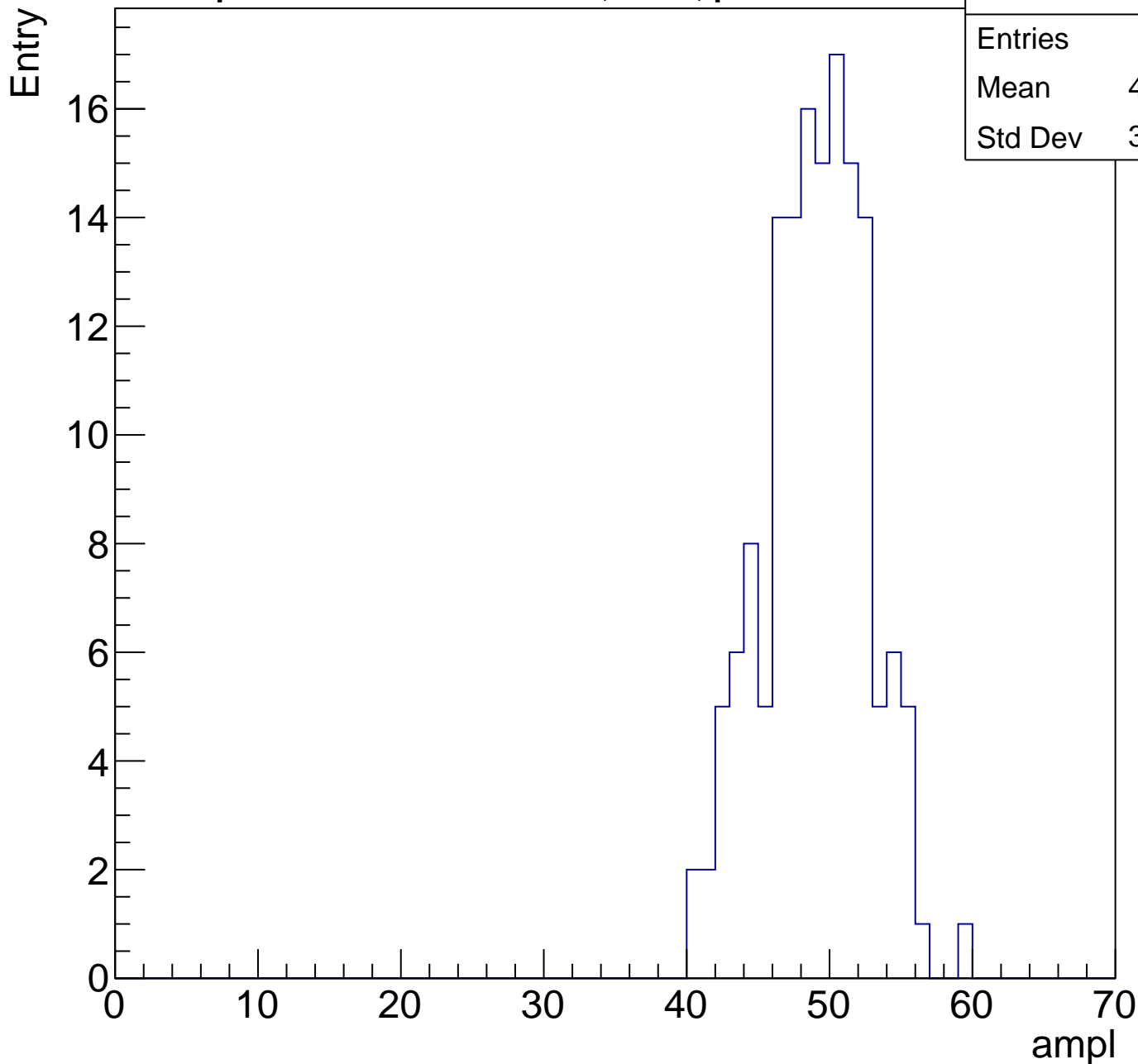
Entry



B1L001S, U22-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

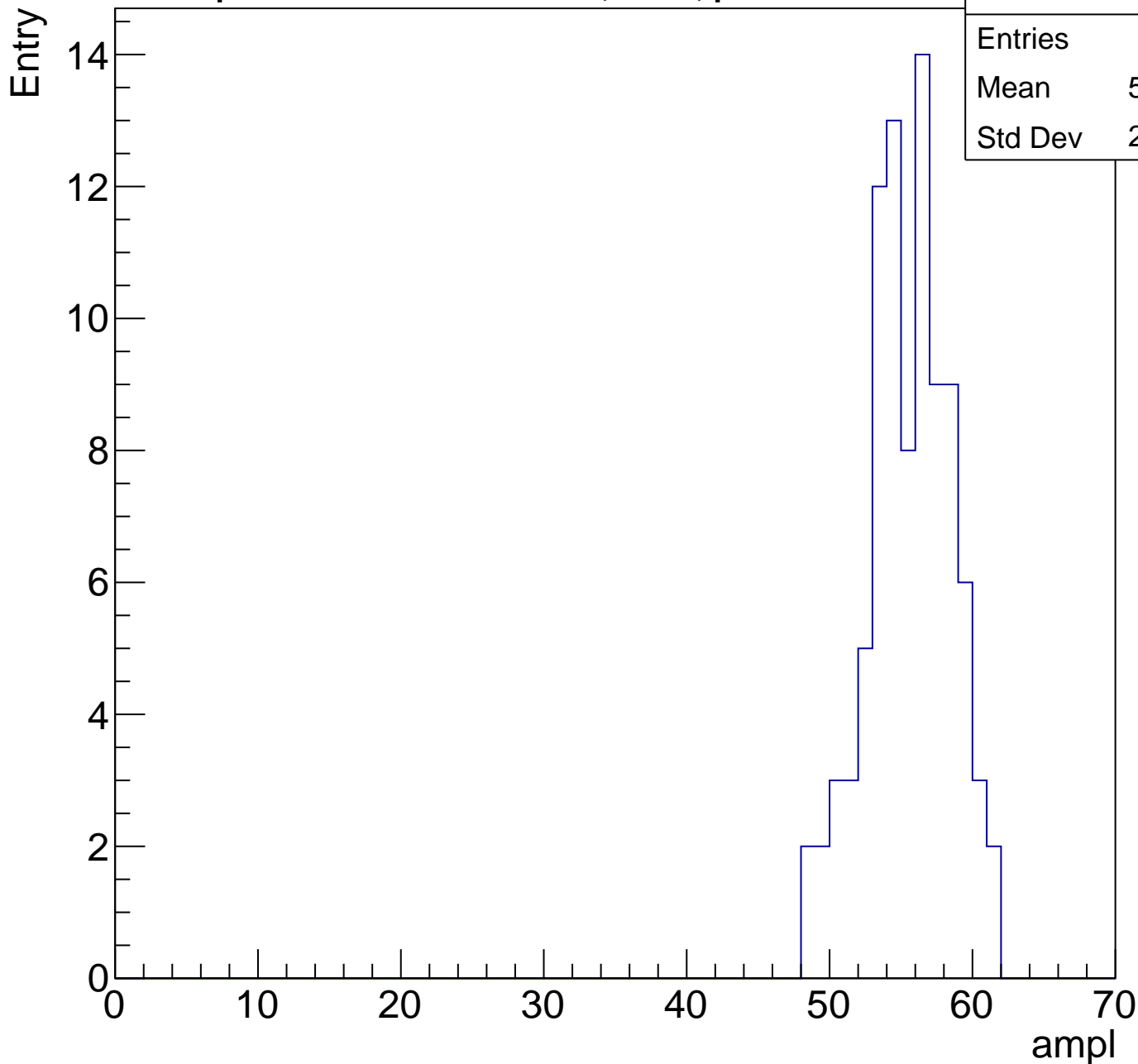
Entries	151
Mean	48.57
Std Dev	3.606



B1L001S, U22-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	91
Mean	55.05
Std Dev	2.914



B1L001S, U22-ch106, adc5

calib_packv5_042523_0143.root, FC#2, port C2

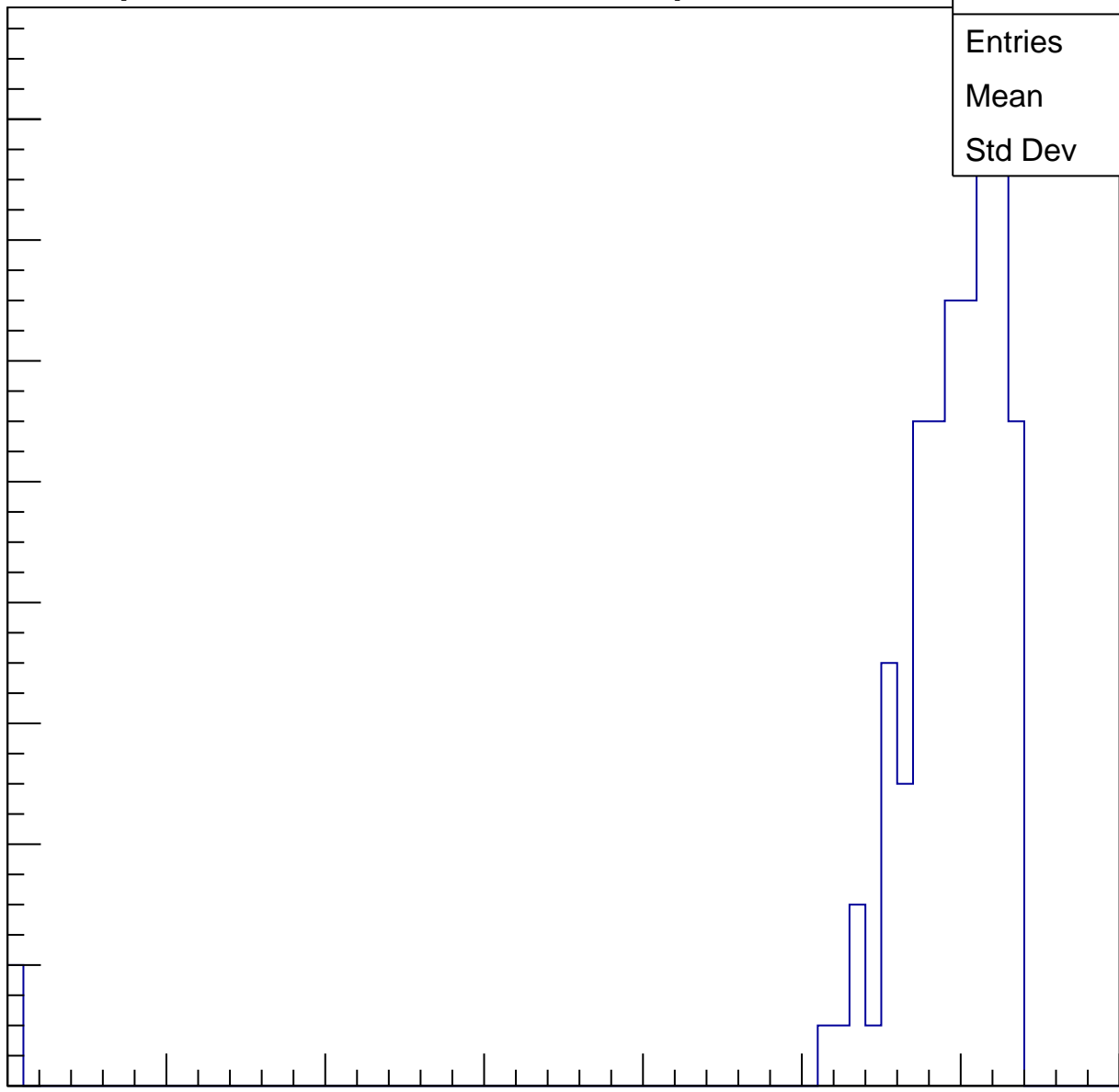
Entries	112
Mean	58.17
Std Dev	8.315

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

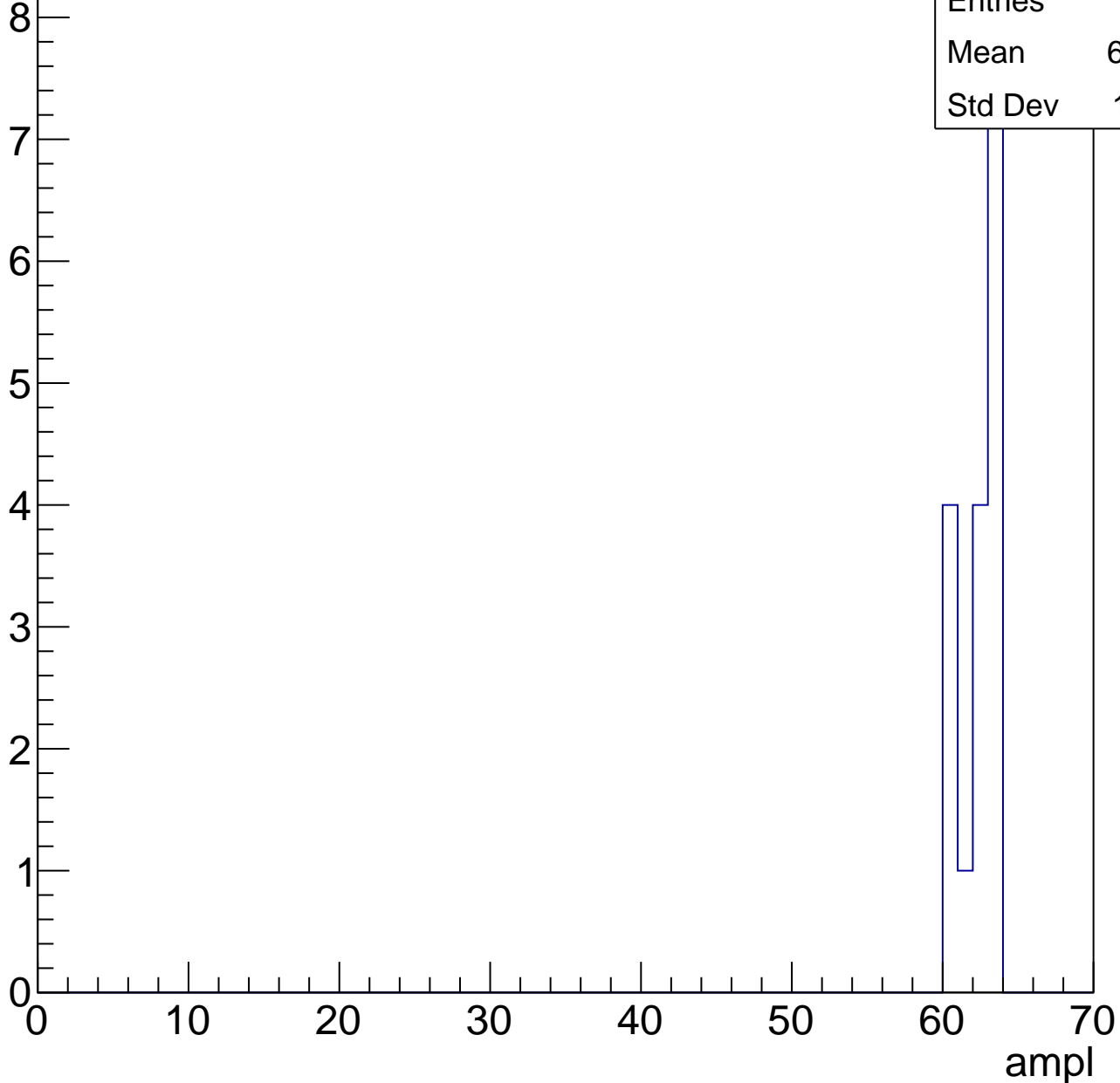


B1L001S, U22-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

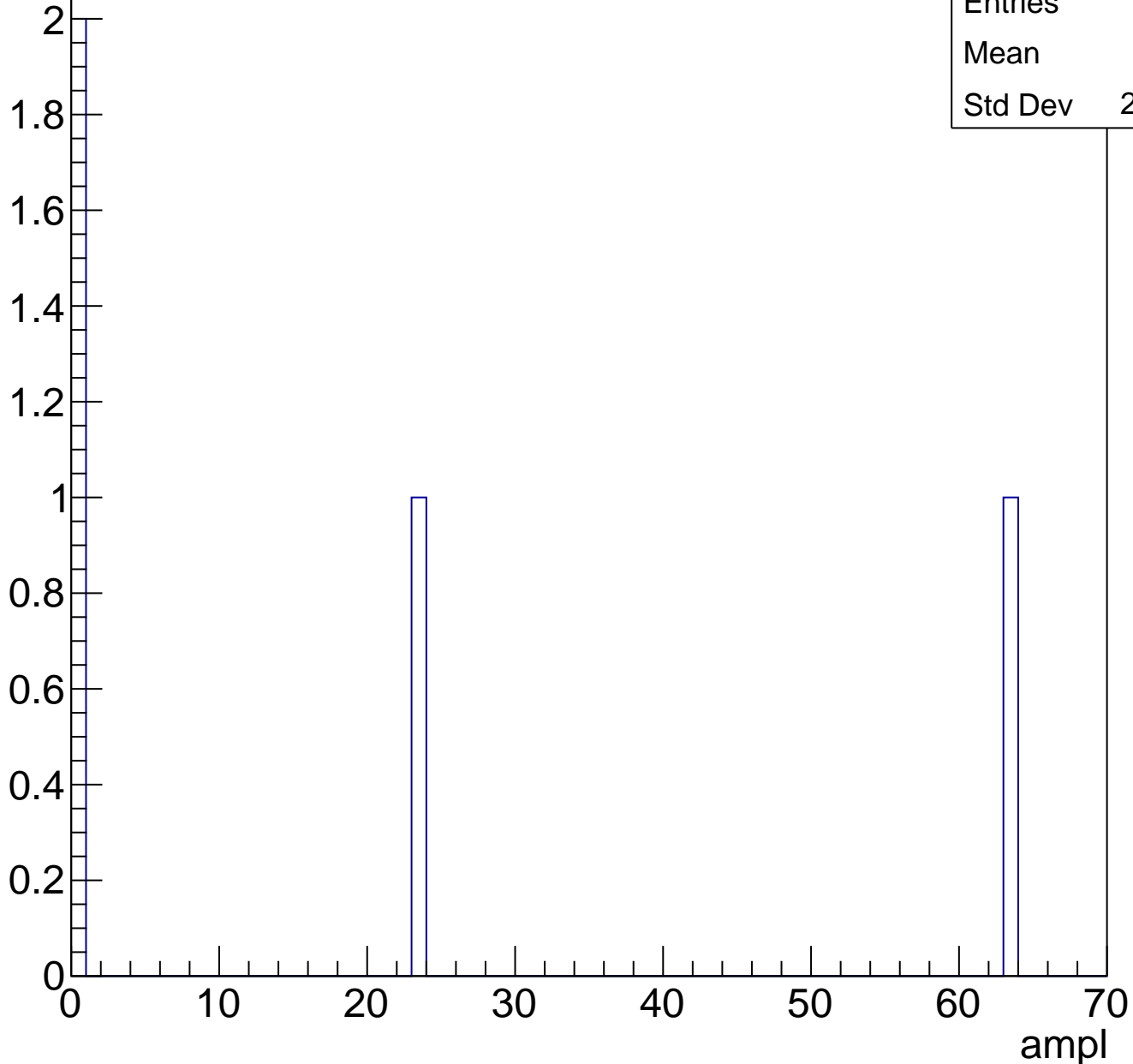
Entries	17
Mean	61.94
Std Dev	1.211



B1L001S, U22-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	21.5
Std Dev	25.73

B1L001S, U22-ch107, adc0

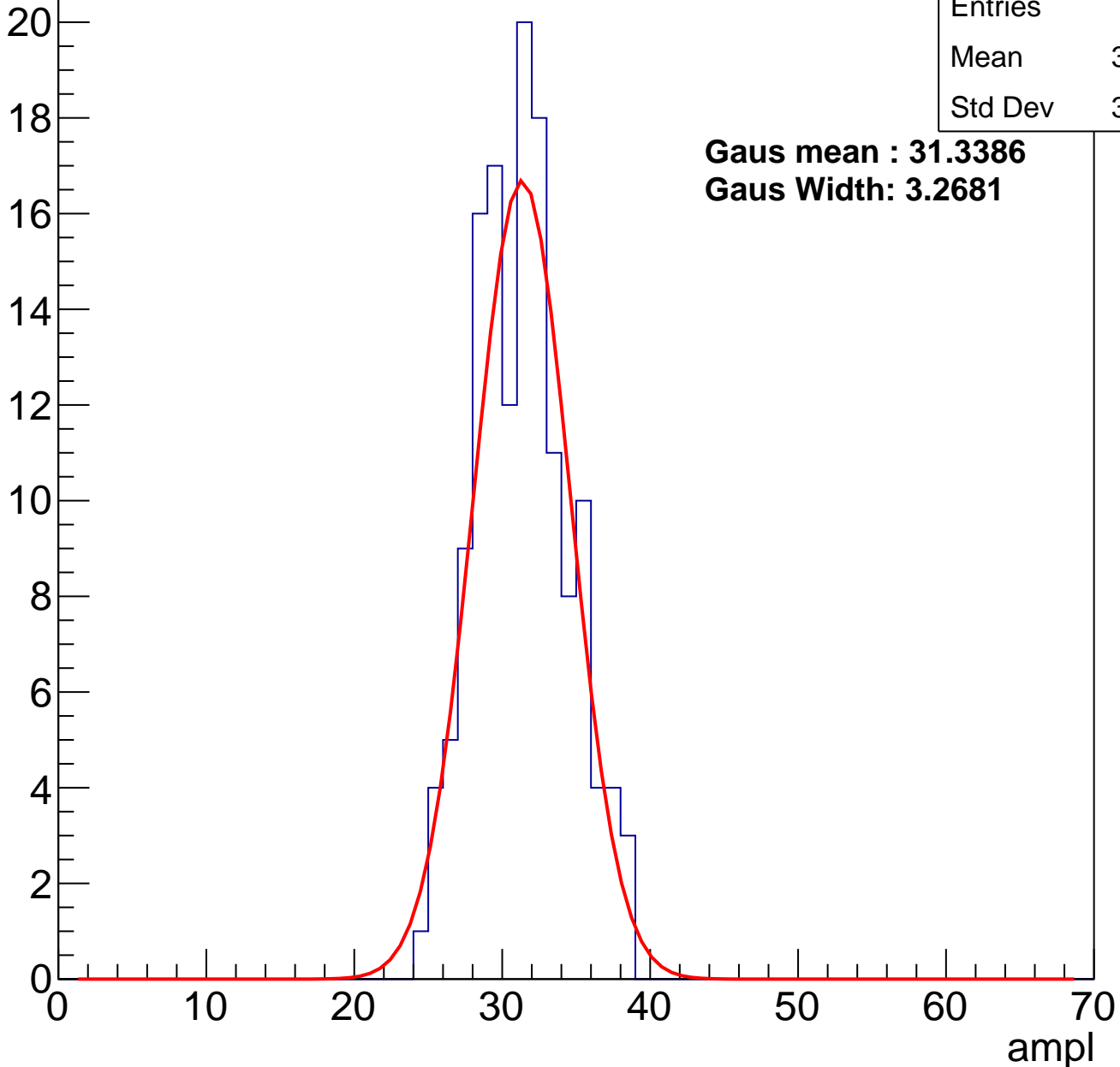
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.88
Std Dev	3.105

Gaus mean : 31.3386

Gaus Width: 3.2681

Entry



B1L001S, U22-ch107, adc1

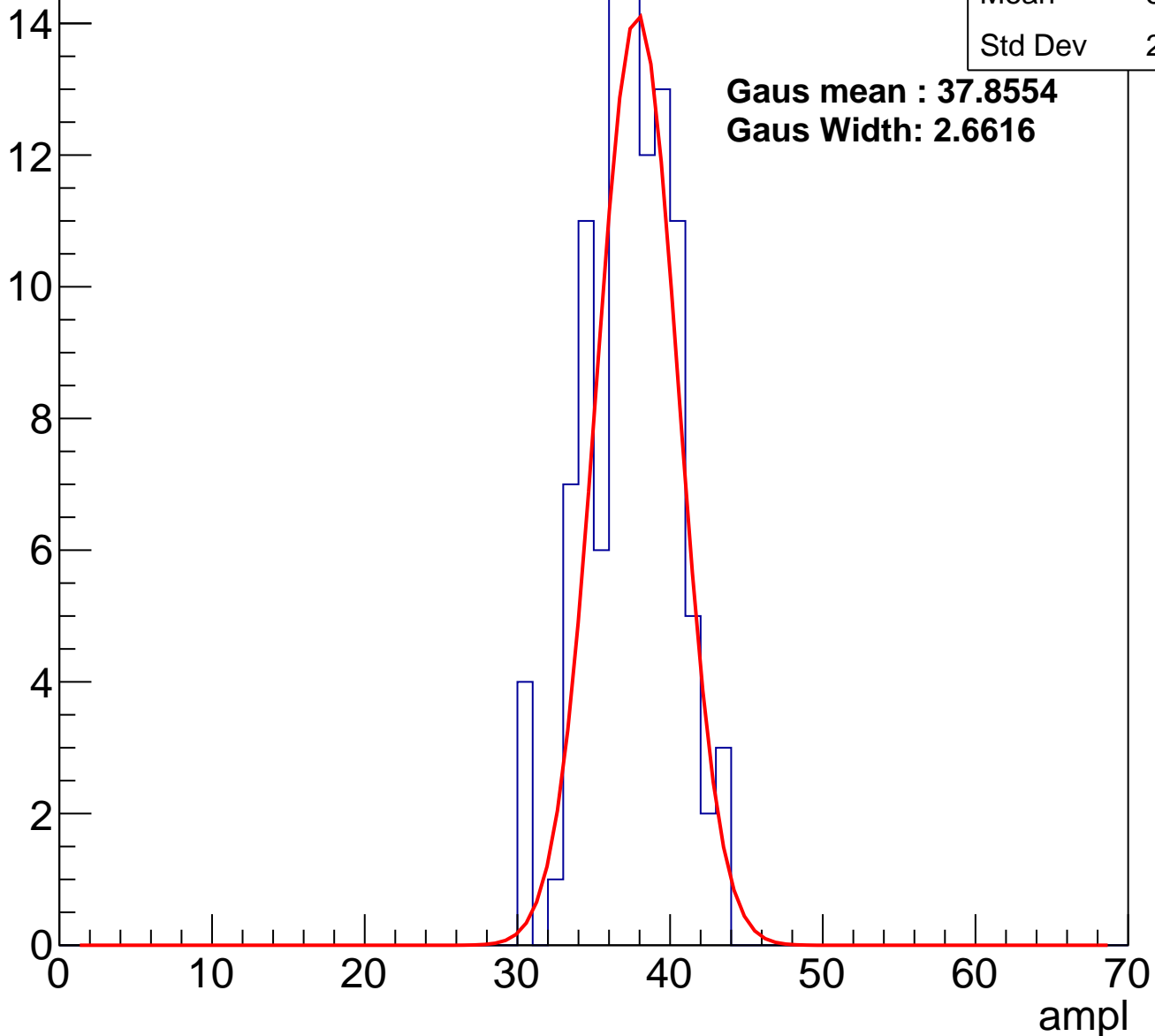
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	36.98
Std Dev	2.882

Gaus mean : 37.8554

Gaus Width: 2.6616

Entry



B1L001S, U22-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

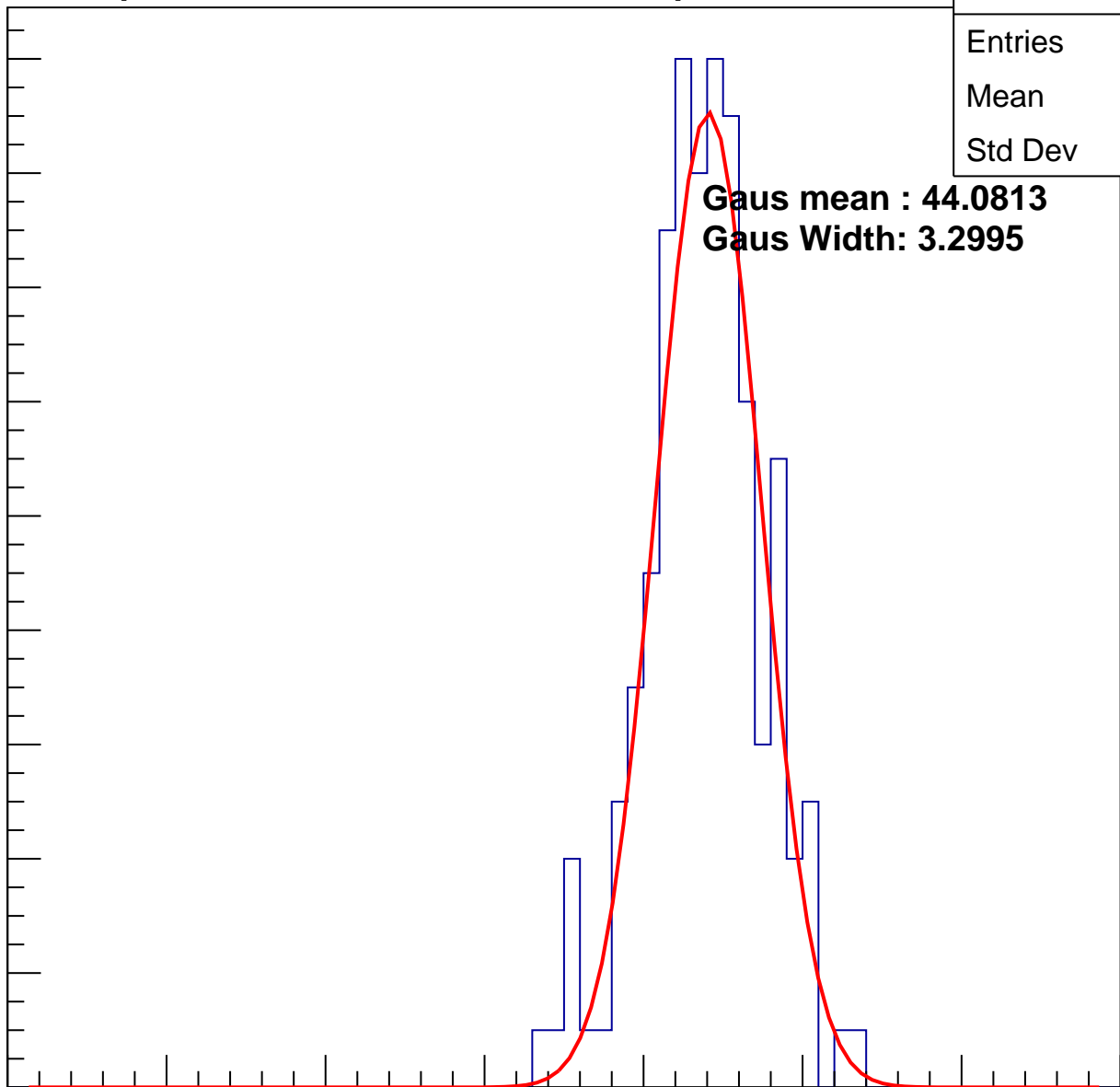
Entries	153
Mean	43.35
Std Dev	3.647

Gaus mean : 44.0813

Gaus Width: 3.2995

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

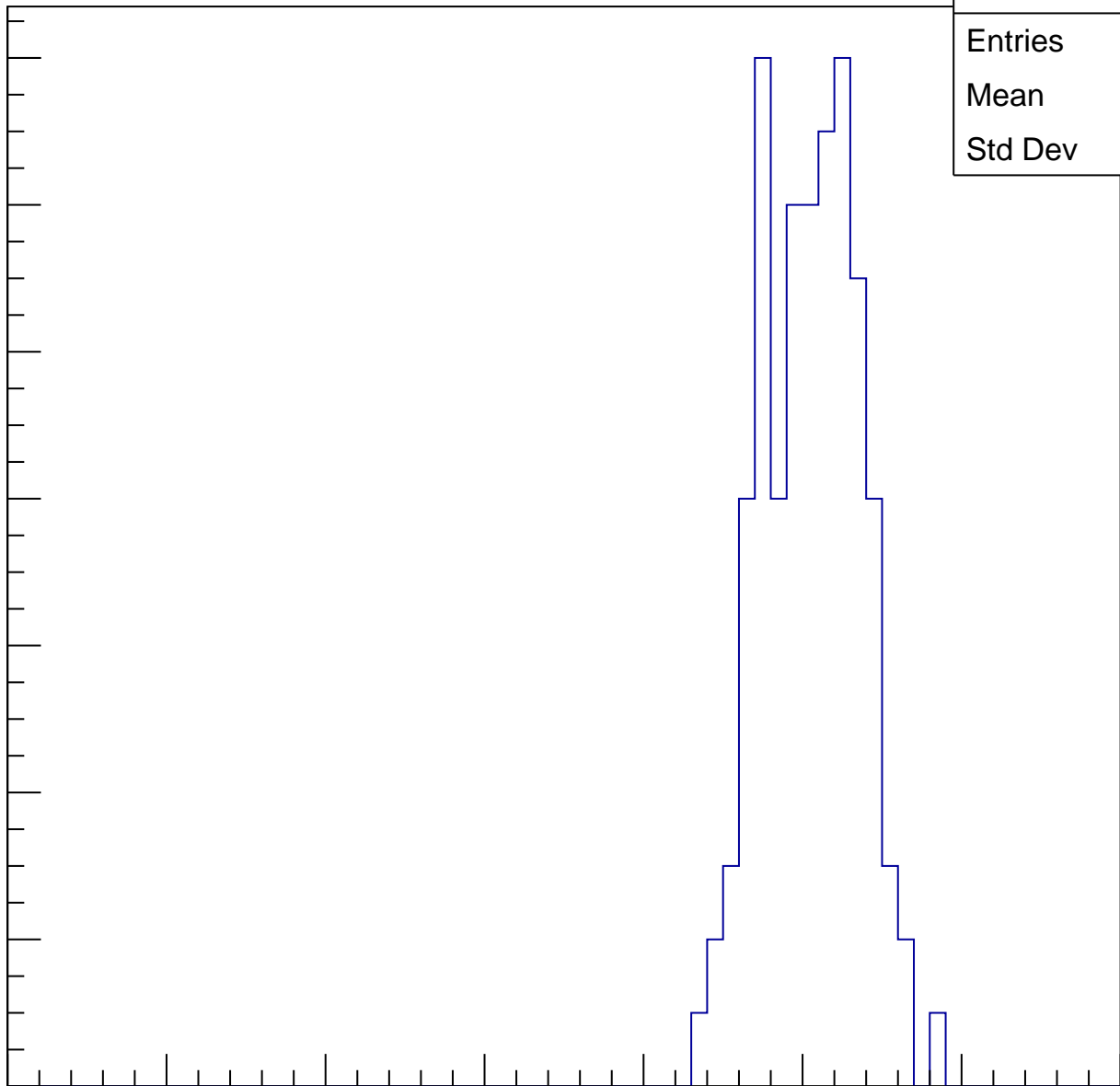
Entries	112
Mean	50.04
Std Dev	2.989

Entry

14
12
10
8
6
4
2
0

ampl

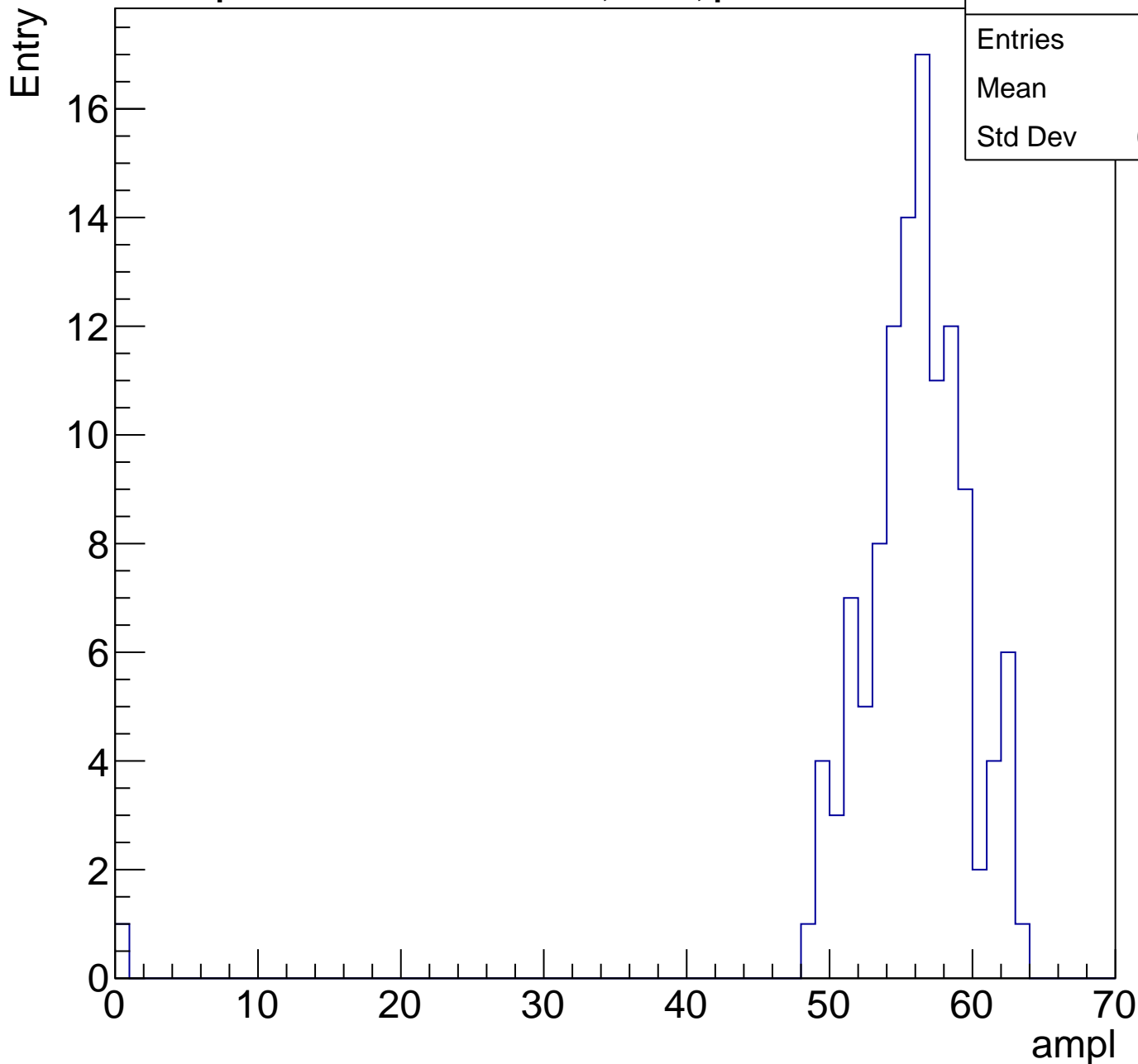
0 10 20 30 40 50 60 70



B1L001S, U22-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	55.2
Std Dev	6.101



B1L001S, U22-ch107, adc5

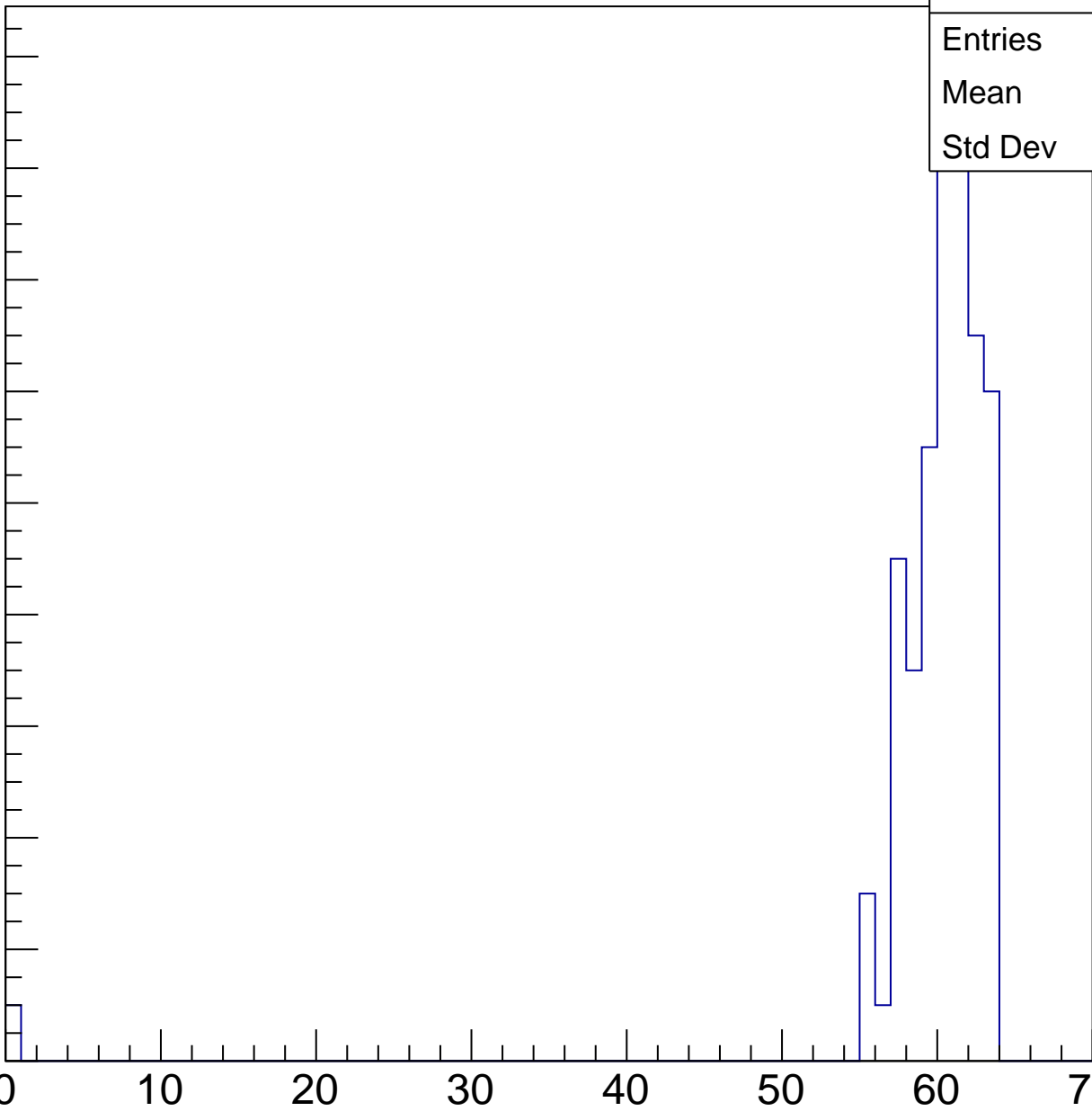
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.42
Std Dev	6.593

ampl

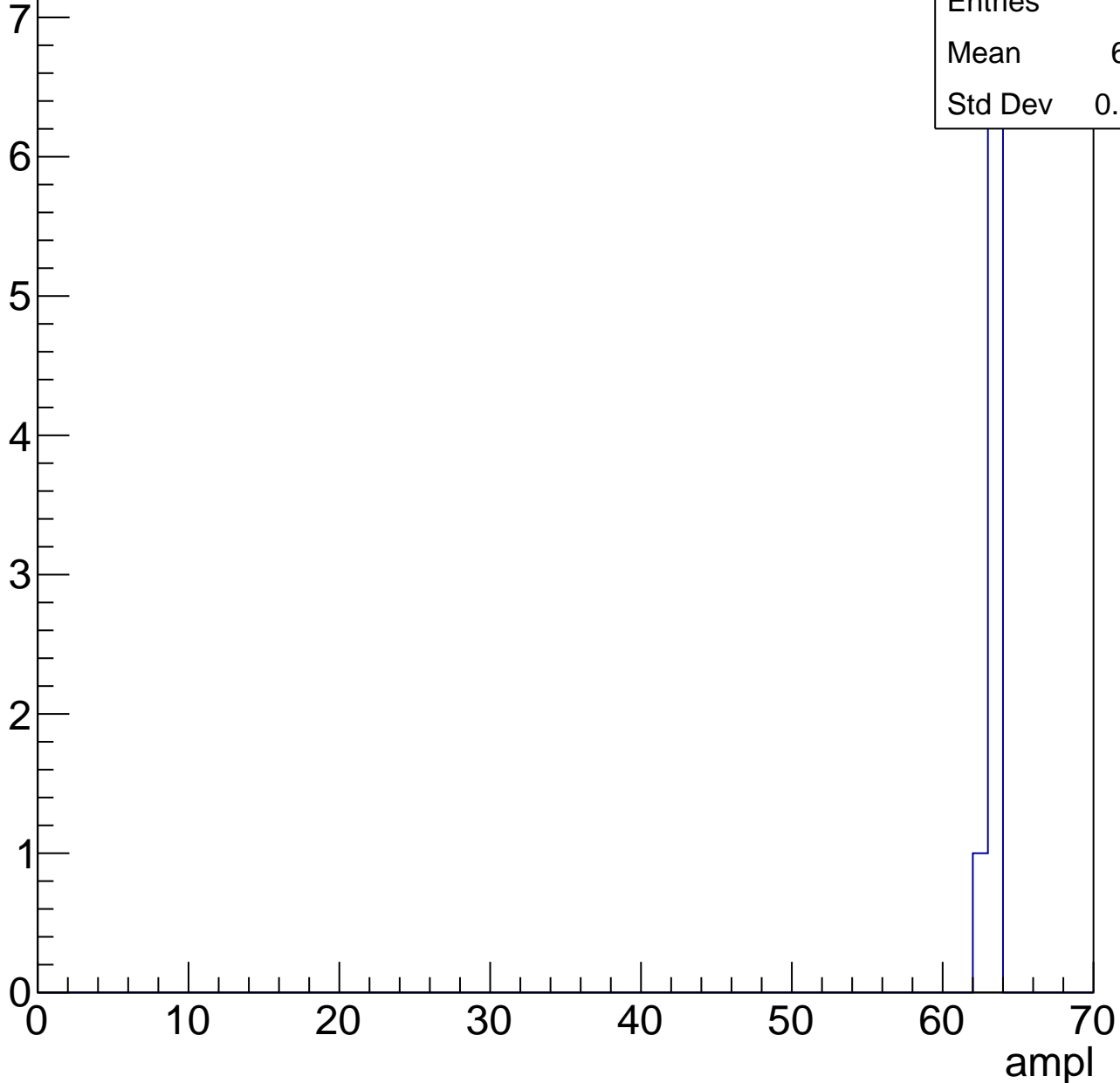


B1L001S, U22-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.88
Std Dev	0.3307



B1L001S, U22-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



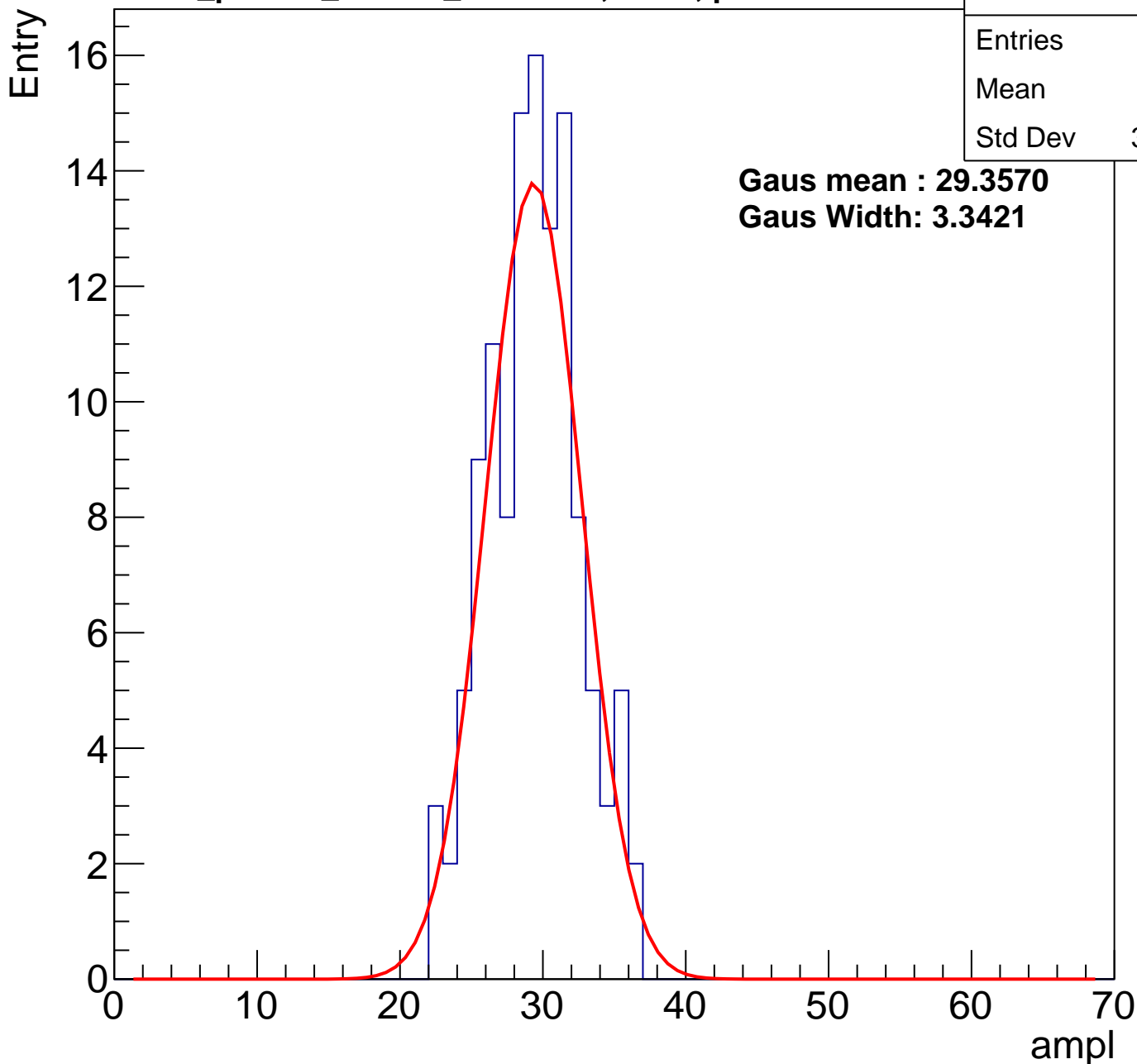
B1L001S, U22-ch108, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	28.9
Std Dev	3.176

Gaus mean : 29.3570

Gaus Width: 3.3421



B1L001S, U22-ch108, adc1

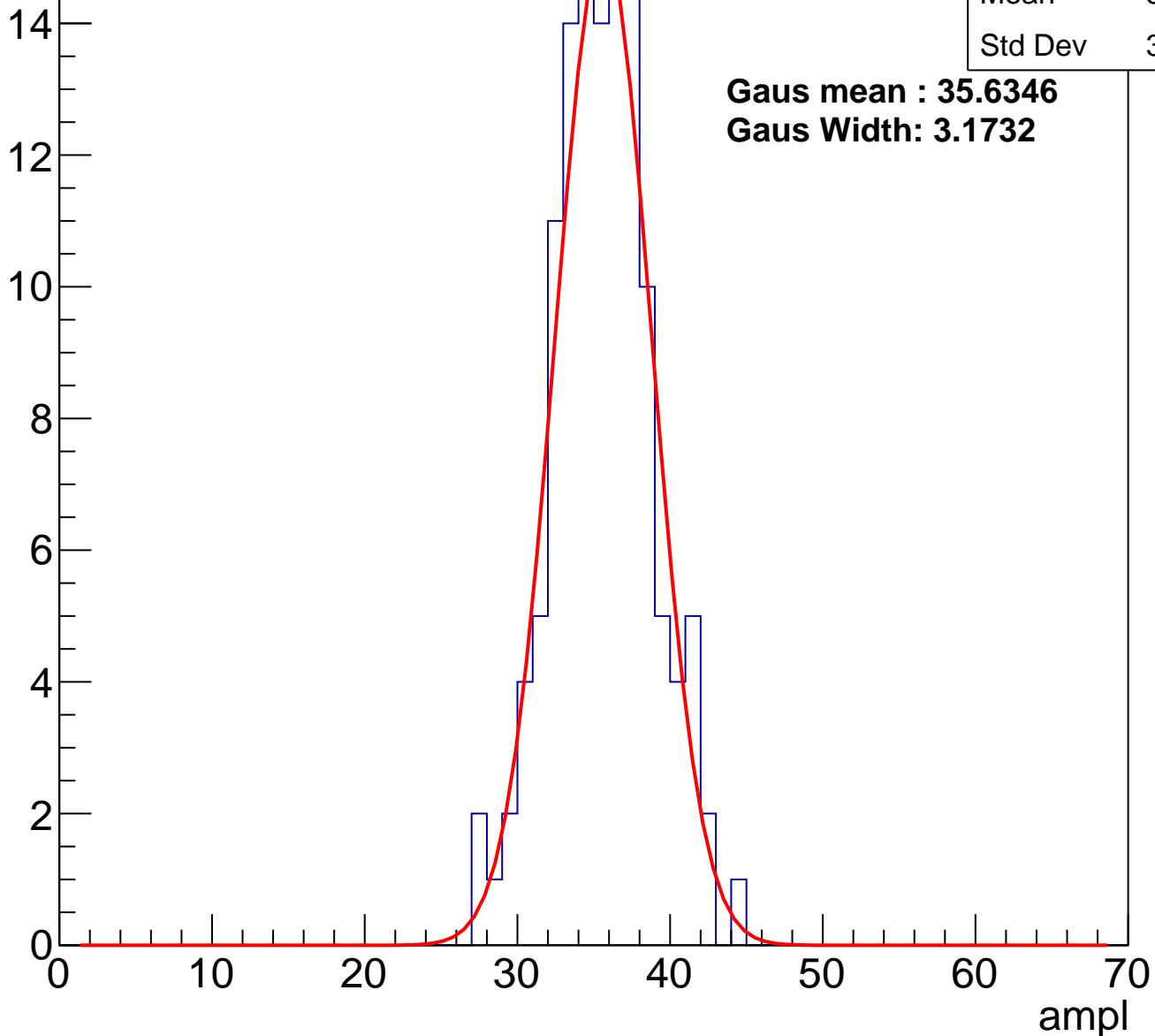
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	35.14
Std Dev	3.226

Gaus mean : 35.6346

Gaus Width: 3.1732

Entry



B1L001S, U22-ch108, adc2

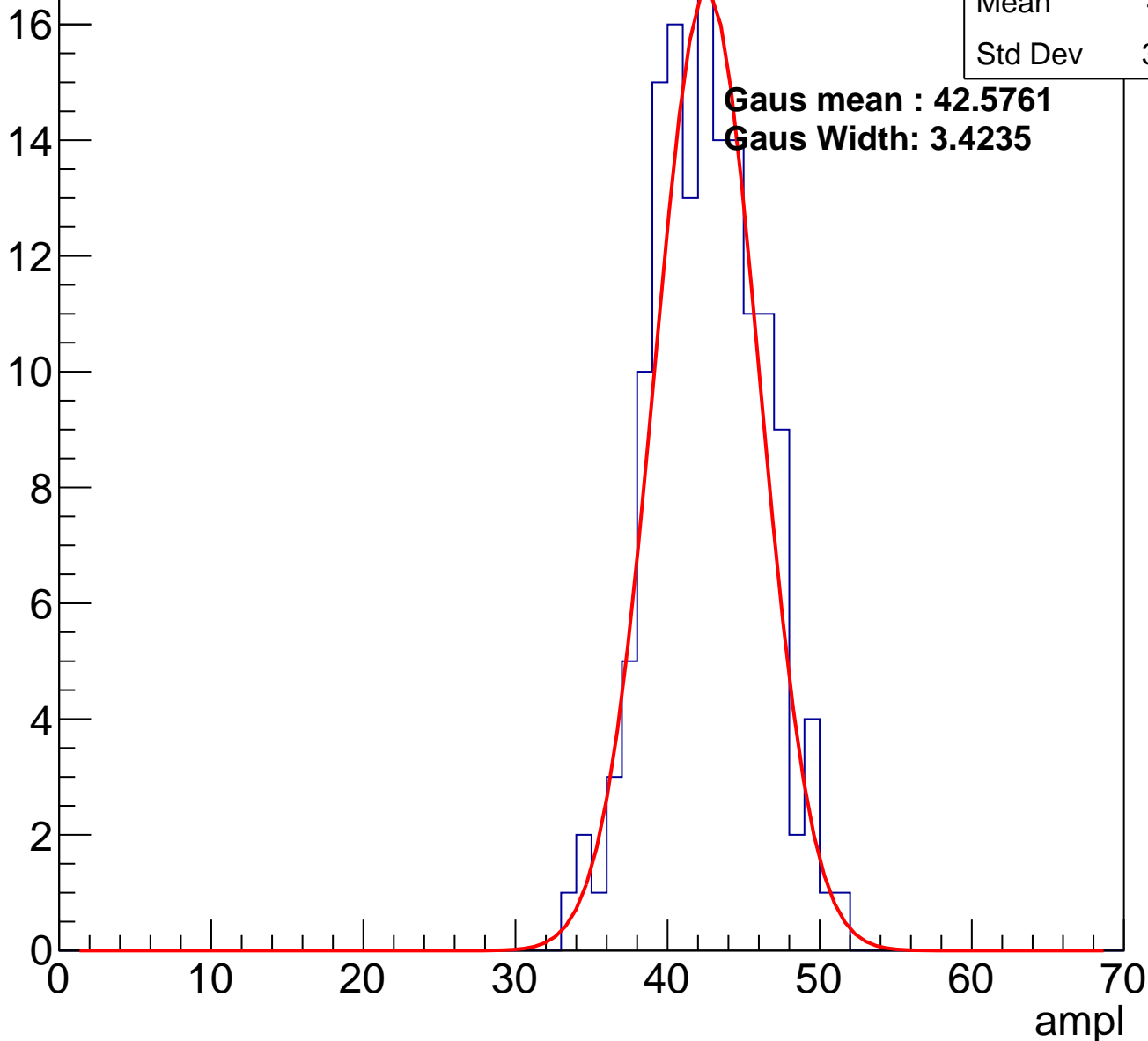
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	150
Mean	42.11
Std Dev	3.506

Gaus mean : 42.5761

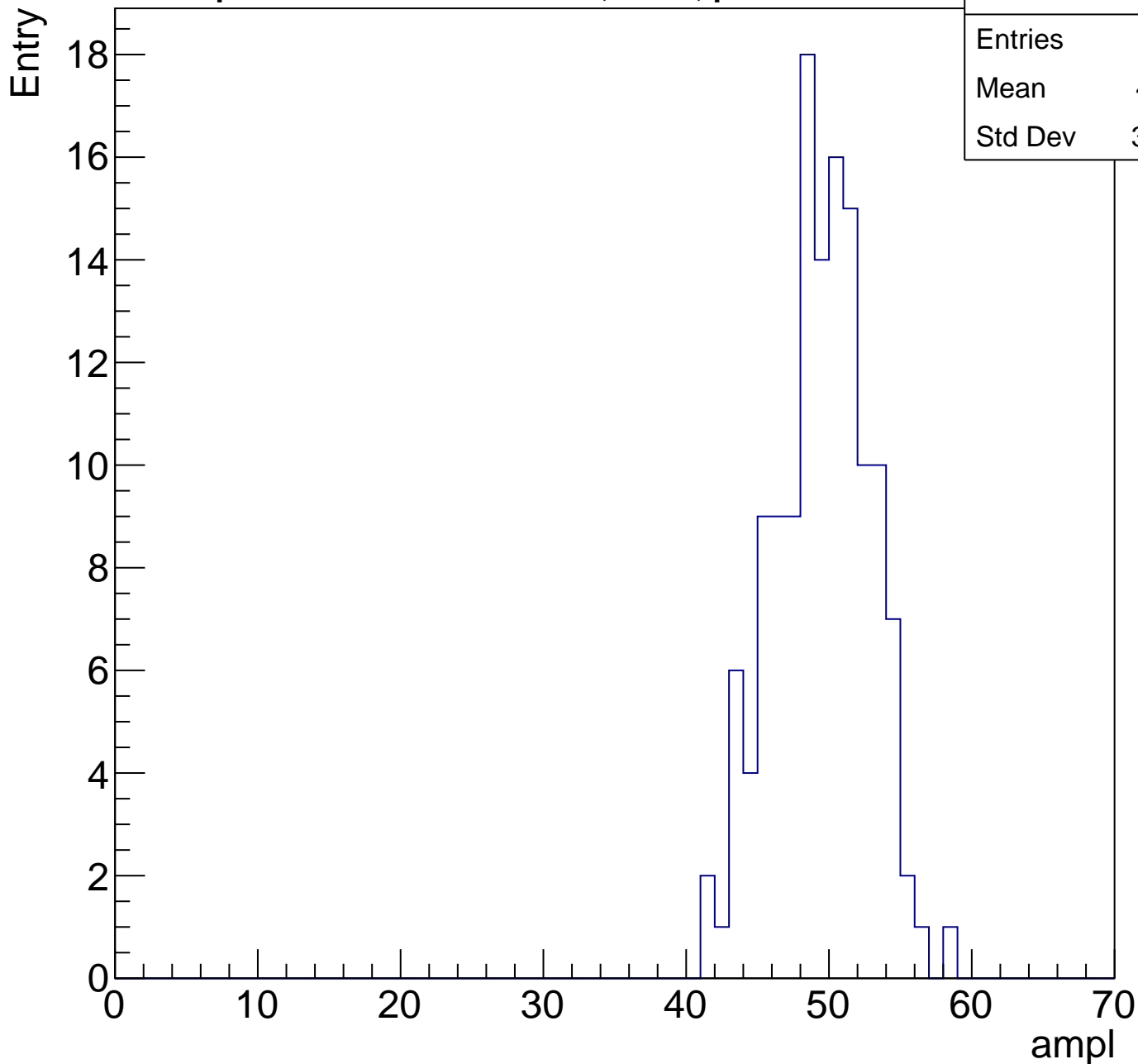
Gaus Width: 3.4235



B1L001S, U22-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	49.01
Std Dev	3.318



B1L001S, U22-ch108, adc4

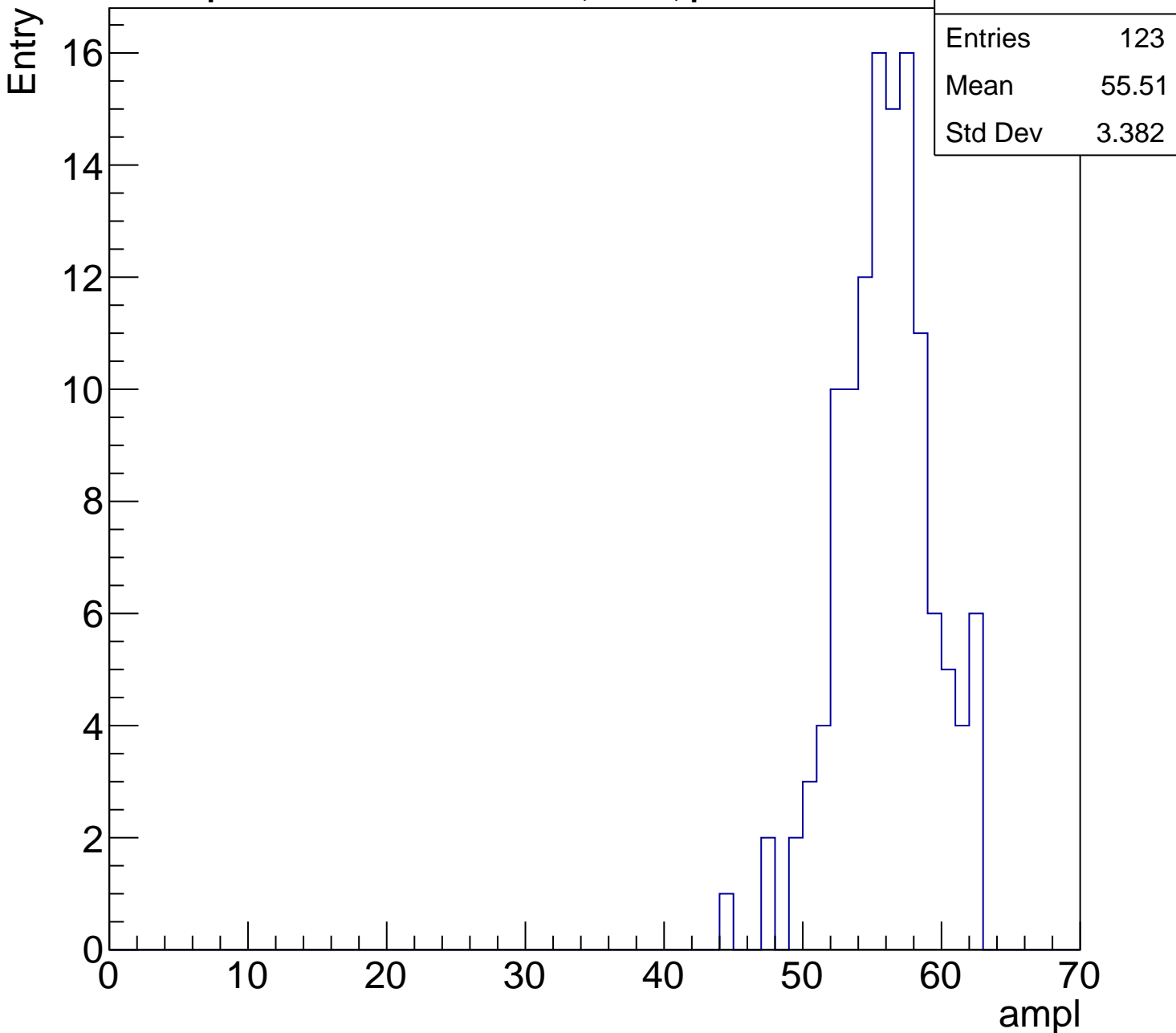
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	55.51
Std Dev	3.382

ampl



B1L001S, U22-ch108, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

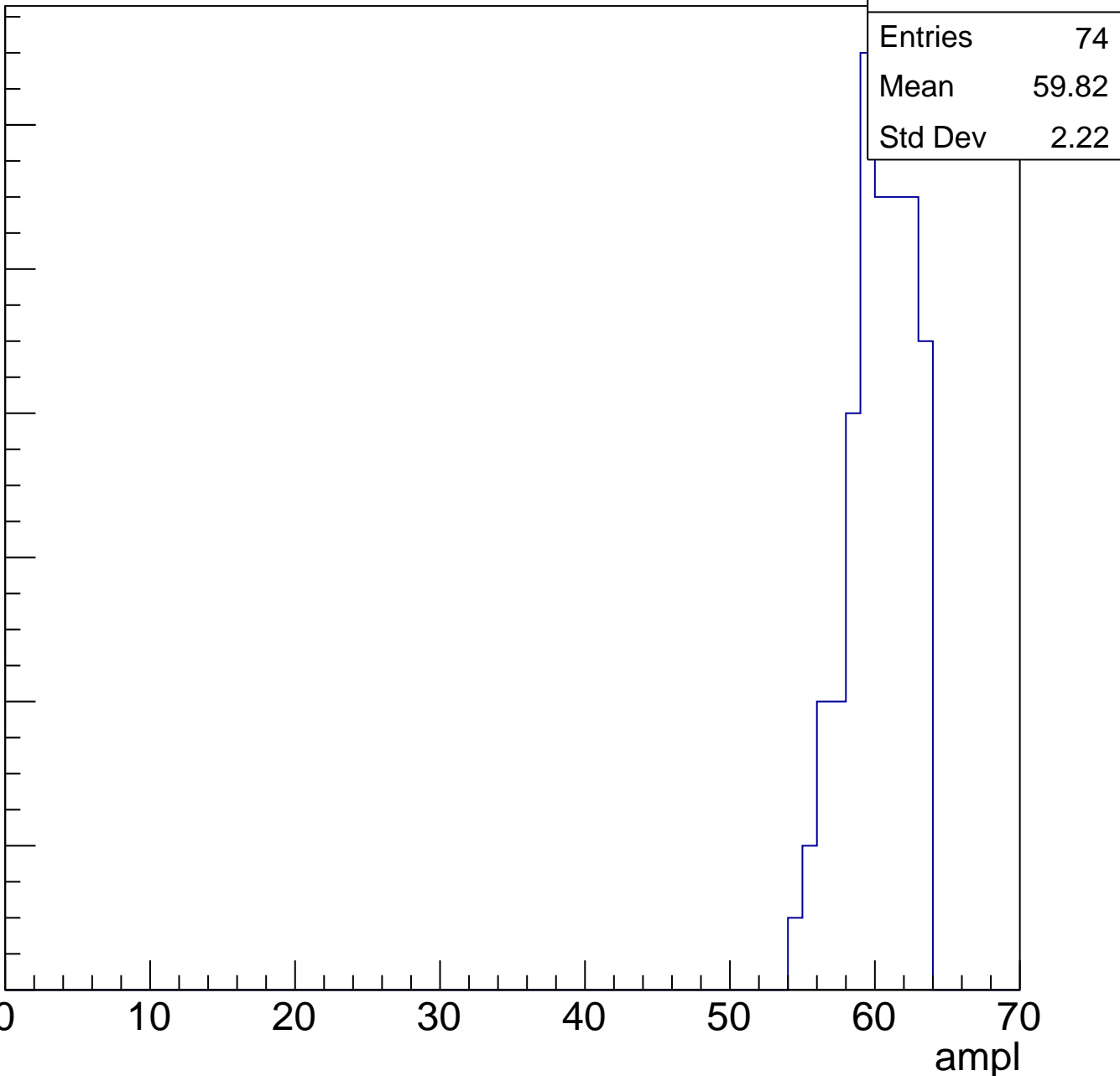
50

60

70

ampl

Entries	74
Mean	59.82
Std Dev	2.22



B1L001S, U22-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

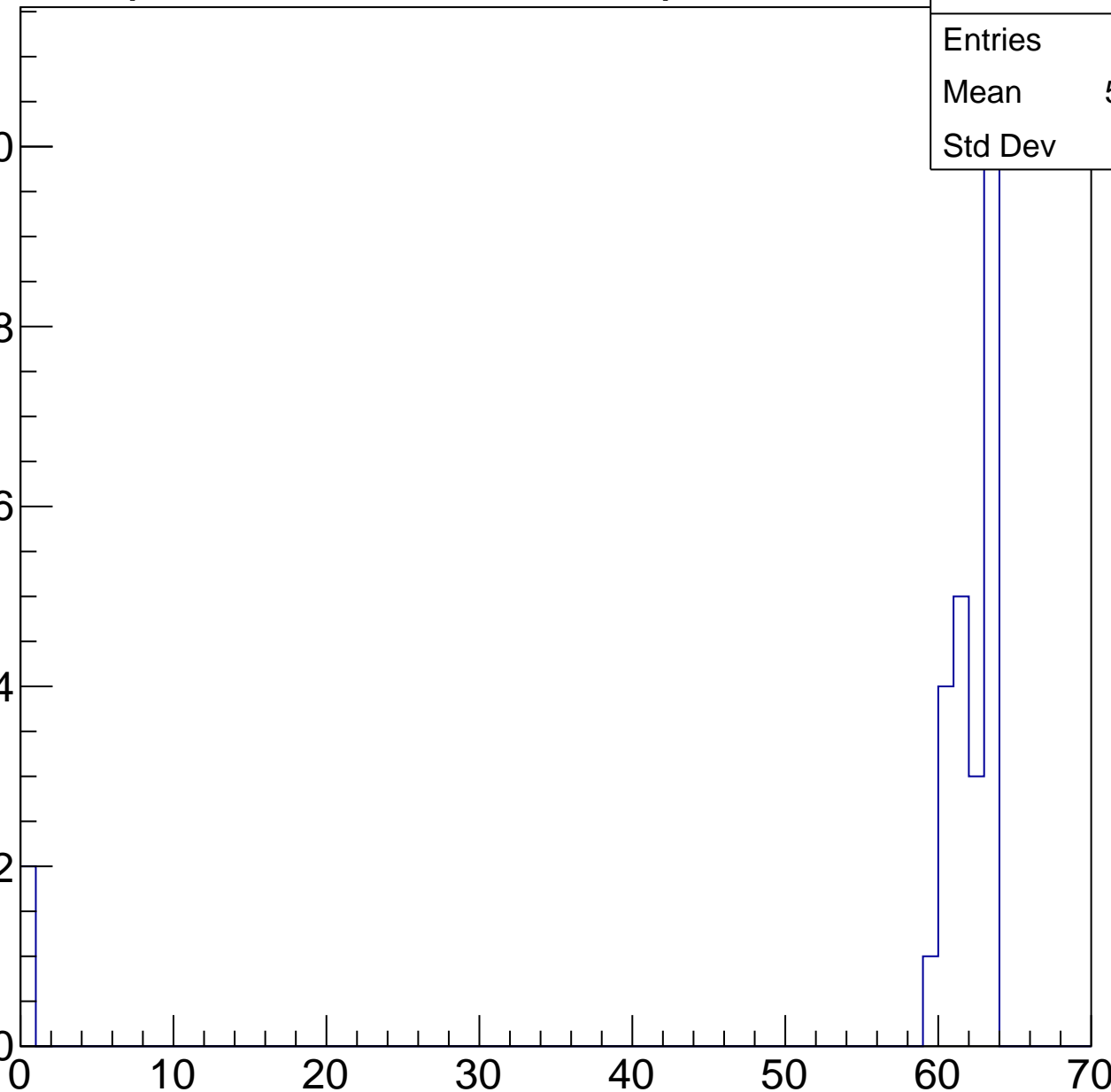
4

2

0

Entries	26
Mean	57.04
Std Dev	16.51

ampl



B1L001S, U22-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

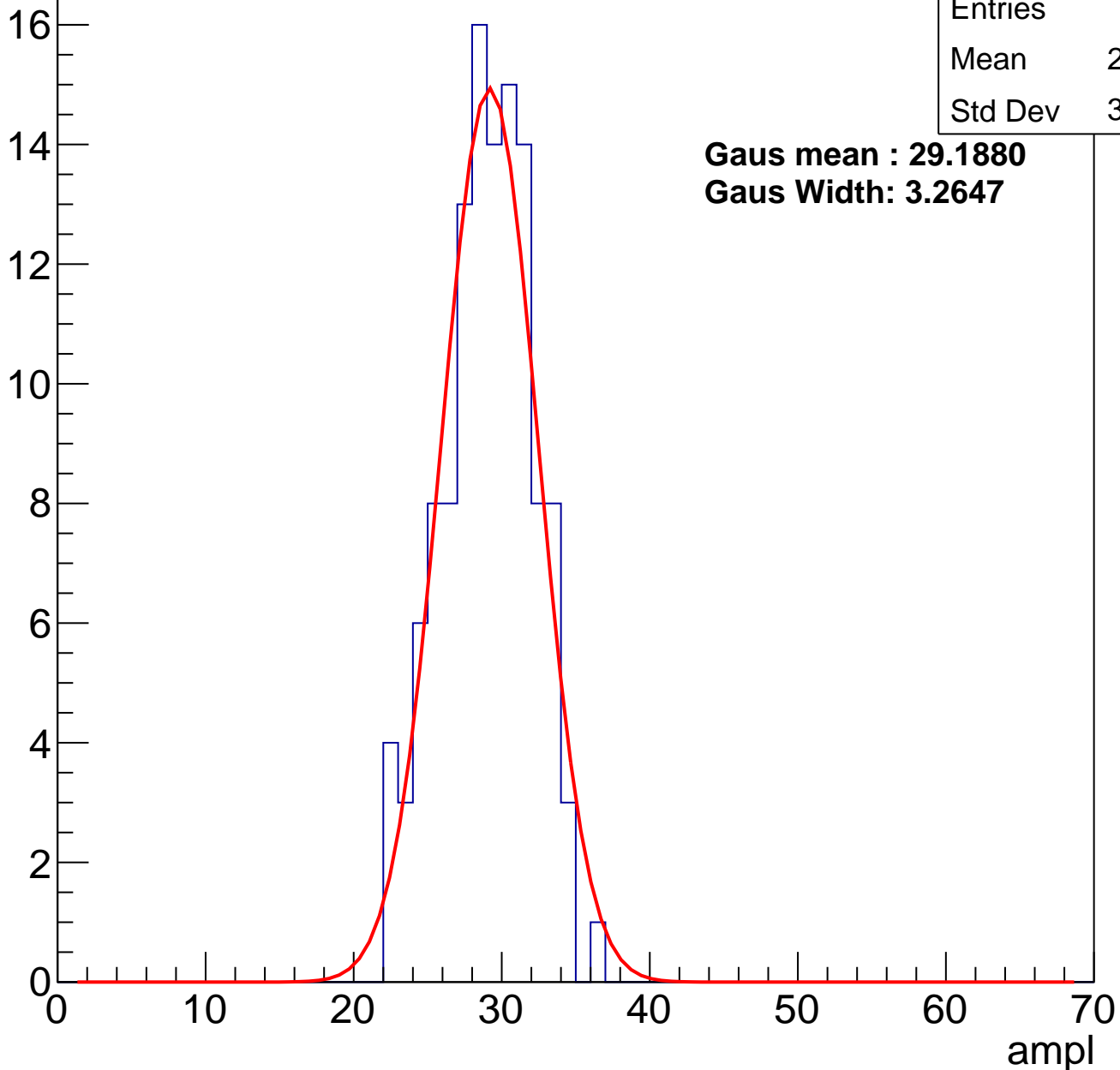
Entry



B1L001S, U22-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch109, adc1

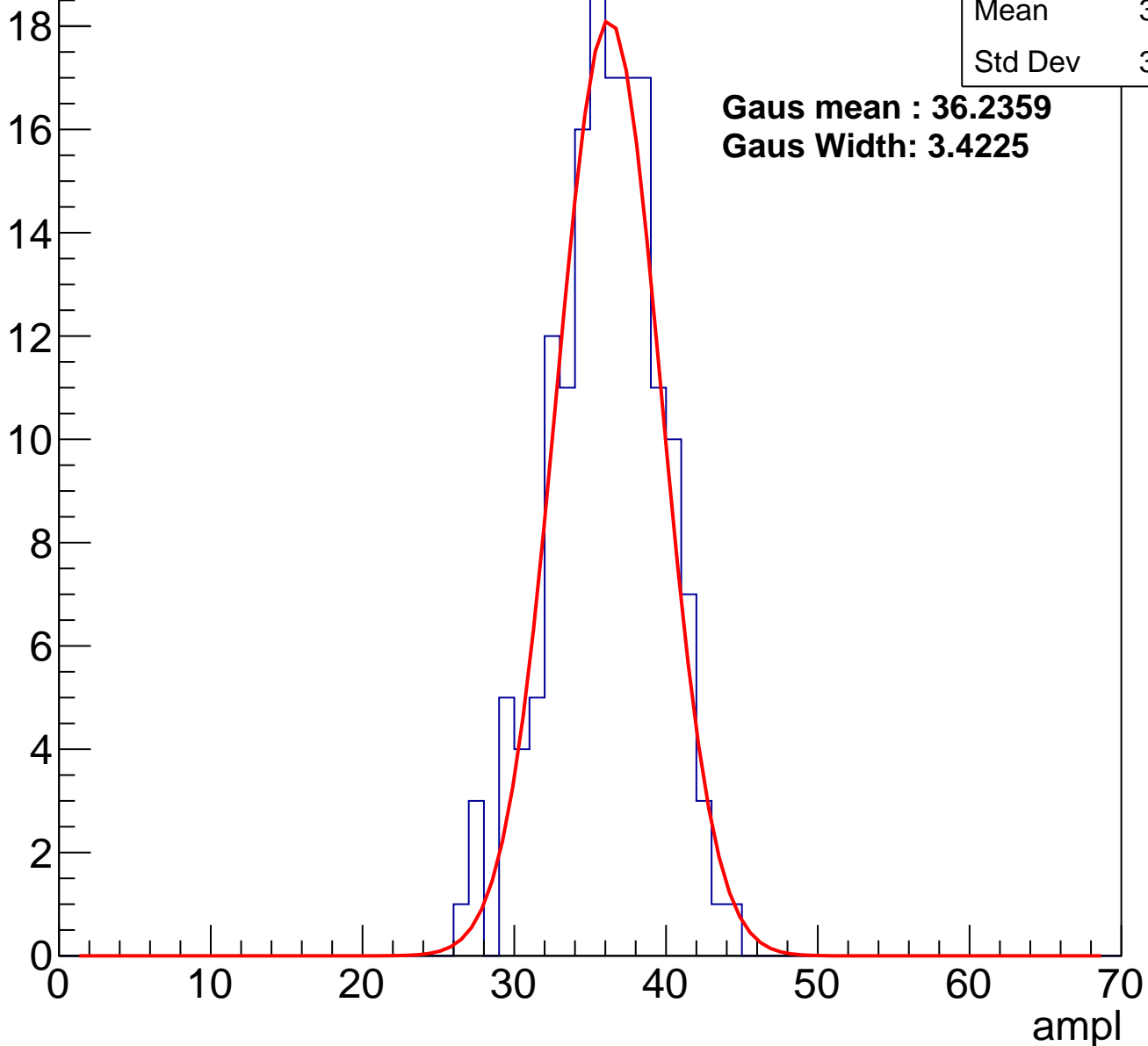
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	35.62
Std Dev	3.489

Gaus mean : 36.2359

Gaus Width: 3.4225

Entry



B1L001S, U22-ch109, adc2

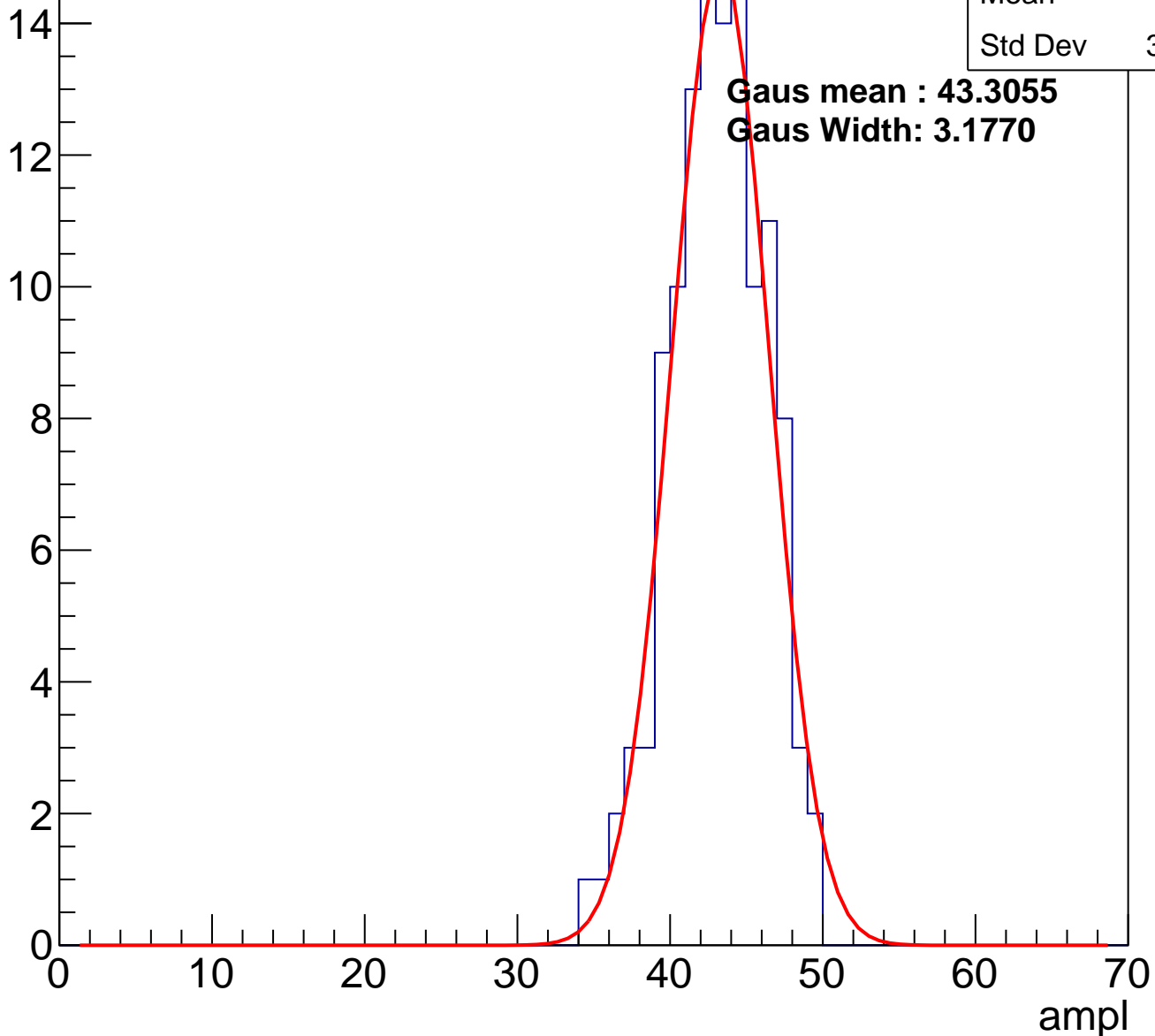
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	42.63
Std Dev	3.082

Gaus mean : 43.3055

Gaus Width: 3.1770

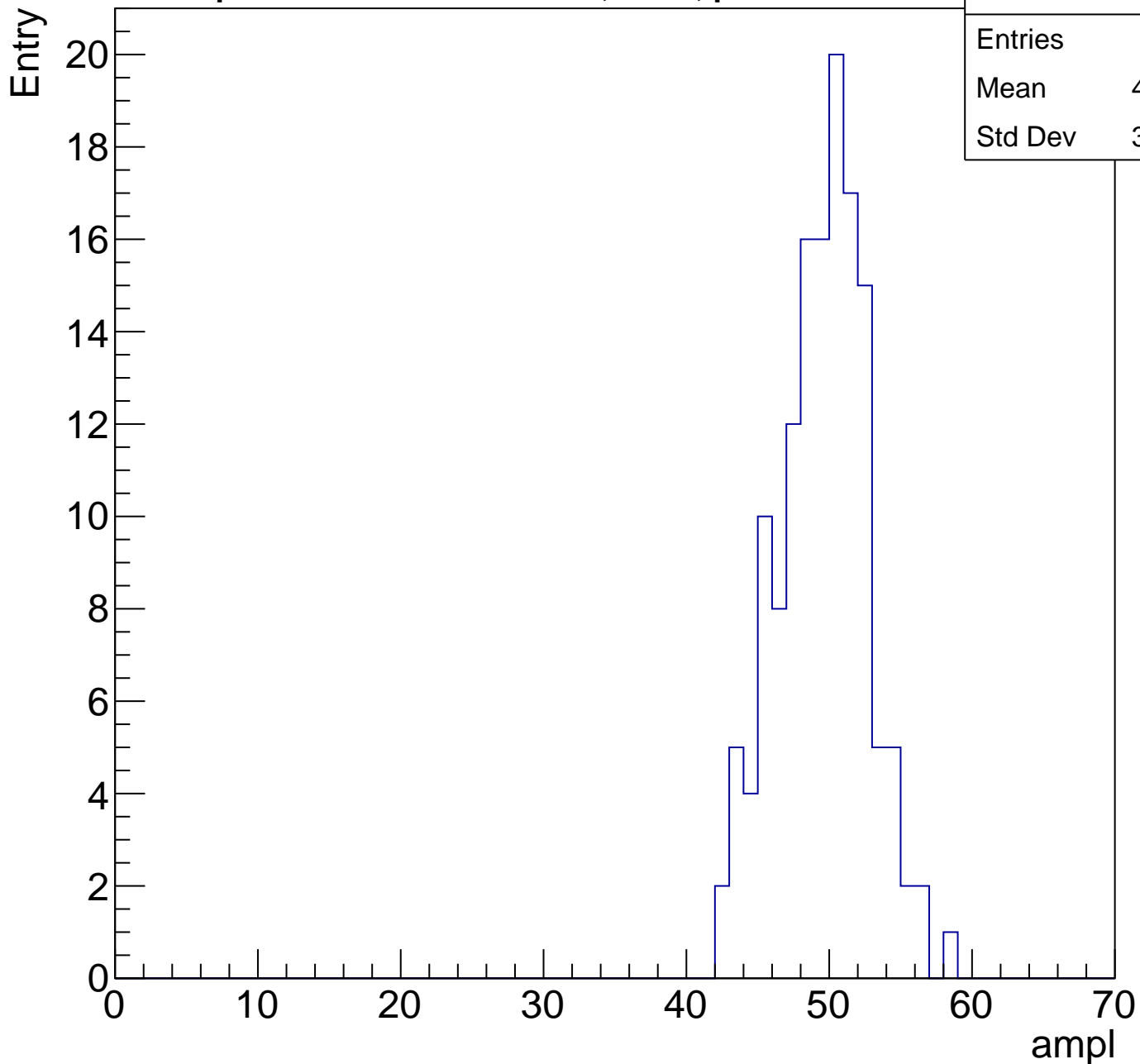
Entry



B1L001S, U22-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	49.08
Std Dev	3.108



B1L001S, U22-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.33
Std Dev	5.766

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

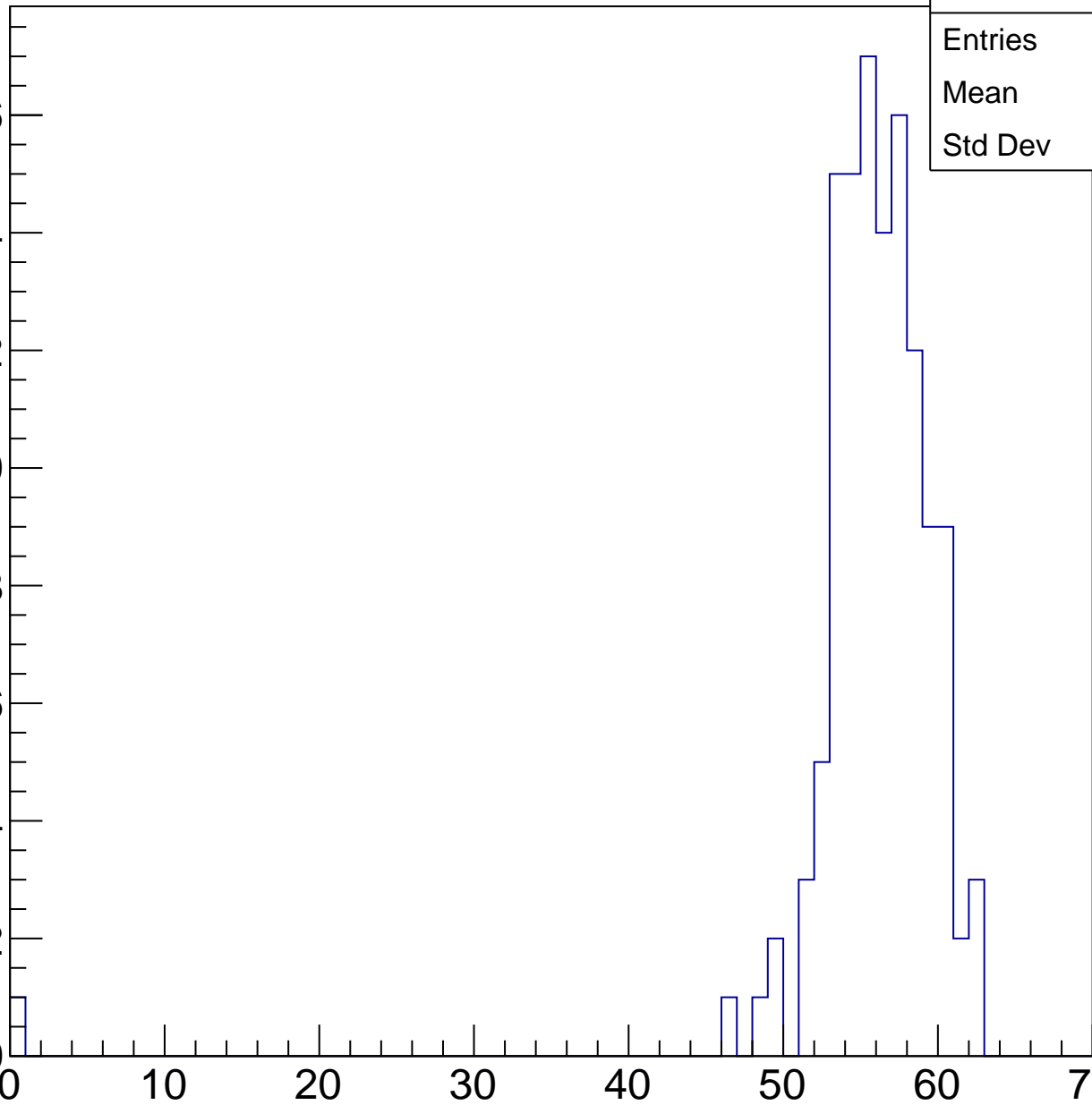
40

50

60

70

ampl



B1L001S, U22-ch109, adc5

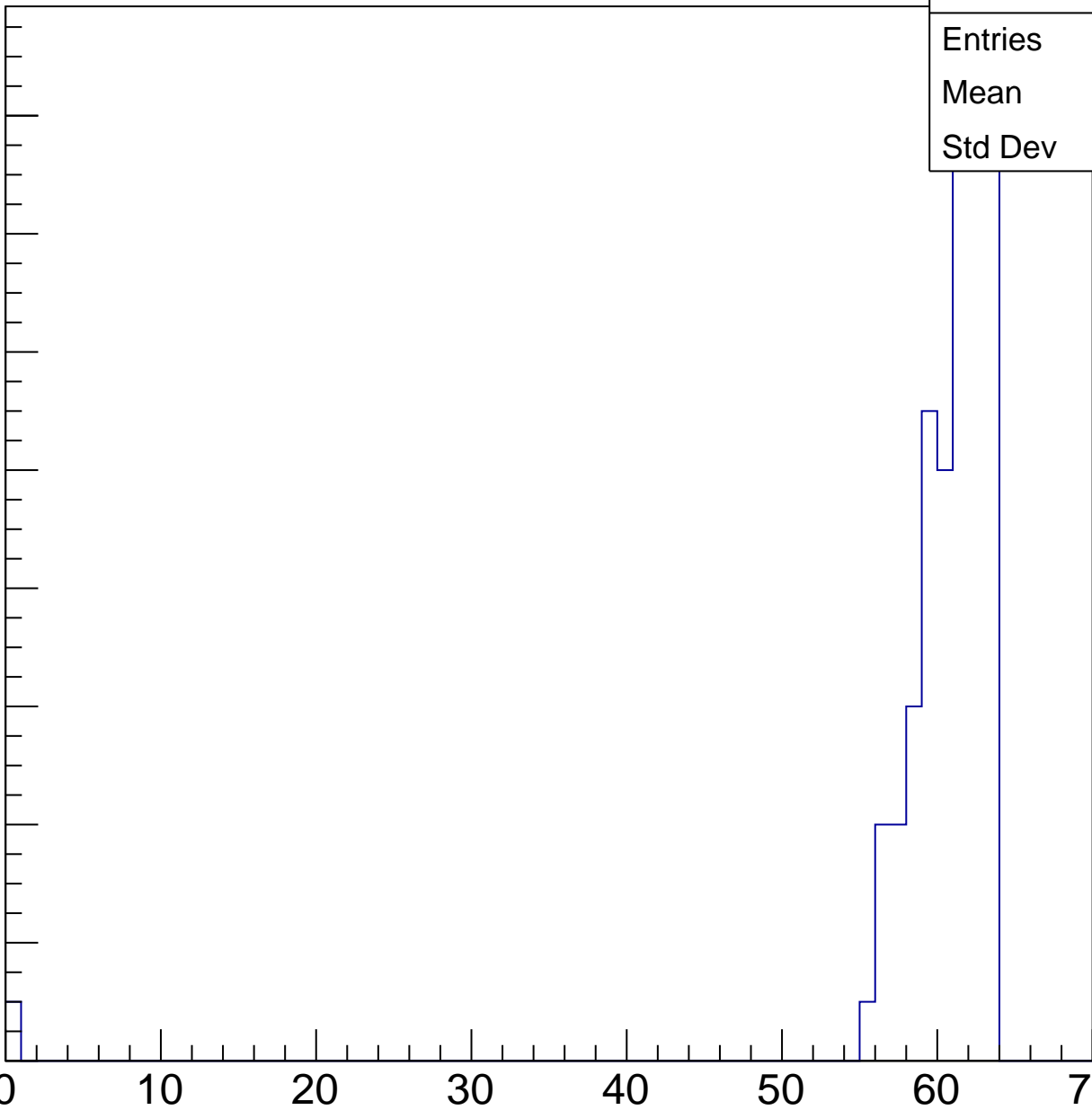
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.78
Std Dev	6.804

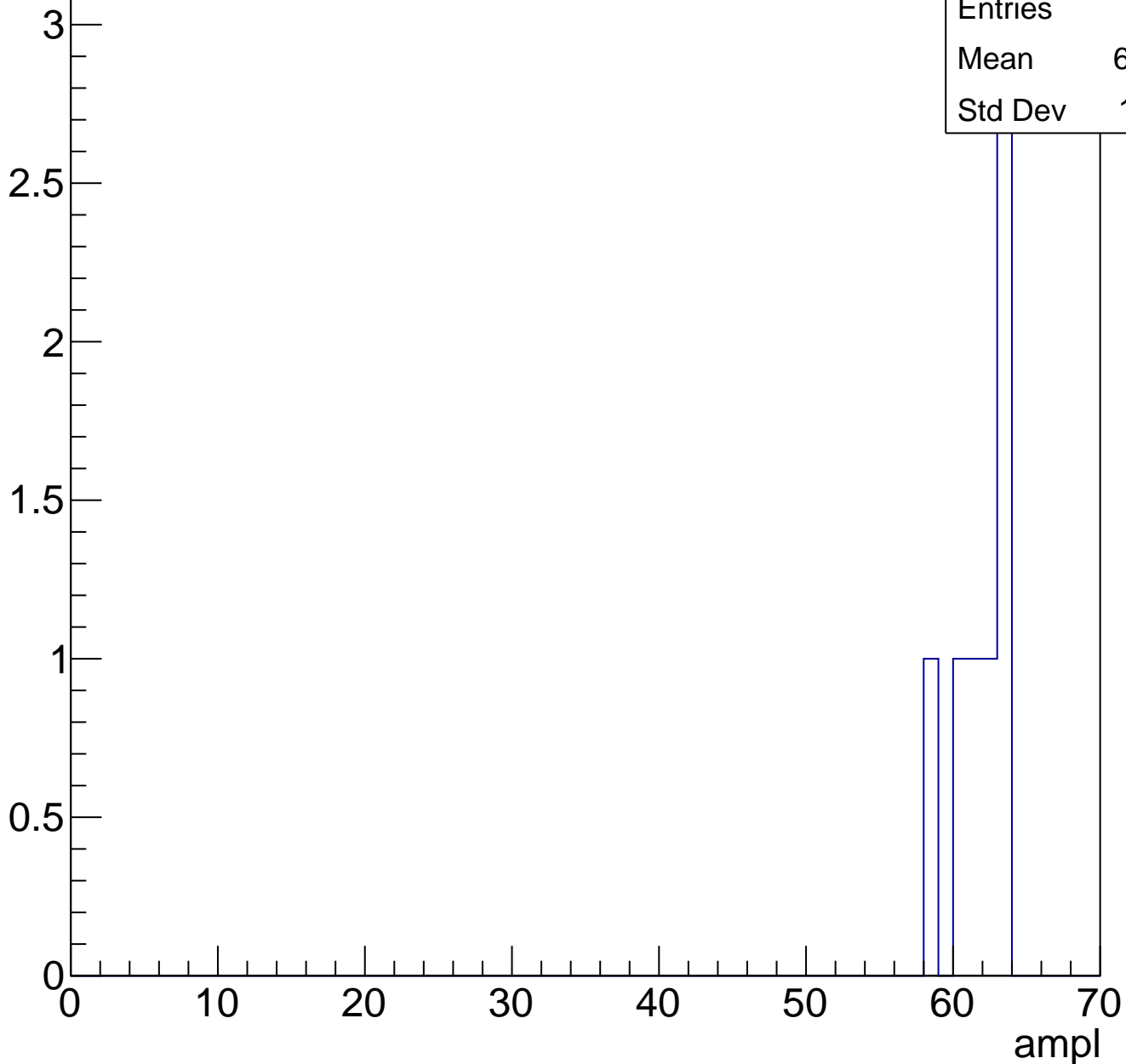
ampl



B1L001S, U22-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

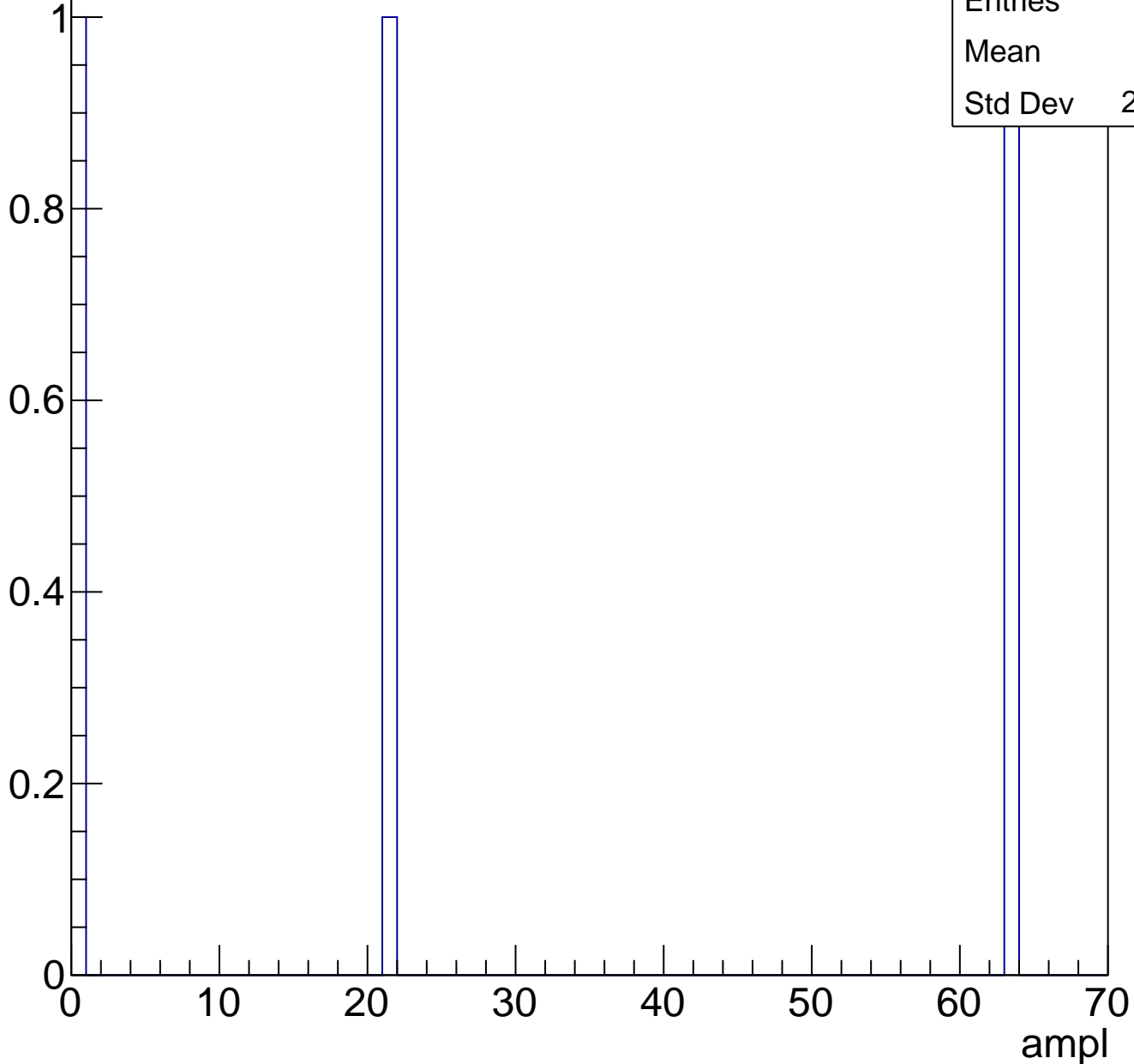
Entry



B1L001S, U22-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L001S, U22-ch110, adc0

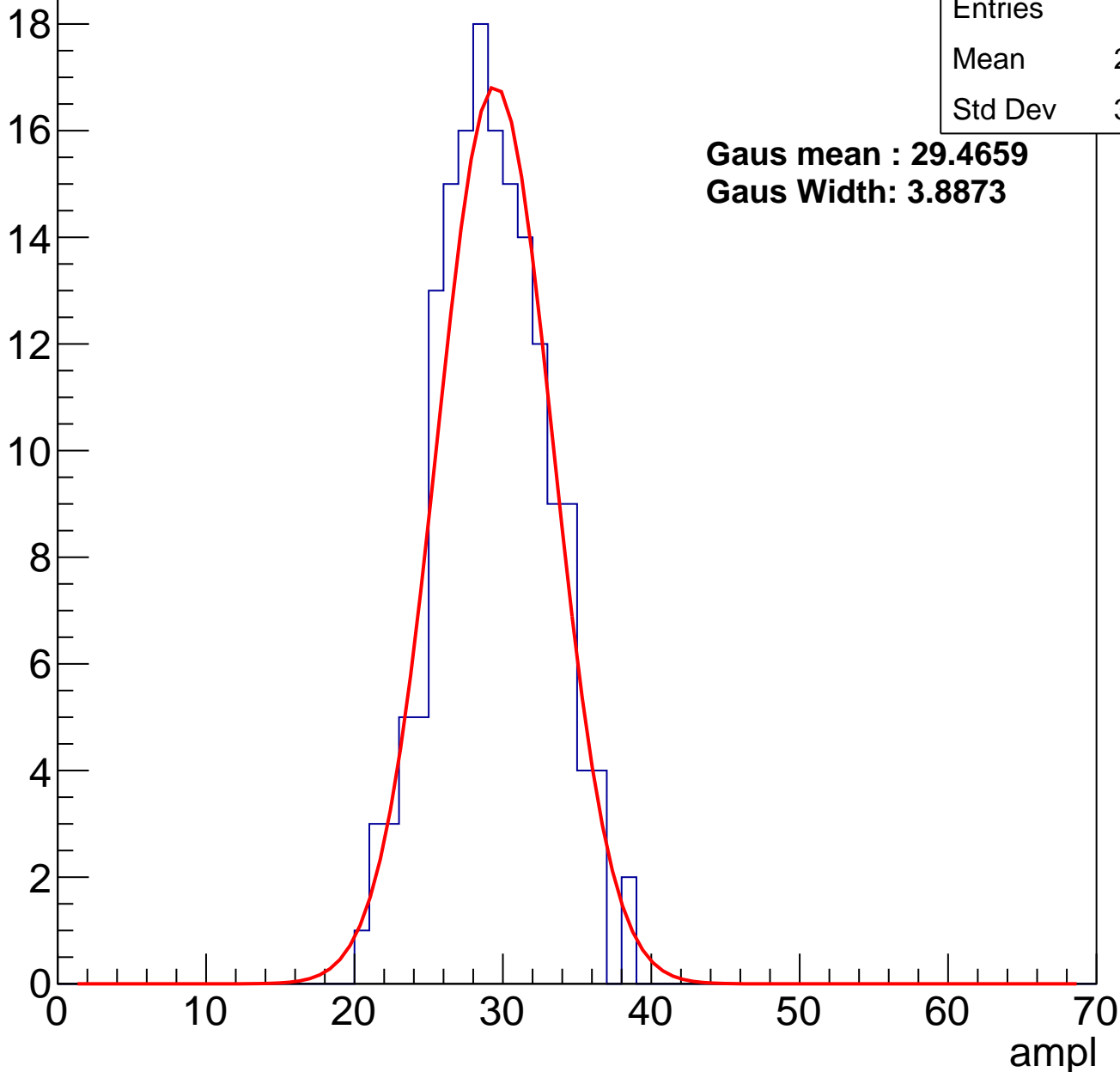
calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	28.84
Std Dev	3.659

Gaus mean : 29.4659

Gaus Width: 3.8873

Entry



B1L001S, U22-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	35.64
Std Dev	3.099

Gaus mean : 36.0489

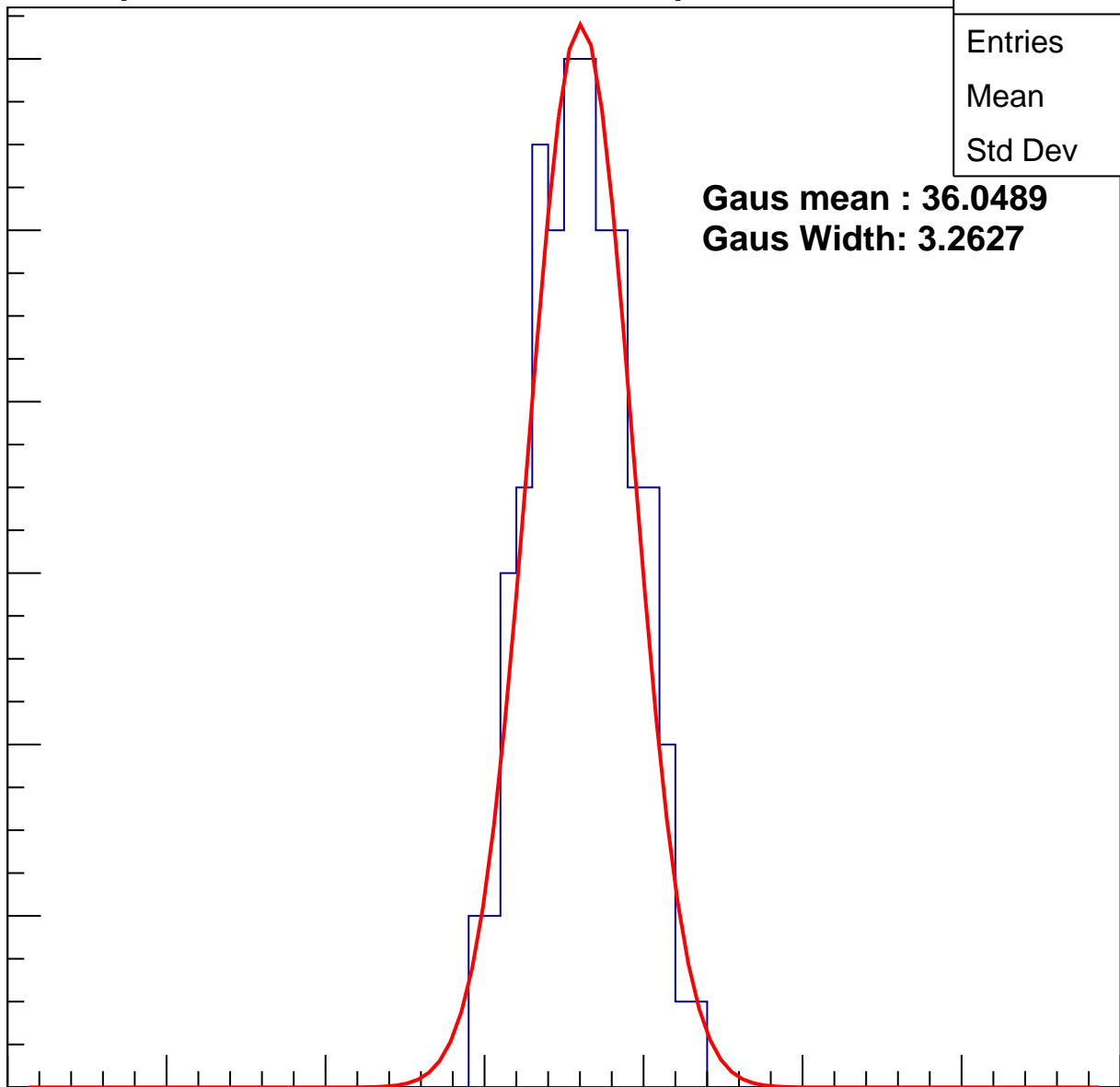
Gaus Width: 3.2627

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

136

Mean

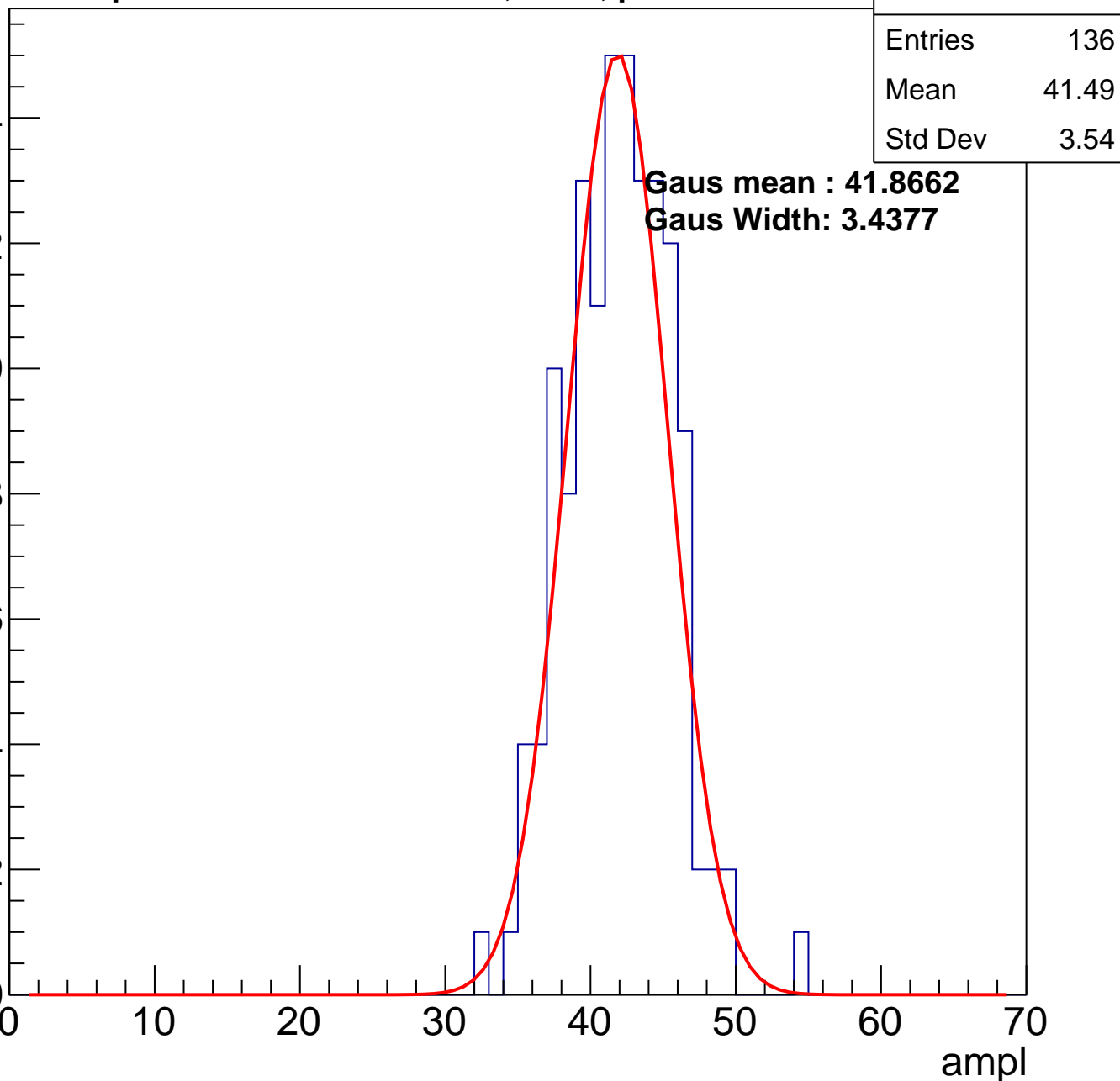
41.49

Std Dev

3.54

Gaus mean : 41.8662

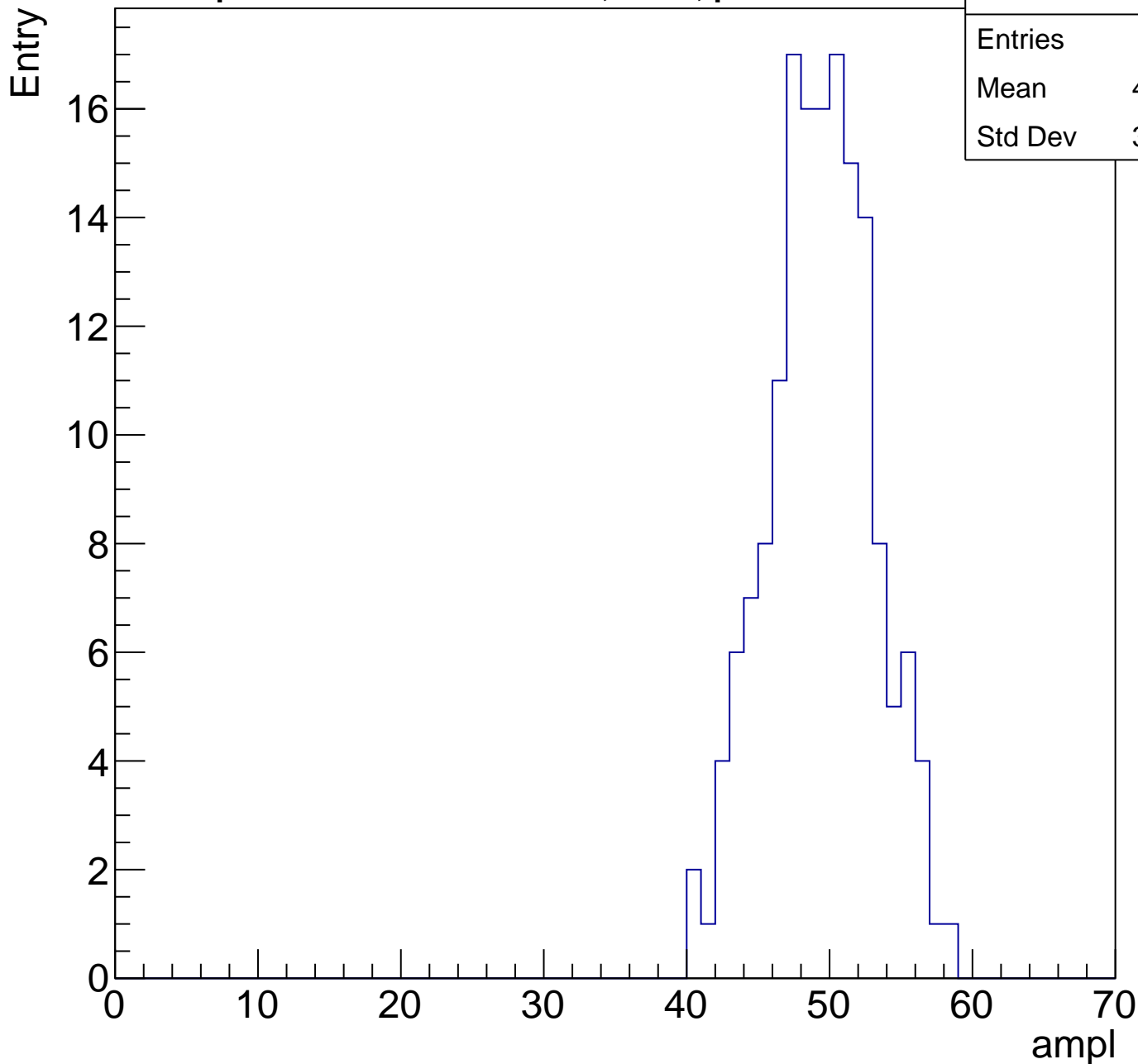
Gaus Width: 3.4377



B1L001S, U22-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	48.92
Std Dev	3.665



B1L001S, U22-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

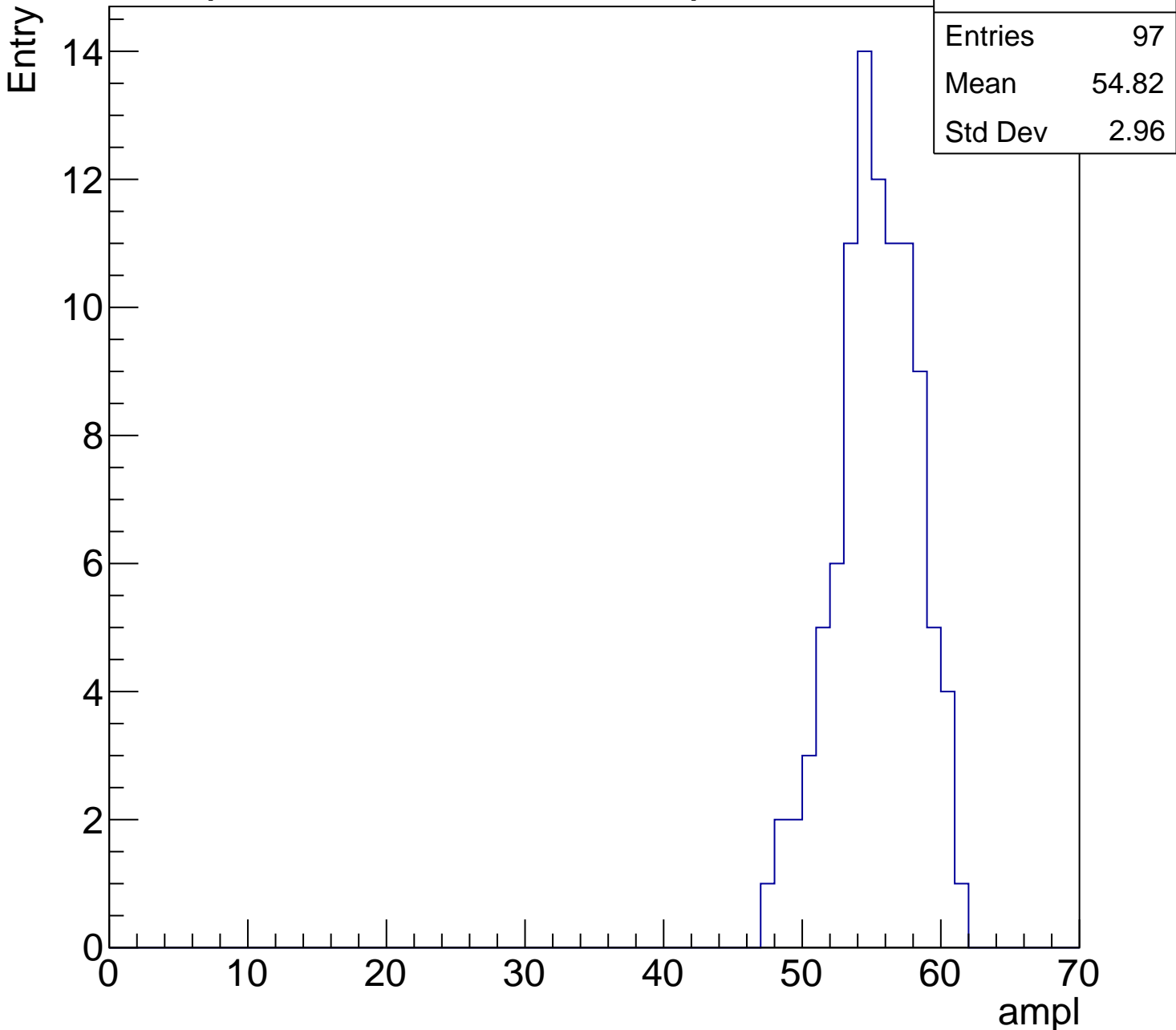
Entries	97
Mean	54.82
Std Dev	2.96

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

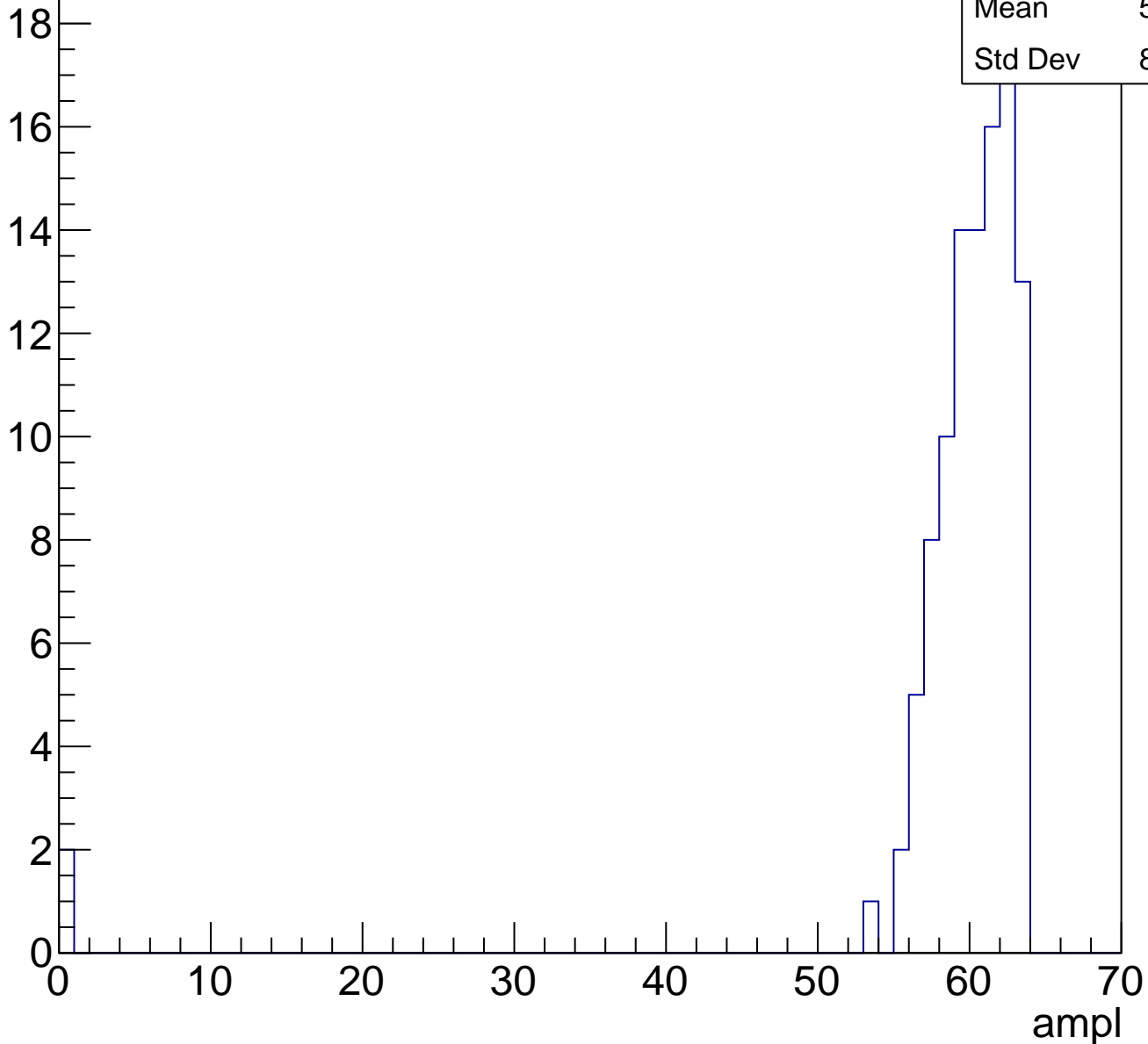


B1L001S, U22-ch110, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	58.83
Std Dev	8.532

Entry

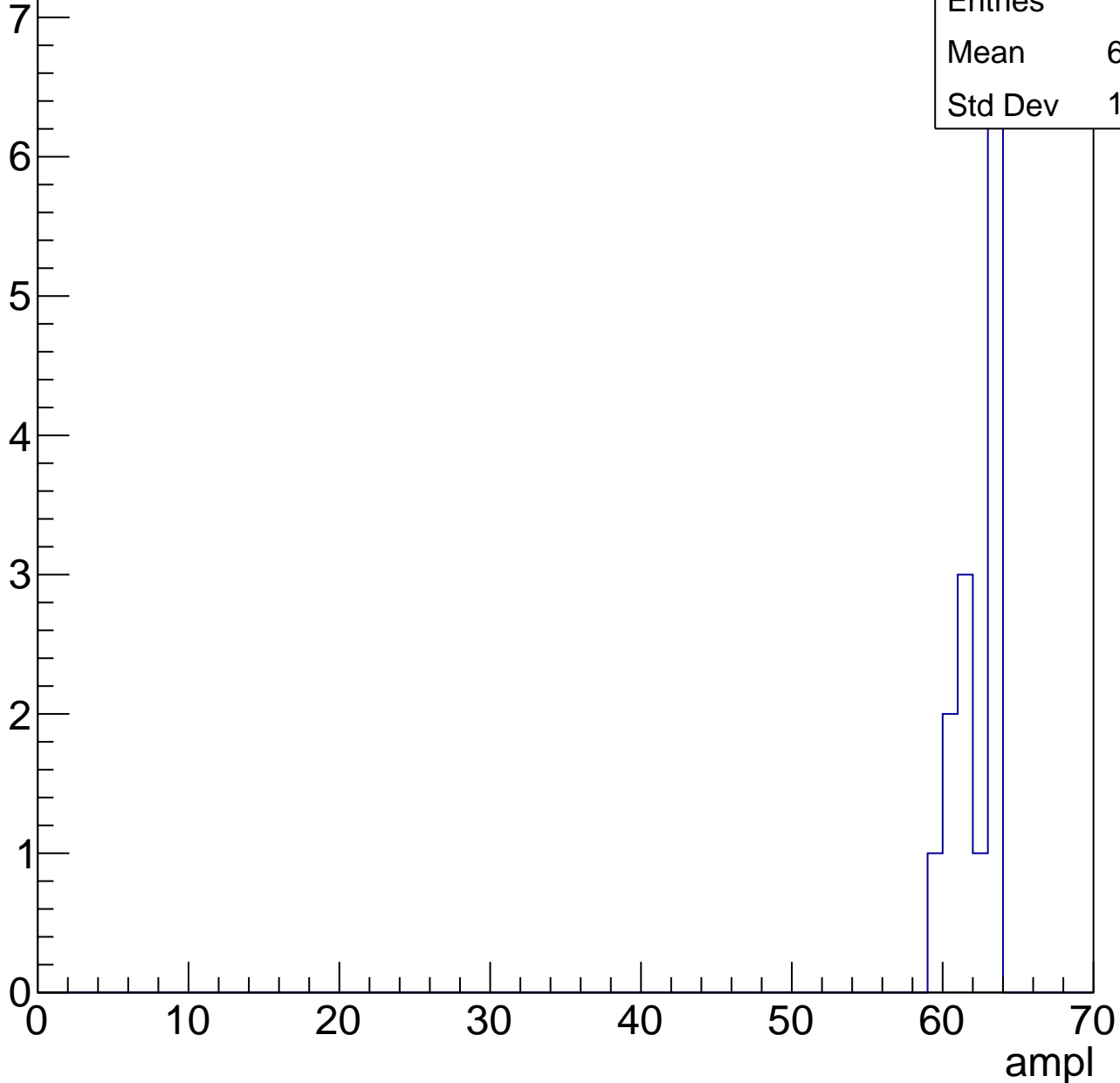


B1L001S, U22-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

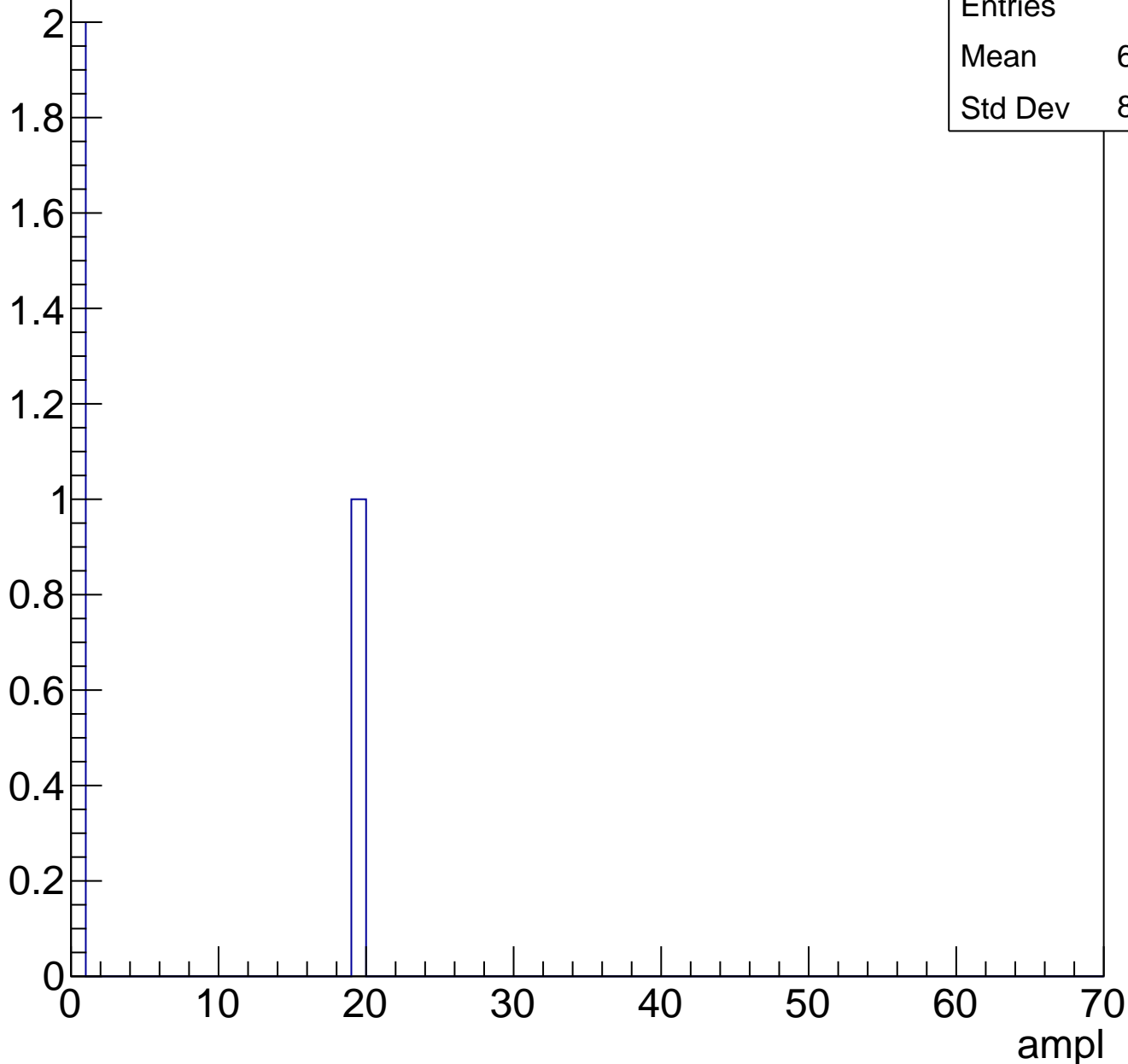
Entries	14
Mean	61.79
Std Dev	1.372



B1L001S, U22-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L001S, U22-ch111, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	30.08
Std Dev	4.103

Gaus mean : 30.7529

Gaus Width: 3.5646

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

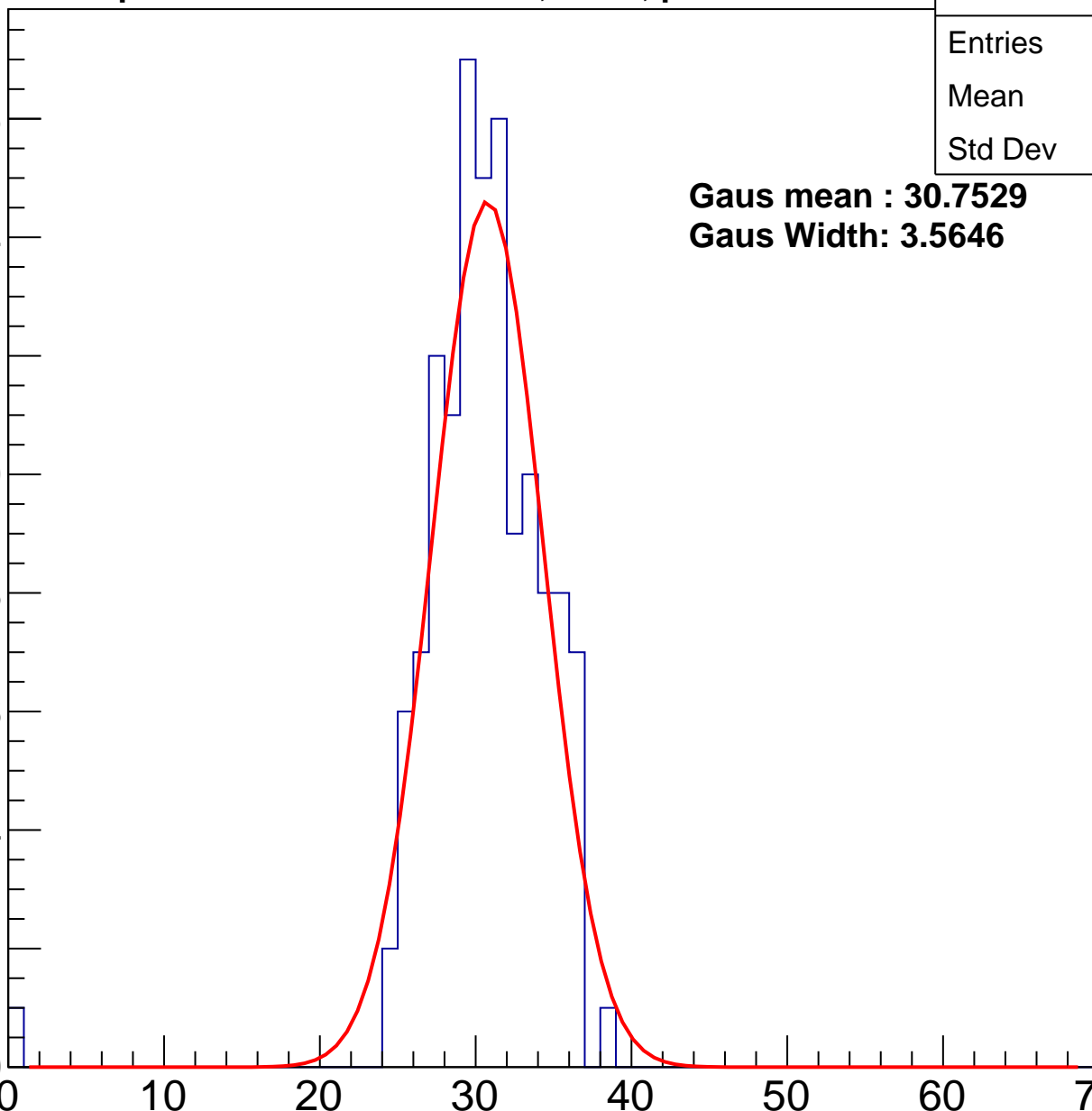
40

50

60

70

ampl



B1L001S, U22-ch111, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	37.17
Std Dev	3.649

Gaus mean : 37.6542

Gaus Width: 3.8845

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

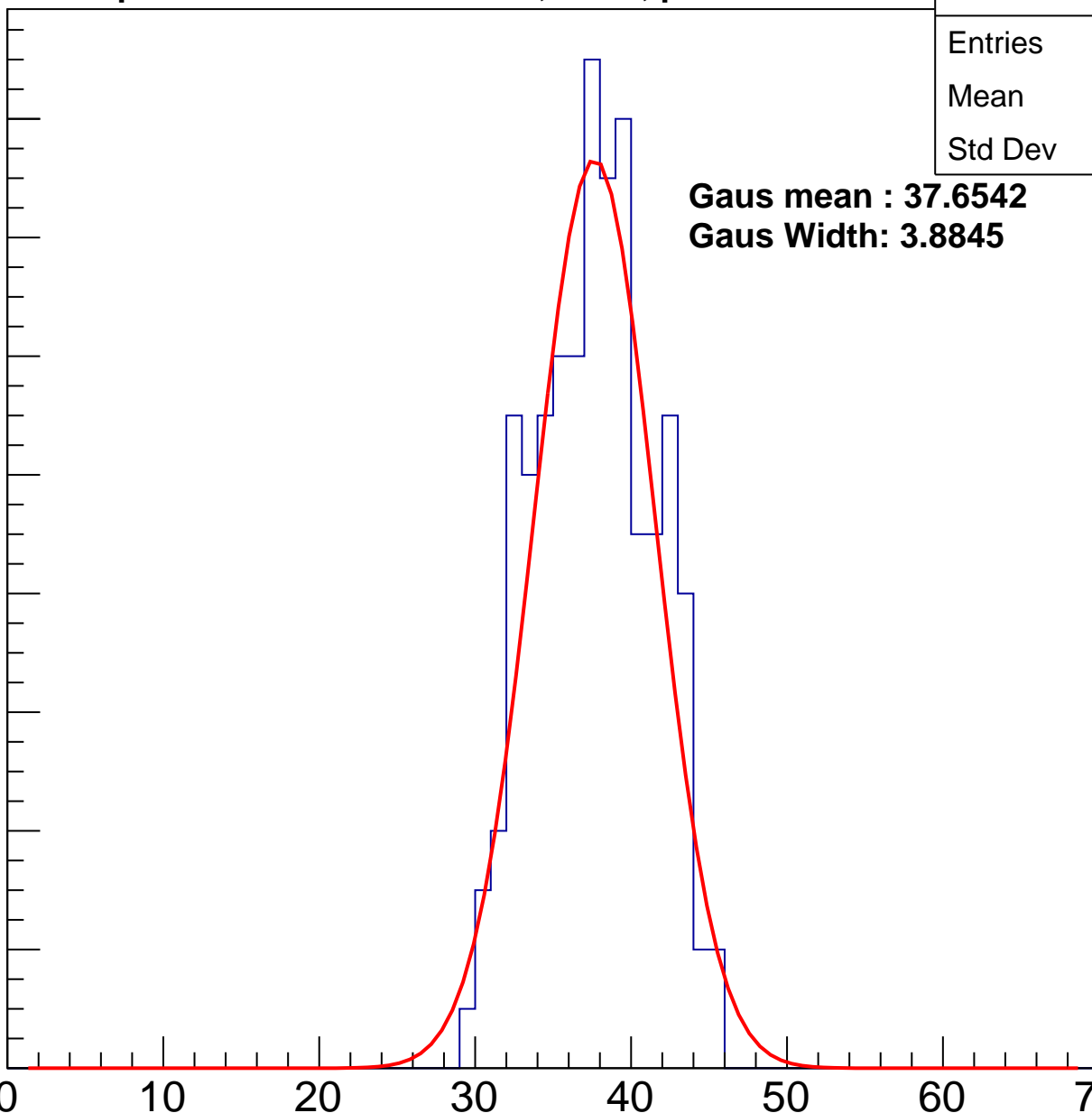
40

50

60

70

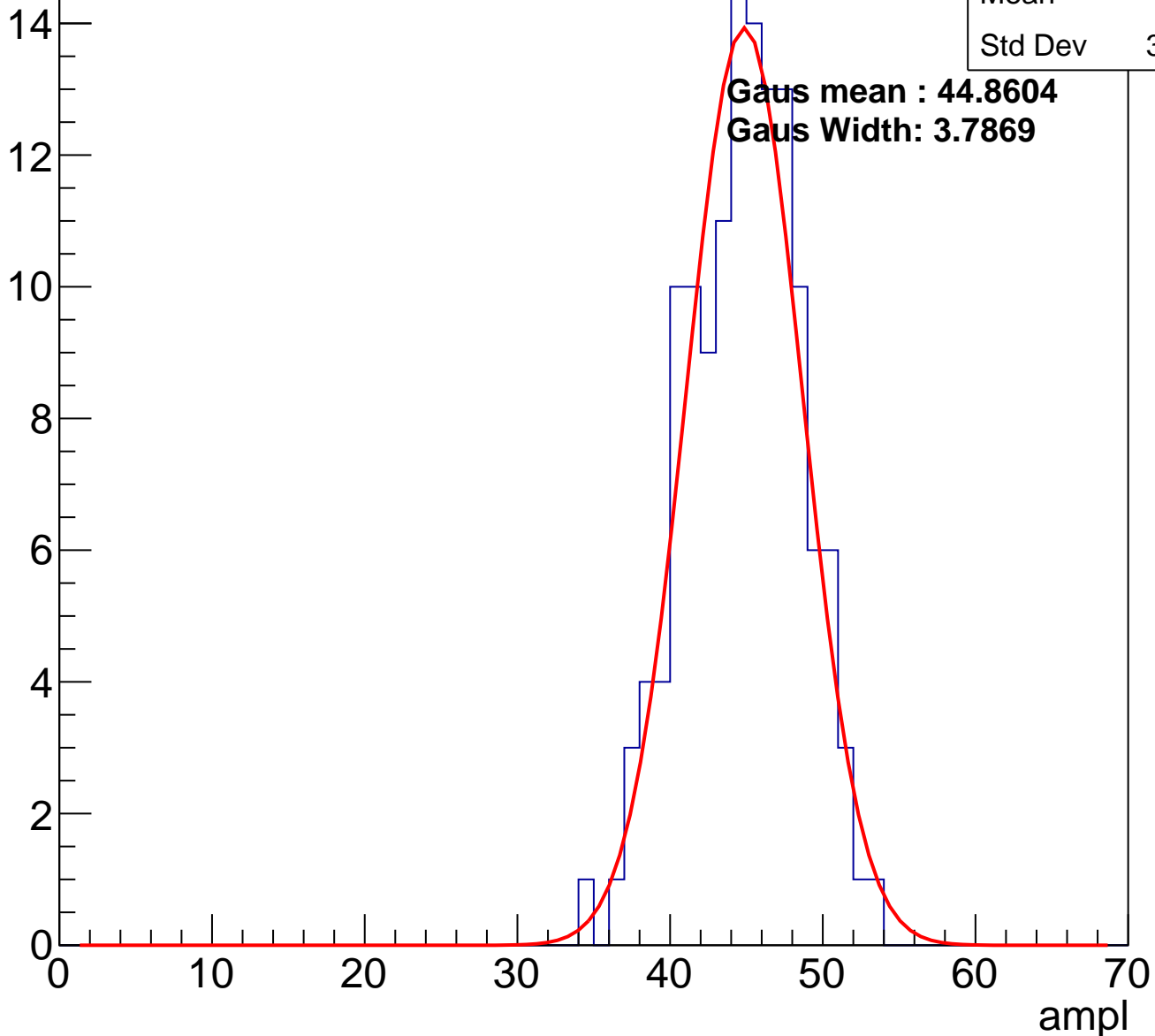
ampl



B1L001S, U22-ch111, adc2

calib_packv5_042523_0143.root, FC#2, port C2

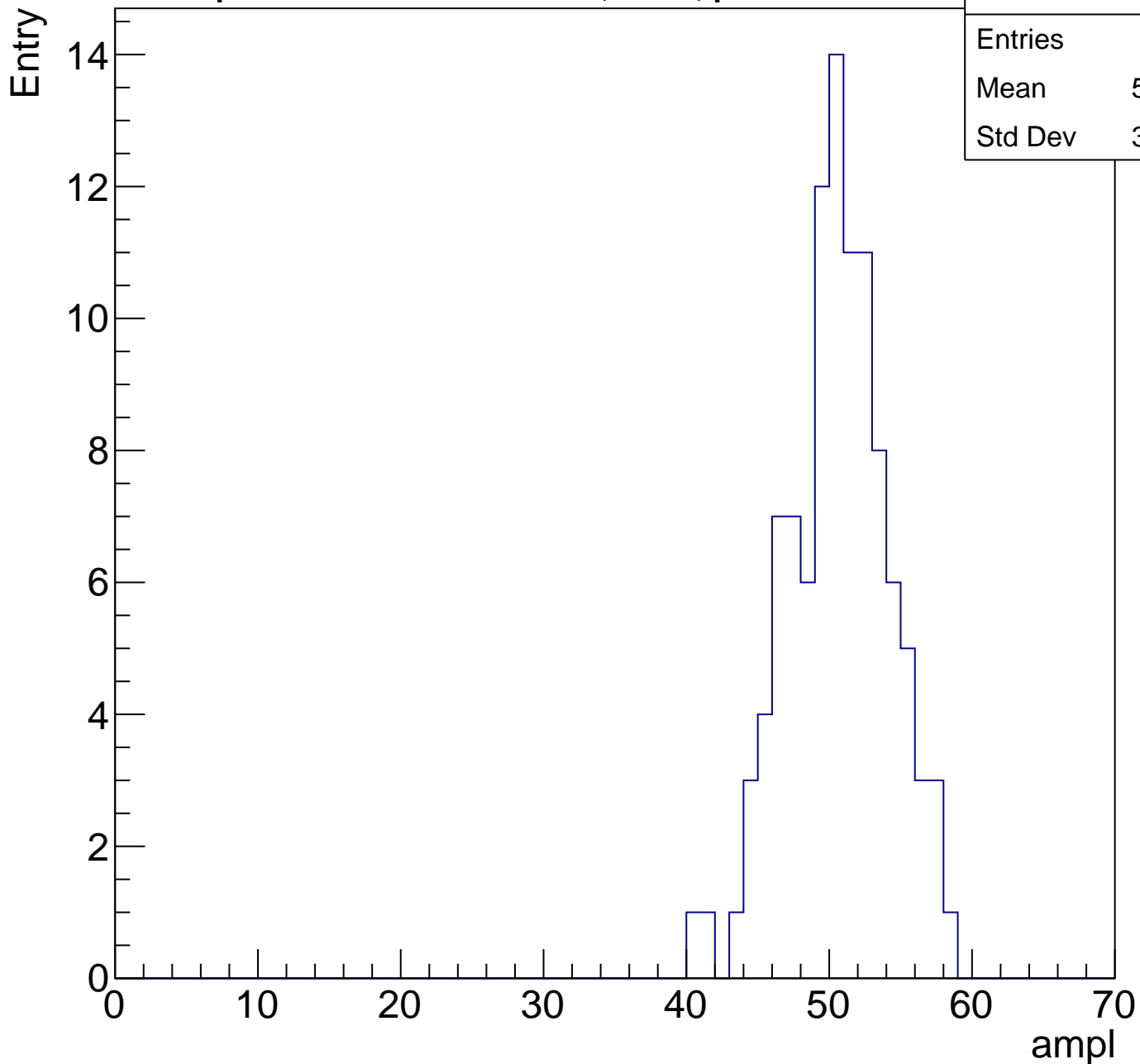
Entry



B1L001S, U22-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	50.15
Std Dev	3.554



B1L001S, U22-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

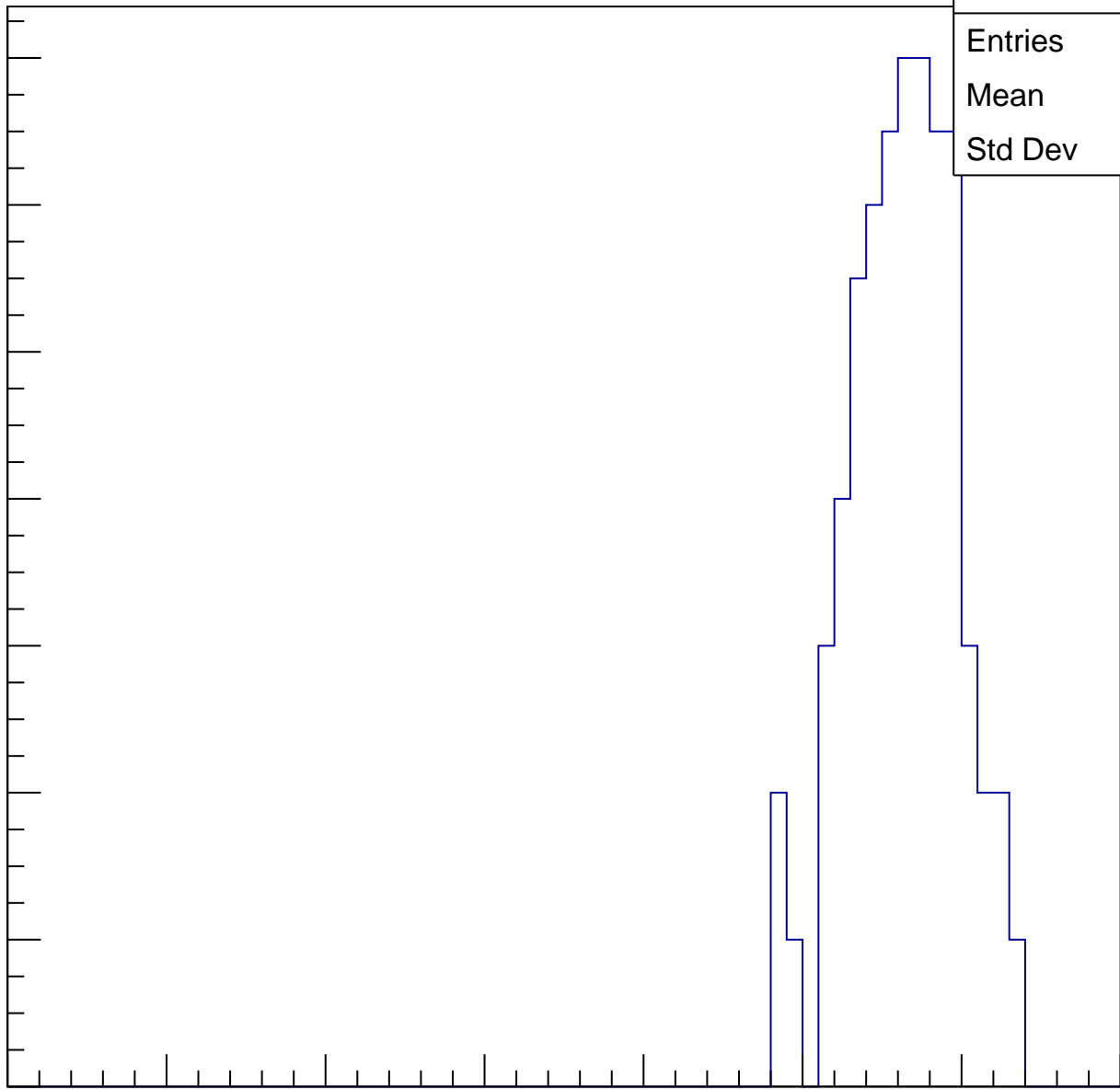
Entries	126
Mean	55.87
Std Dev	3.346

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch111, adc5

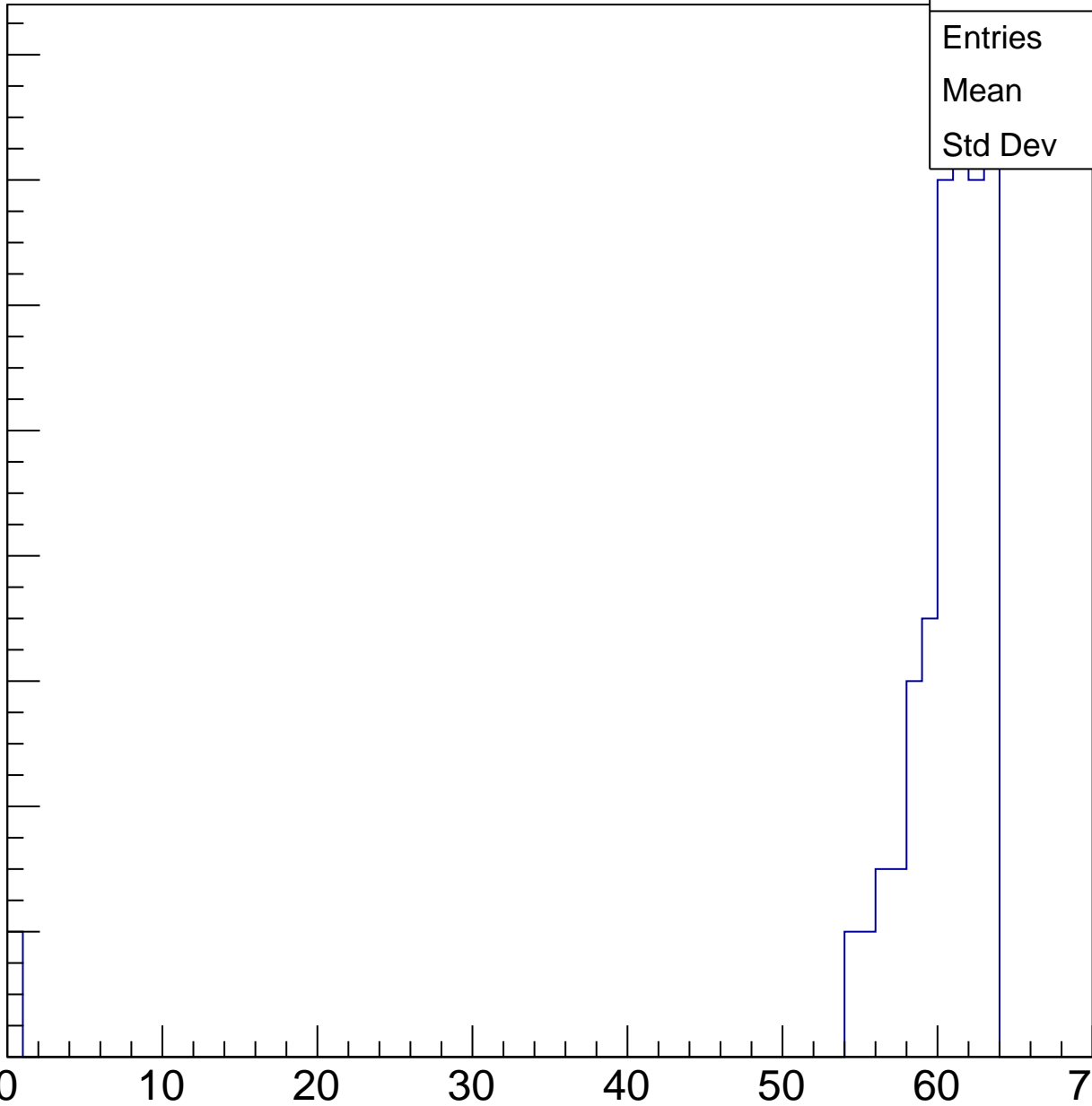
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.89
Std Dev	9.469

ampl



B1L001S, U22-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch112, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	29.26
Std Dev	4.138

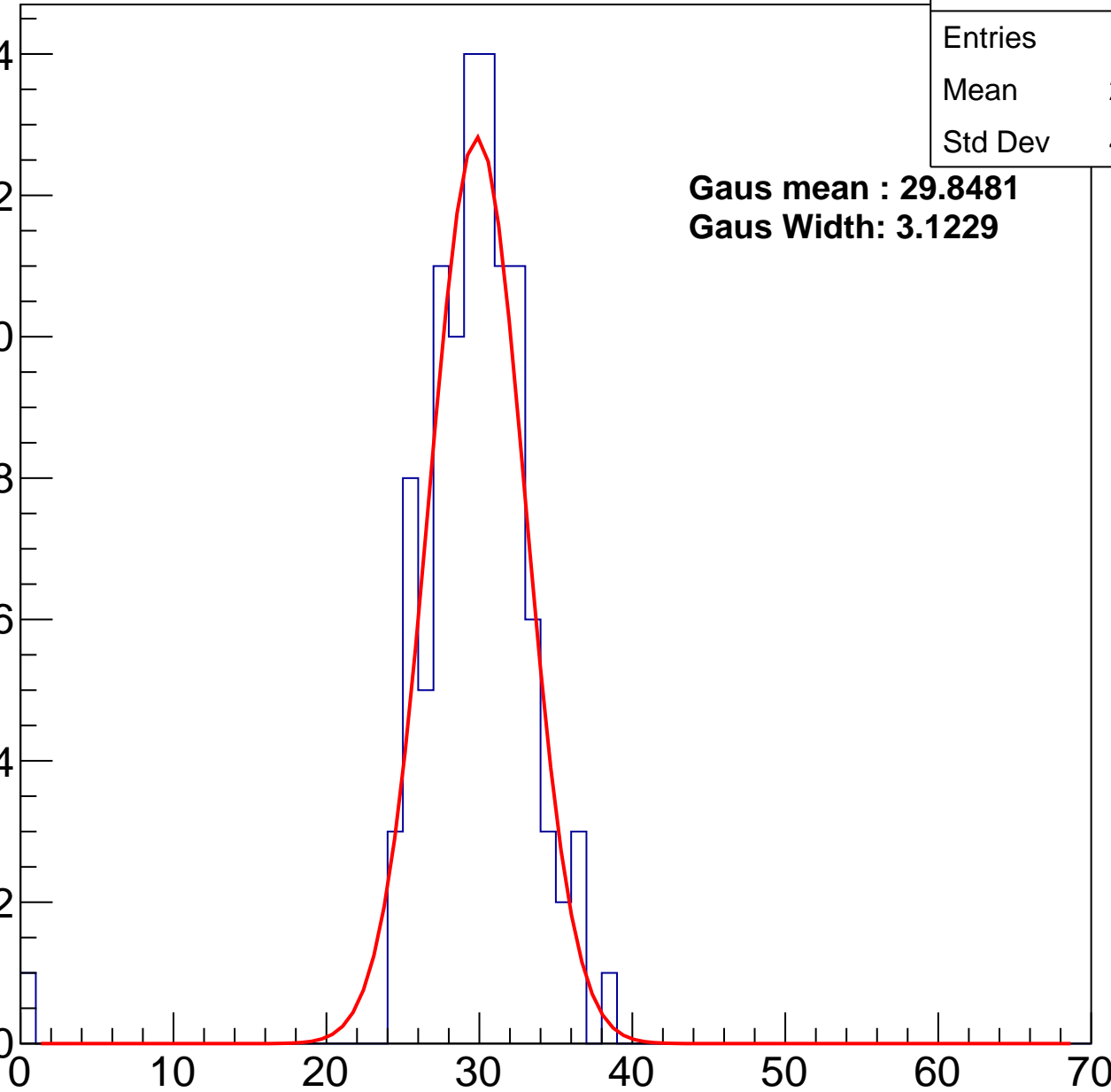
Gaus mean : 29.8481

Gaus Width: 3.1229

Entry

14
12
10
8
6
4
2
0

ampl

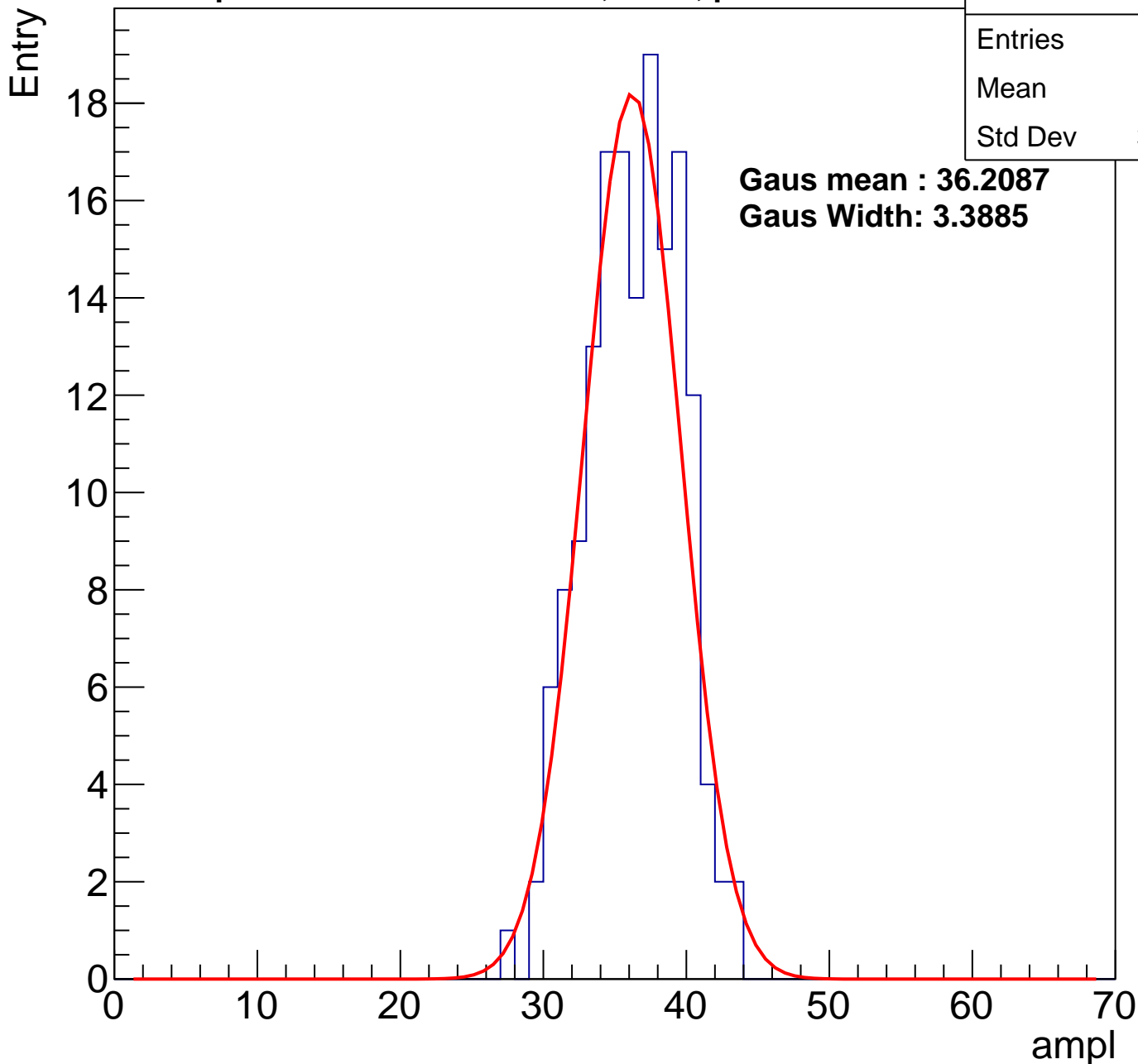


B1L001S, U22-ch112, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	35.8
Std Dev	3.211

Gaus mean : 36.2087
Gaus Width: 3.3885



B1L001S, U22-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	43.23
Std Dev	3.406

Gaus mean : 44.0046

Gaus Width: 3.1931

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

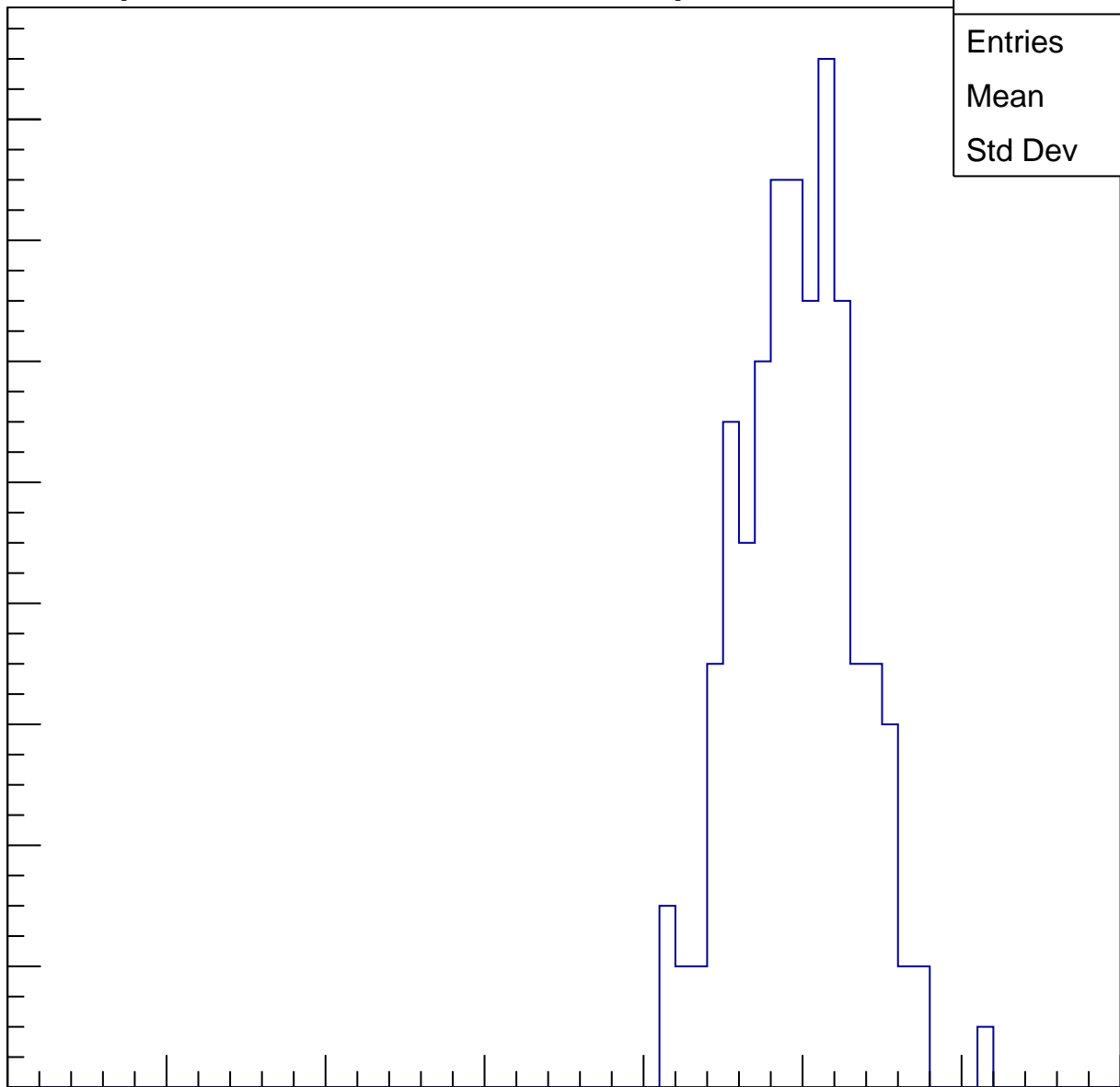
Entries	144
Mean	49.22
Std Dev	3.652

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	55.93
Std Dev	2.996

Entry

14

12

10

8

6

4

2

0

0

10

20

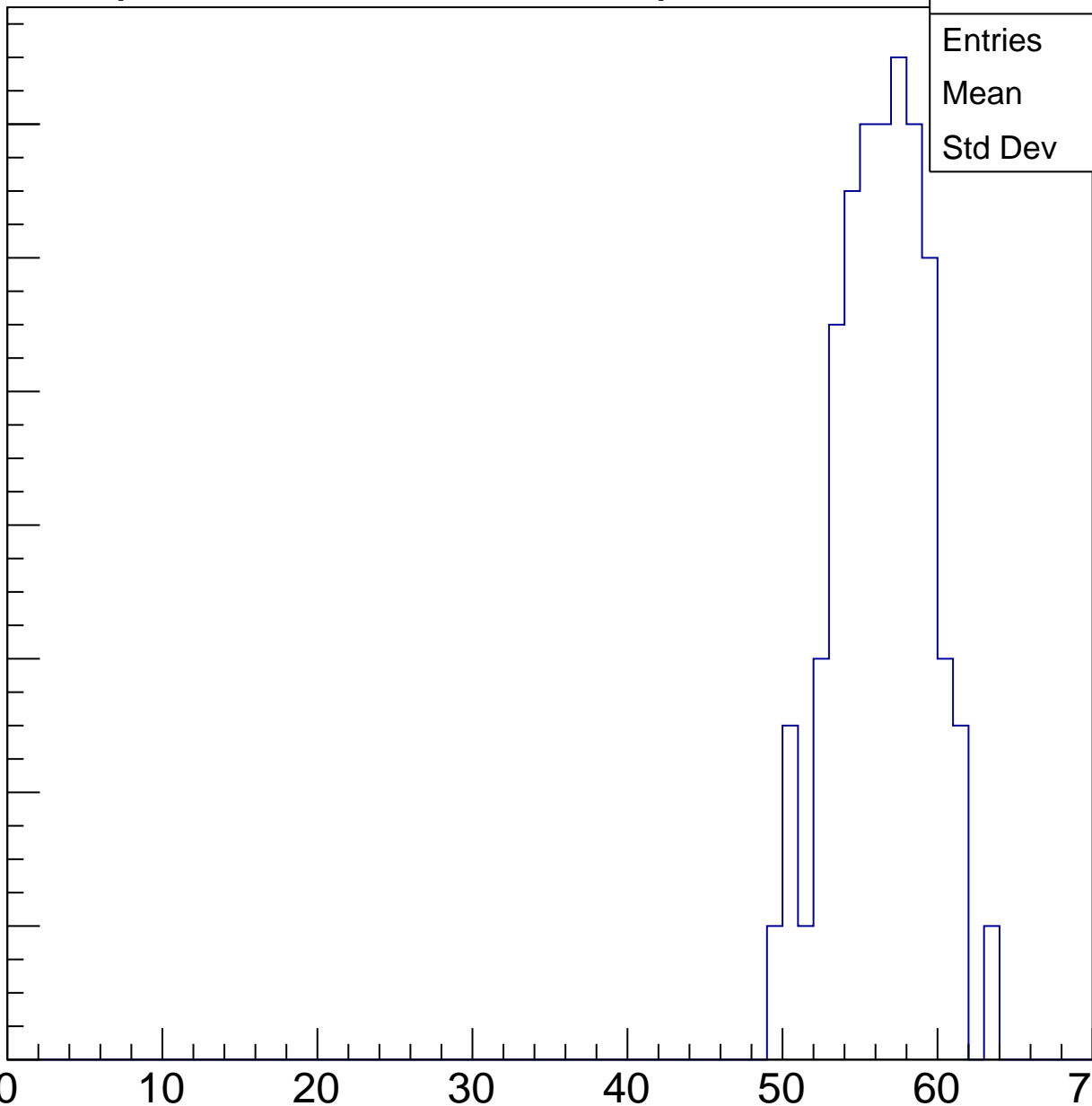
30

40

50

60

ampl



B1L001S, U22-ch112, adc5

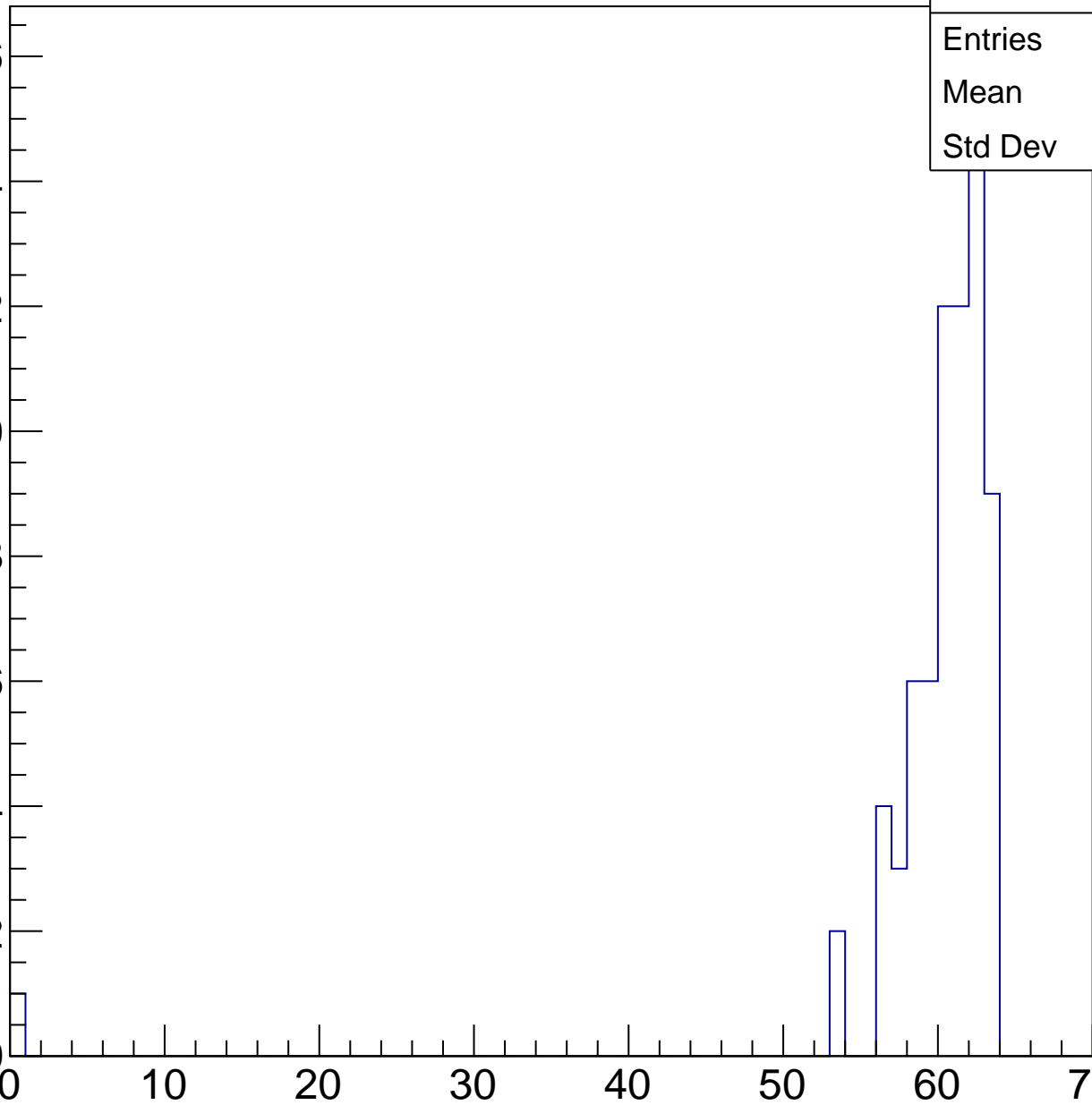
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.35
Std Dev	7.455

ampl

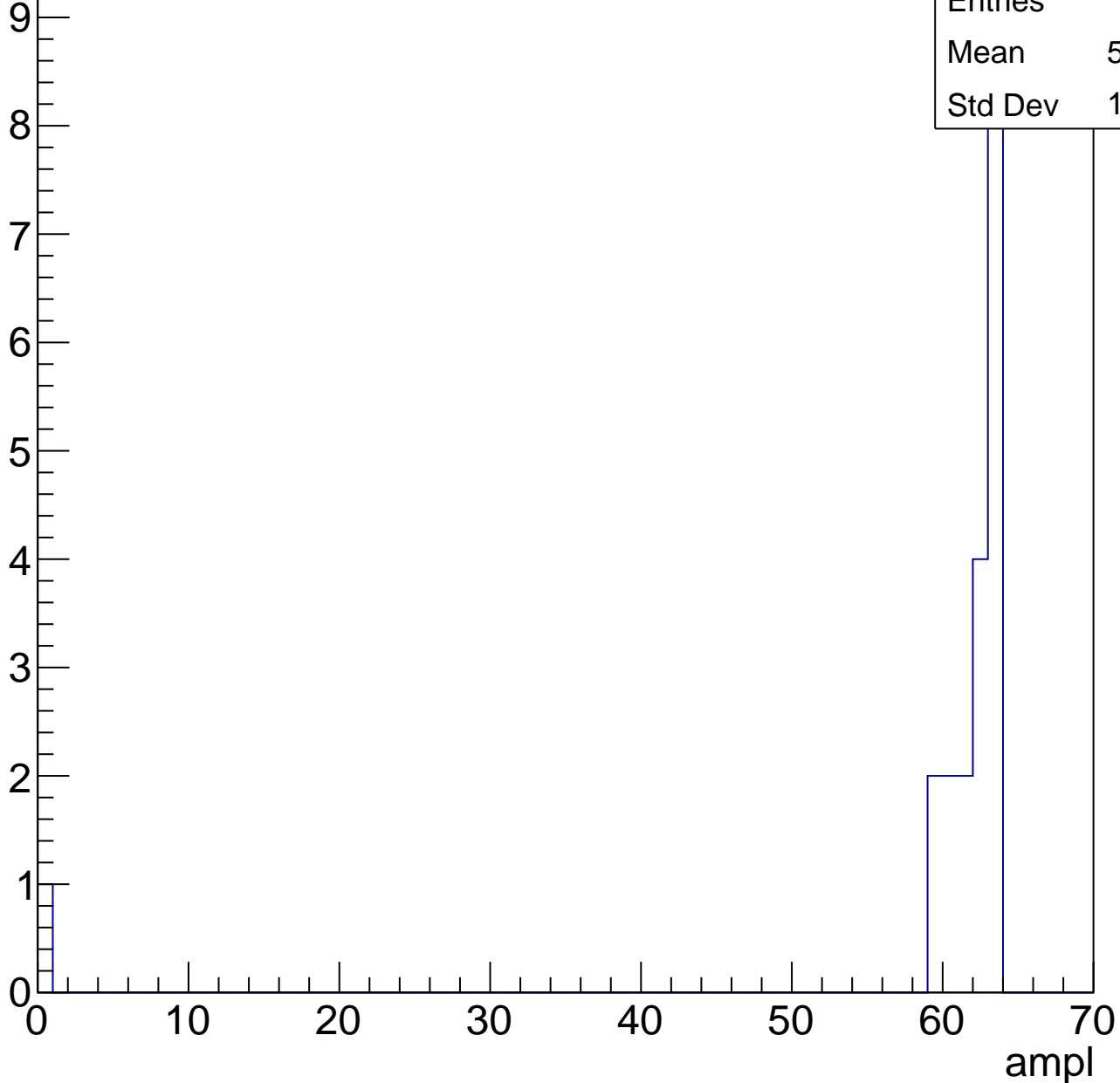


B1L001S, U22-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	58.75
Std Dev	13.55



B1L001S, U22-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	30.41
Std Dev	4.156

Gaus mean : 31.1129

Gaus Width: 3.2197

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

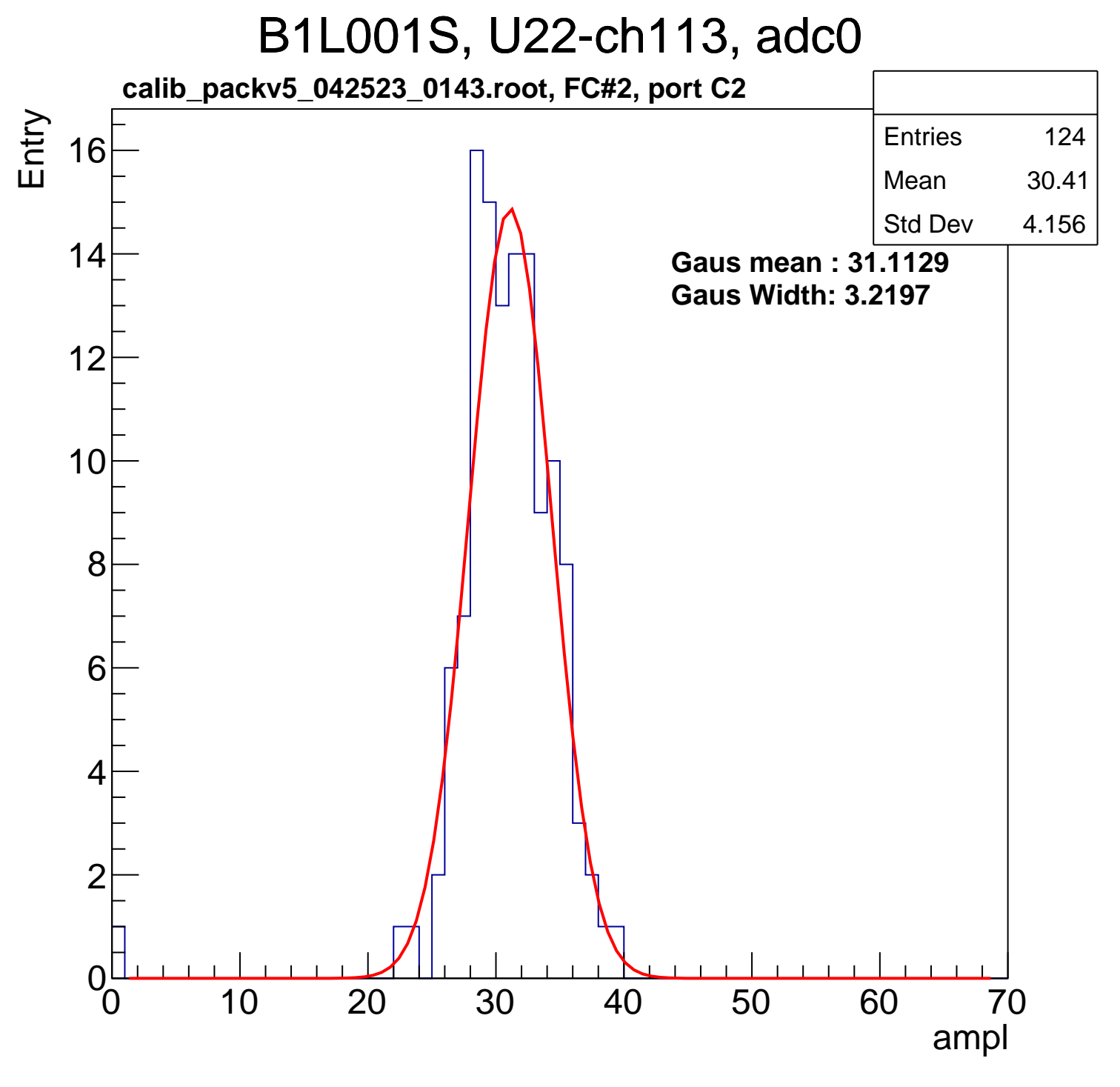
30

40

50

60

70



B1L001S, U22-ch113, adc1

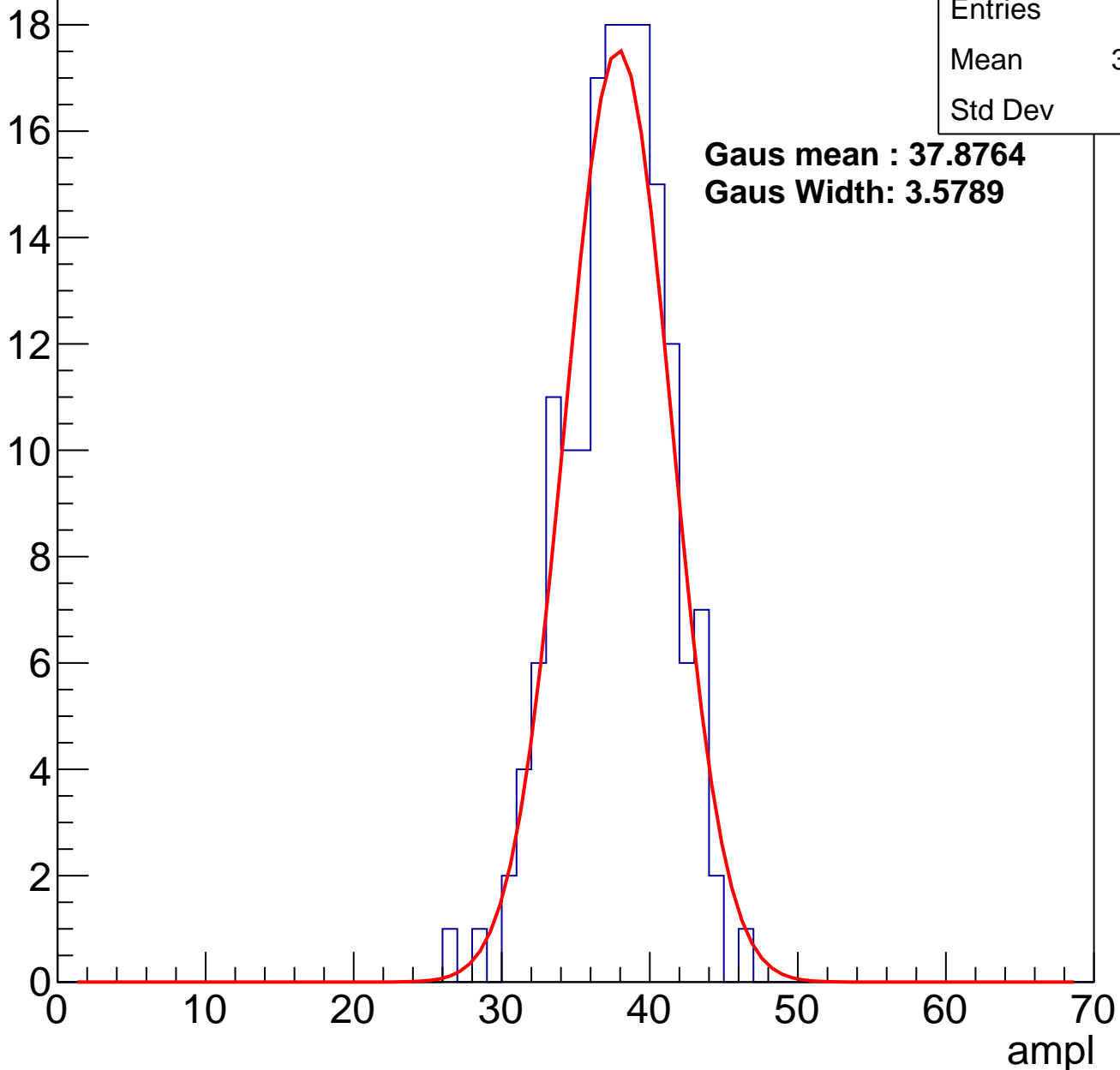
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.27
Std Dev	3.46

Gaus mean : 37.8764

Gaus Width: 3.5789

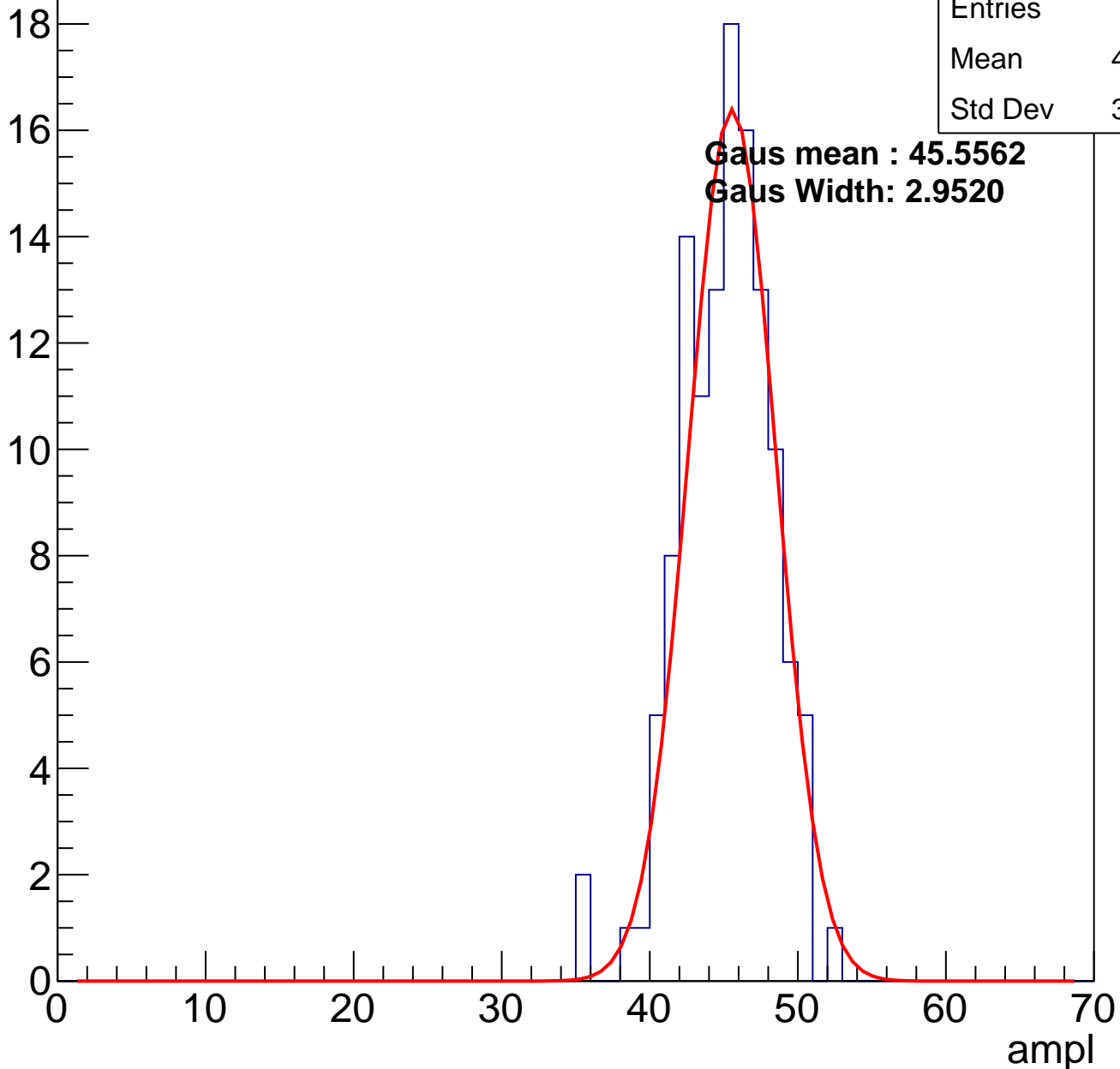
Entry



B1L001S, U22-ch113, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch113, adc3

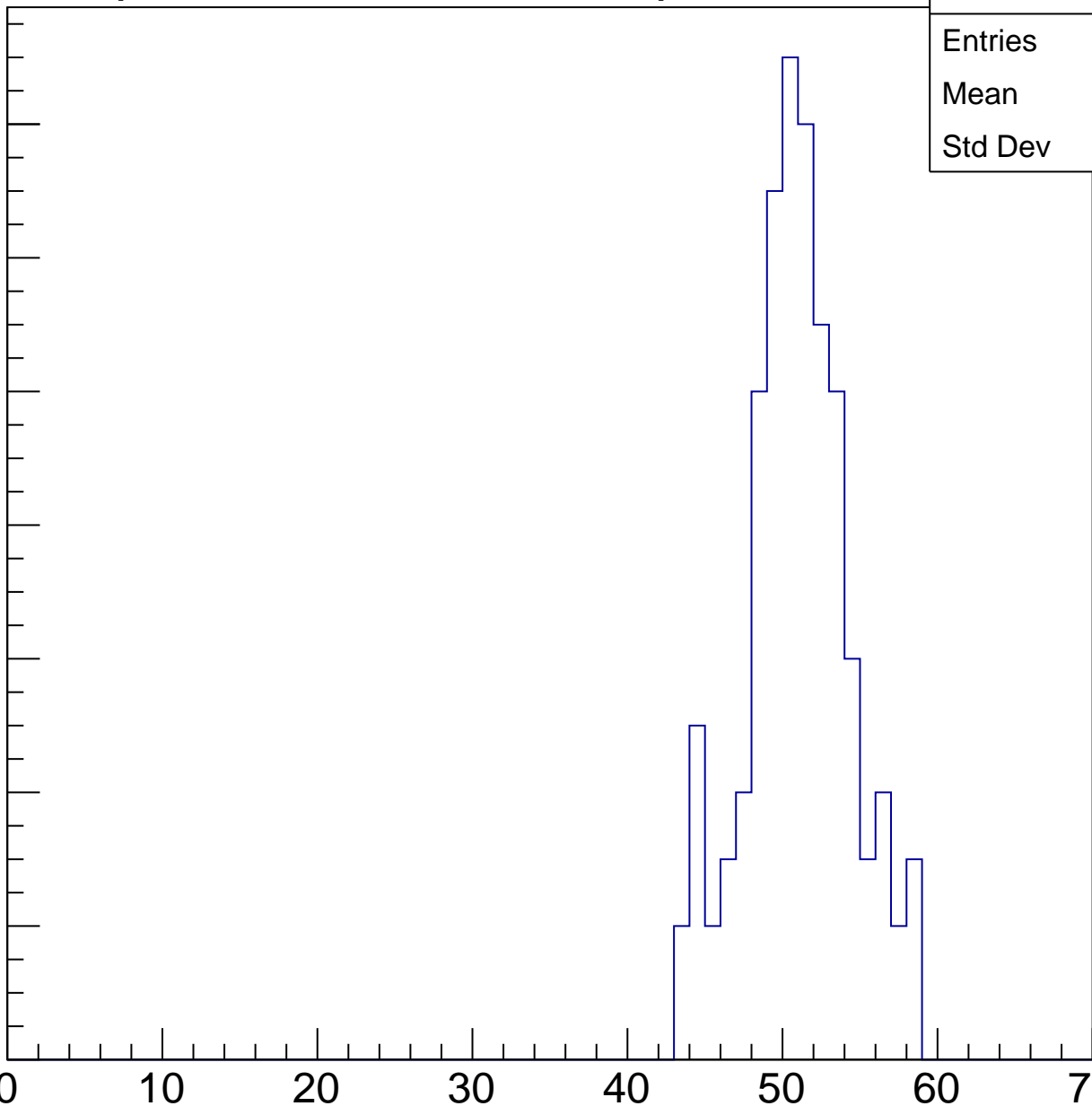
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	50.52
Std Dev	3.333

ampl

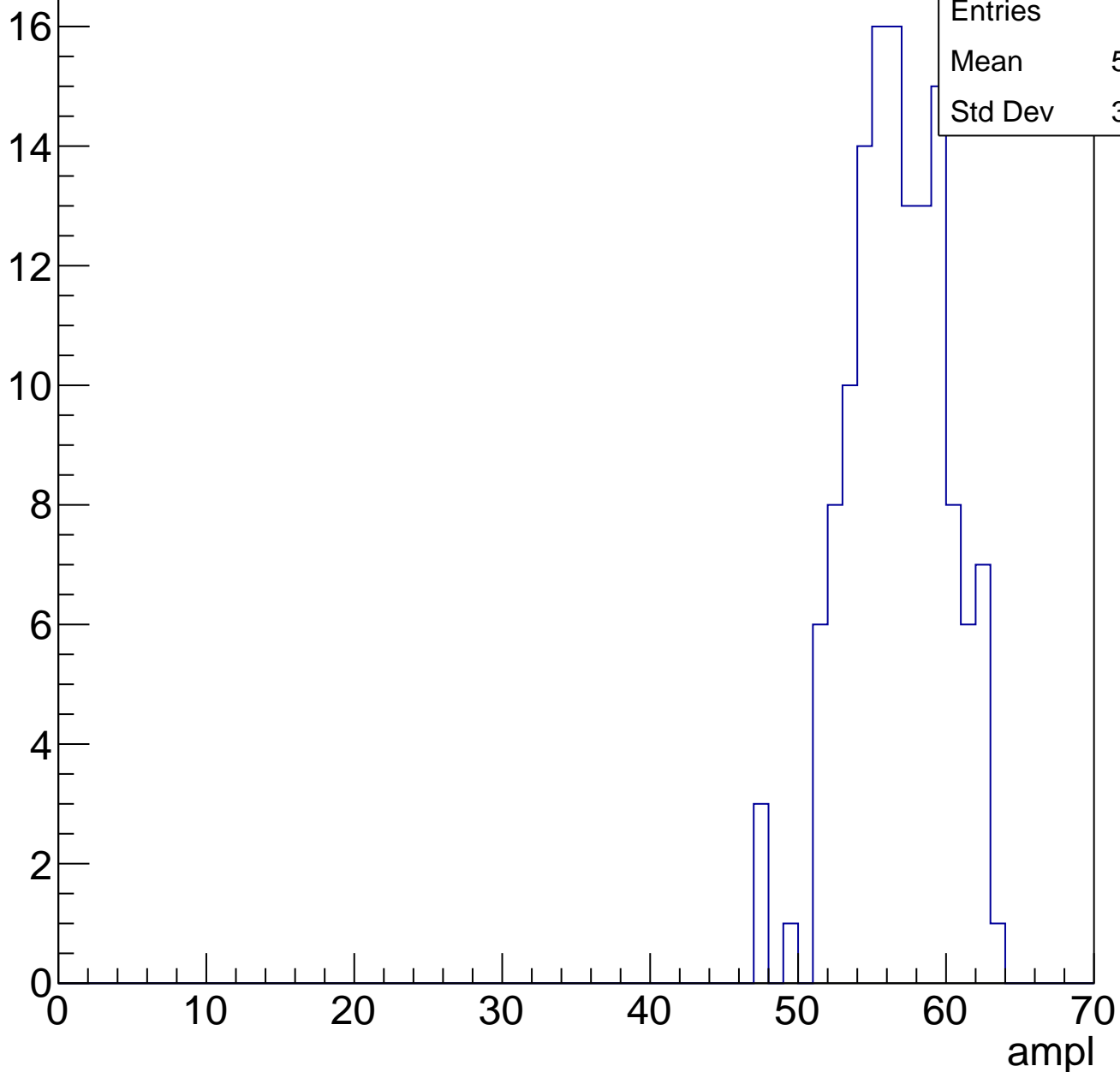


B1L001S, U22-ch113, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	56.18
Std Dev	3.316

Entry

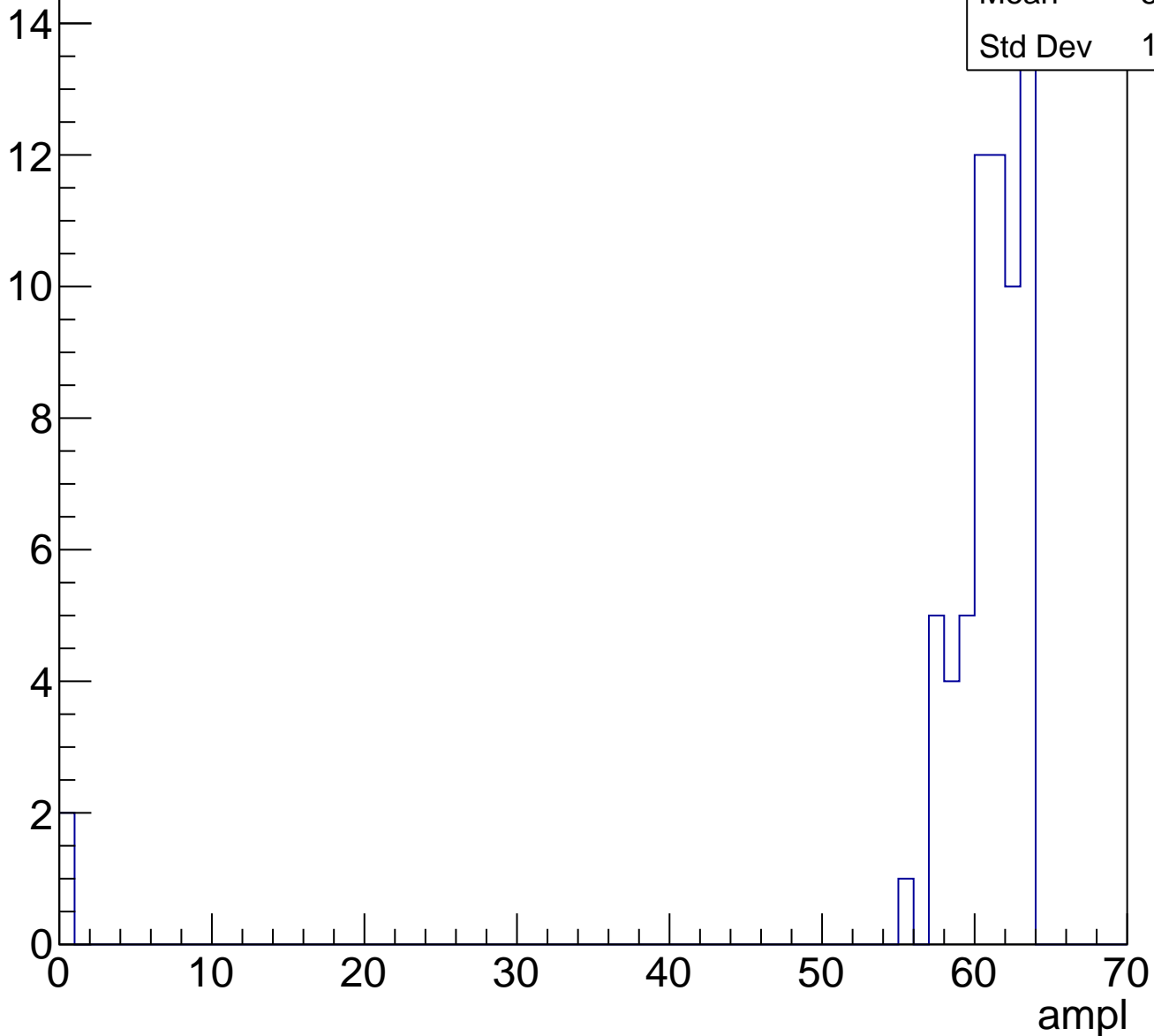


B1L001S, U22-ch113, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	66
Mean	58.85
Std Dev	10.58

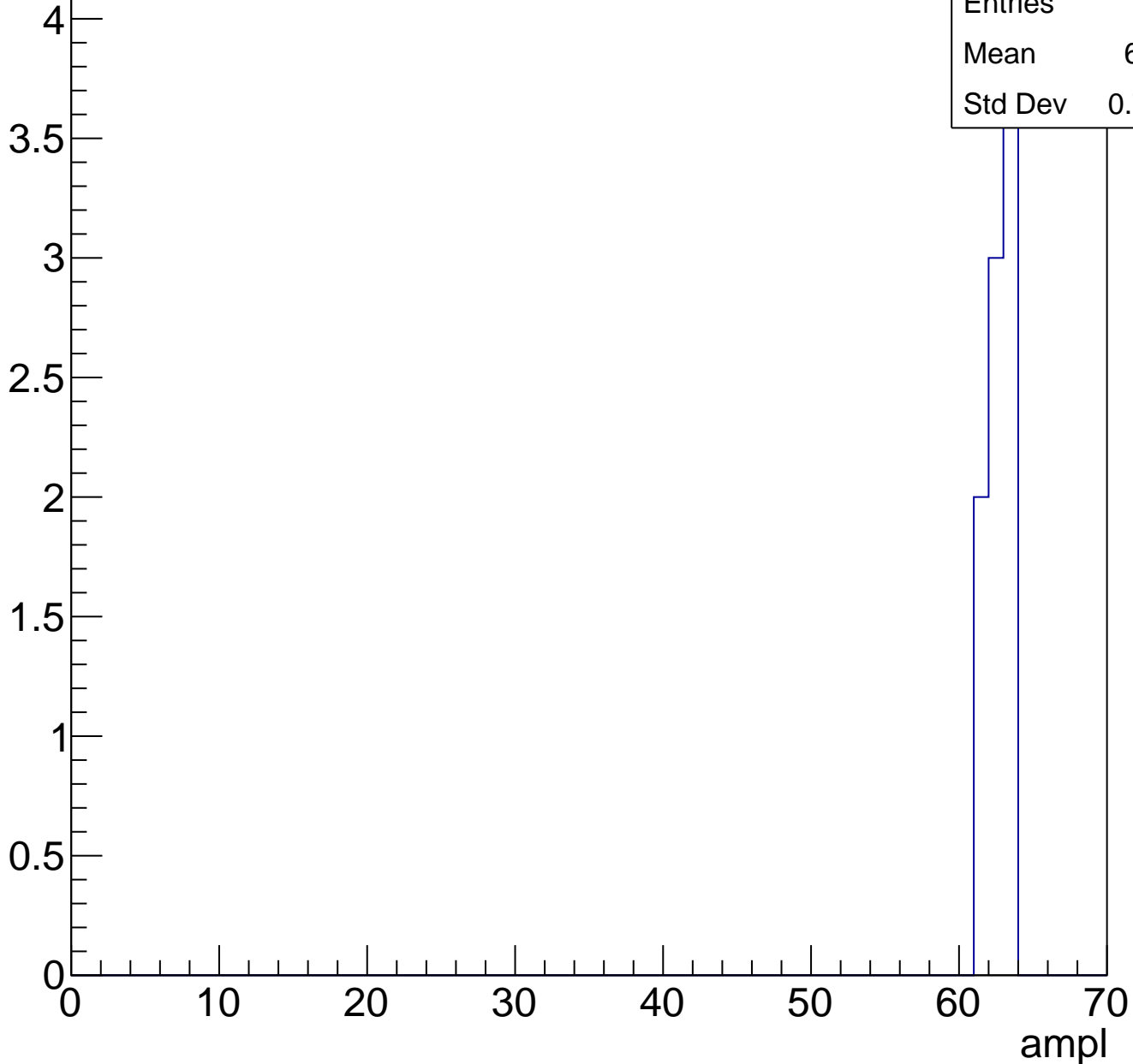
Entry



B1L001S, U22-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch114, adc0

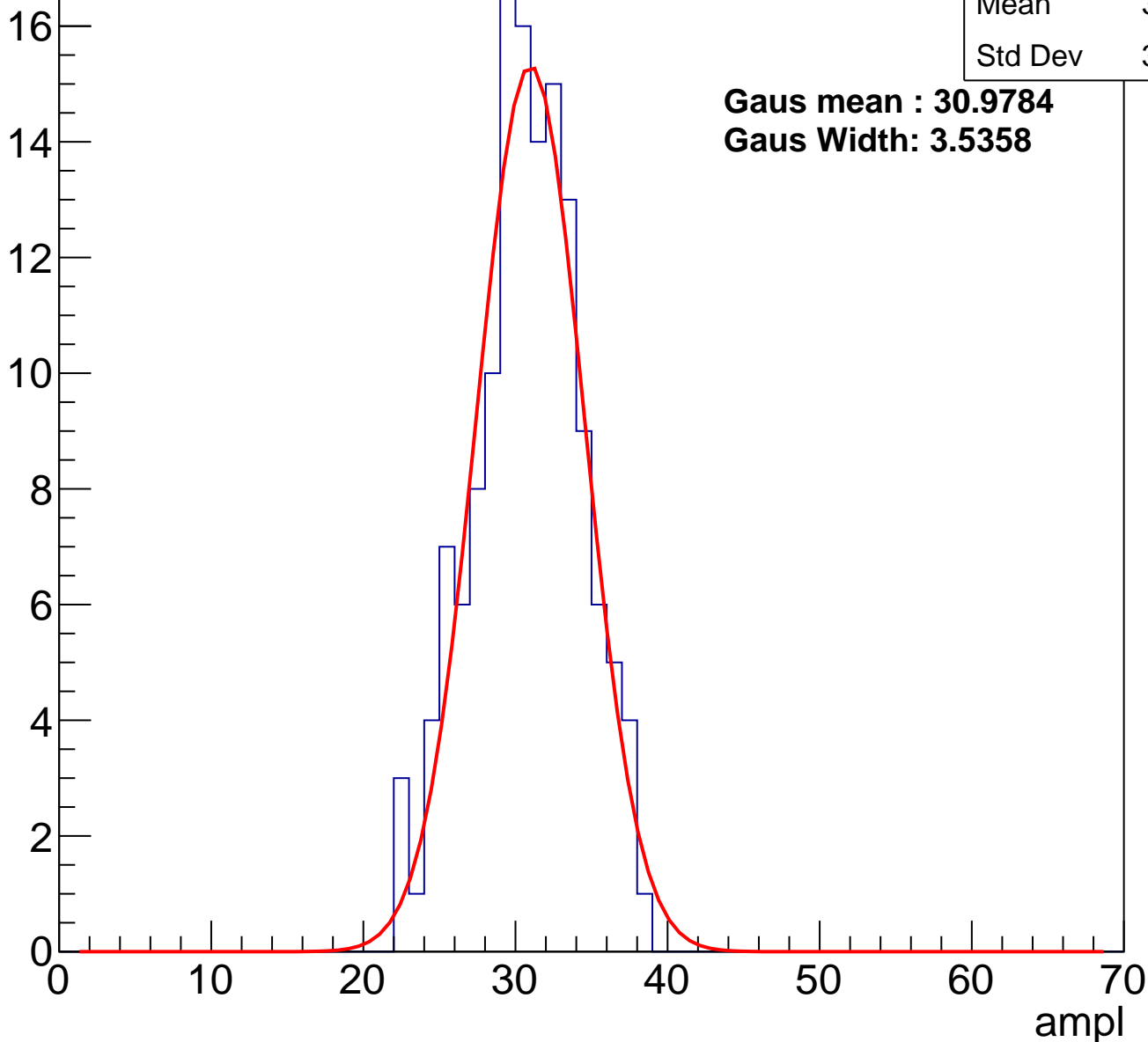
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	139
Mean	30.29
Std Dev	3.496

Gaus mean : 30.9784

Gaus Width: 3.5358



B1L001S, U22-ch114, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.22
Std Dev	3.316

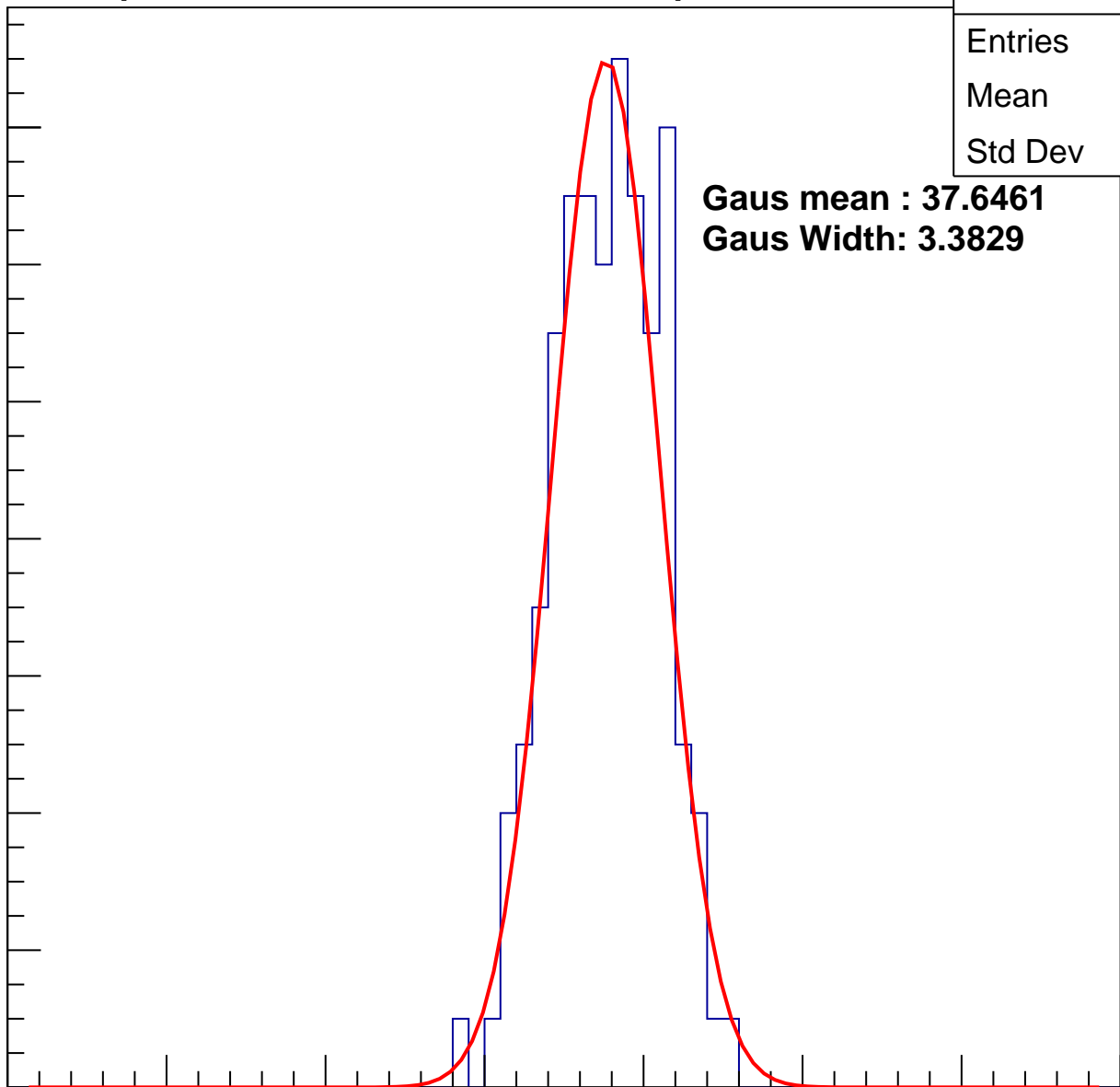
Gaus mean : 37.6461
Gaus Width: 3.3829

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

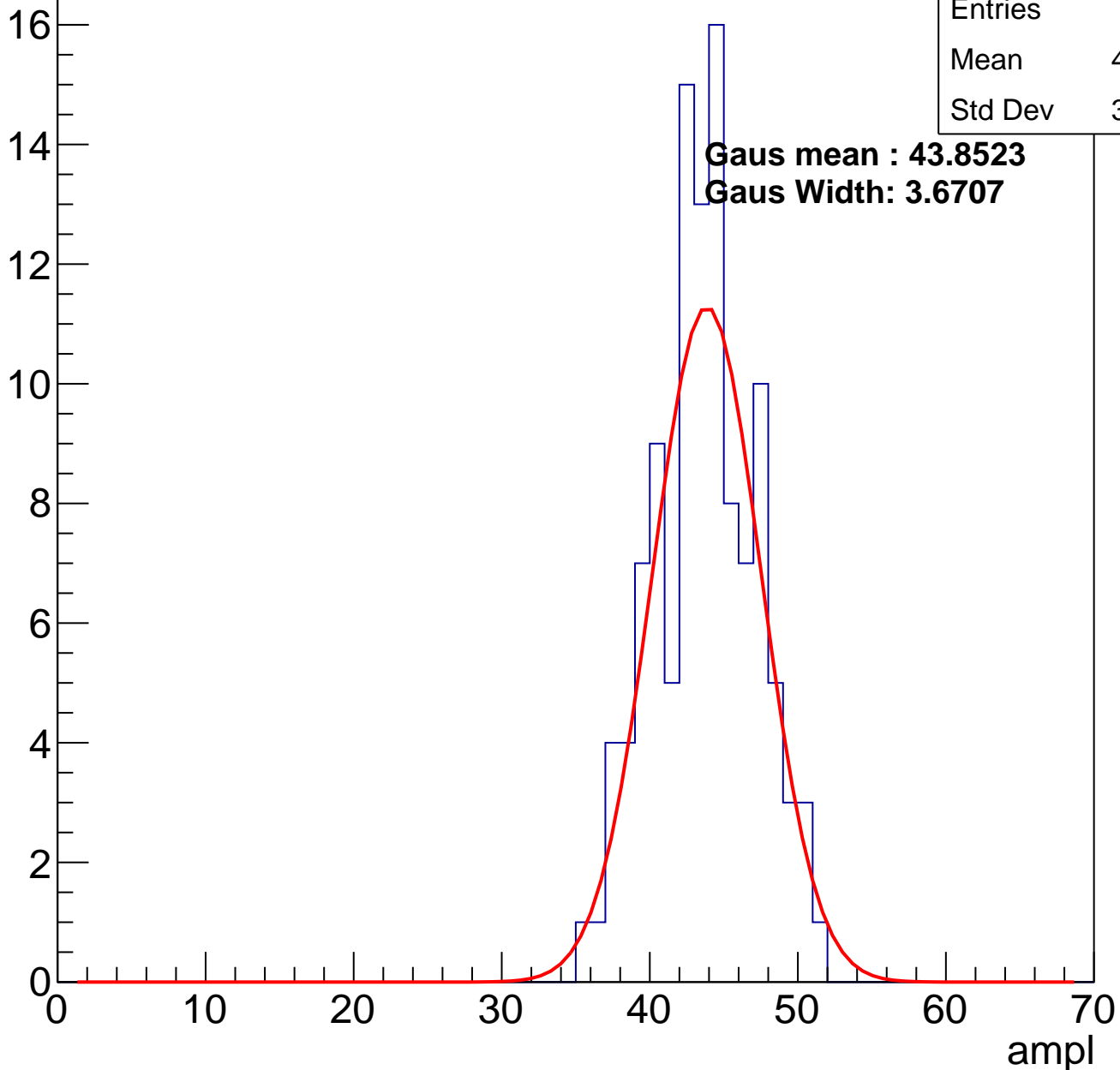
ampl



B1L001S, U22-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

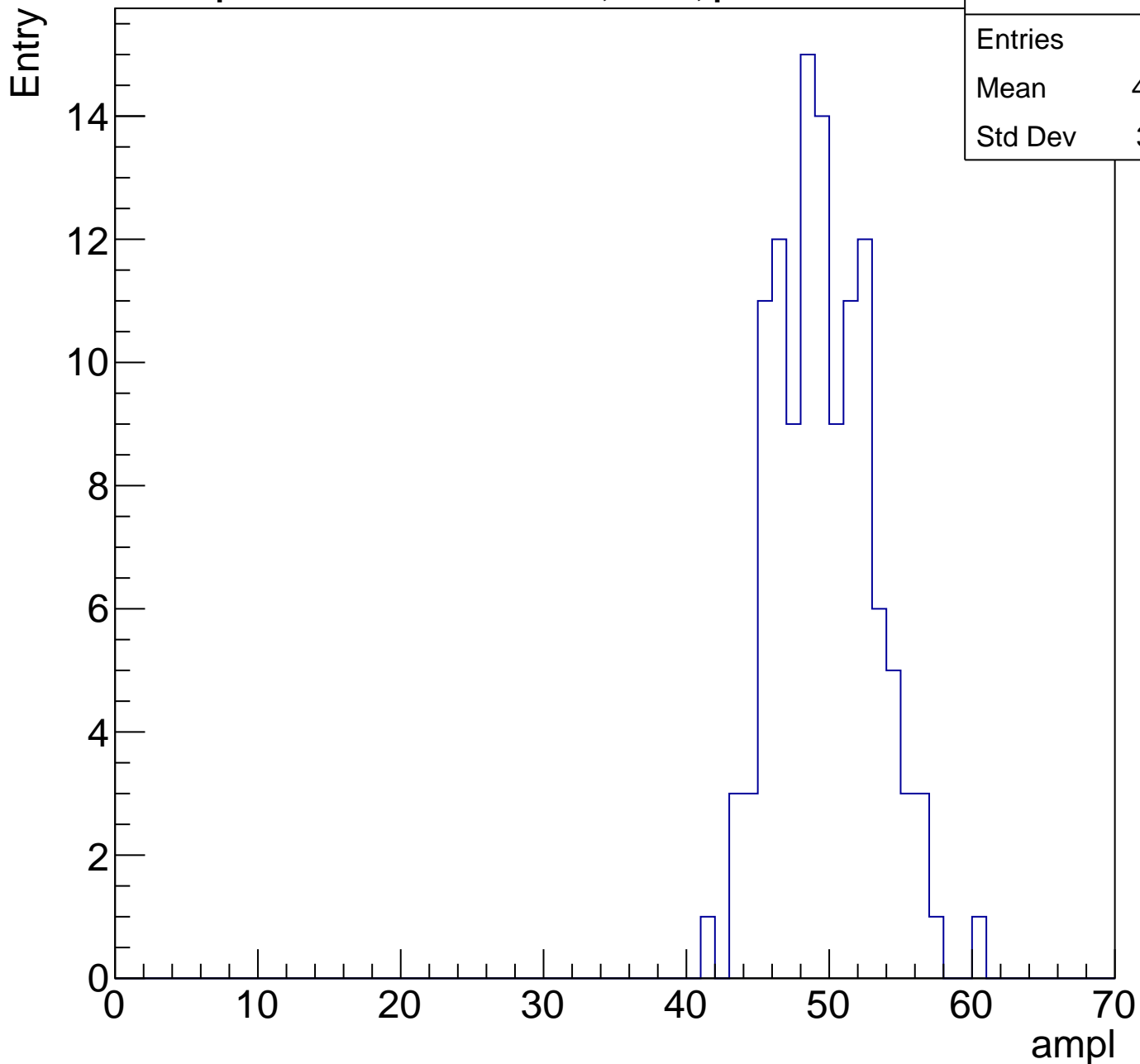
Entry



B1L001S, U22-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

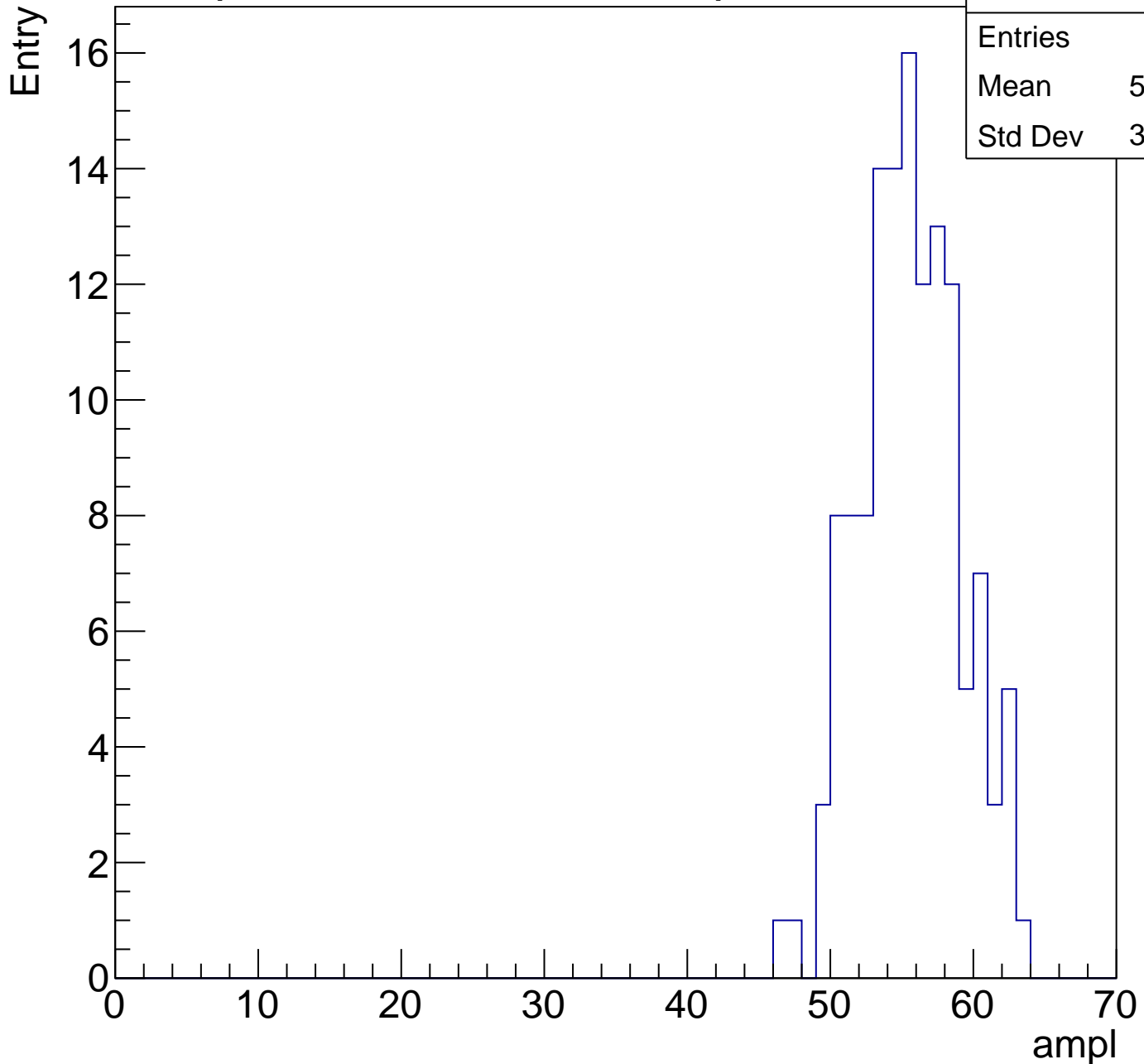
Entries	119
Mean	49.17
Std Dev	3.431



B1L001S, U22-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	55.13
Std Dev	3.456



B1L001S, U22-ch114, adc5

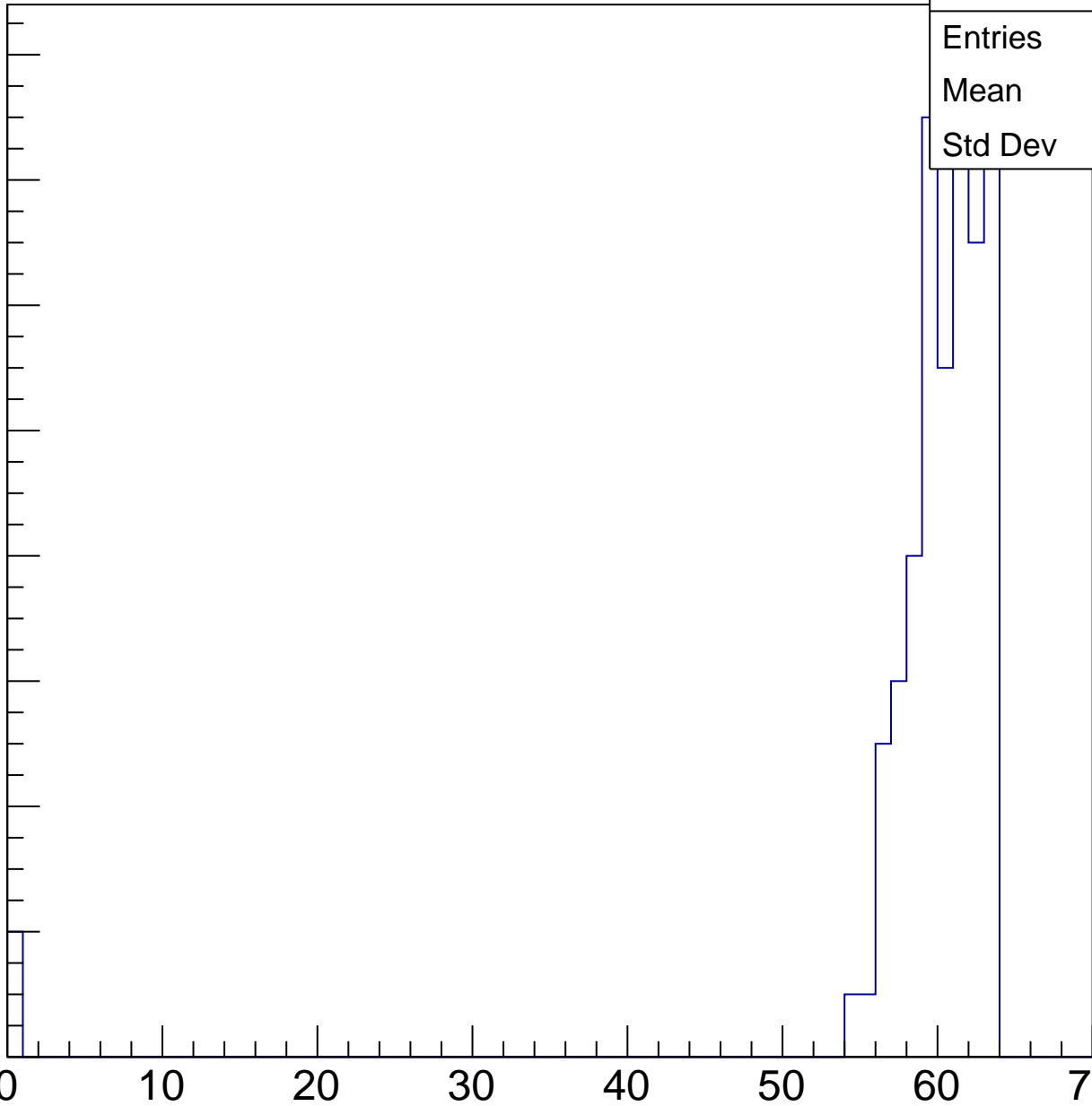
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.81
Std Dev	8.993

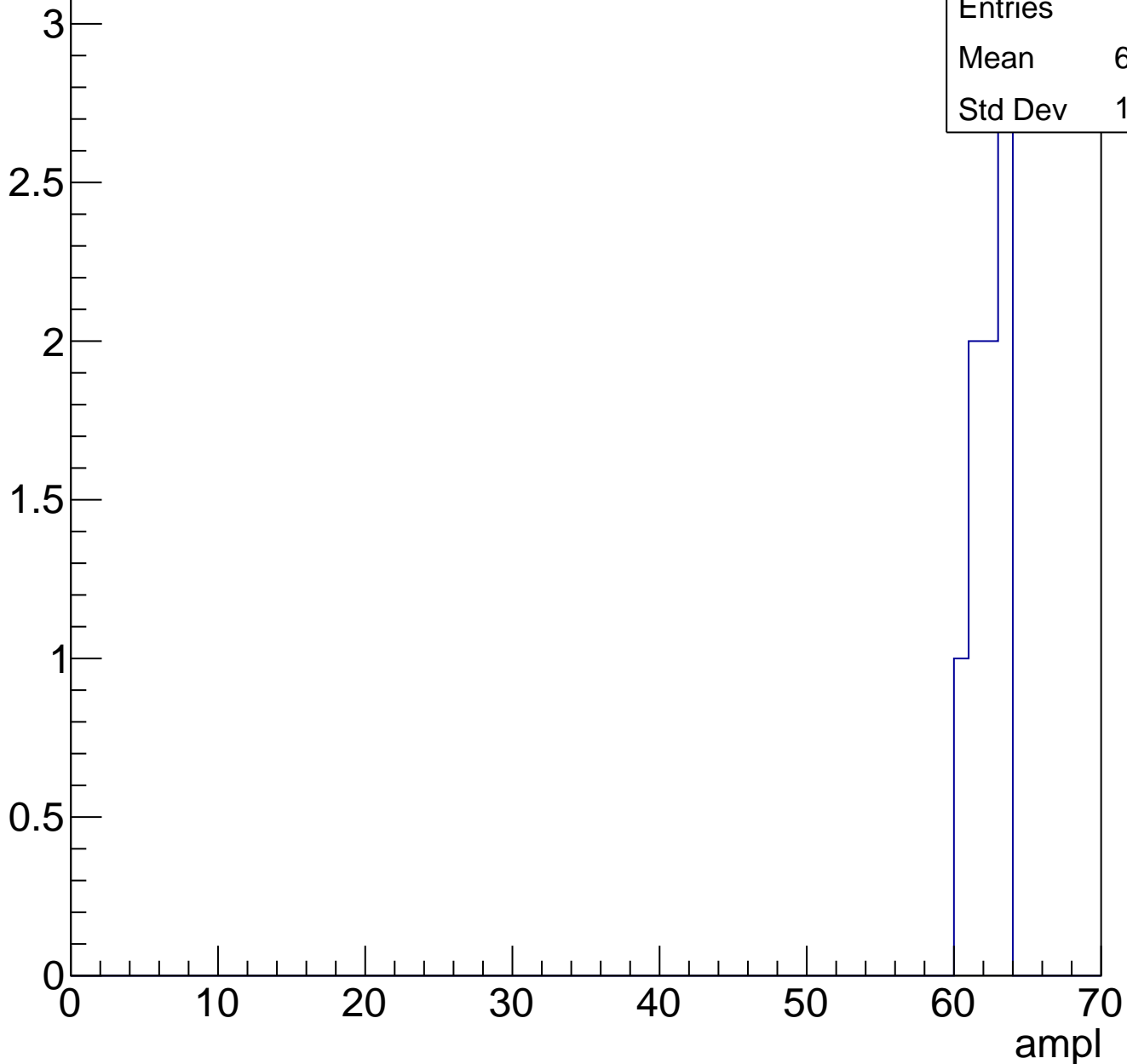
ampl



B1L001S, U22-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	8
Mean	61.88
Std Dev	1.053

B1L001S, U22-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch115, adc0

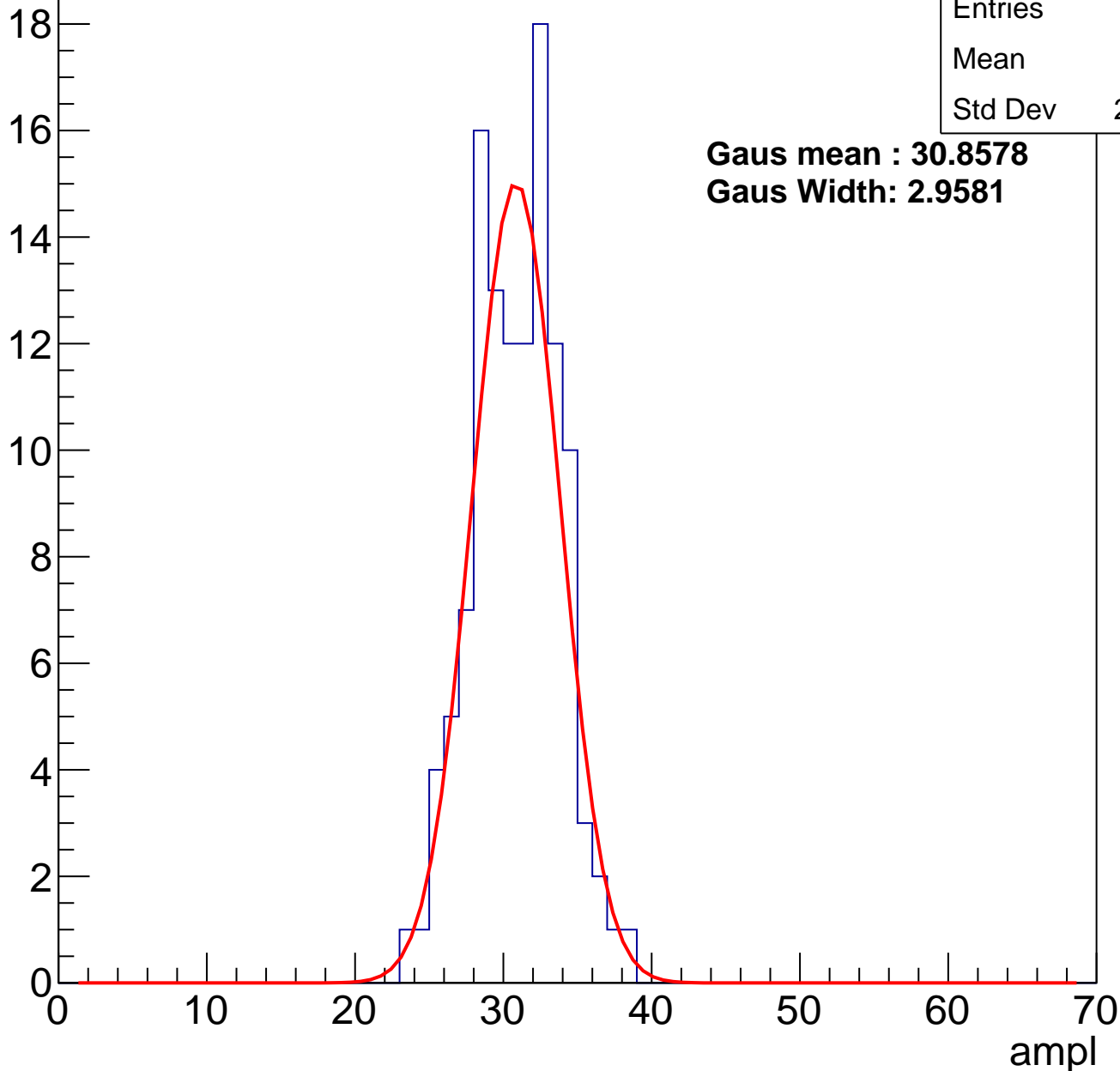
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	30.4
Std Dev	2.909

Gaus mean : 30.8578

Gaus Width: 2.9581

Entry



B1L001S, U22-ch115, adc1

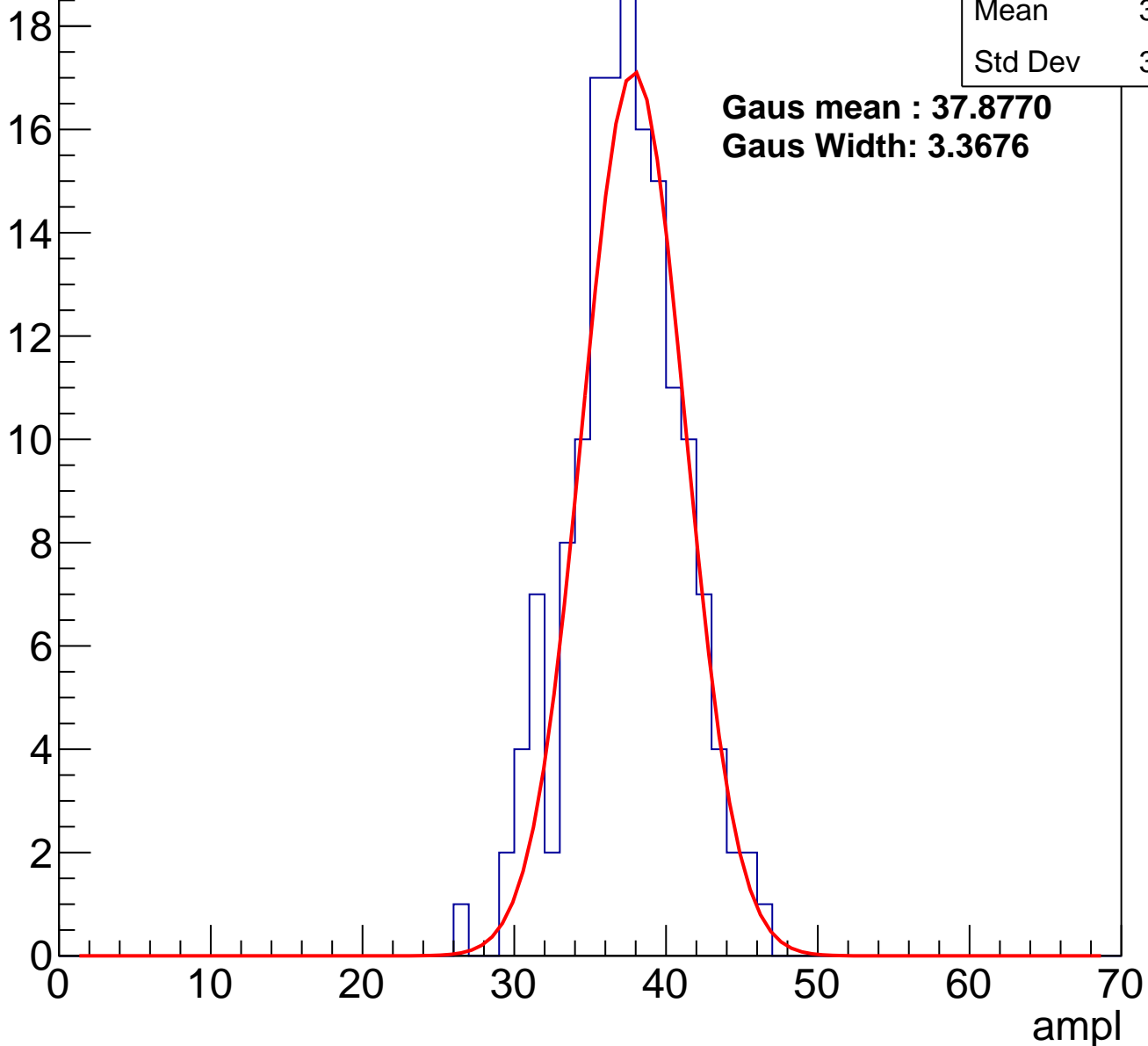
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.98
Std Dev	3.602

Gaus mean : 37.8770

Gaus Width: 3.3676

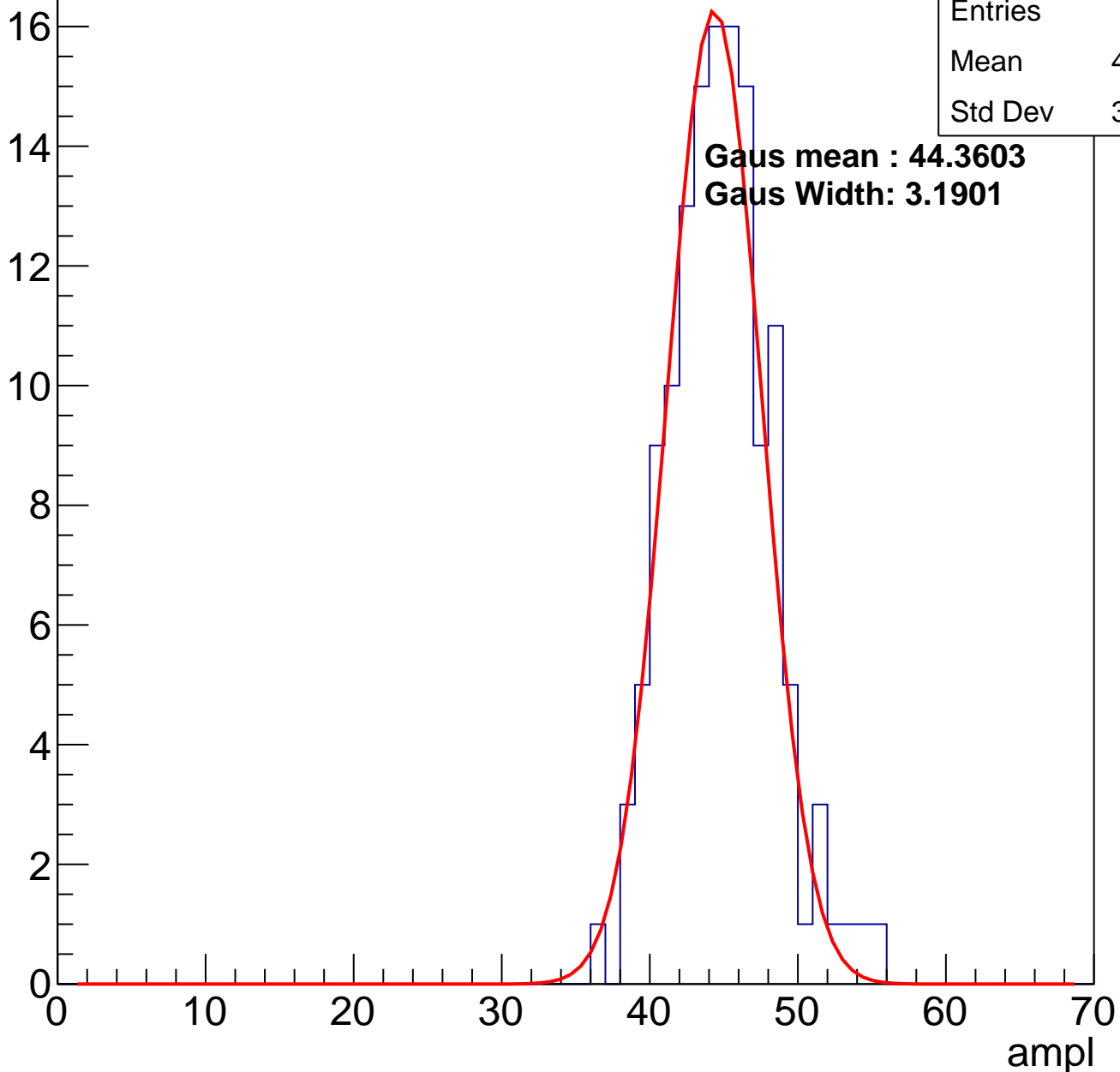
Entry



B1L001S, U22-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	136
Mean	44.36
Std Dev	3.416

B1L001S, U22-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

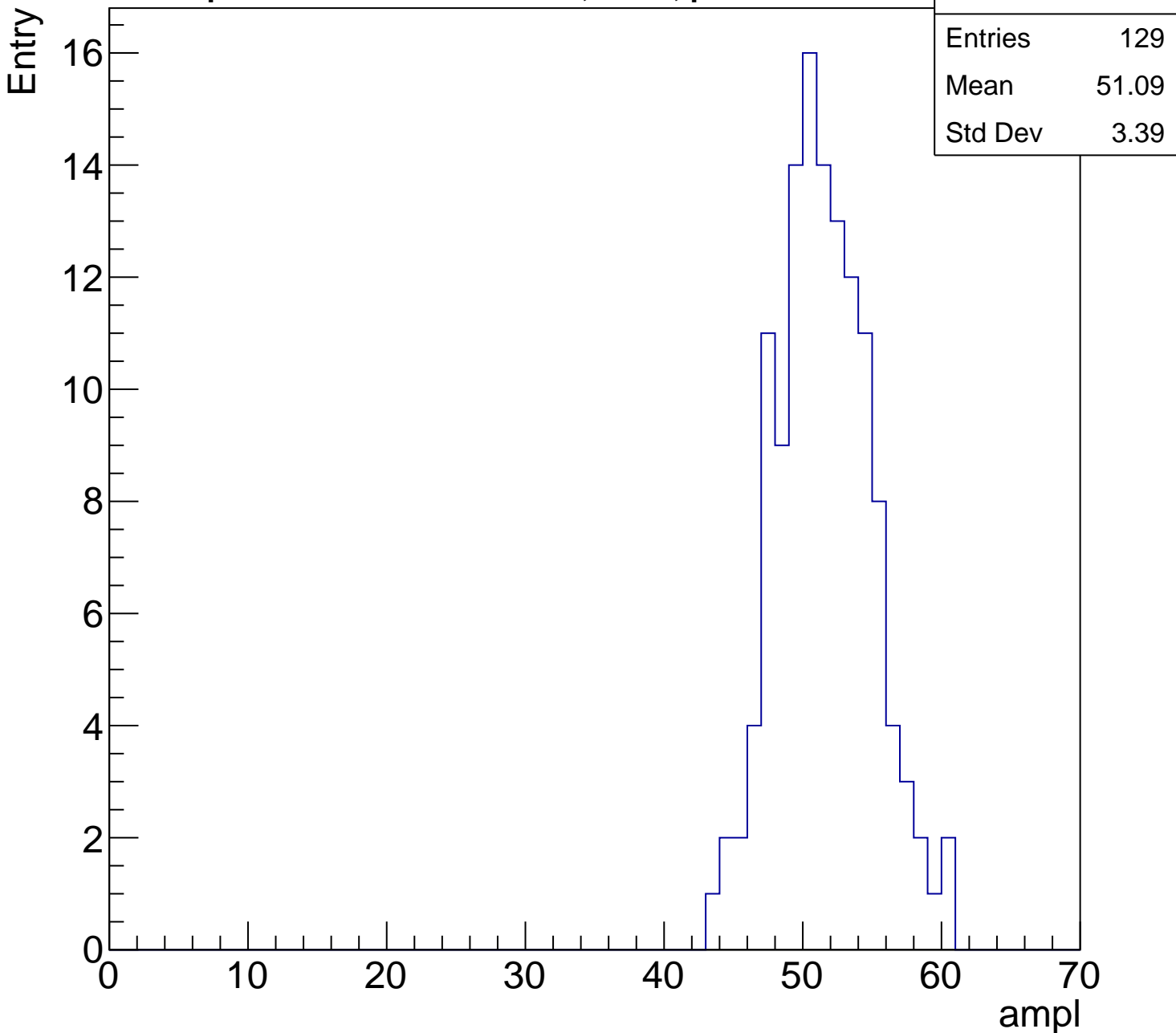
Entries	129
Mean	51.09
Std Dev	3.39

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

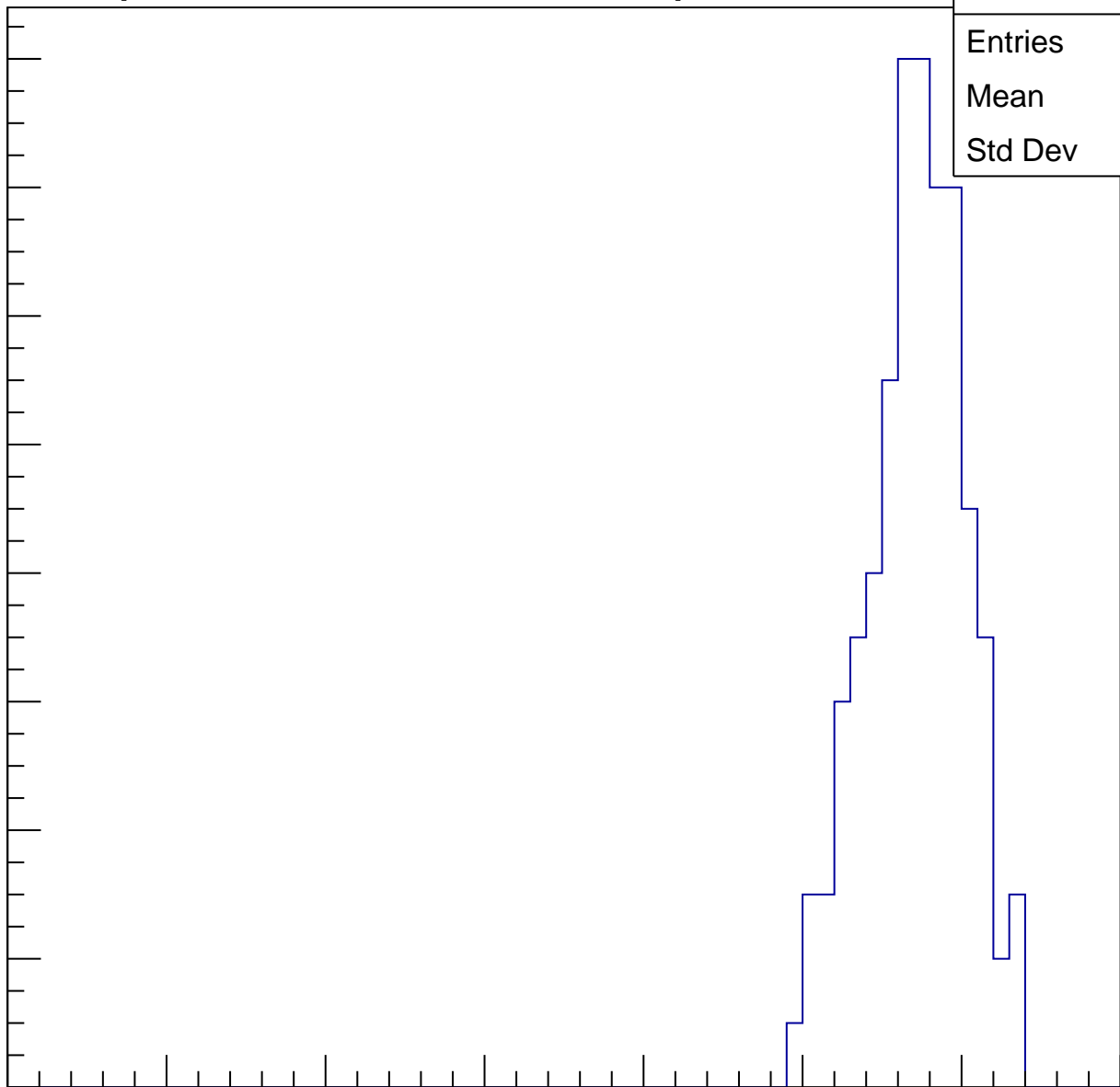
Entries	120
Mean	56.65
Std Dev	3.054

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch115, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	67
Mean	59.42
Std Dev	10.54

Entry

16
14
12
10
8
6
4
2
0

0

10

20

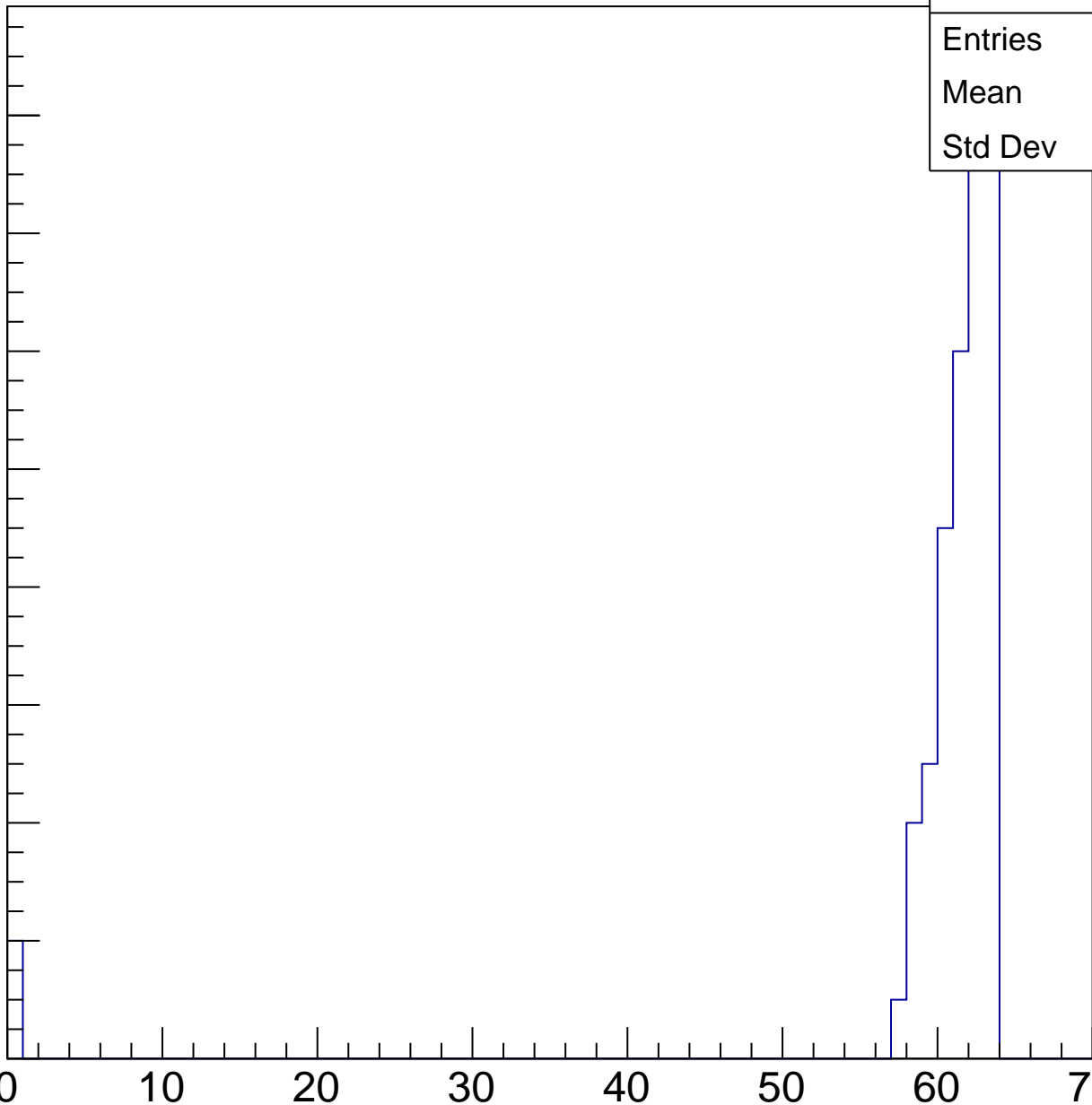
30

40

50

60

ampl



B1L001S, U22-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

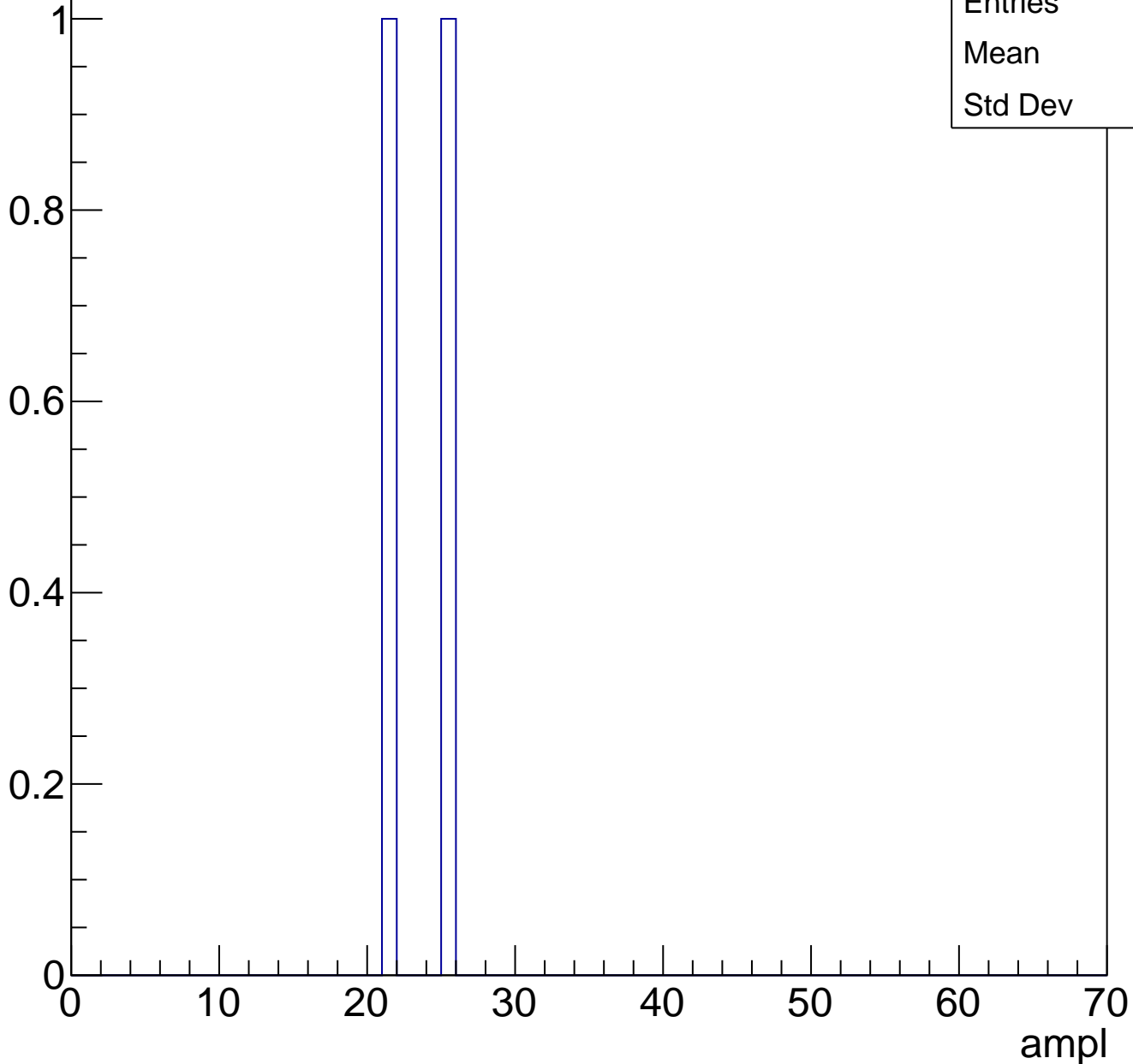
Entry



B1L001S, U22-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

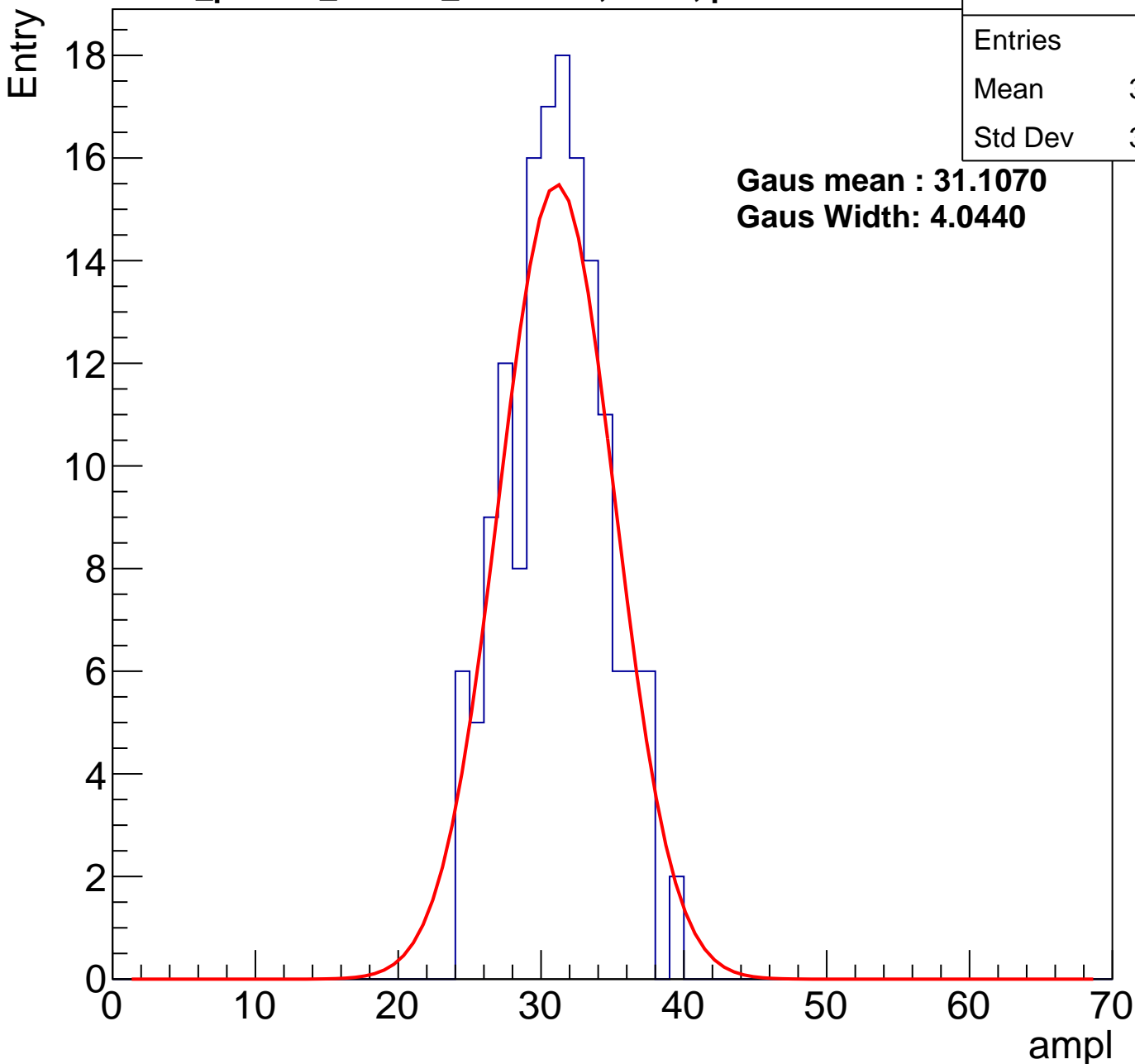


B1L001S, U22-ch116, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	30.64
Std Dev	3.427

Gaus mean : 31.1070
Gaus Width: 4.0440



B1L001S, U22-ch116, adc1

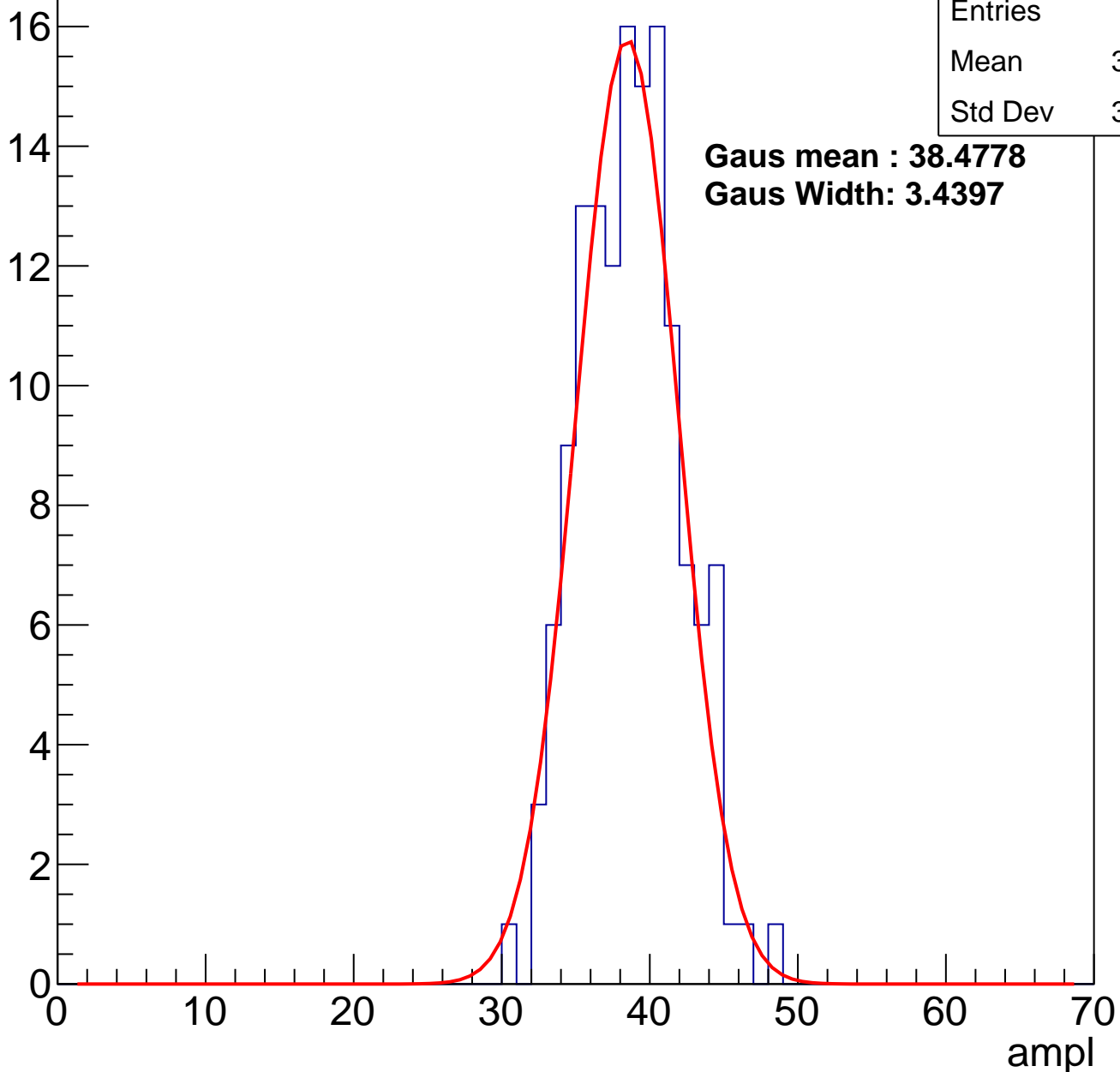
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	38.26
Std Dev	3.343

Gaus mean : 38.4778

Gaus Width: 3.4397

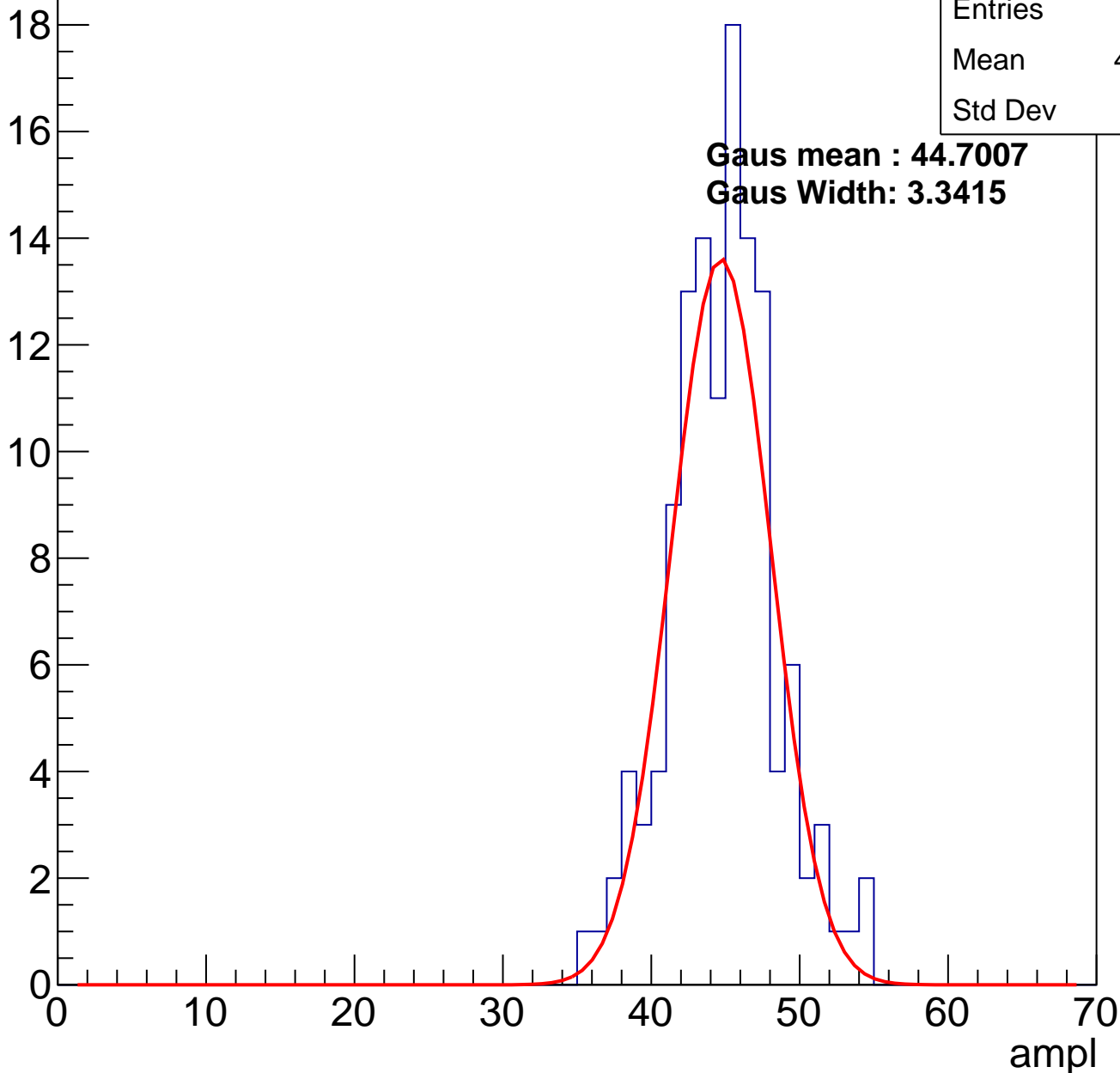
Entry



B1L001S, U22-ch116, adc2

calib_packv5_042523_0143.root, FC#2, port C2

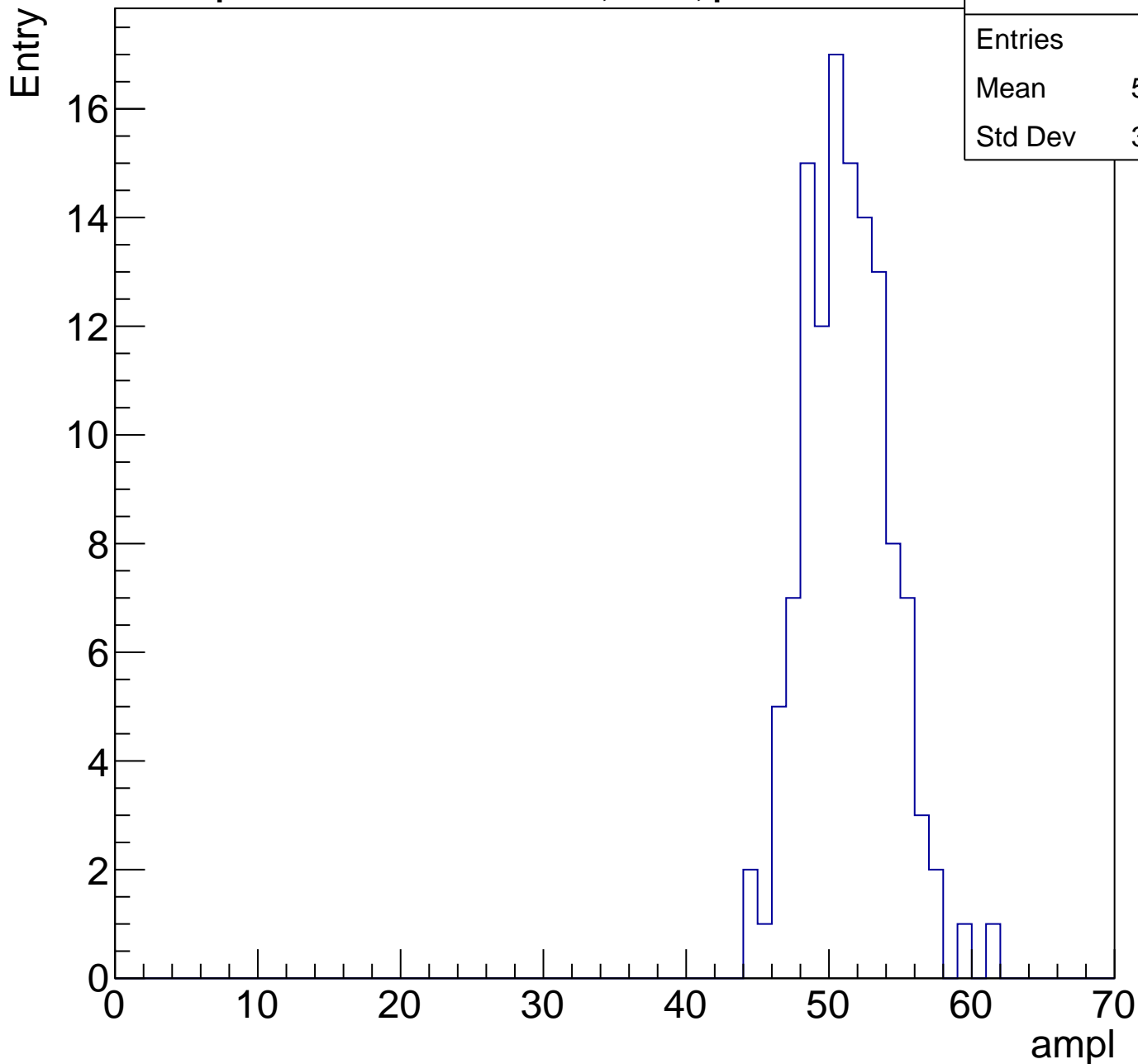
Entry



B1L001S, U22-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

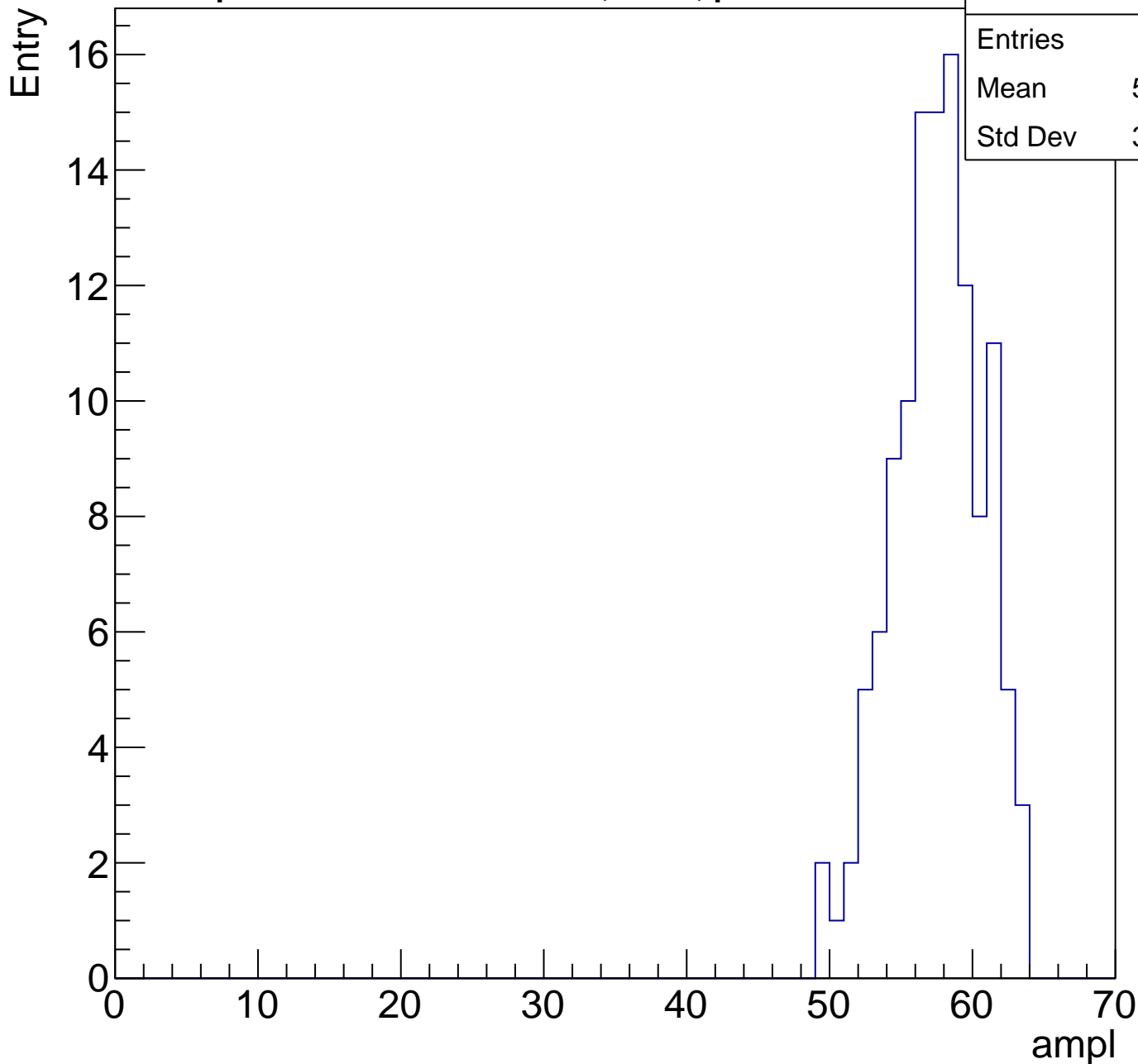
Entries	123
Mean	50.82
Std Dev	3.028



B1L001S, U22-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	57.04
Std Dev	3.097



B1L001S, U22-ch116, adc5

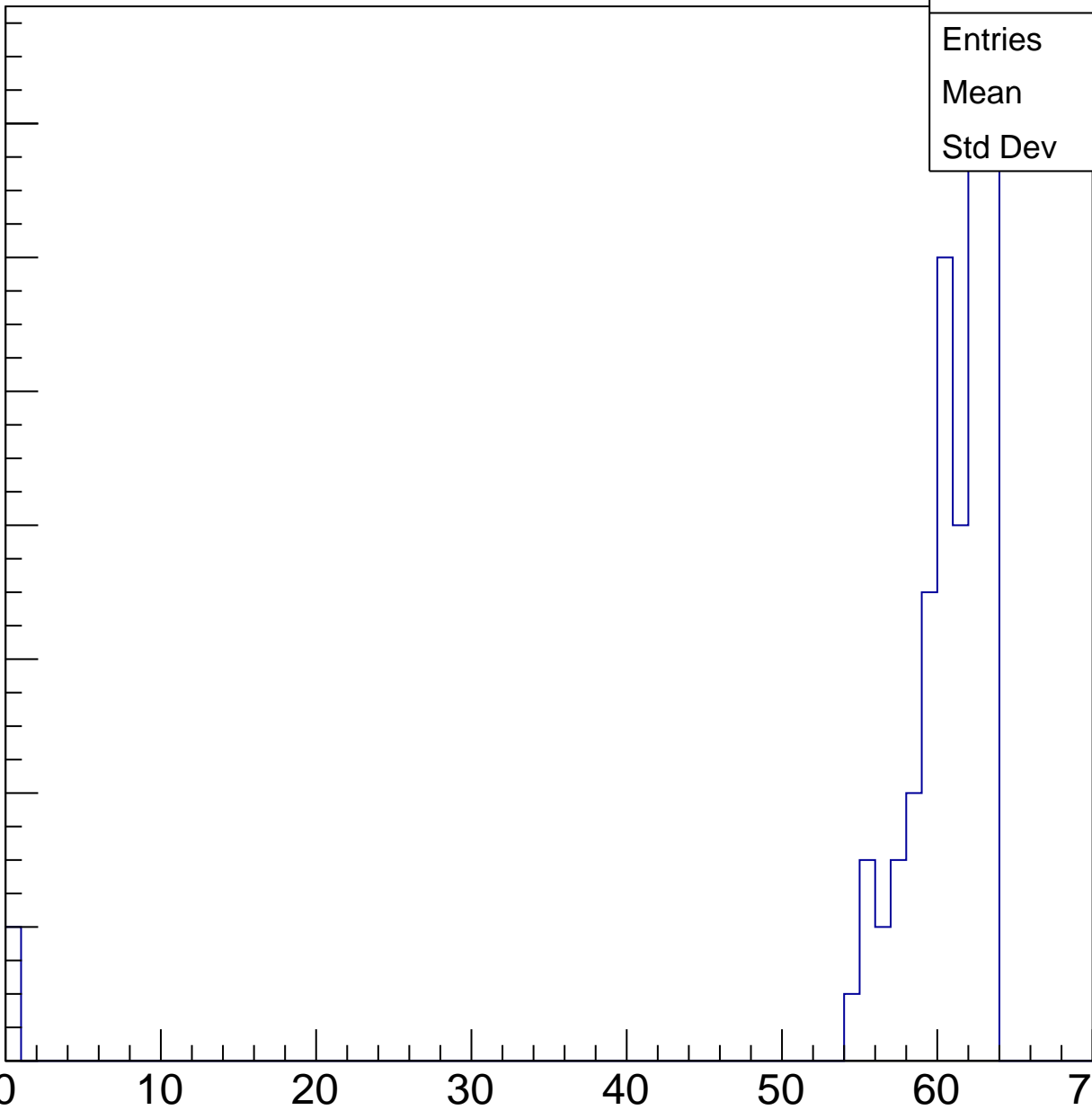
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.75
Std Dev	10.19

ampl



B1L001S, U22-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	63
Std Dev	0

B1L001S, U22-ch116, adc7

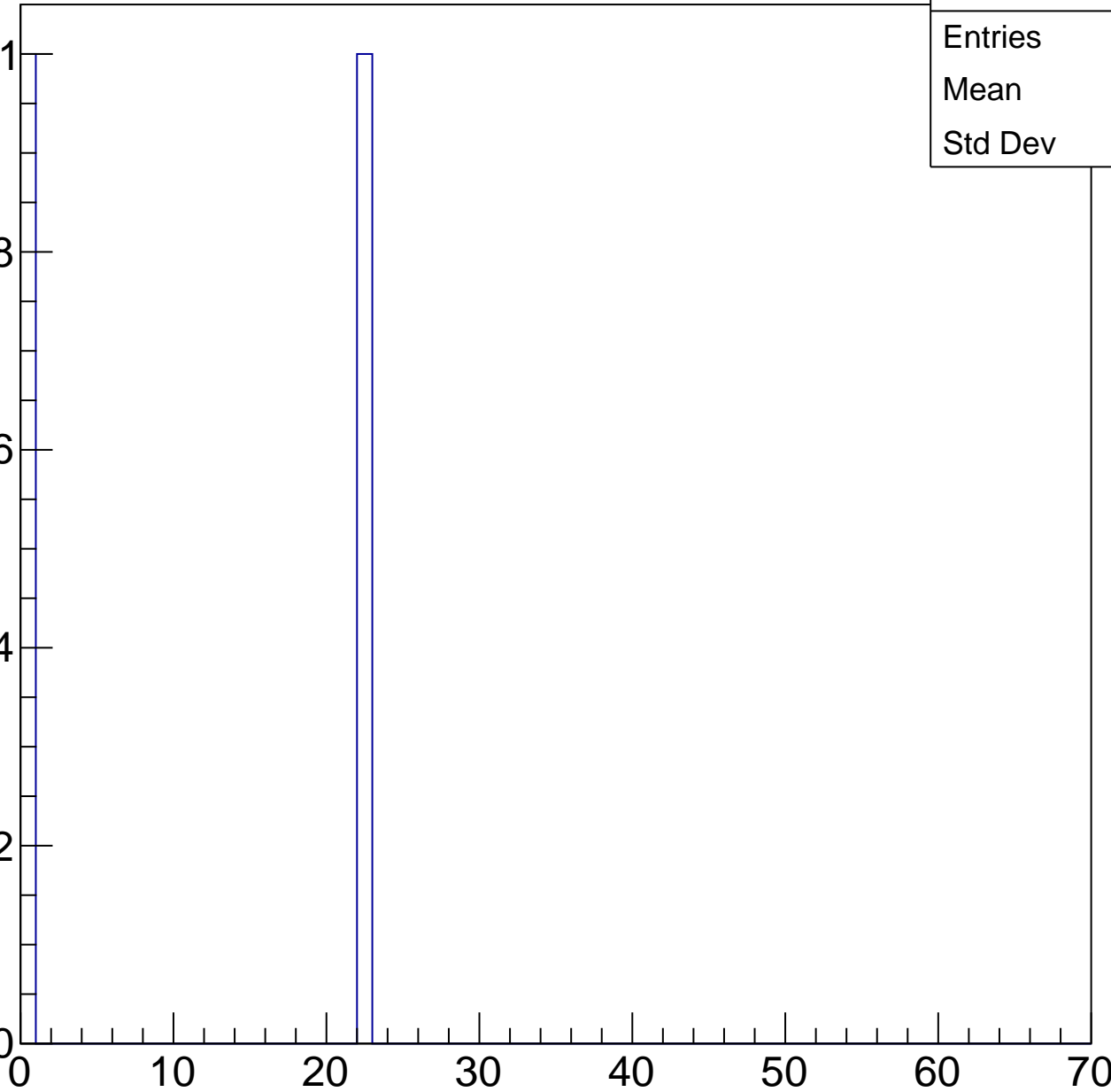
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	11
Std Dev	11

ampl



B1L001S, U22-ch117, adc0

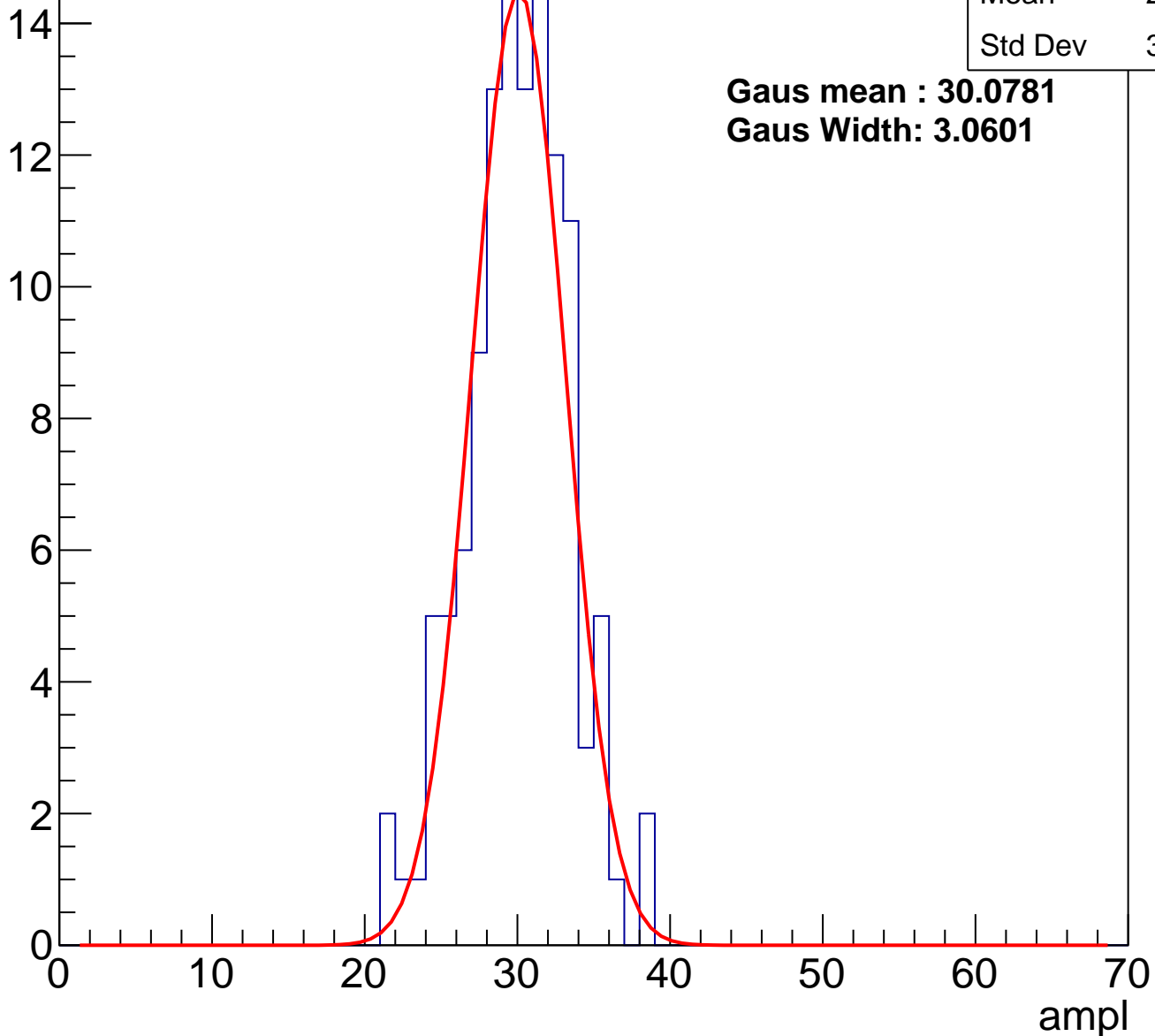
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.59
Std Dev	3.309

Gaus mean : 30.0781

Gaus Width: 3.0601

Entry



B1L001S, U22-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.72
Std Dev	3.532

Gaus mean : 37.0647

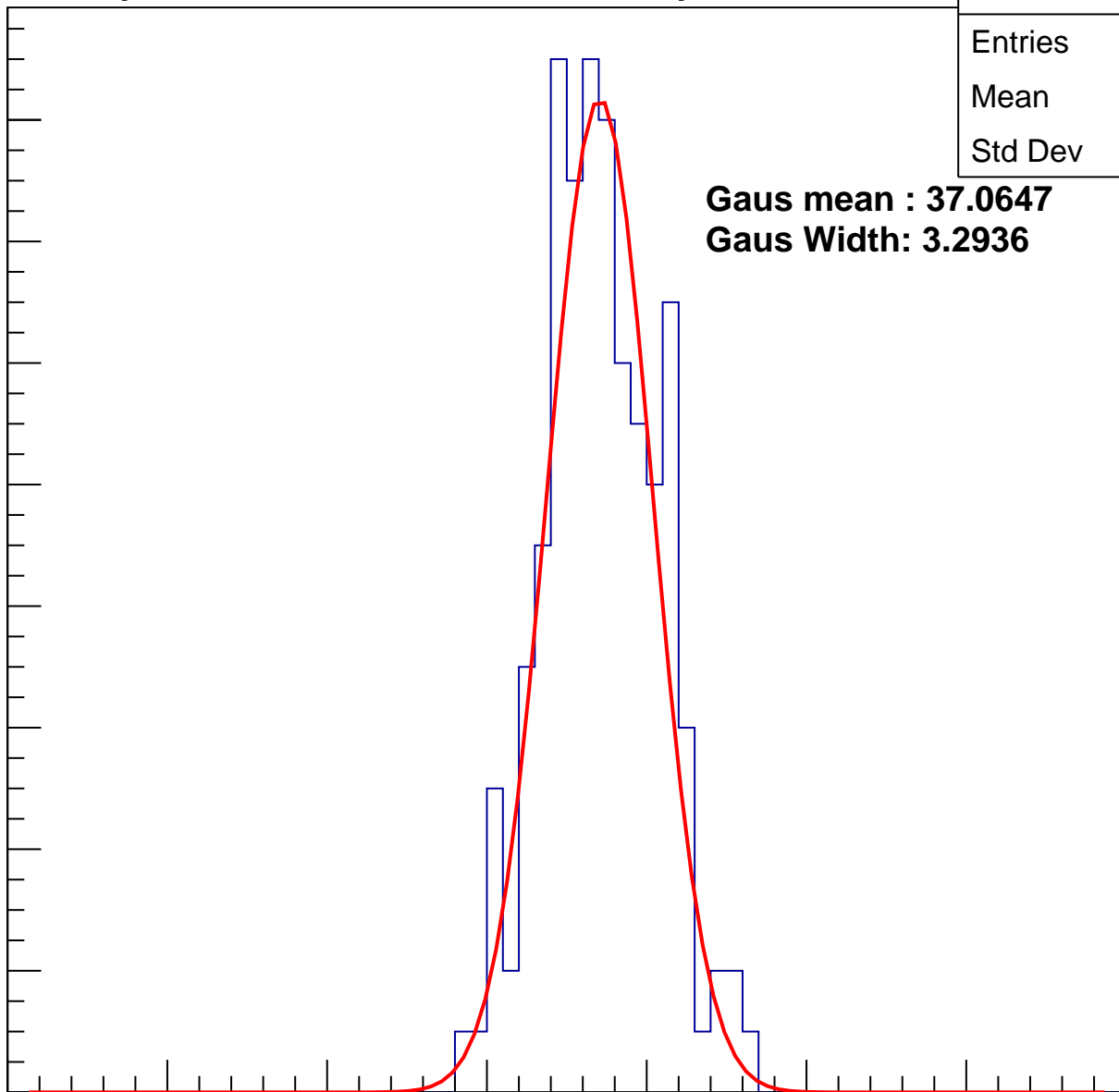
Gaus Width: 3.2936

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch117, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

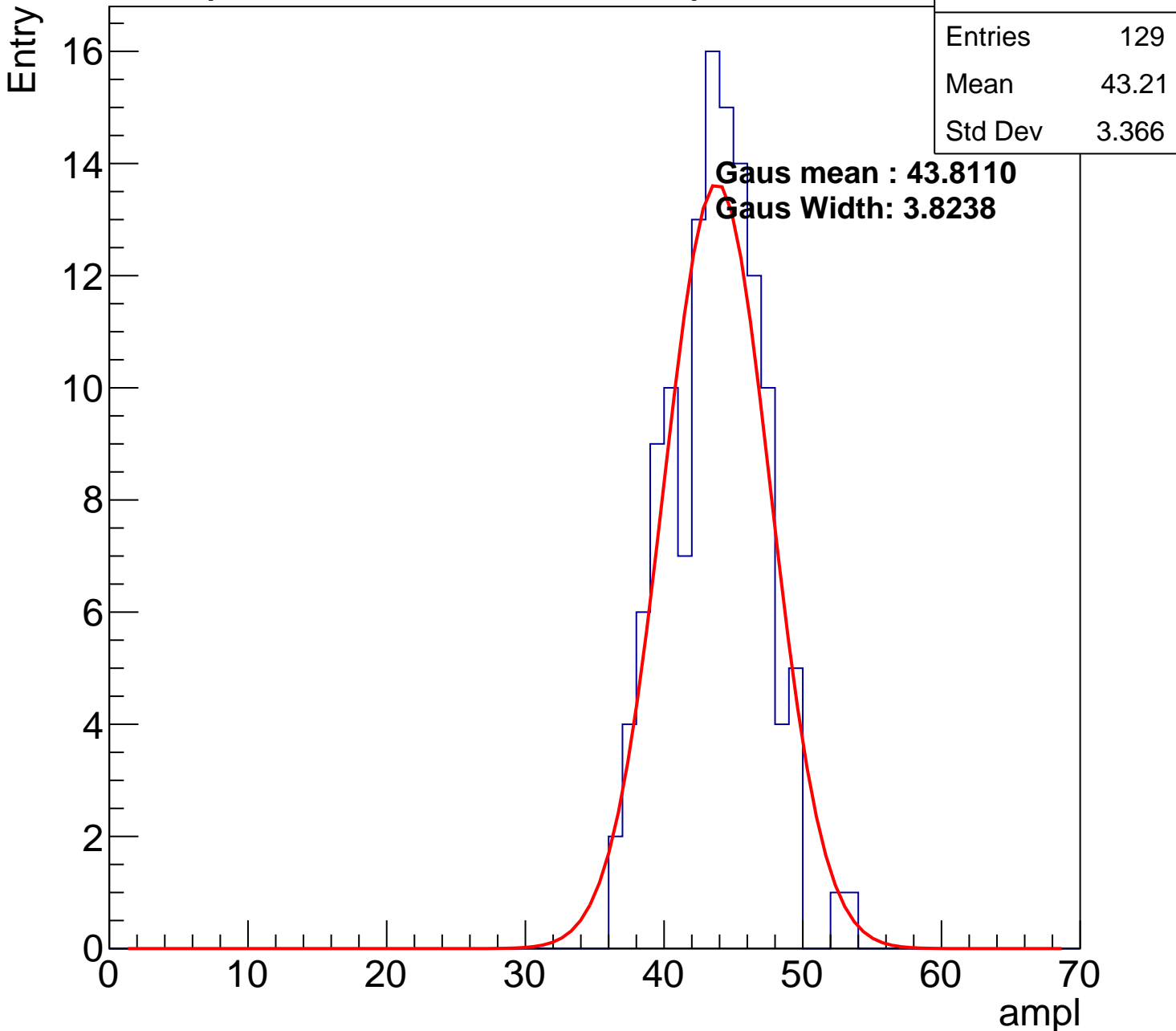
16
14
12
10
8
6
4
2
0

Entries	129
Mean	43.21
Std Dev	3.366

Gaus mean : 43.8110

Gaus Width: 3.8238

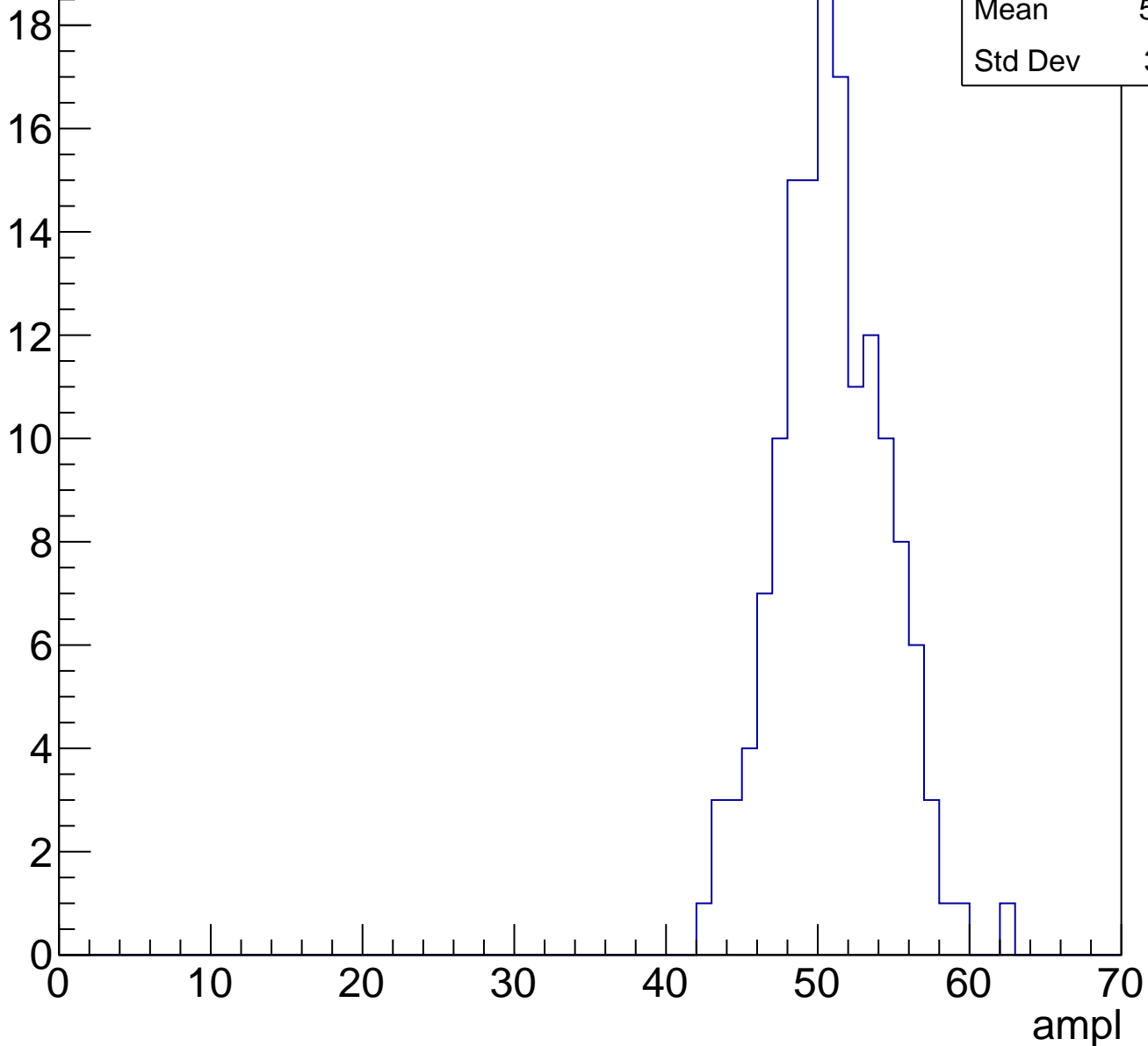
0 10 20 30 40 50 60 70
ampl



B1L001S, U22-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	147
Mean	50.48
Std Dev	3.541

B1L001S, U22-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	56.24
Std Dev	2.96

Entry

12

10

8

6

4

2

0

0

10

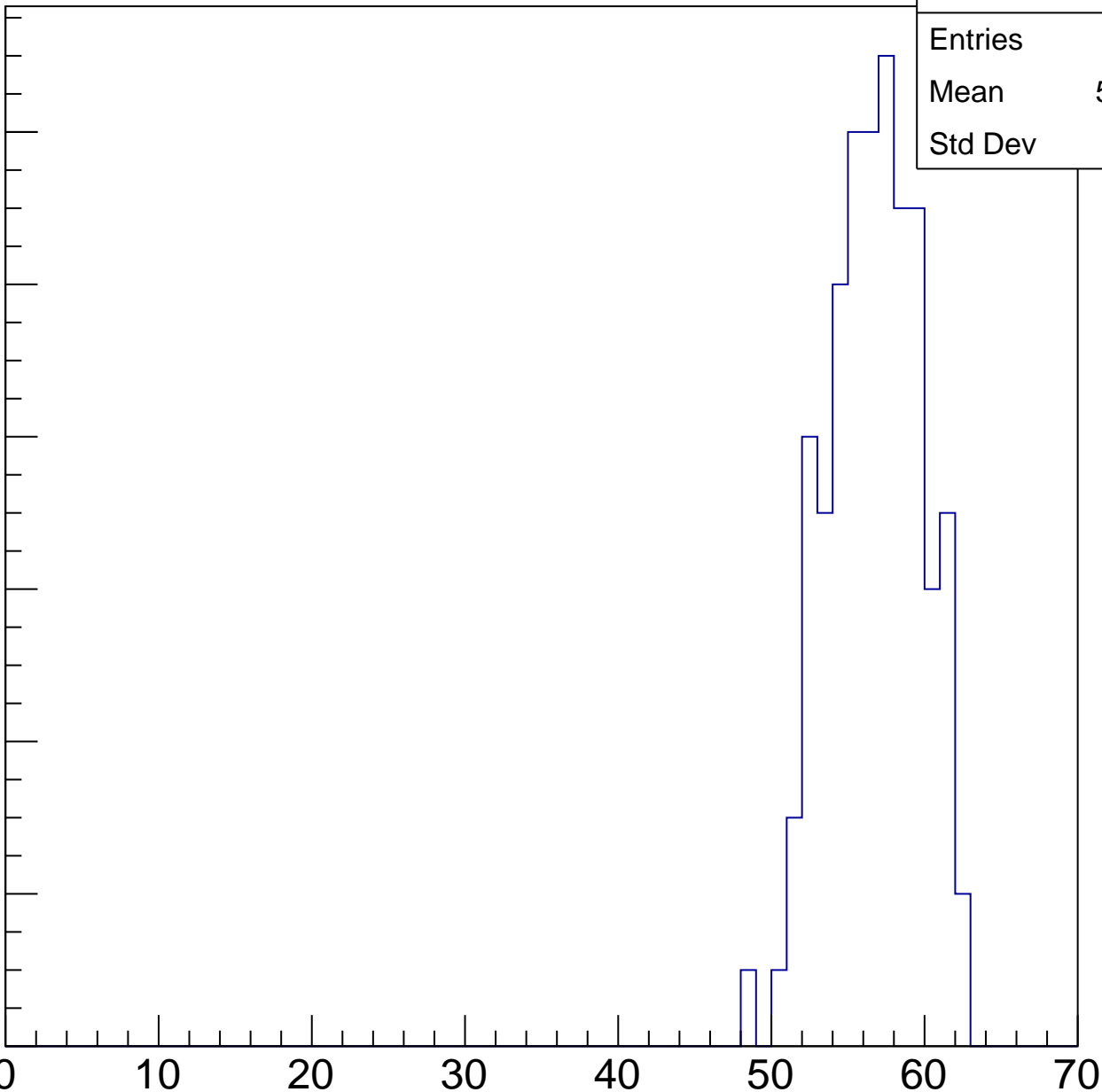
20

30

40

50

ampl



B1L001S, U22-ch117, adc5

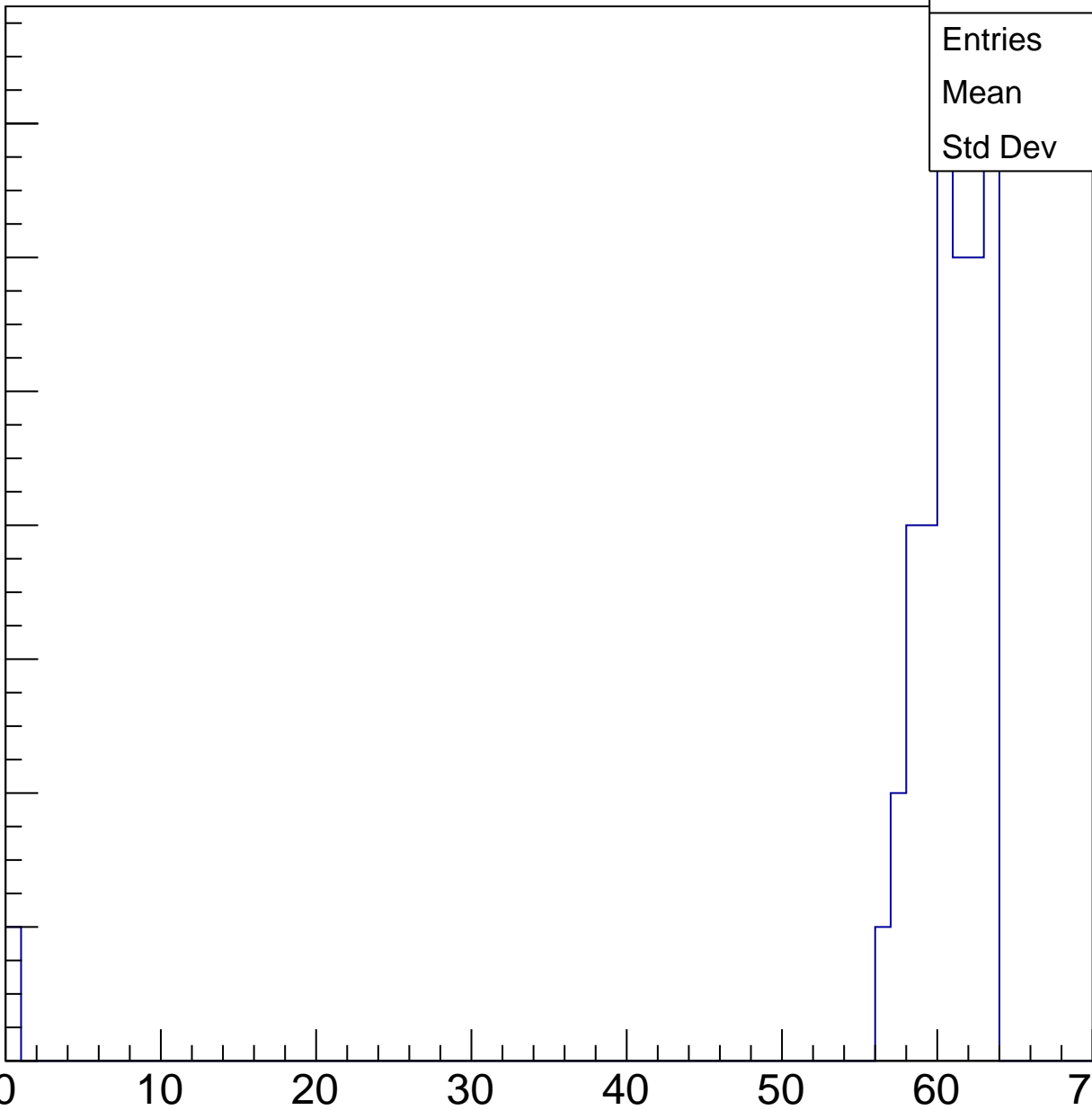
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.92
Std Dev	9.812

ampl

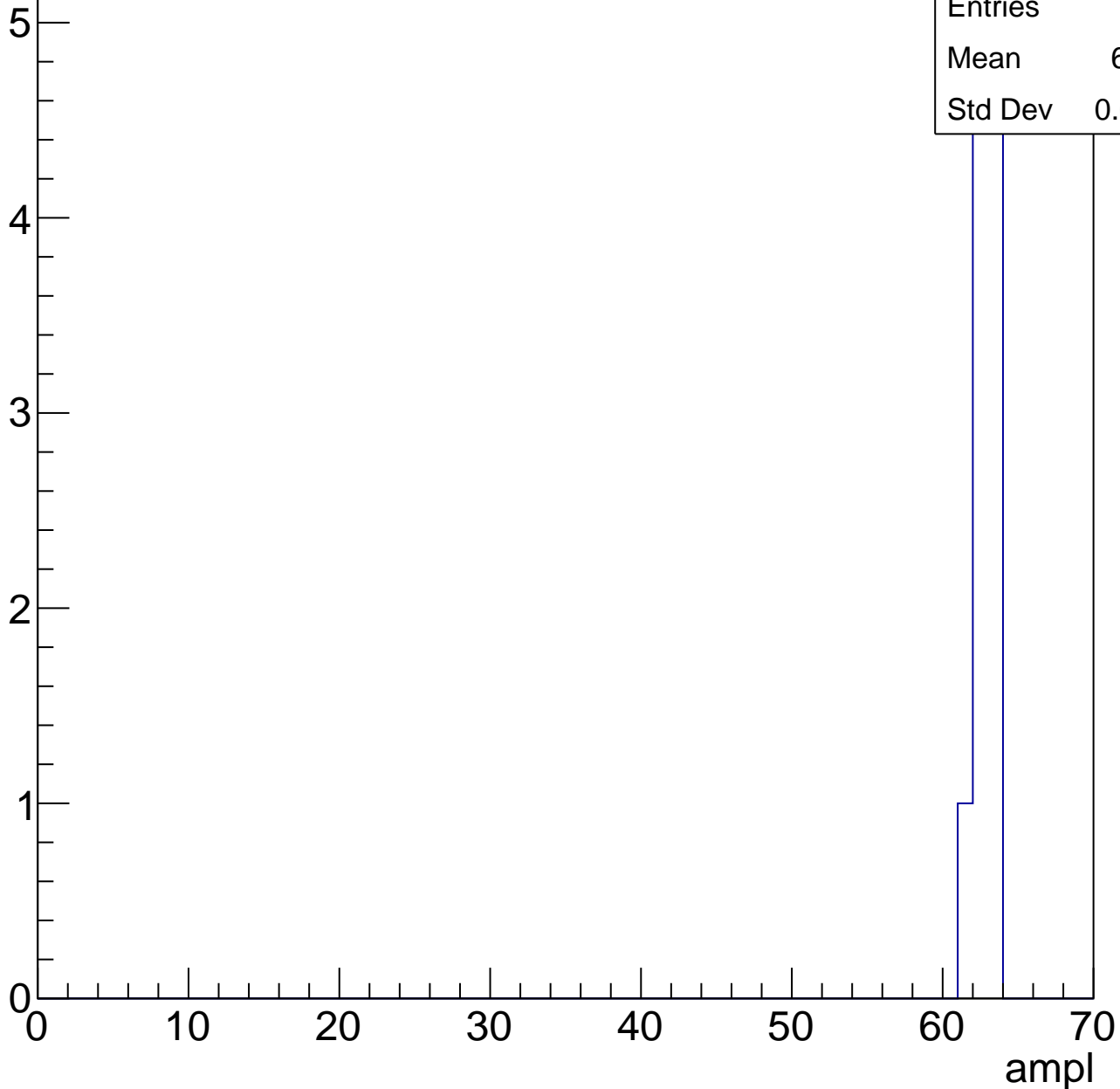


B1L001S, U22-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.36
Std Dev	0.6428



B1L001S, U22-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch118, adc0

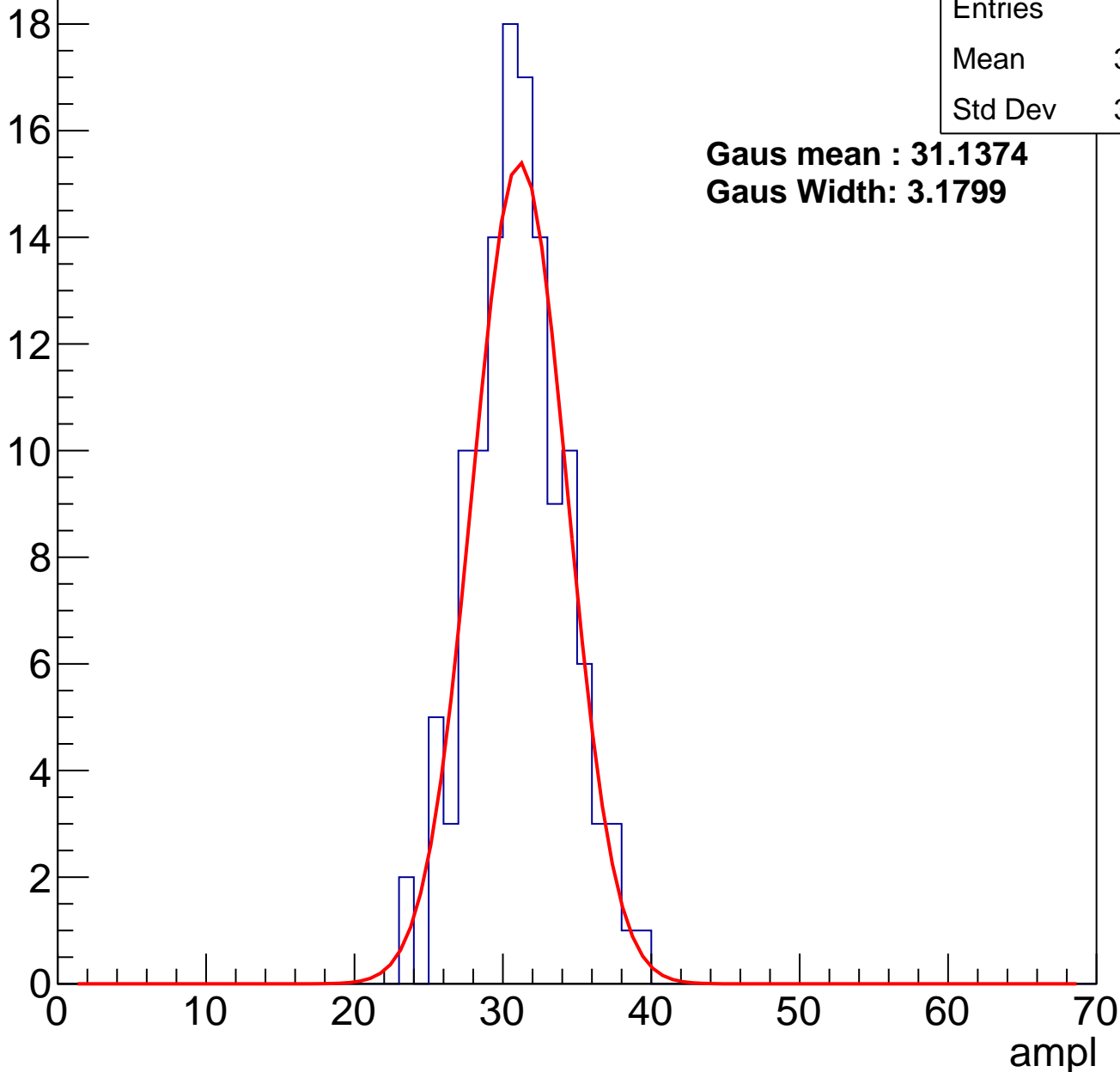
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	30.66
Std Dev	3.117

Gaus mean : 31.1374

Gaus Width: 3.1799

Entry



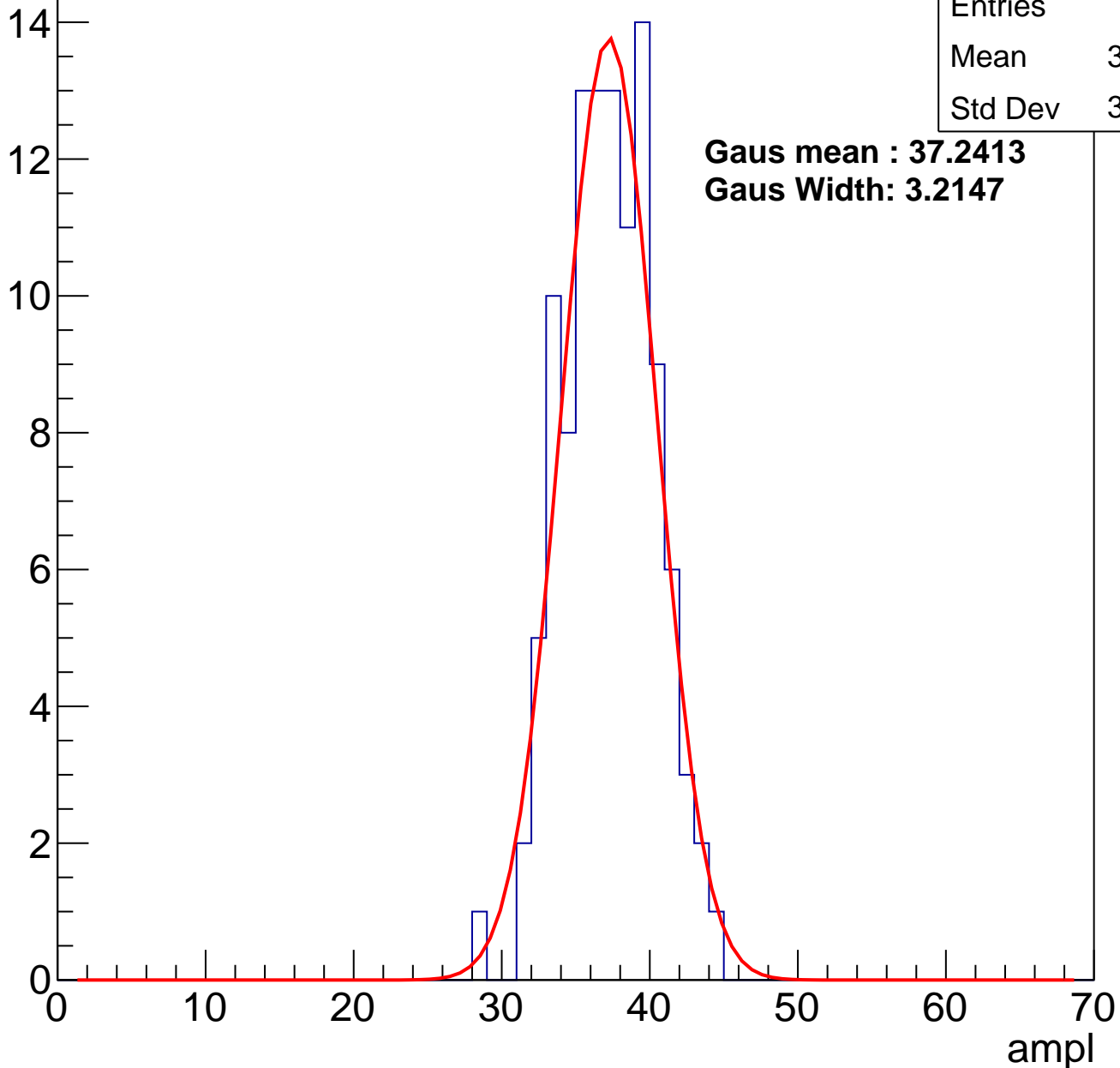
B1L001S, U22-ch118, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.77
Std Dev	3.016

Gaus mean : 37.2413
Gaus Width: 3.2147

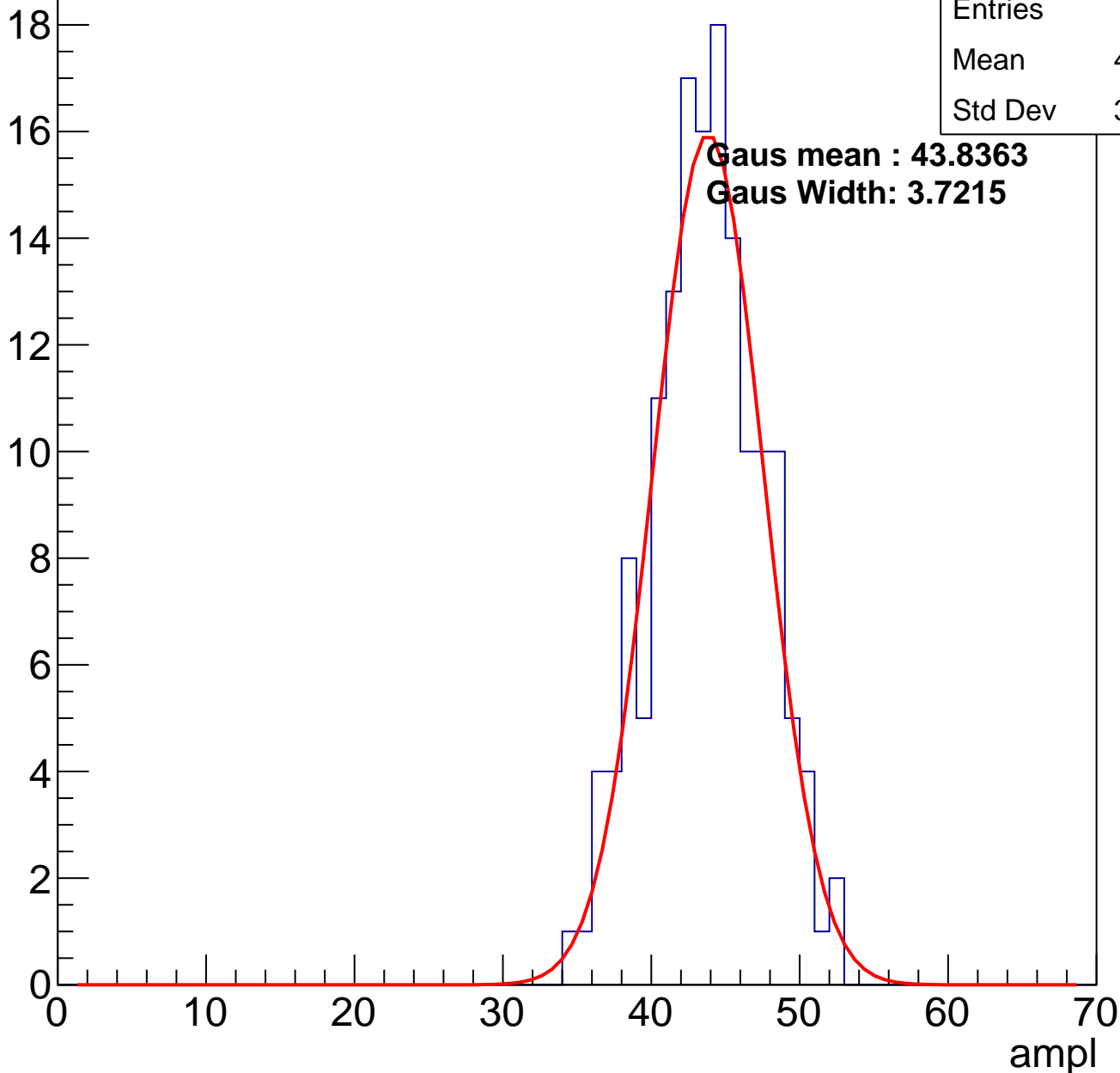
Entry



B1L001S, U22-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	49.75
Std Dev	3.167

Entry

14

12

10

8

6

4

2

0

0

10

20

30

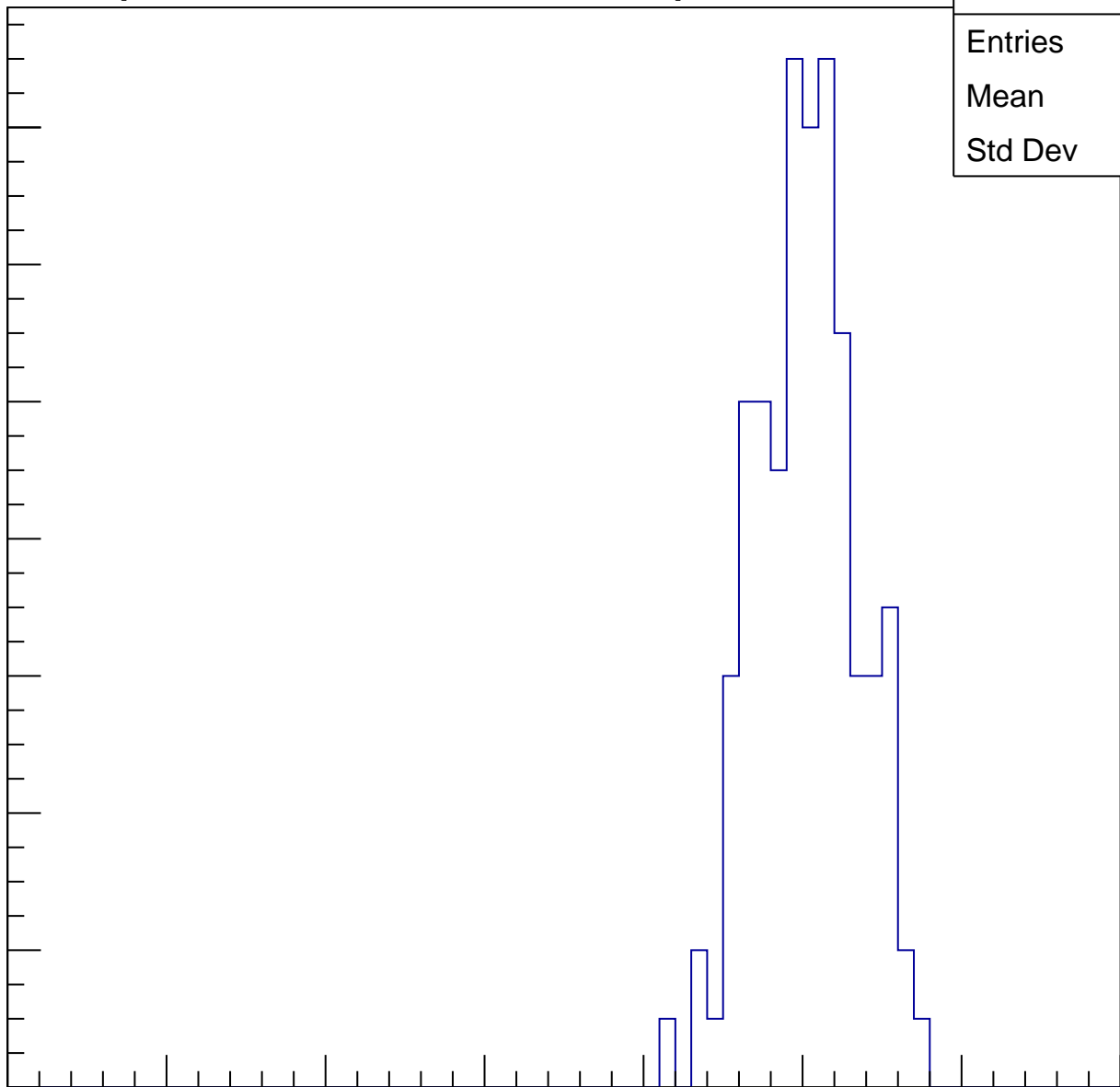
40

50

60

70

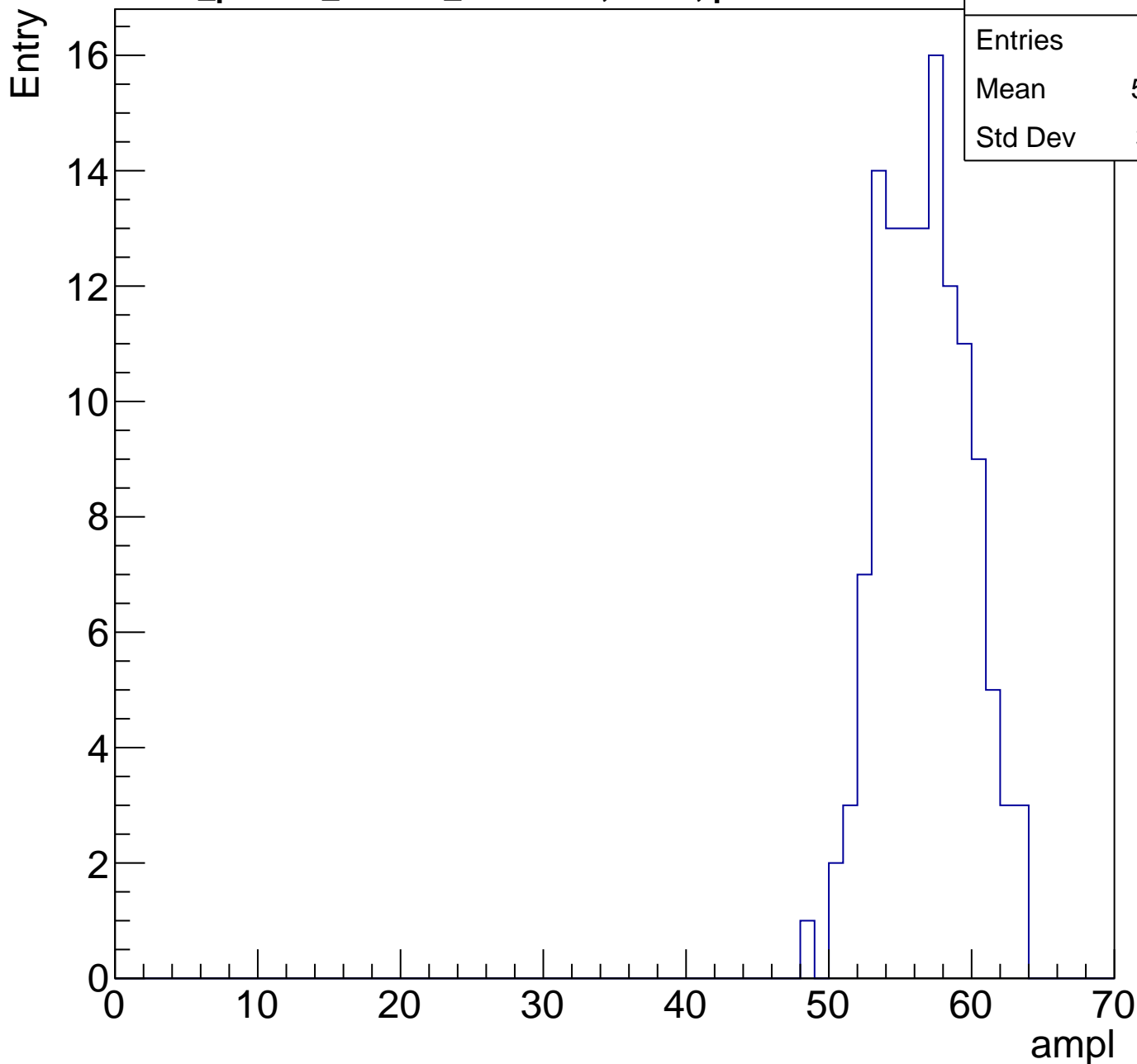
ampl



B1L001S, U22-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	56.23
Std Dev	3.091



B1L001S, U22-ch118, adc5

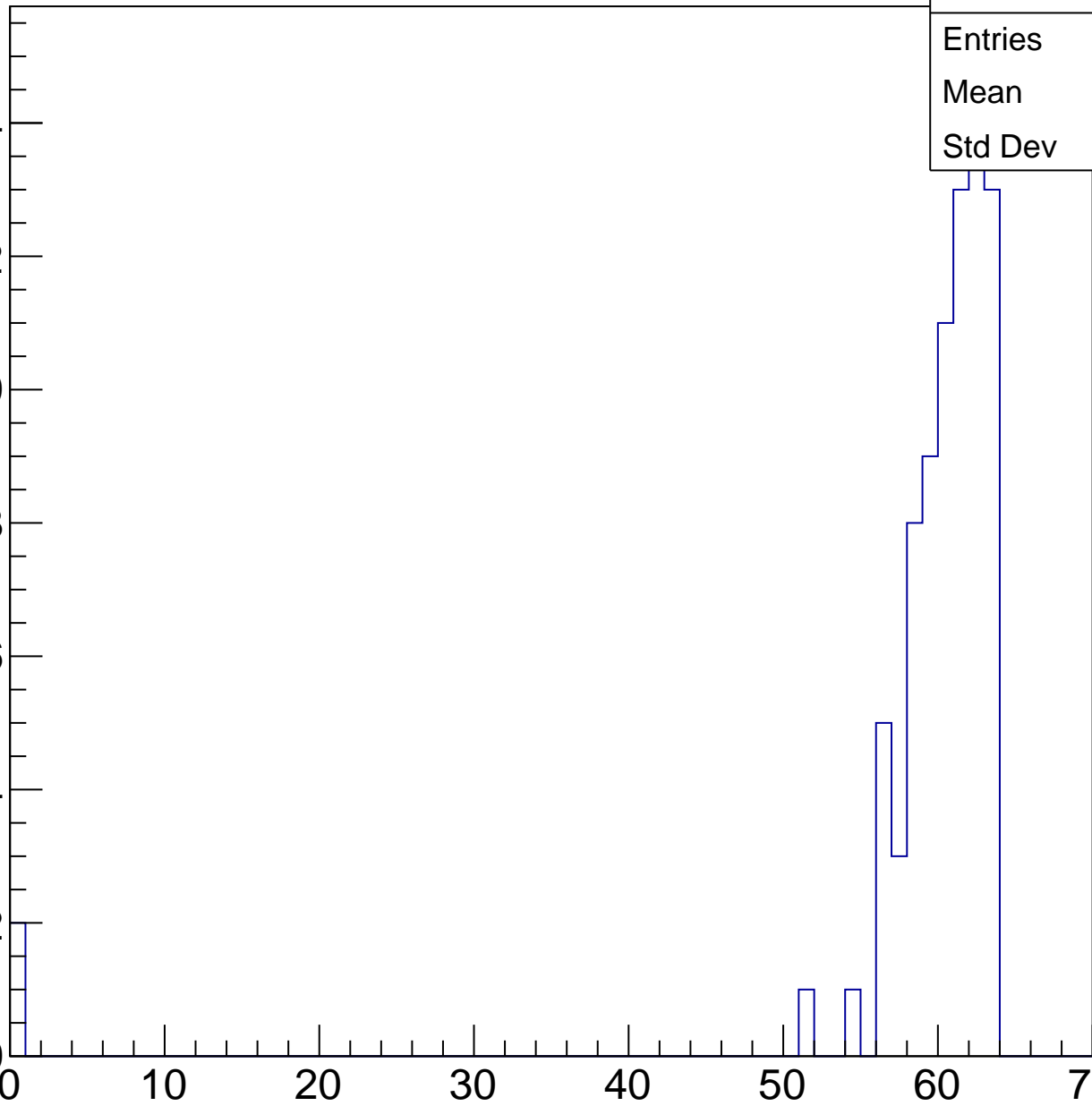
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.68
Std Dev	9.631

ampl



B1L001S, U22-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	11
Std Dev	11

ampl

B1L001S, U22-ch119, adc0

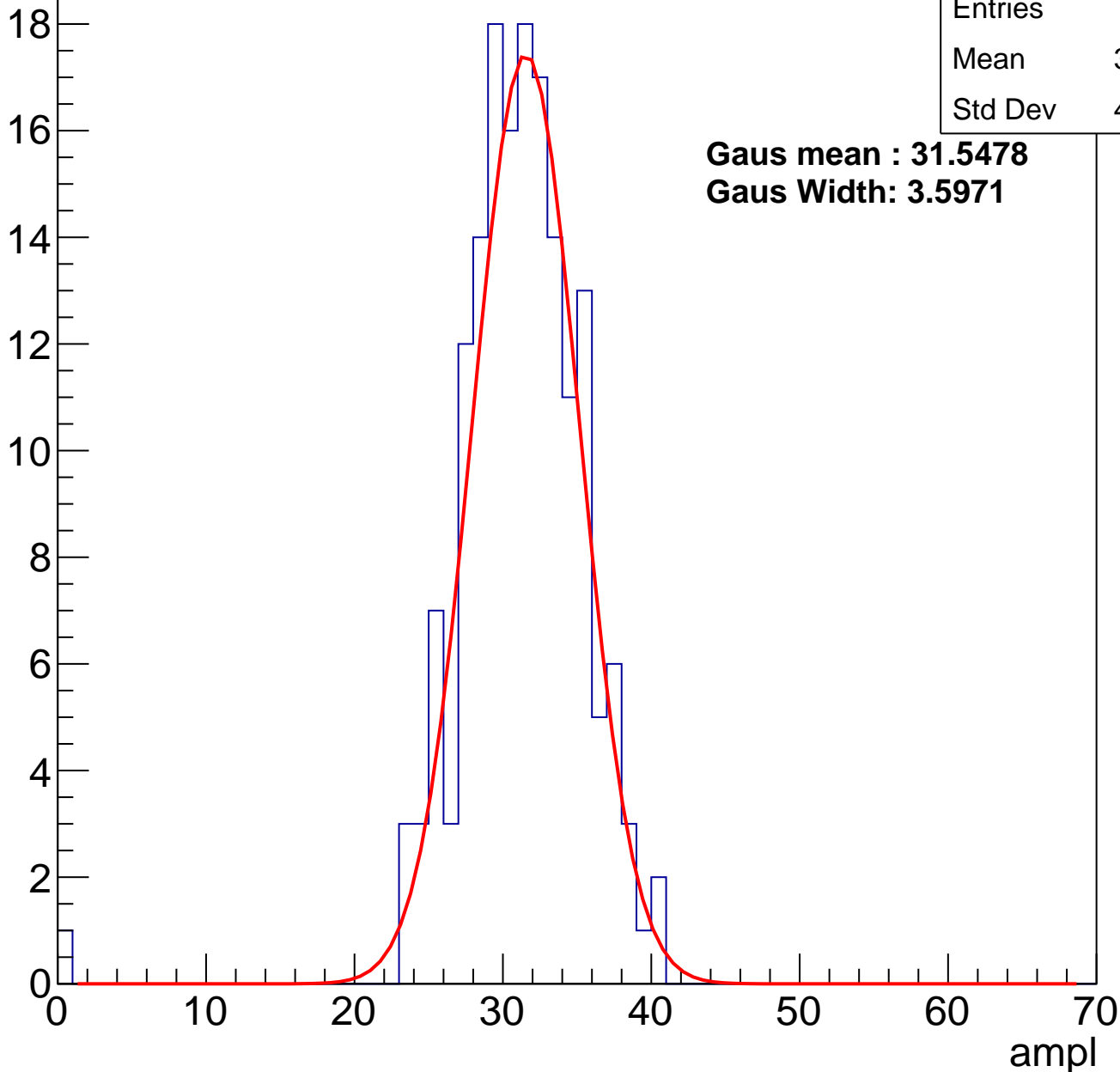
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	30.78
Std Dev	4.322

Gaus mean : 31.5478

Gaus Width: 3.5971

Entry



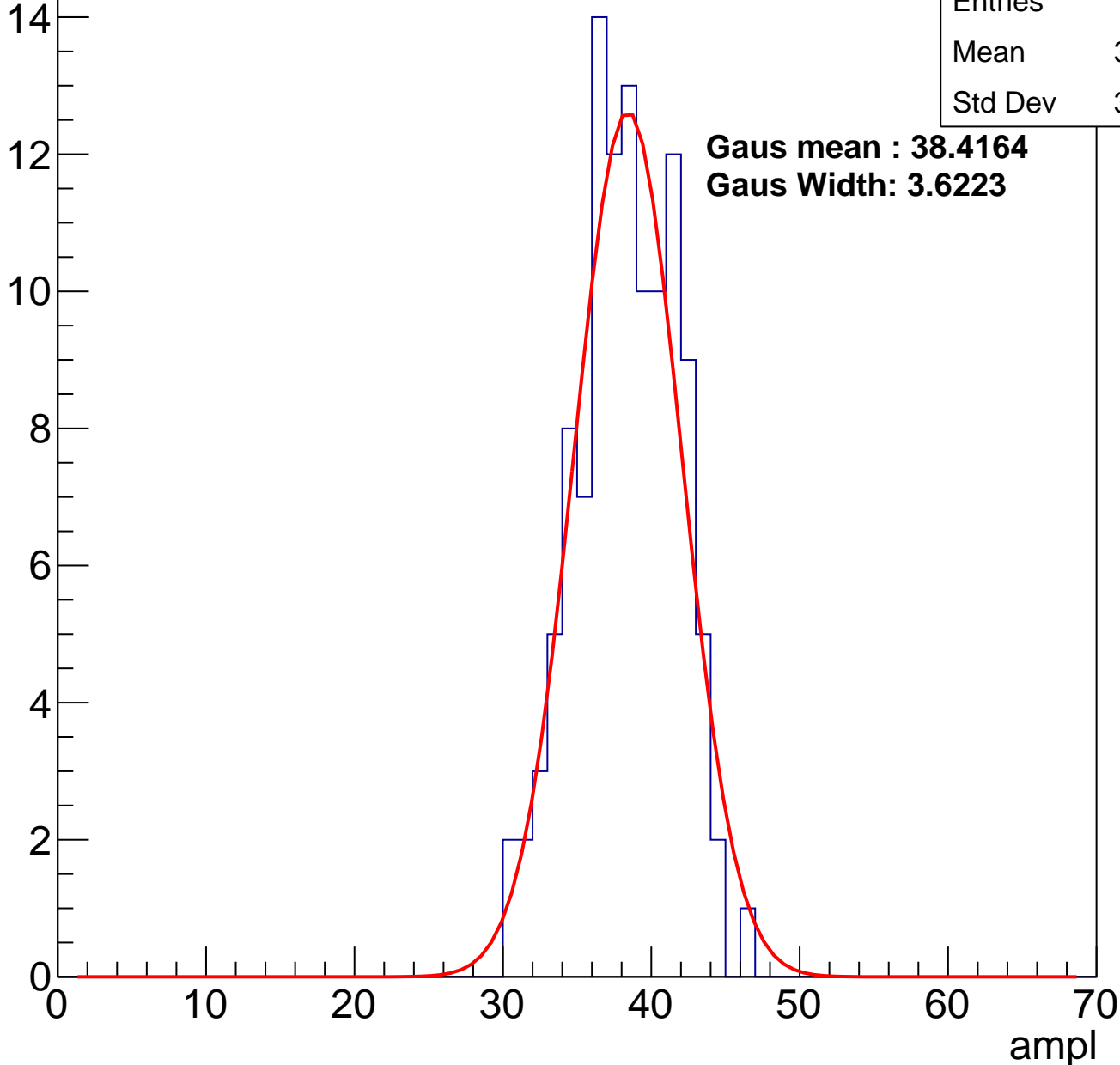
B1L001S, U22-ch119, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	37.83
Std Dev	3.333

Gaus mean : 38.4164
Gaus Width: 3.6223

Entry



B1L001S, U22-ch119, adc2

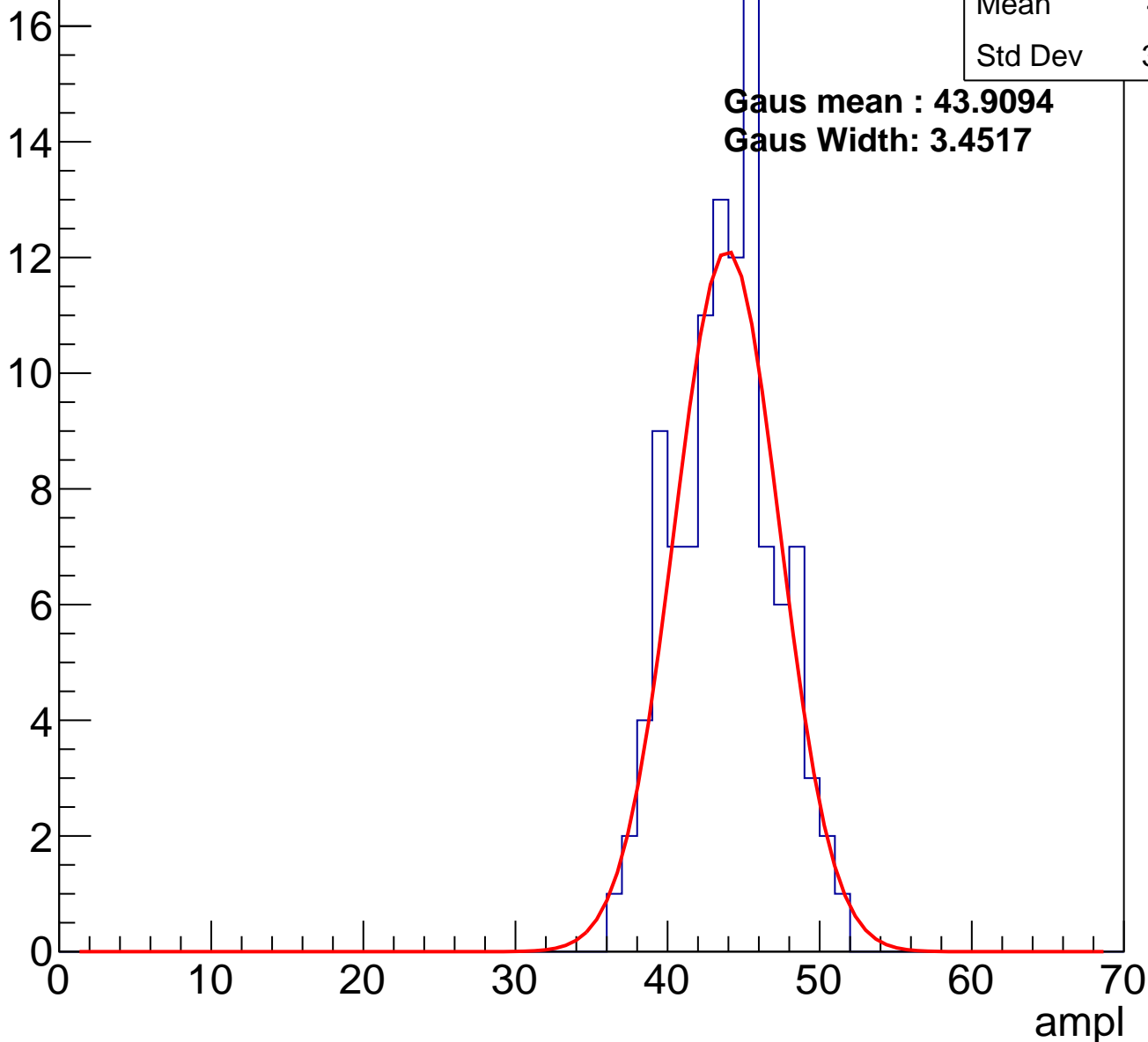
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	109
Mean	43.41
Std Dev	3.223

Gaus mean : 43.9094

Gaus Width: 3.4517

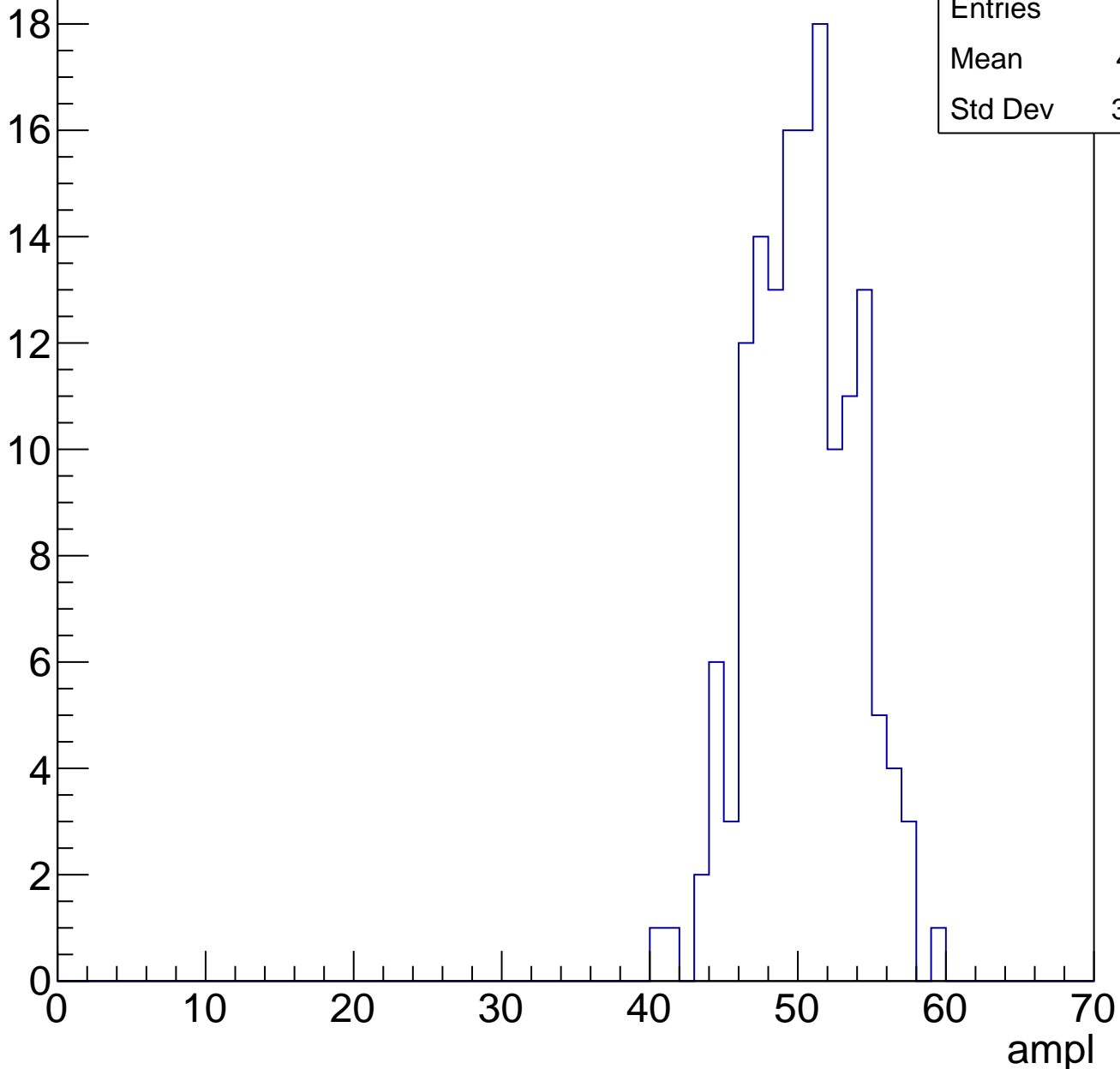


B1L001S, U22-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	149
Mean	49.91
Std Dev	3.492



B1L001S, U22-ch119, adc4

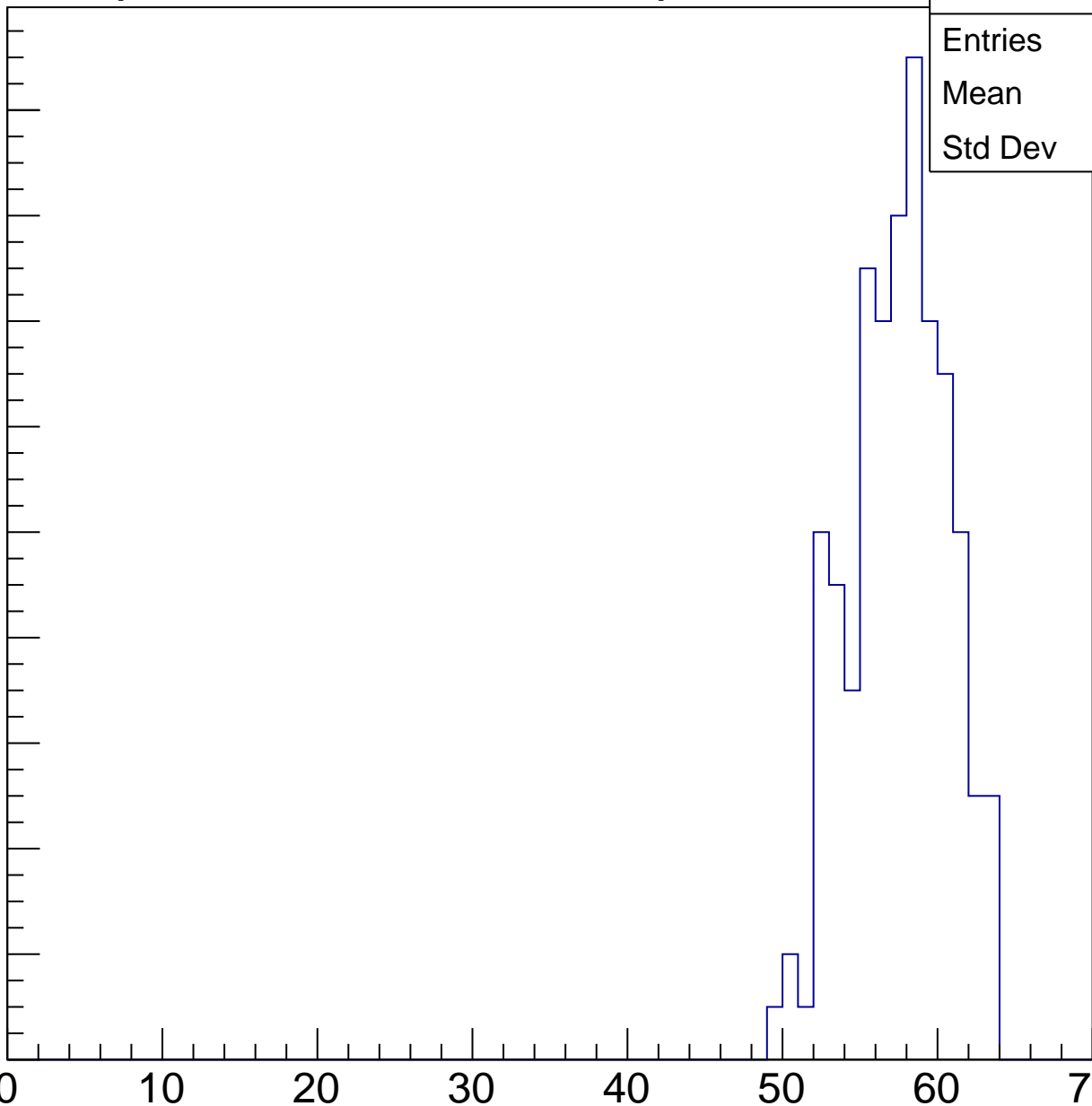
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	141
Mean	57.01
Std Dev	3.135

ampl



B1L001S, U22-ch119, adc5

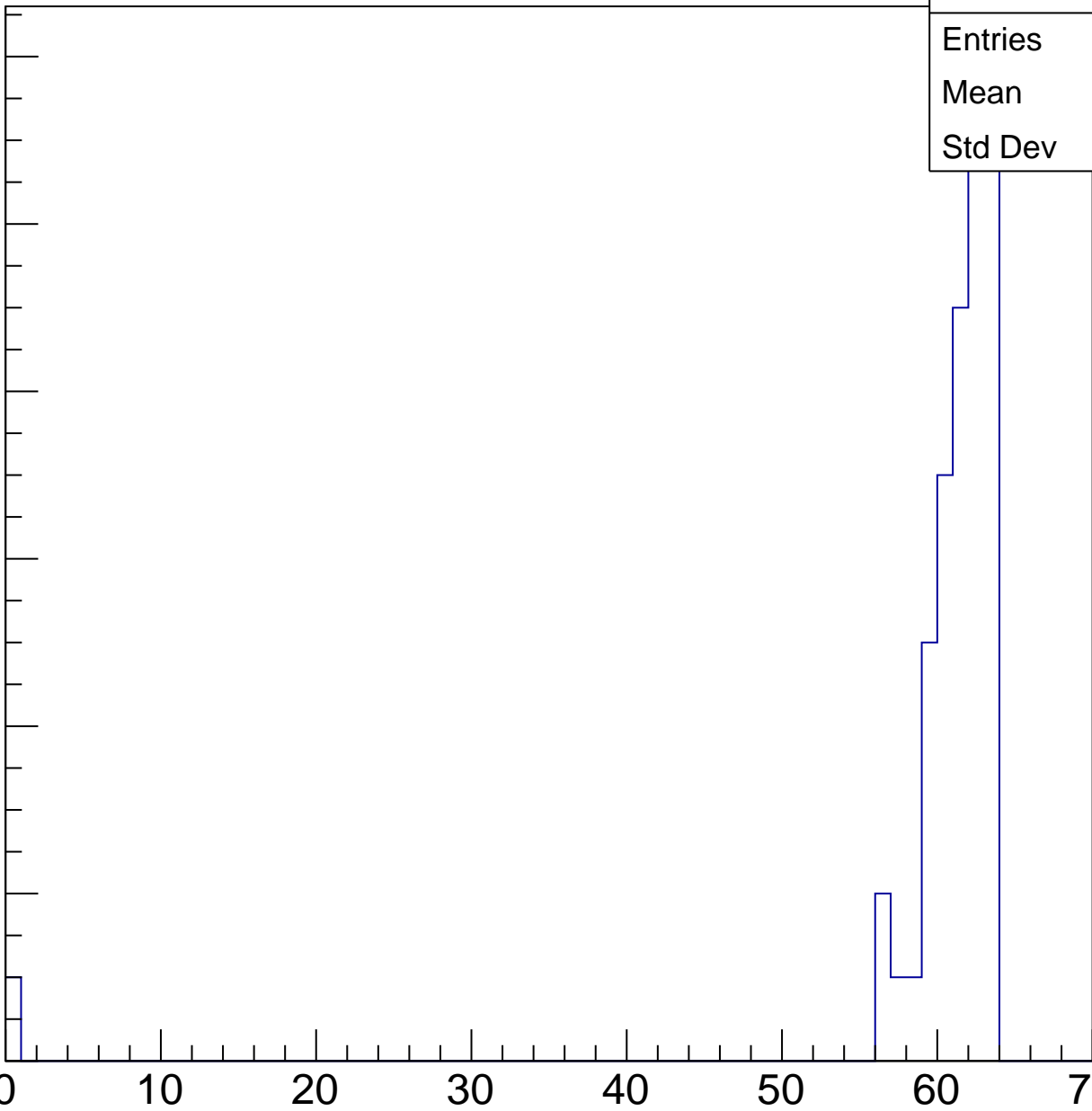
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	49
Mean	59.78
Std Dev	8.816

ampl



B1L001S, U22-ch119, adc6

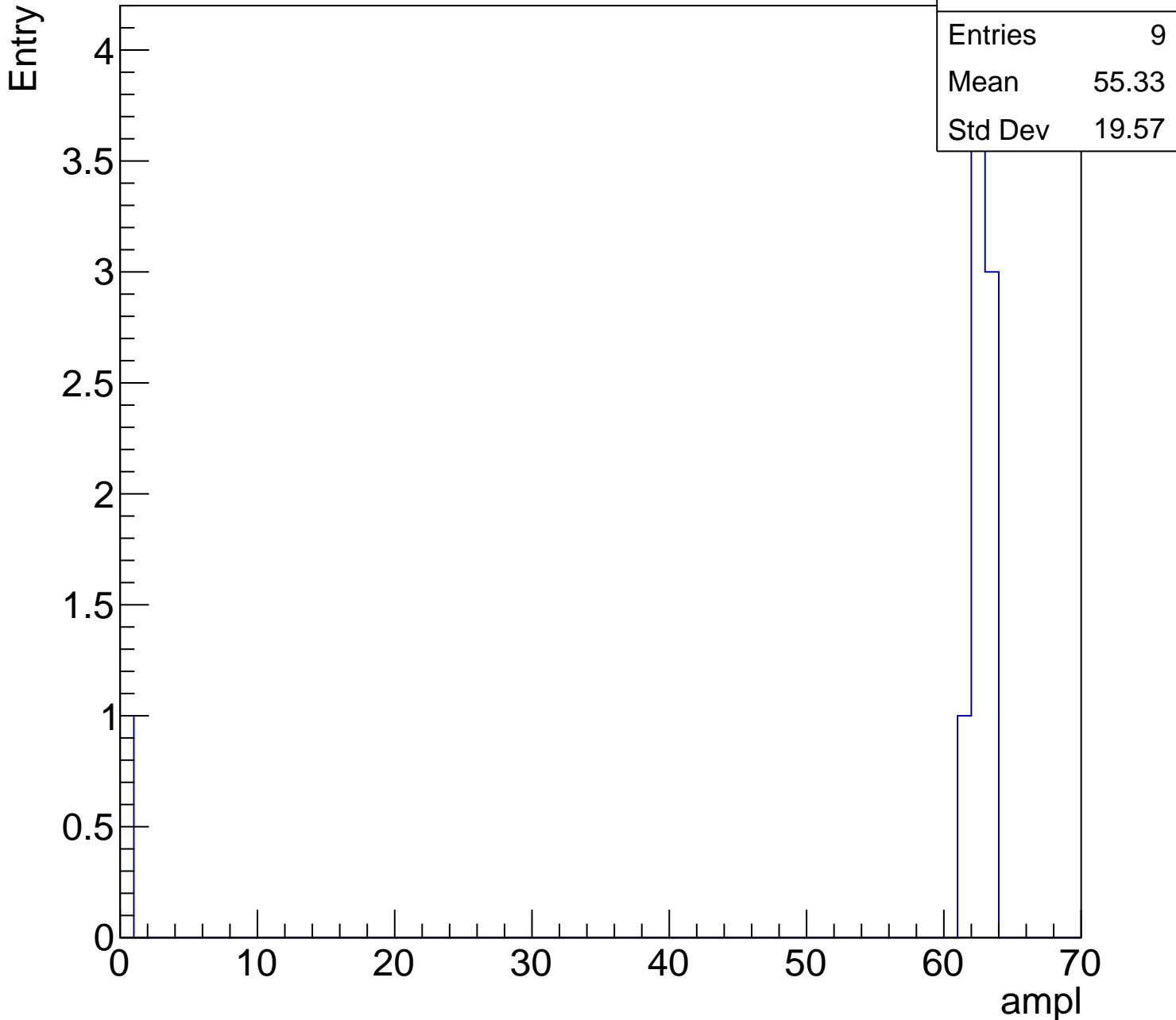
calib_packv5_042523_0143.root, FC#2, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	55.33
Std Dev	19.57

ampl



B1L001S, U22-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

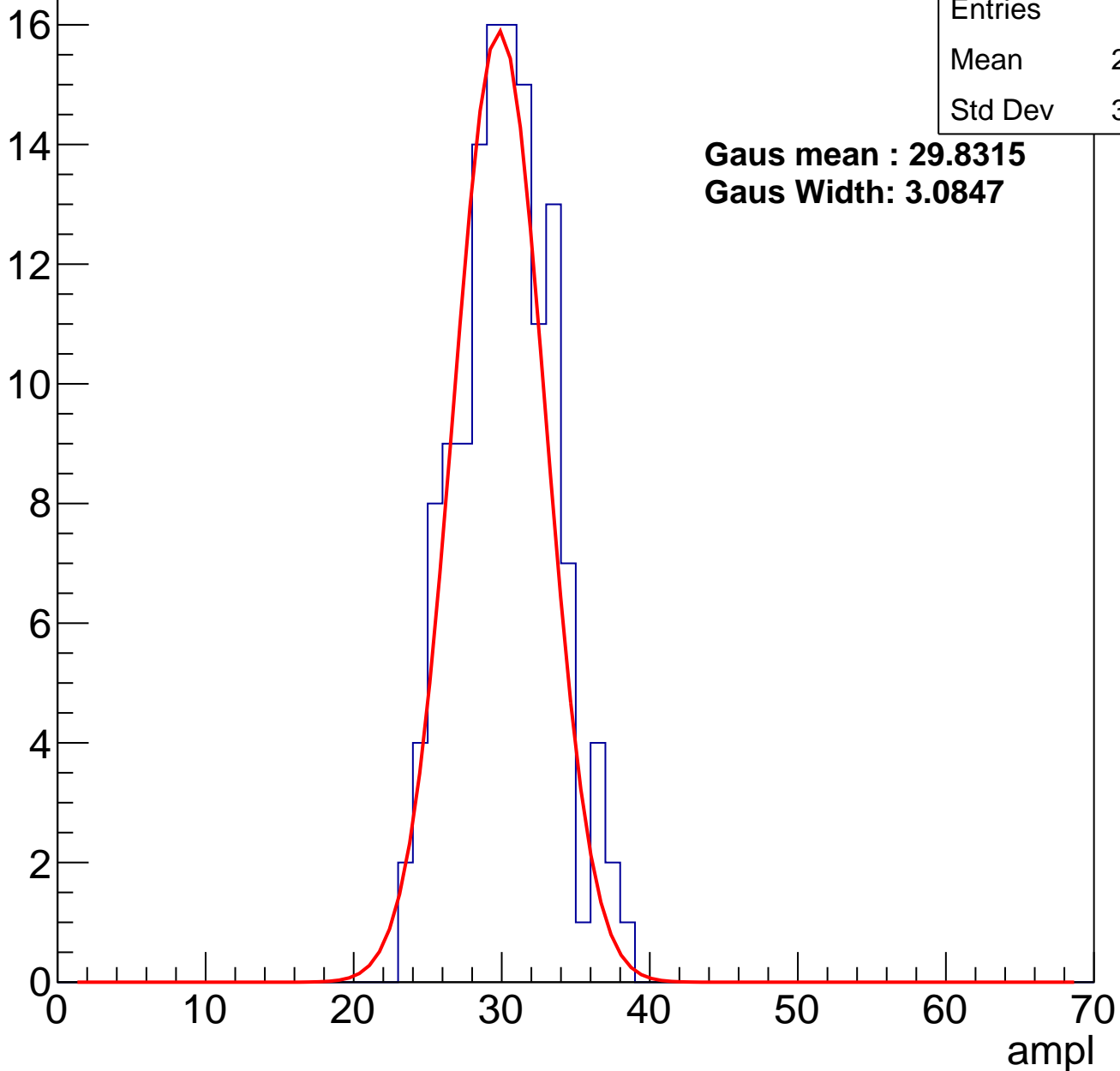


Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch120, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch120, adc1

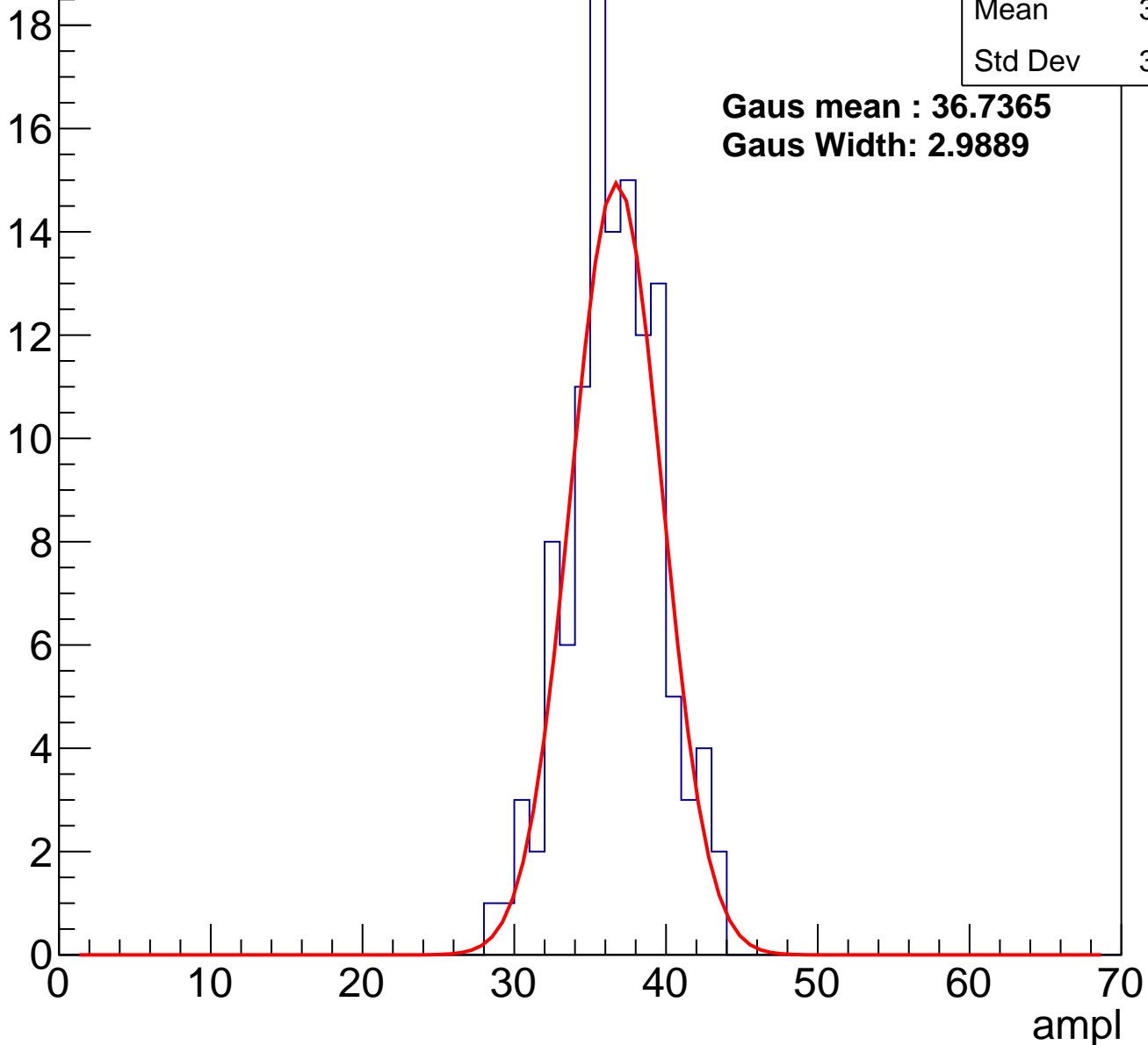
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	36.14
Std Dev	3.027

Gaus mean : 36.7365

Gaus Width: 2.9889

Entry



B1L001S, U22-ch120, adc2

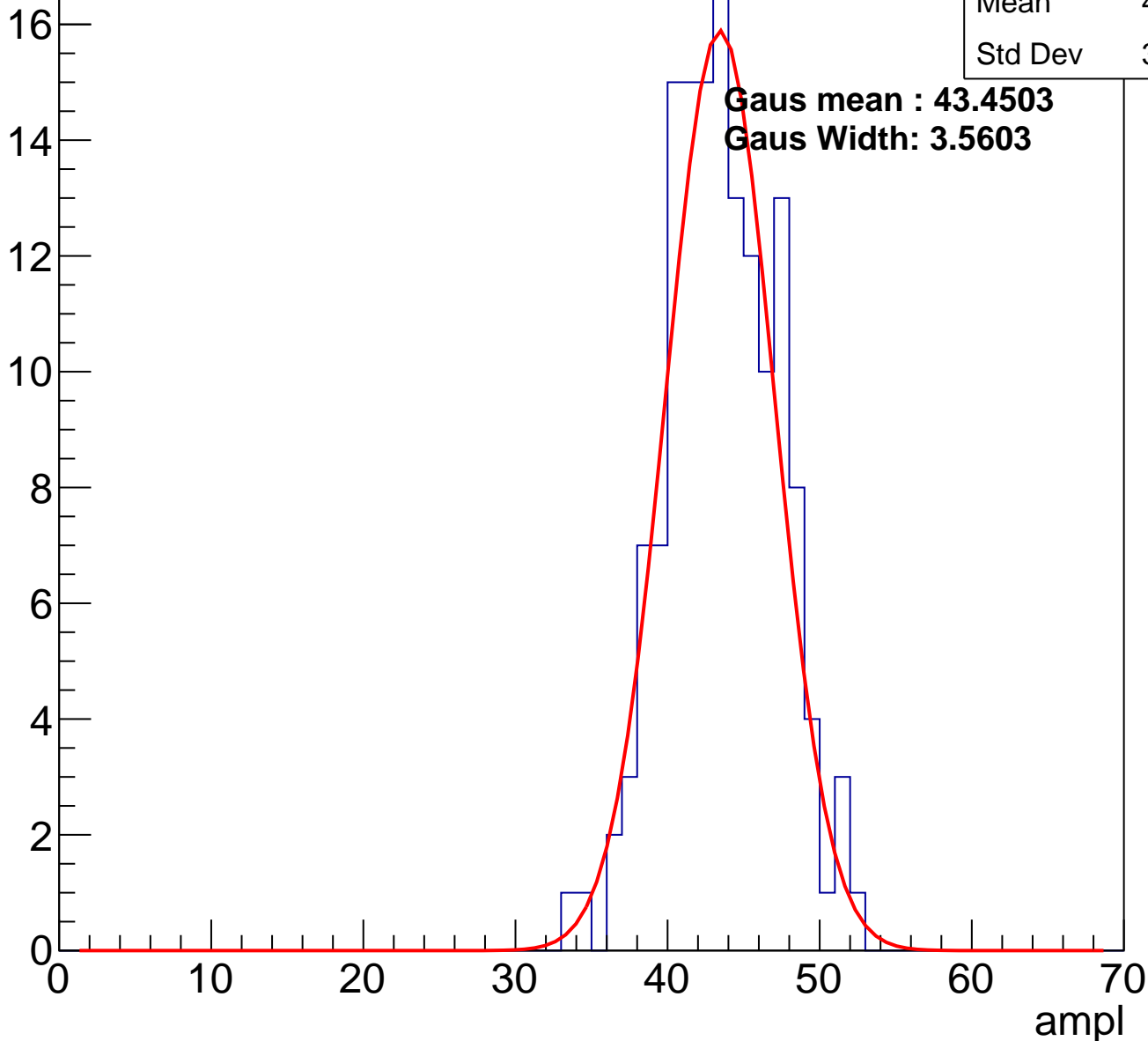
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	148
Mean	43.13
Std Dev	3.589

Gaus mean : 43.4503

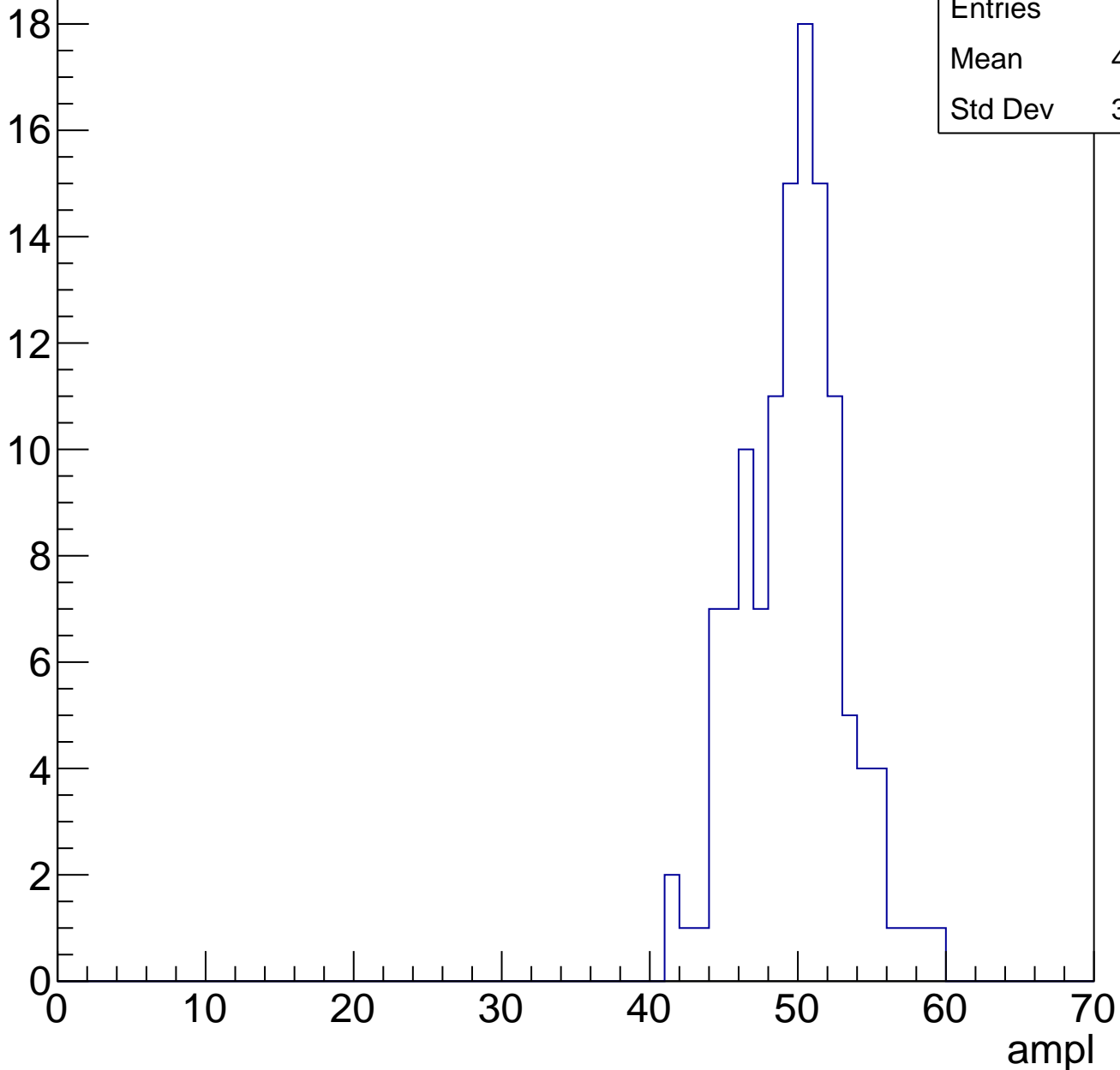
Gaus Width: 3.5603



B1L001S, U22-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch120, adc4

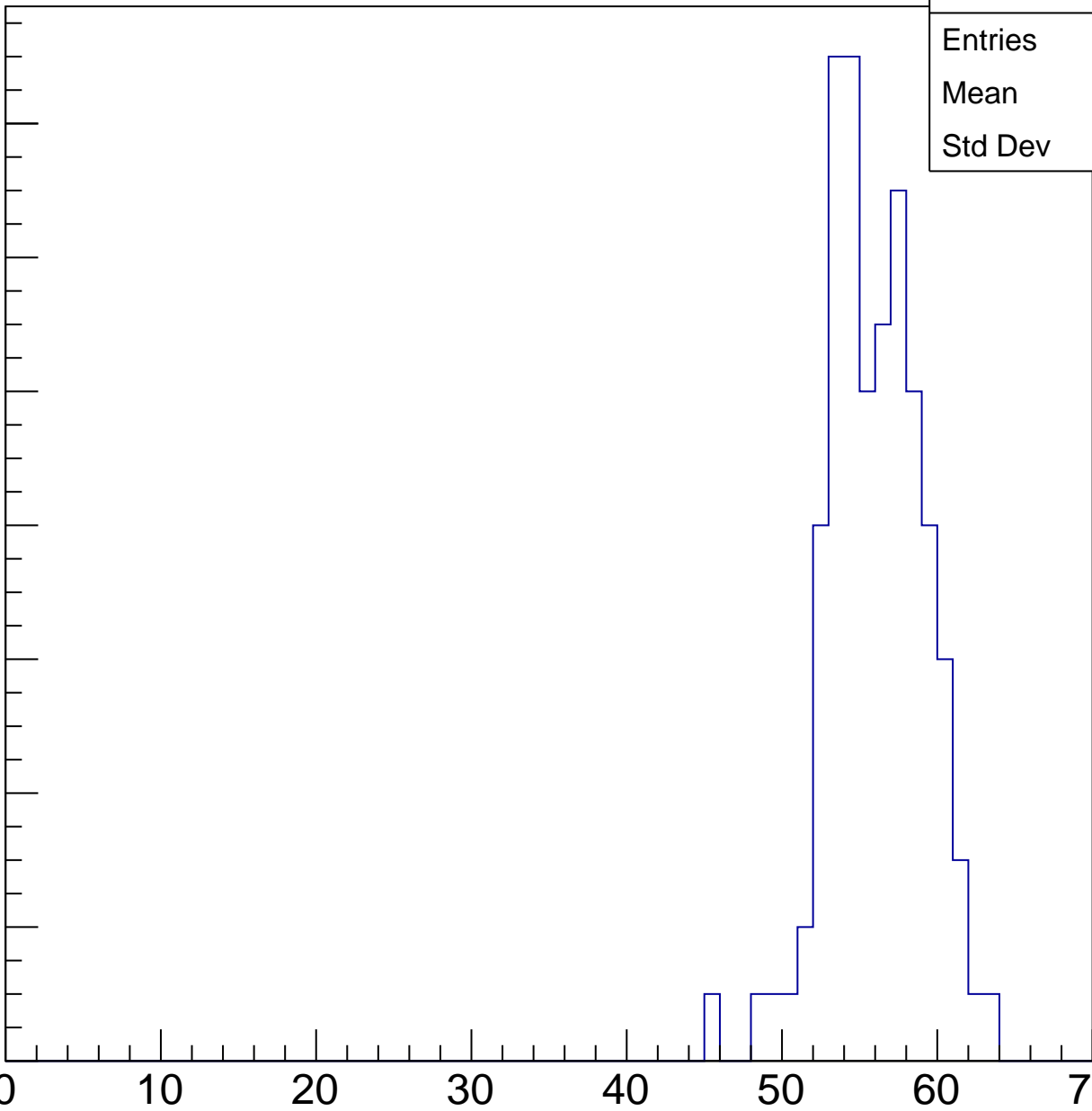
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	55.53
Std Dev	3.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch120, adc5

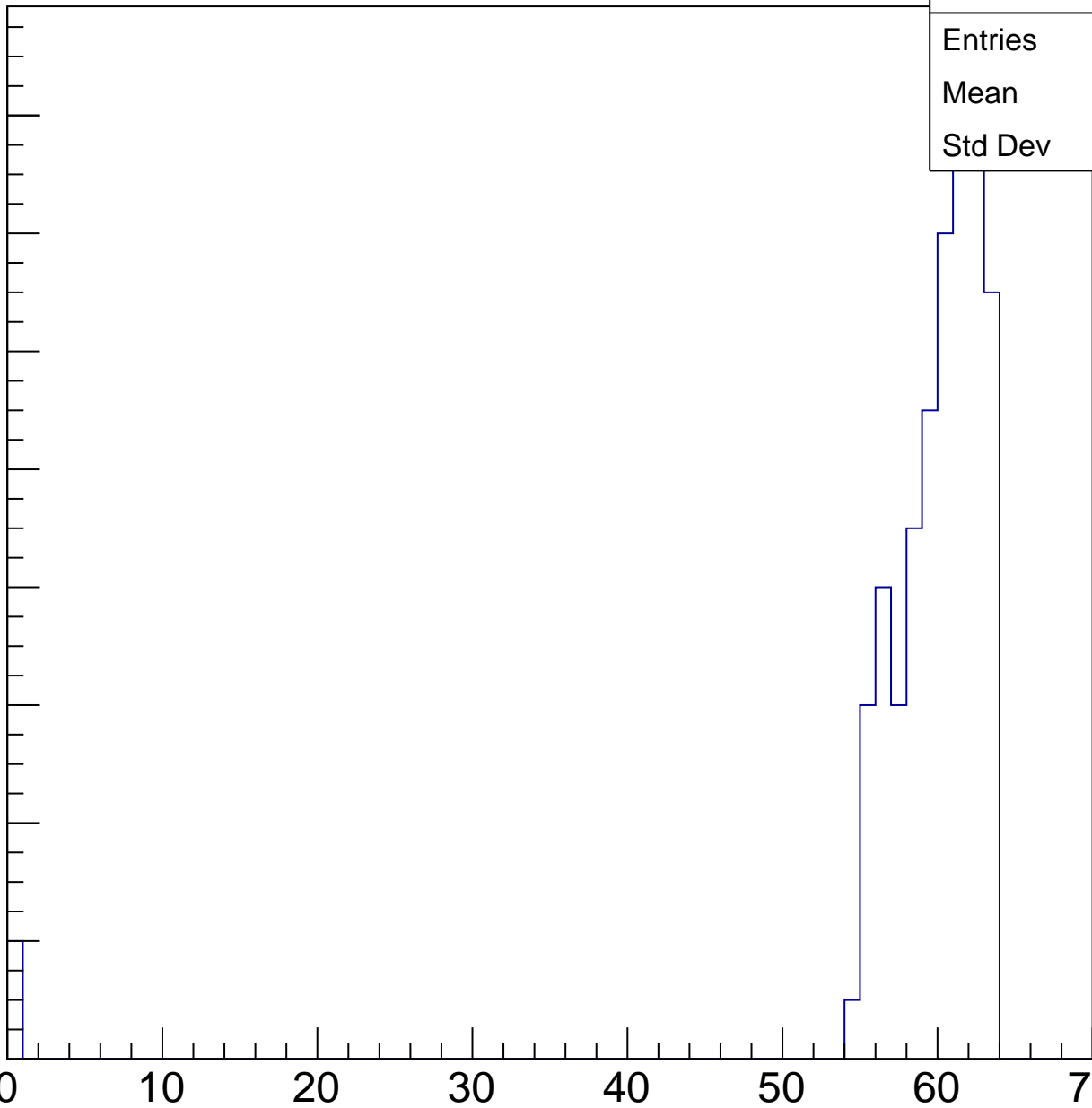
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.58
Std Dev	8.595

ampl

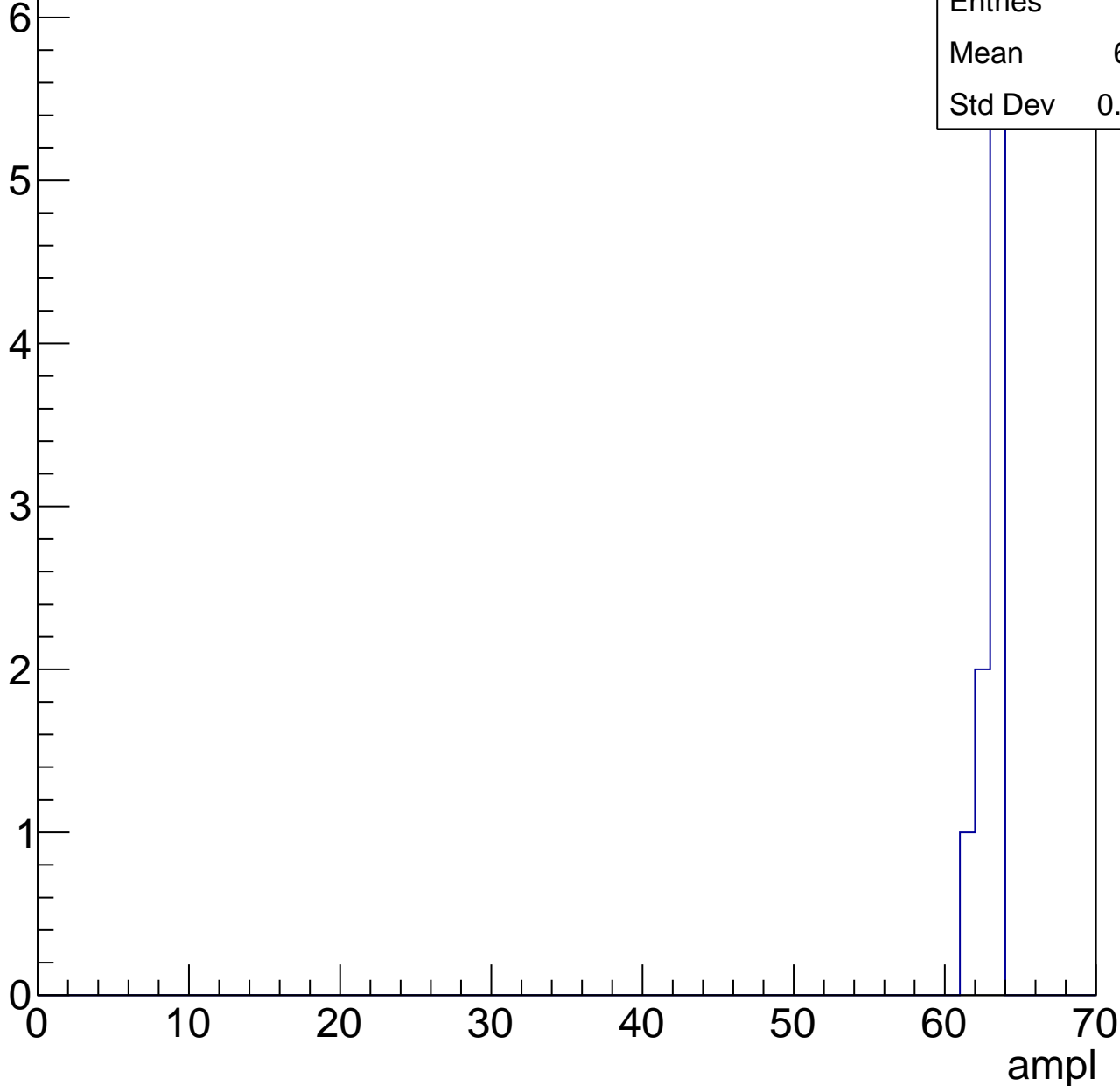


B1L001S, U22-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

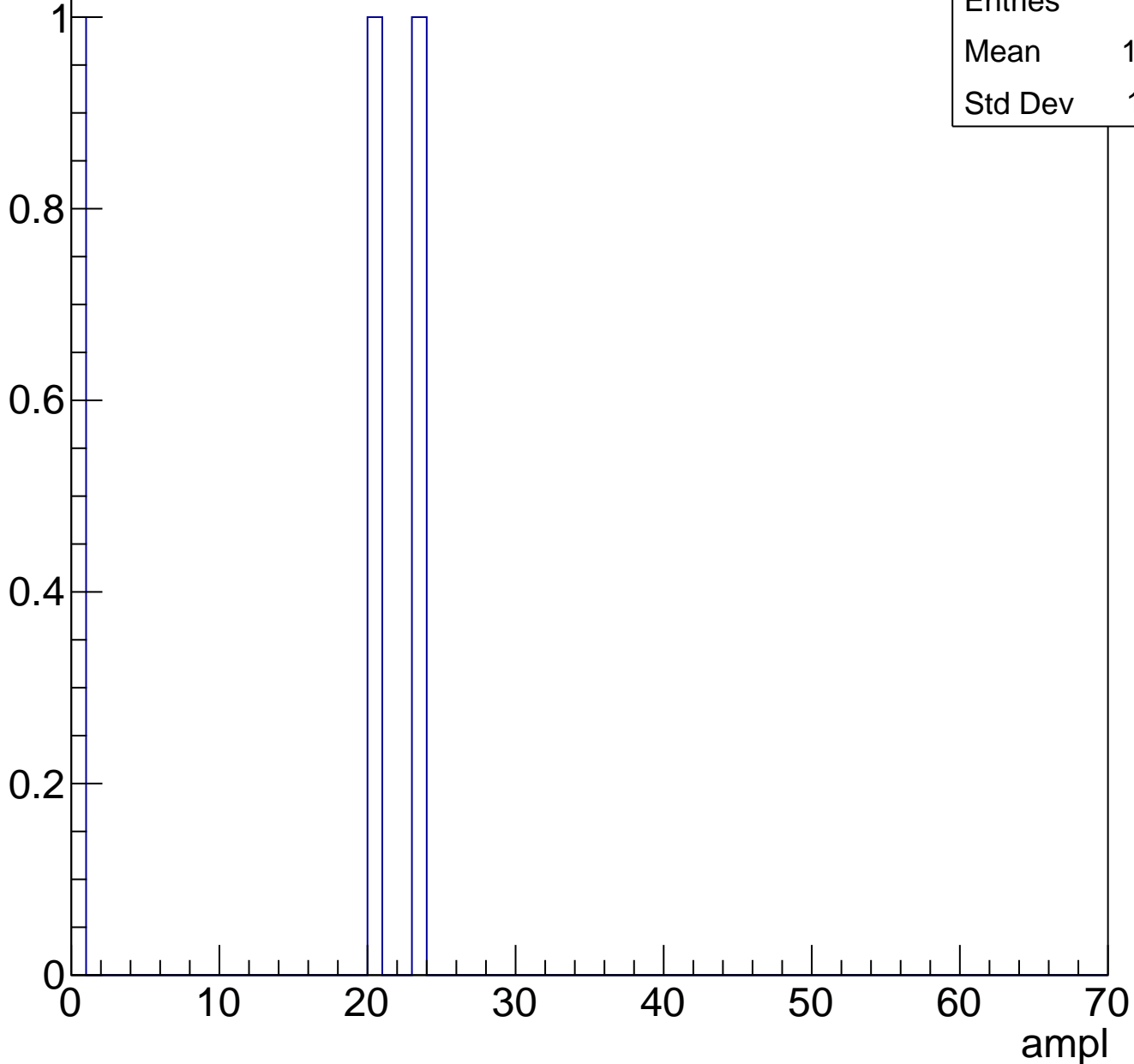
Entries	9
Mean	62.56
Std Dev	0.6849



B1L001S, U22-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch121, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	29.06
Std Dev	3.269

Gaus mean : 29.3952

Gaus Width: 3.9722

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

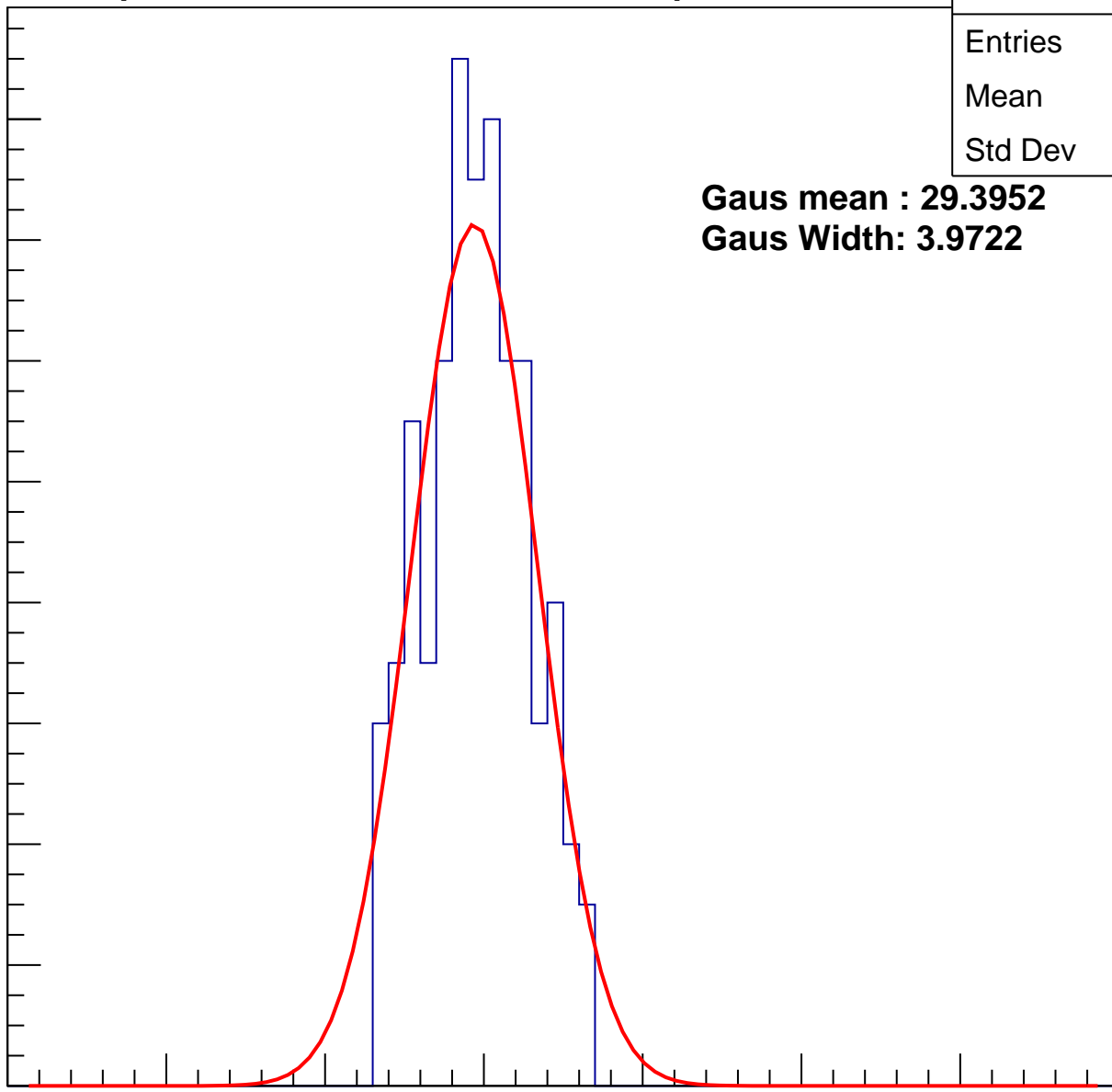
40

50

60

70

ampl



B1L001S, U22-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.35
Std Dev	3.227

Gaus mean : 35.9453

Gaus Width: 2.9942

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

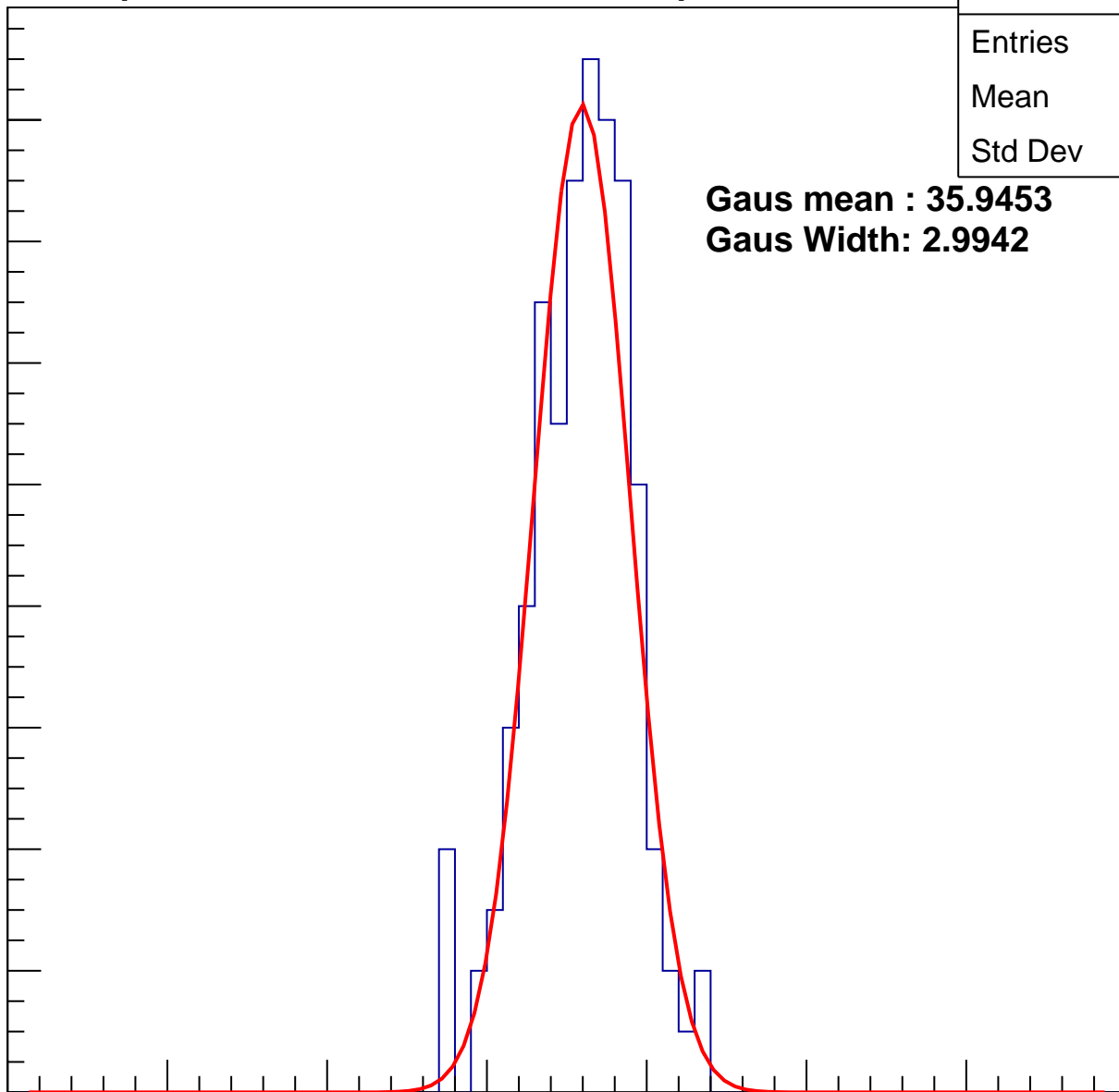
40

50

60

70

ampl



B1L001S, U22-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

128

Mean

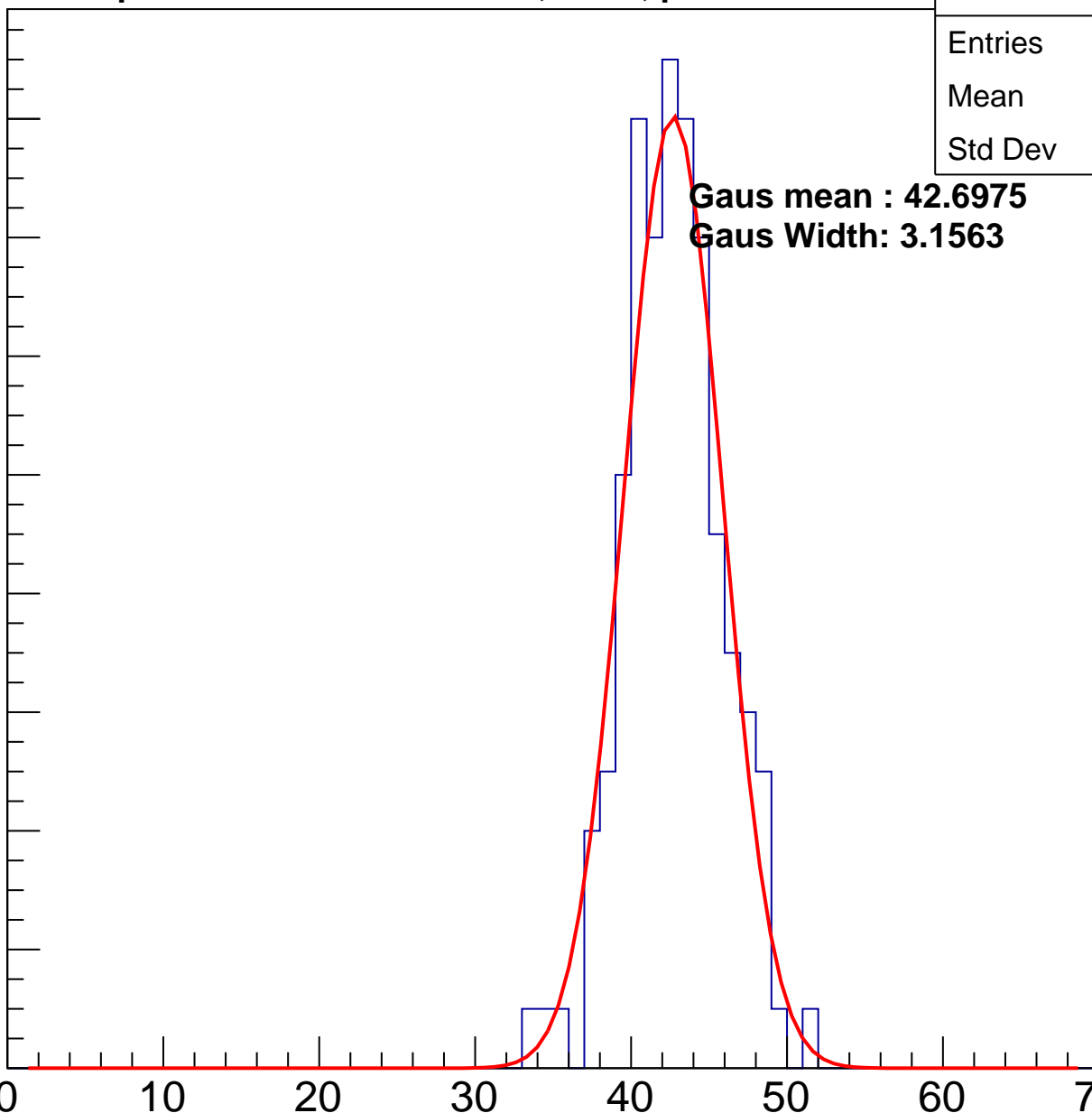
42.27

Std Dev

3.134

Gaus mean : 42.6975

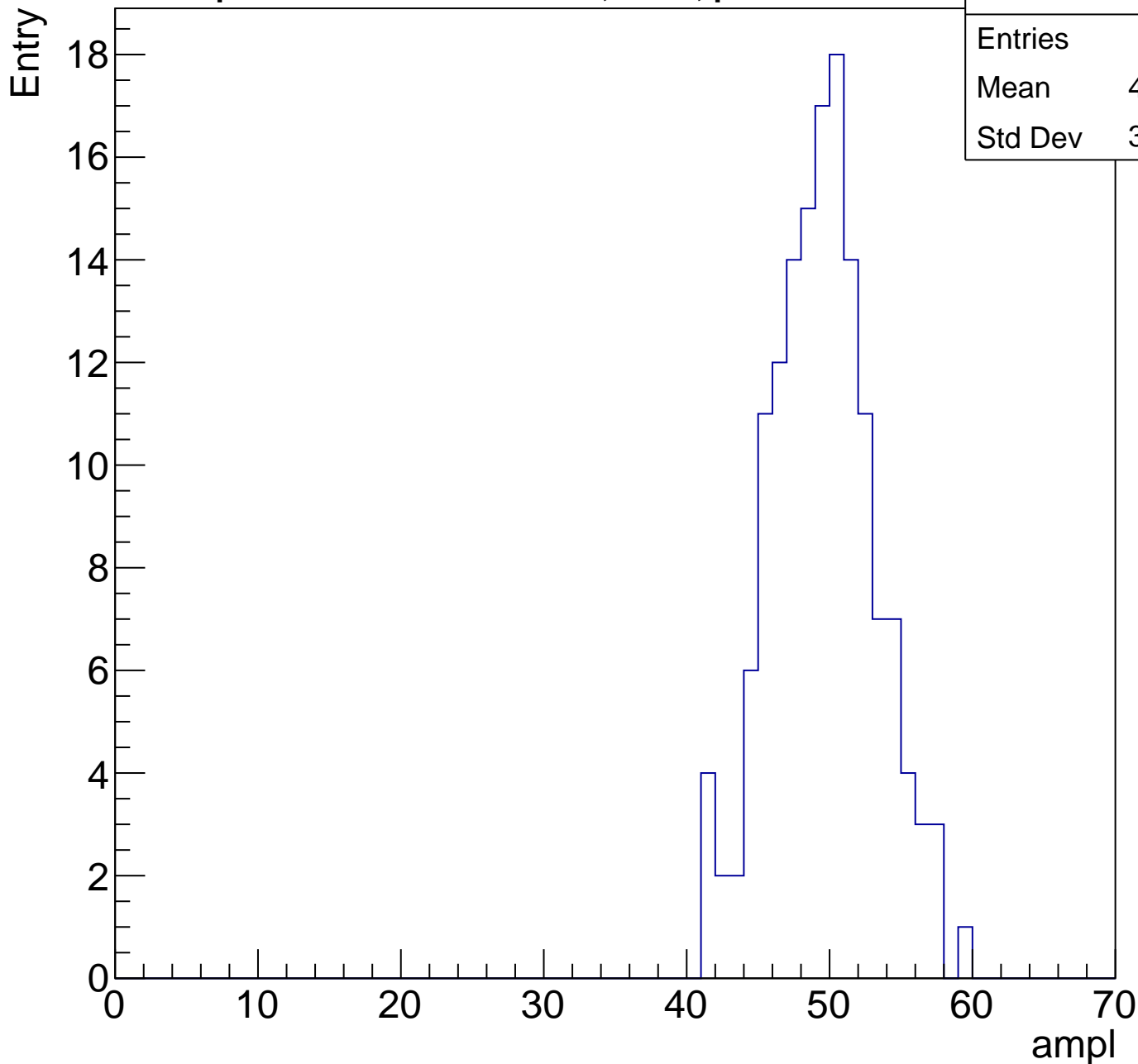
Gaus Width: 3.1563



B1L001S, U22-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	49.07
Std Dev	3.615



B1L001S, U22-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

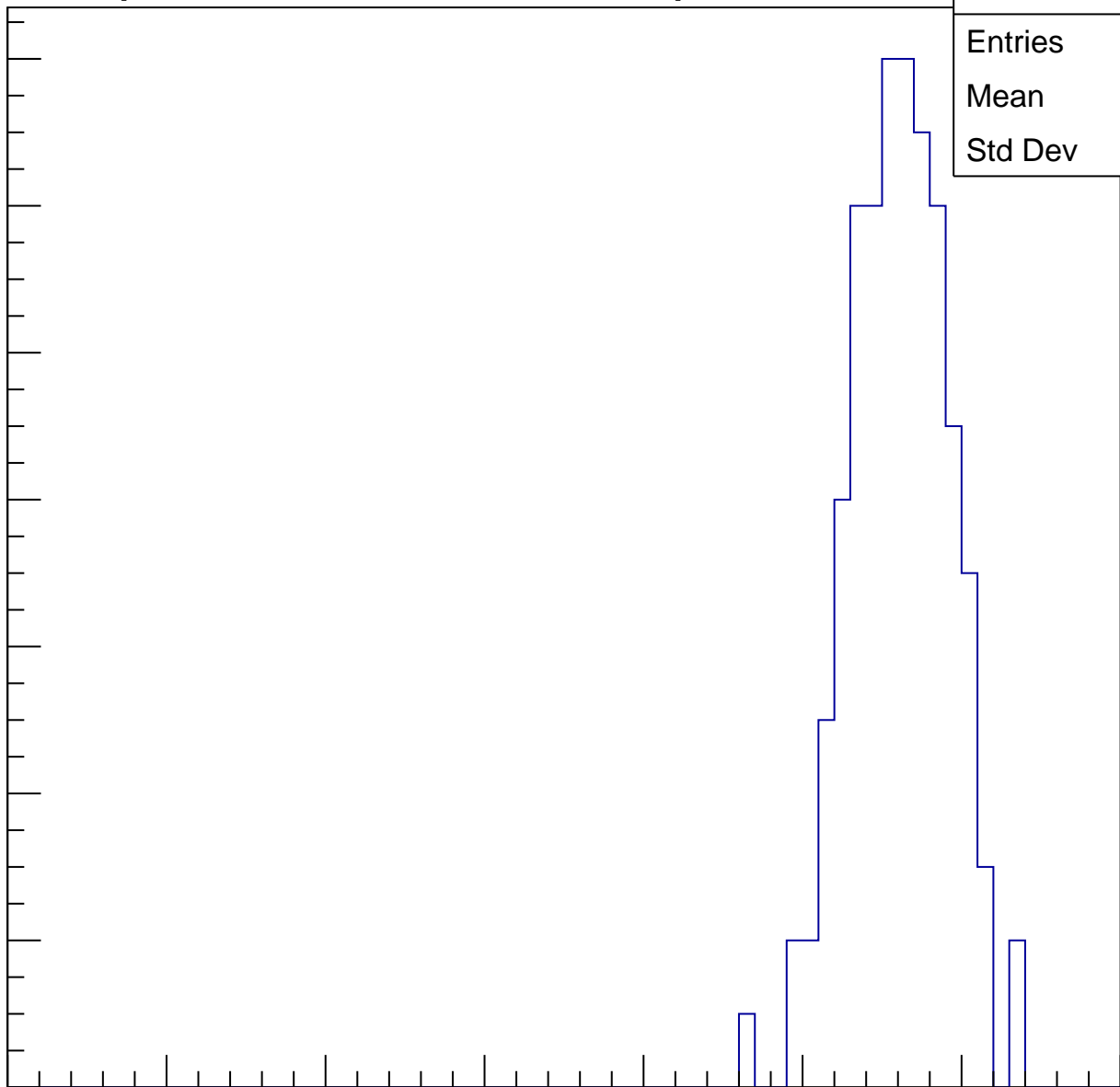
Entries	116
Mean	55.6
Std Dev	3.082

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch121, adc5

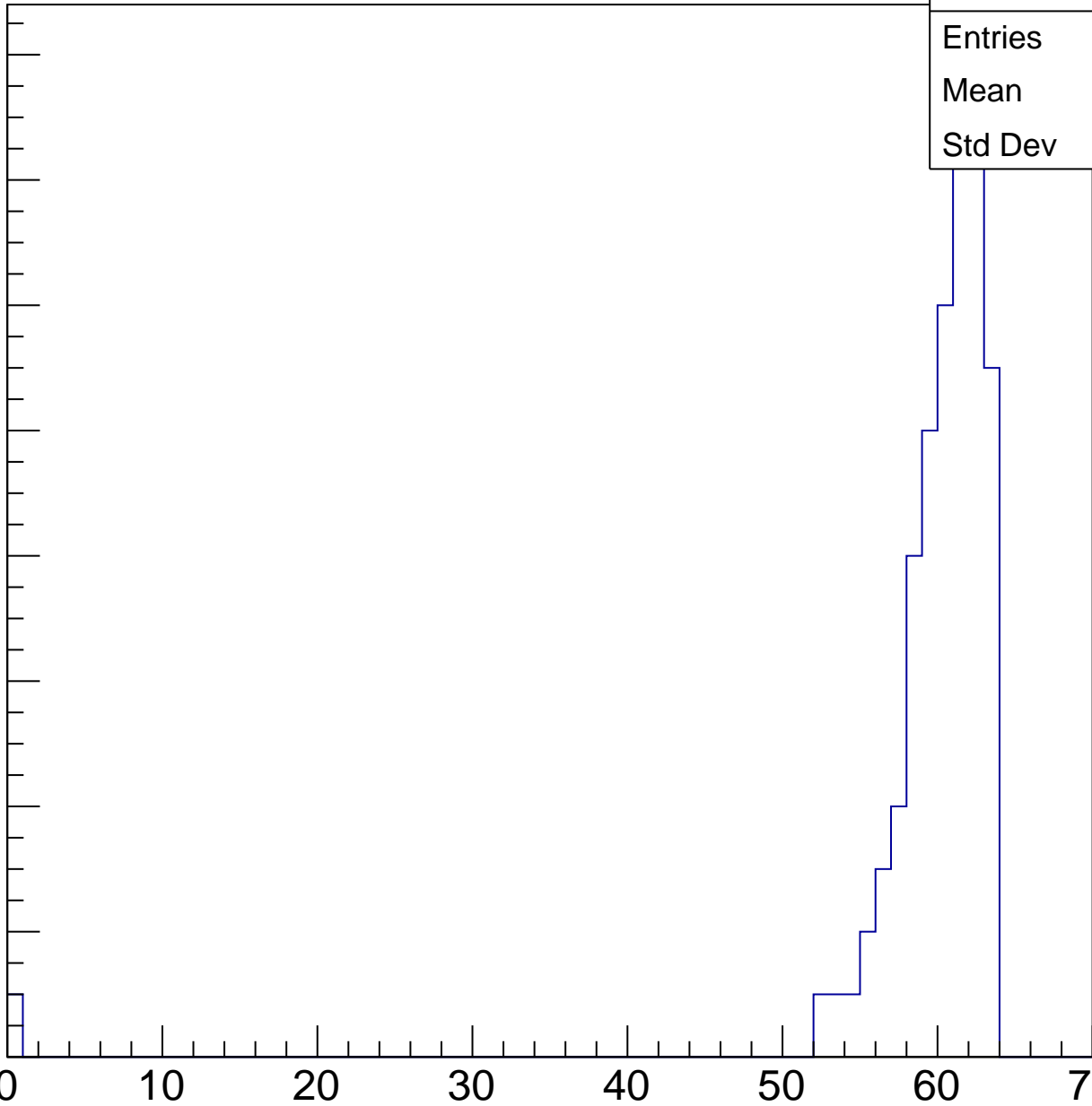
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.28
Std Dev	6.911

ampl

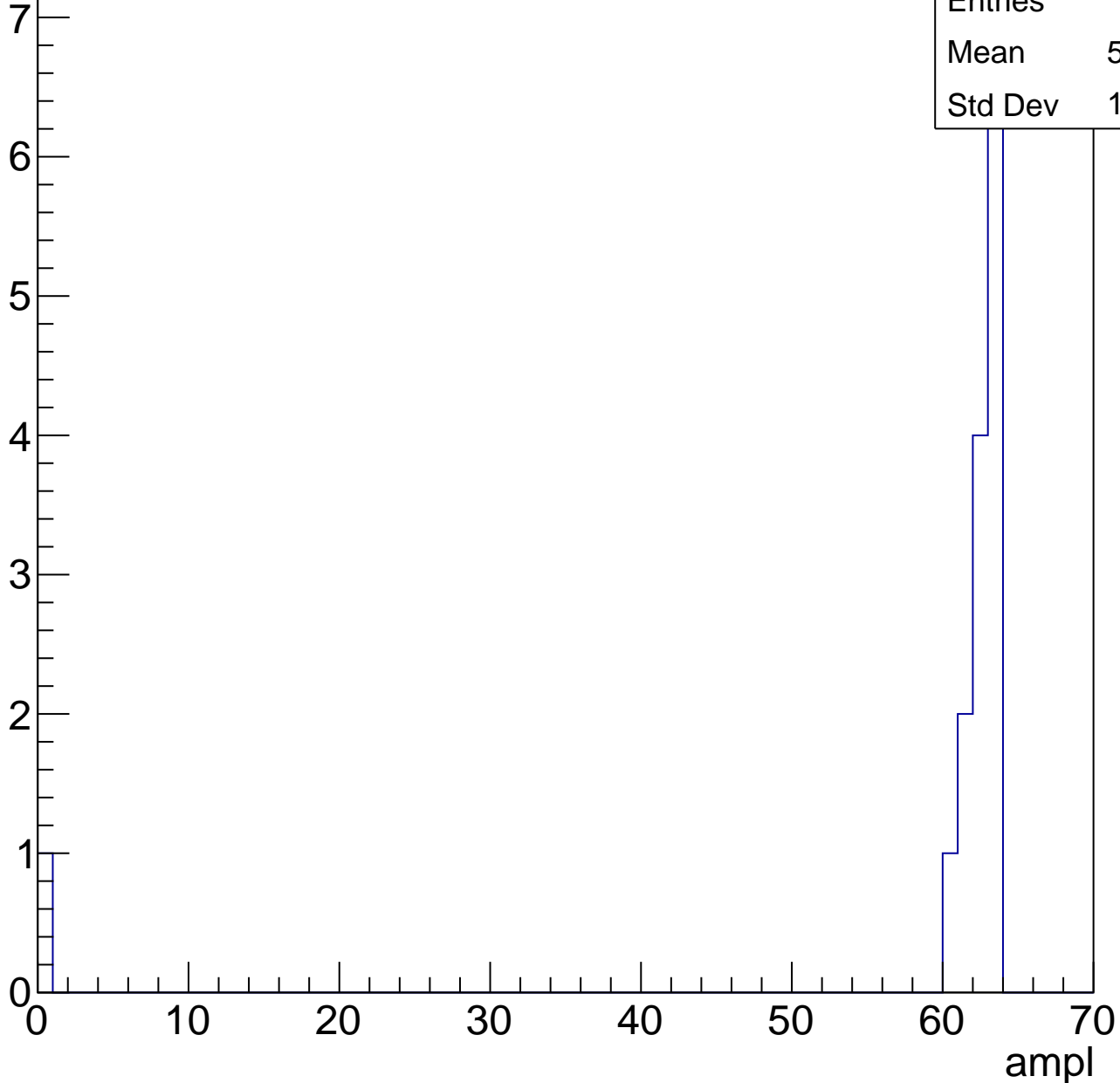


B1L001S, U22-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	58.07
Std Dev	15.55



B1L001S, U22-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch122, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	30.48
Std Dev	3.082

Gaus mean : 30.7372

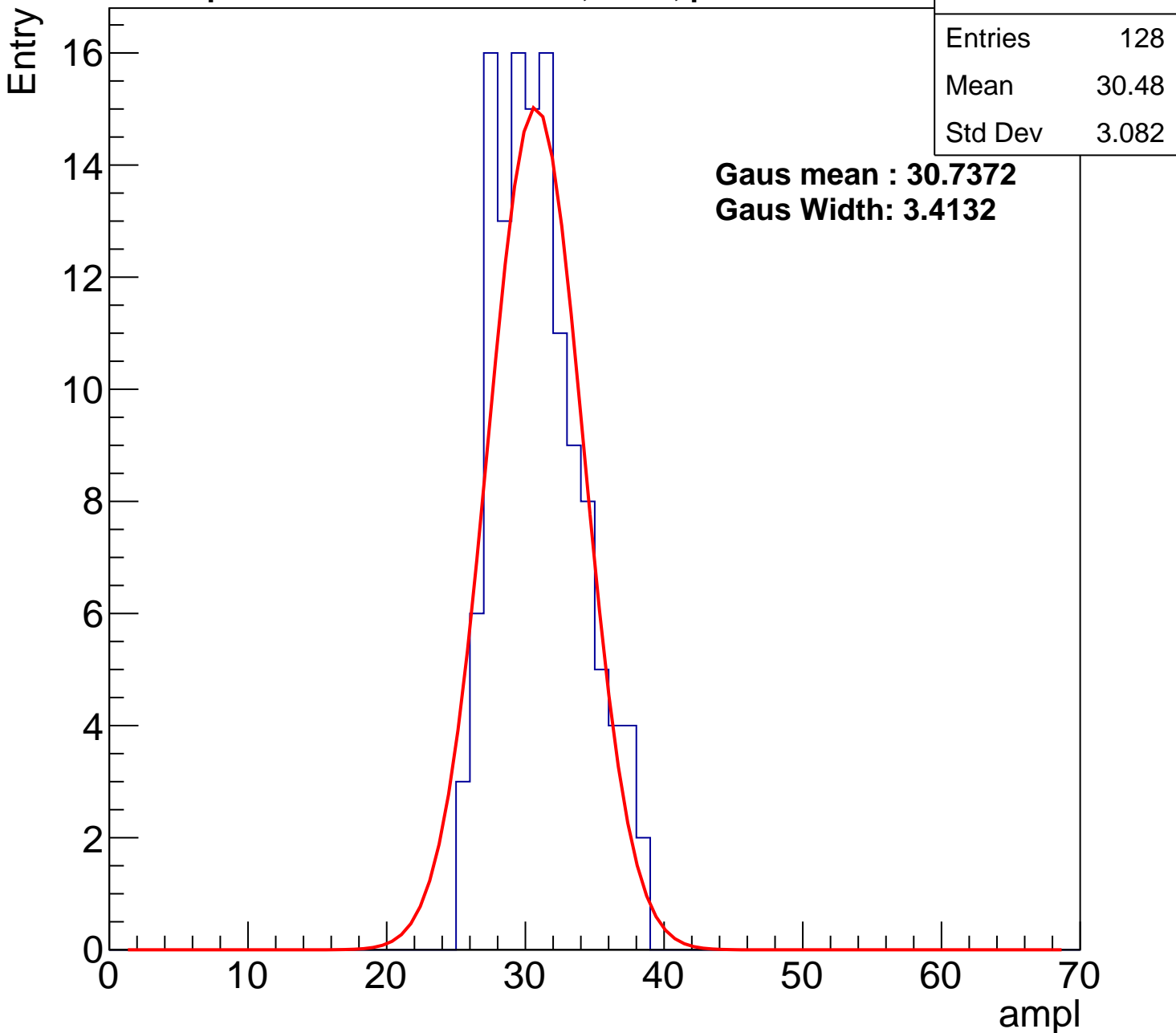
Gaus Width: 3.4132

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch122, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.58
Std Dev	3.545

Gaus mean : 38.1049

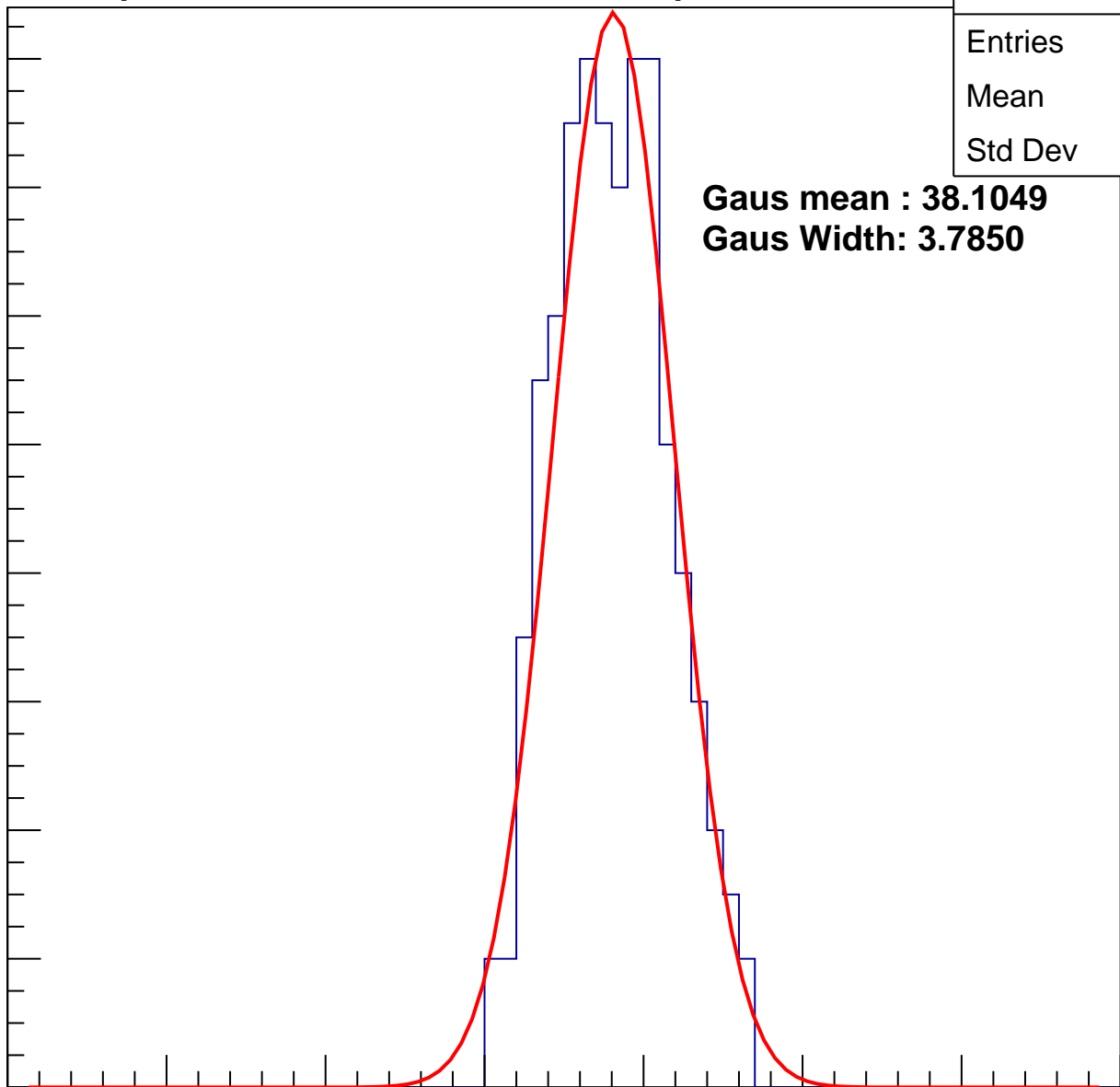
Gaus Width: 3.7850

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U22-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

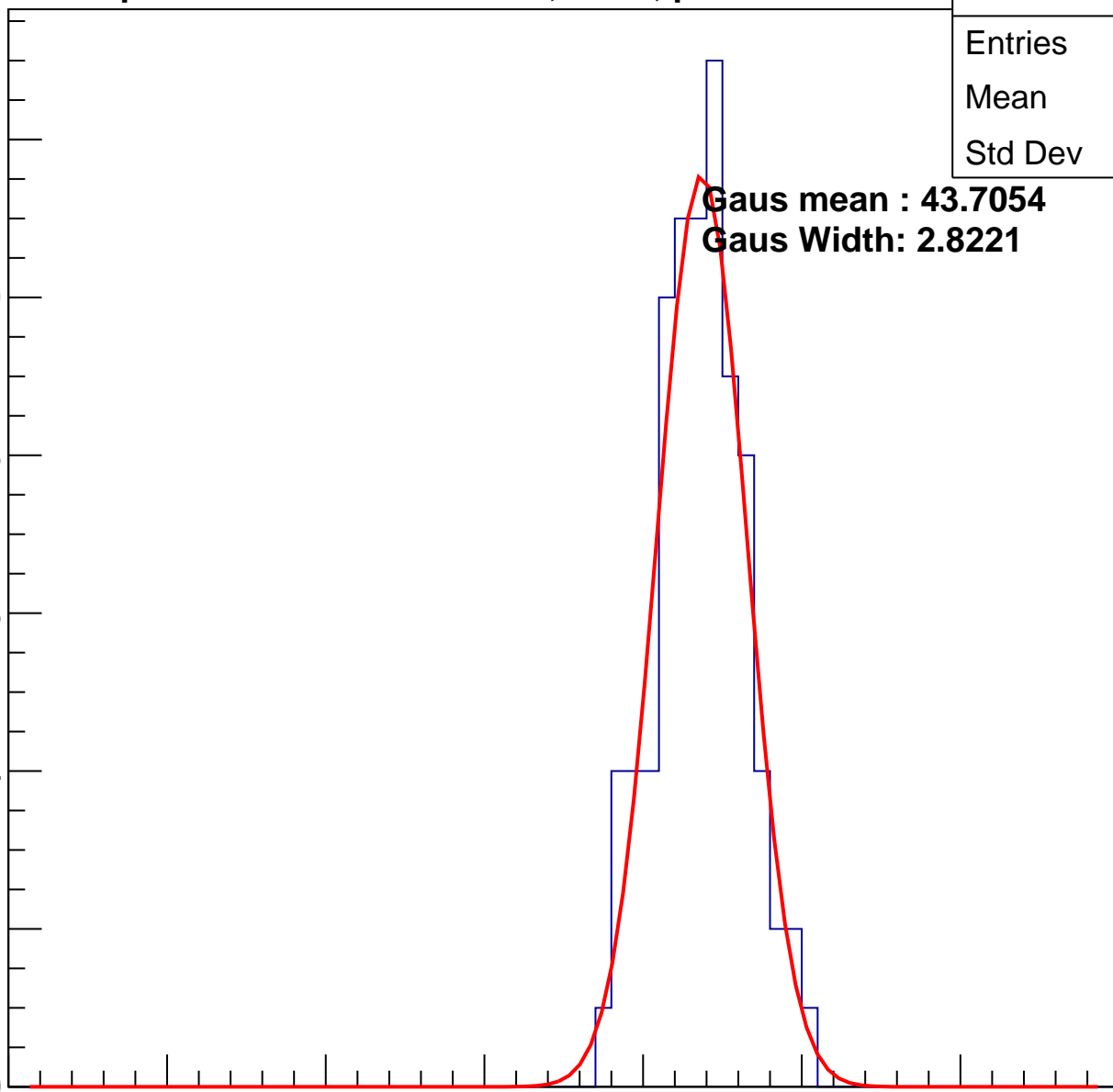
70

ampl

Entries	84
Mean	43.18
Std Dev	2.761

Gaus mean : 43.7054

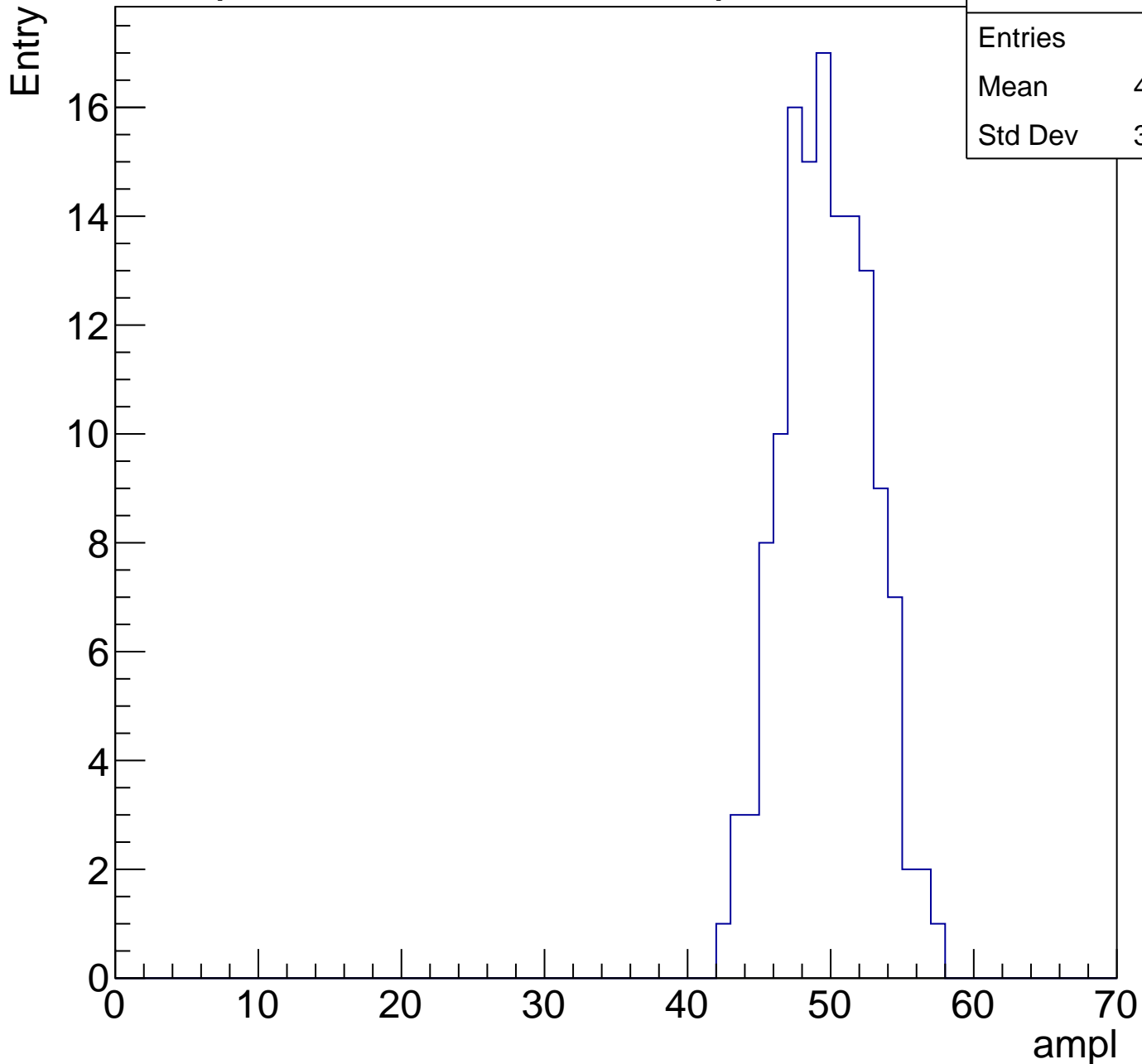
Gaus Width: 2.8221



B1L001S, U22-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

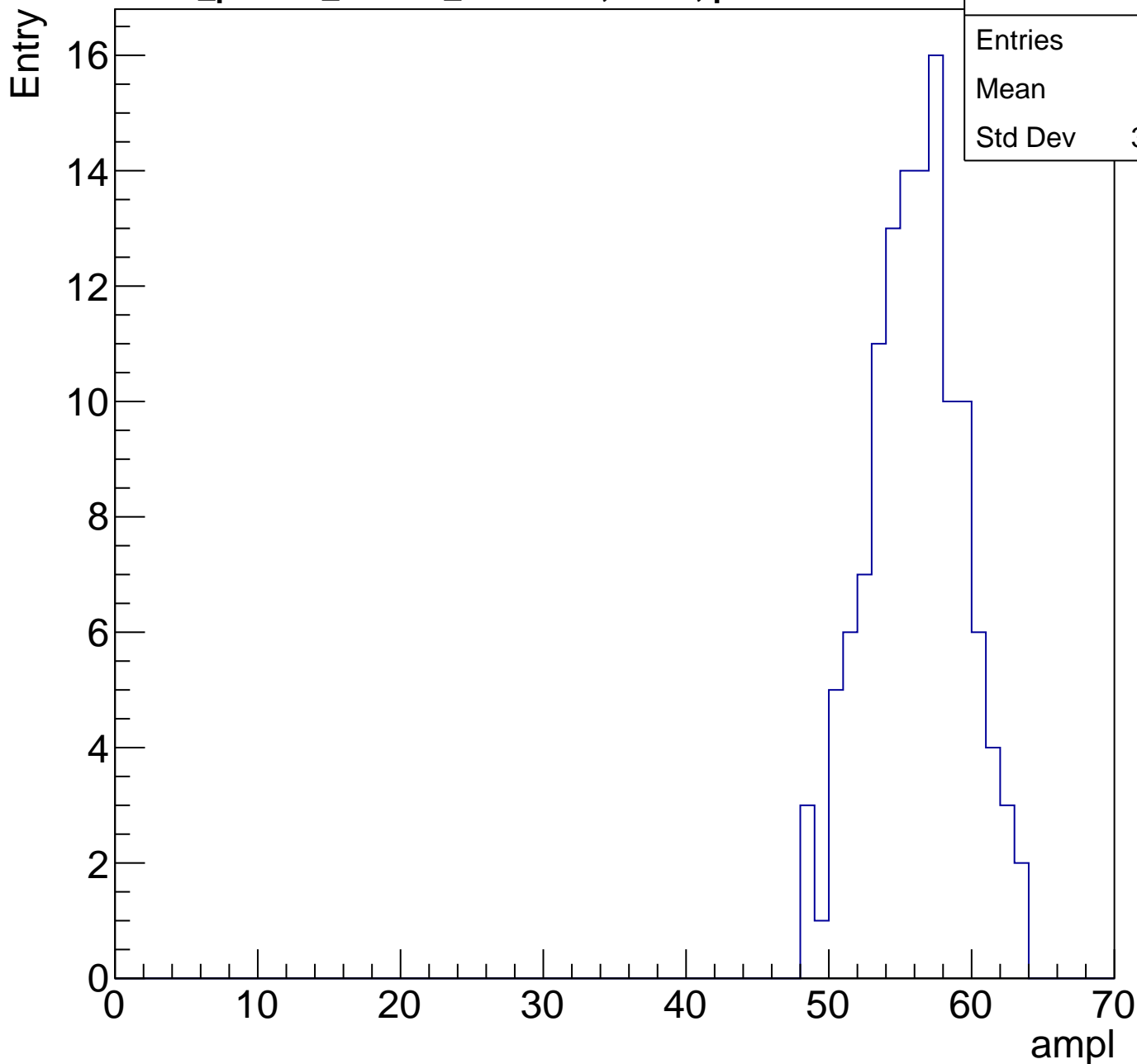
Entries	135
Mean	49.27
Std Dev	3.054



B1L001S, U22-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.6
Std Dev	3.318



B1L001S, U22-ch122, adc5

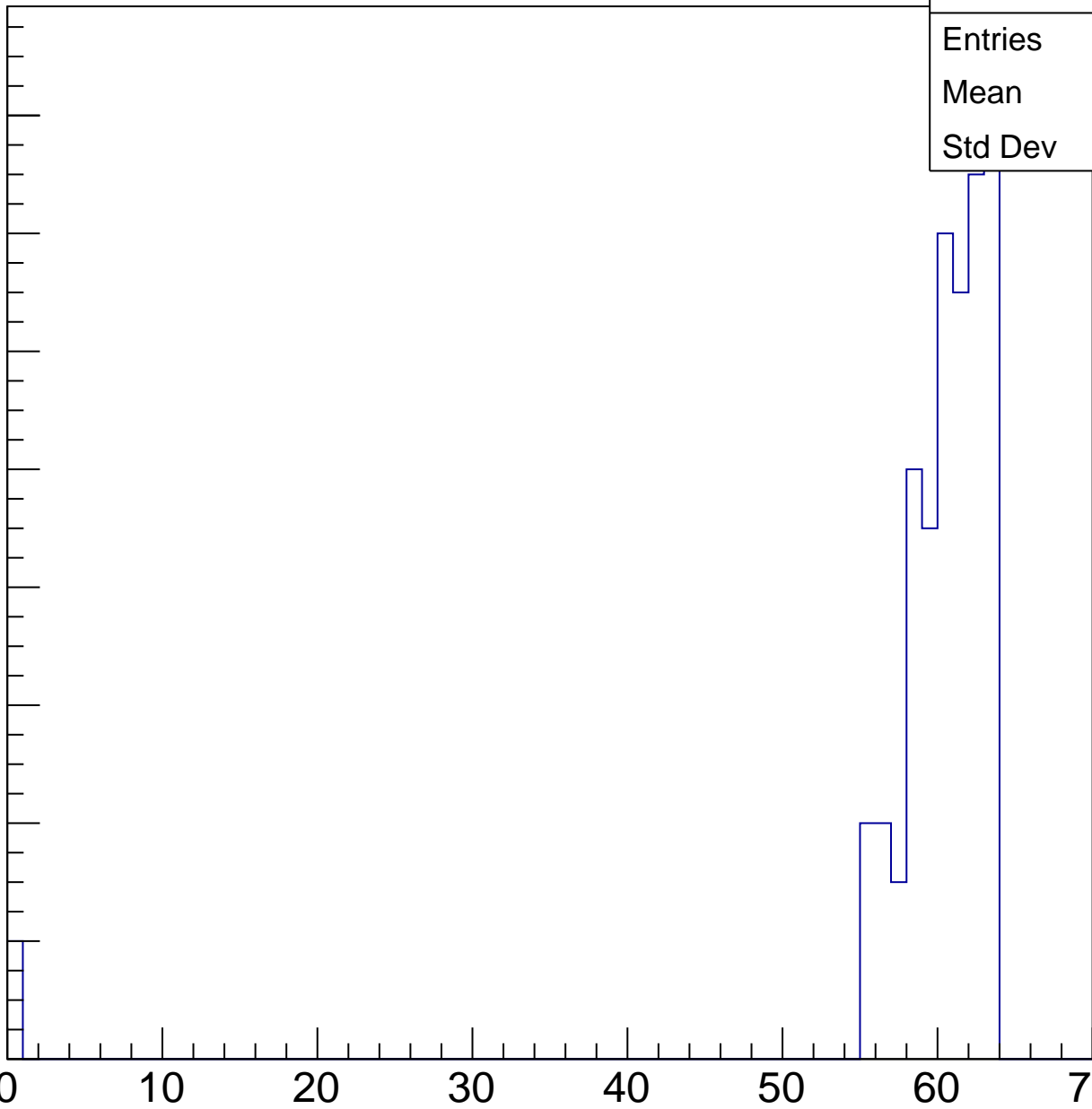
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.9
Std Dev	9.112

ampl



B1L001S, U22-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

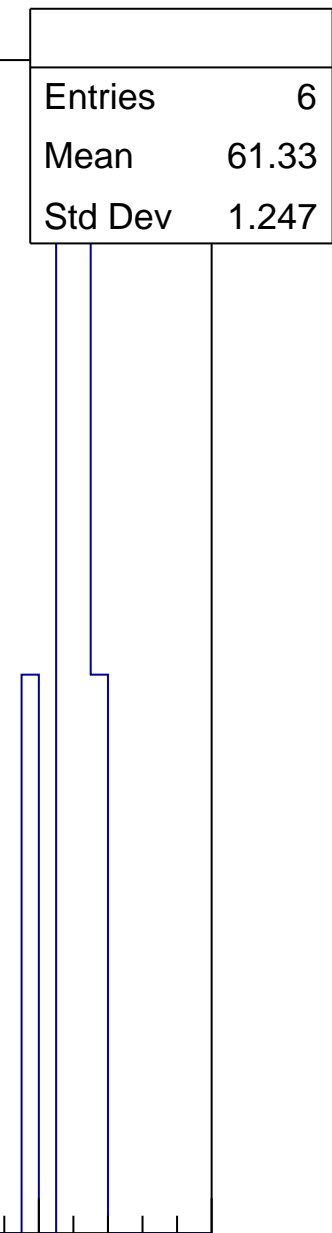
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.247

0 10 20 30 40 50 60 70

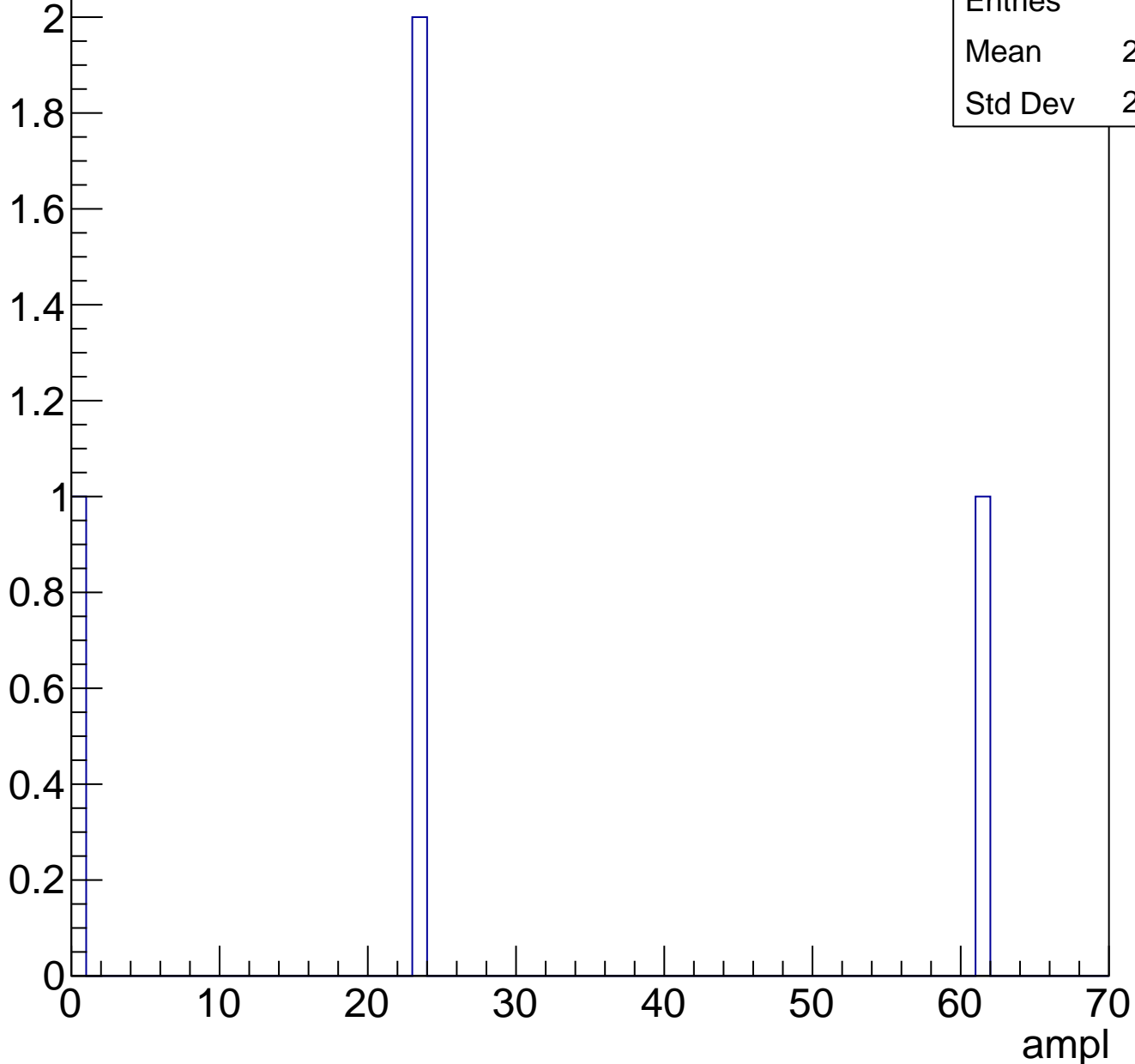
ampl



B1L001S, U22-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	26.75
Std Dev	21.89

B1L001S, U22-ch123, adc0

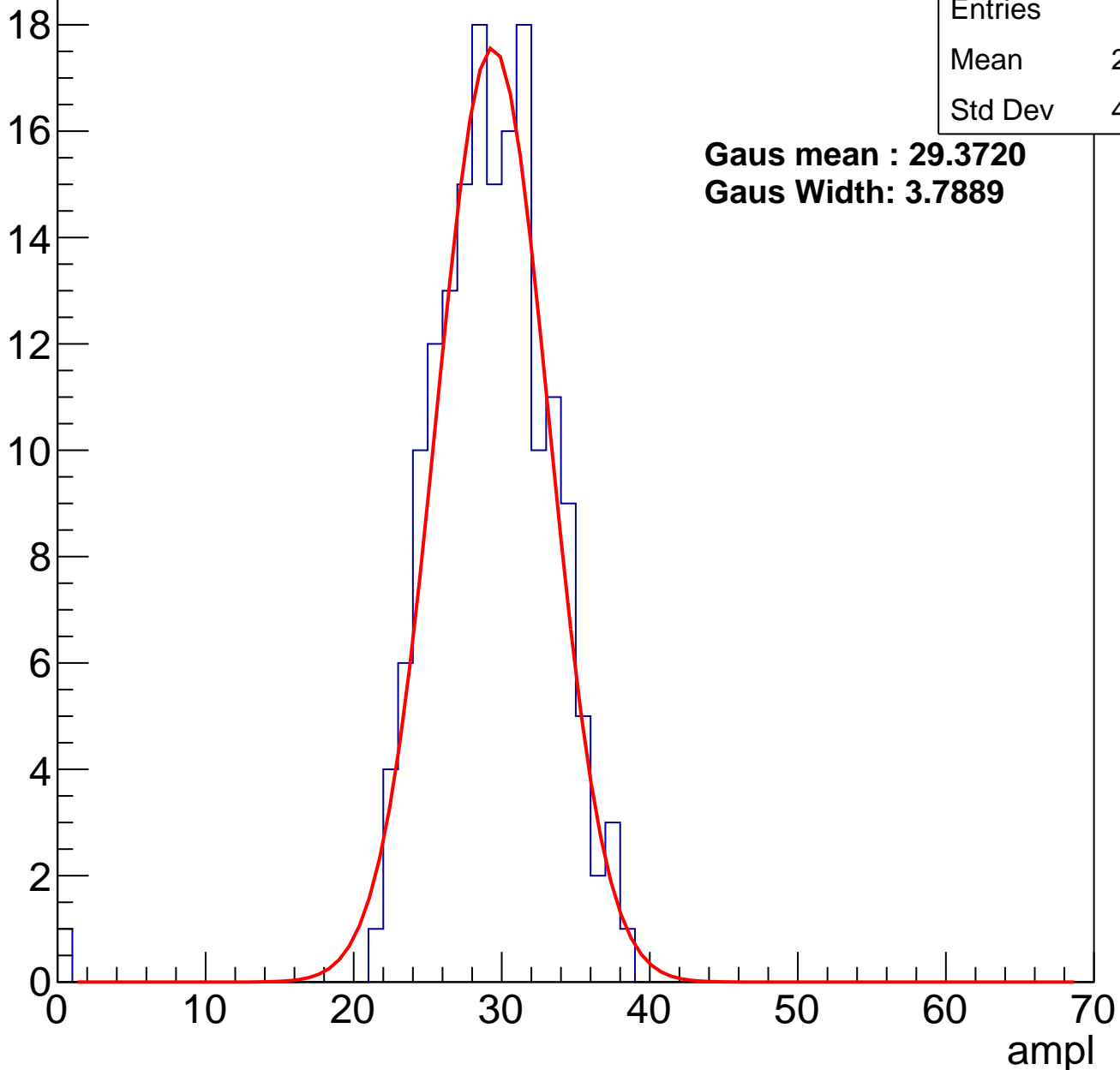
calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	28.78
Std Dev	4.253

Gaus mean : 29.3720

Gaus Width: 3.7889

Entry



B1L001S, U22-ch123, adc1

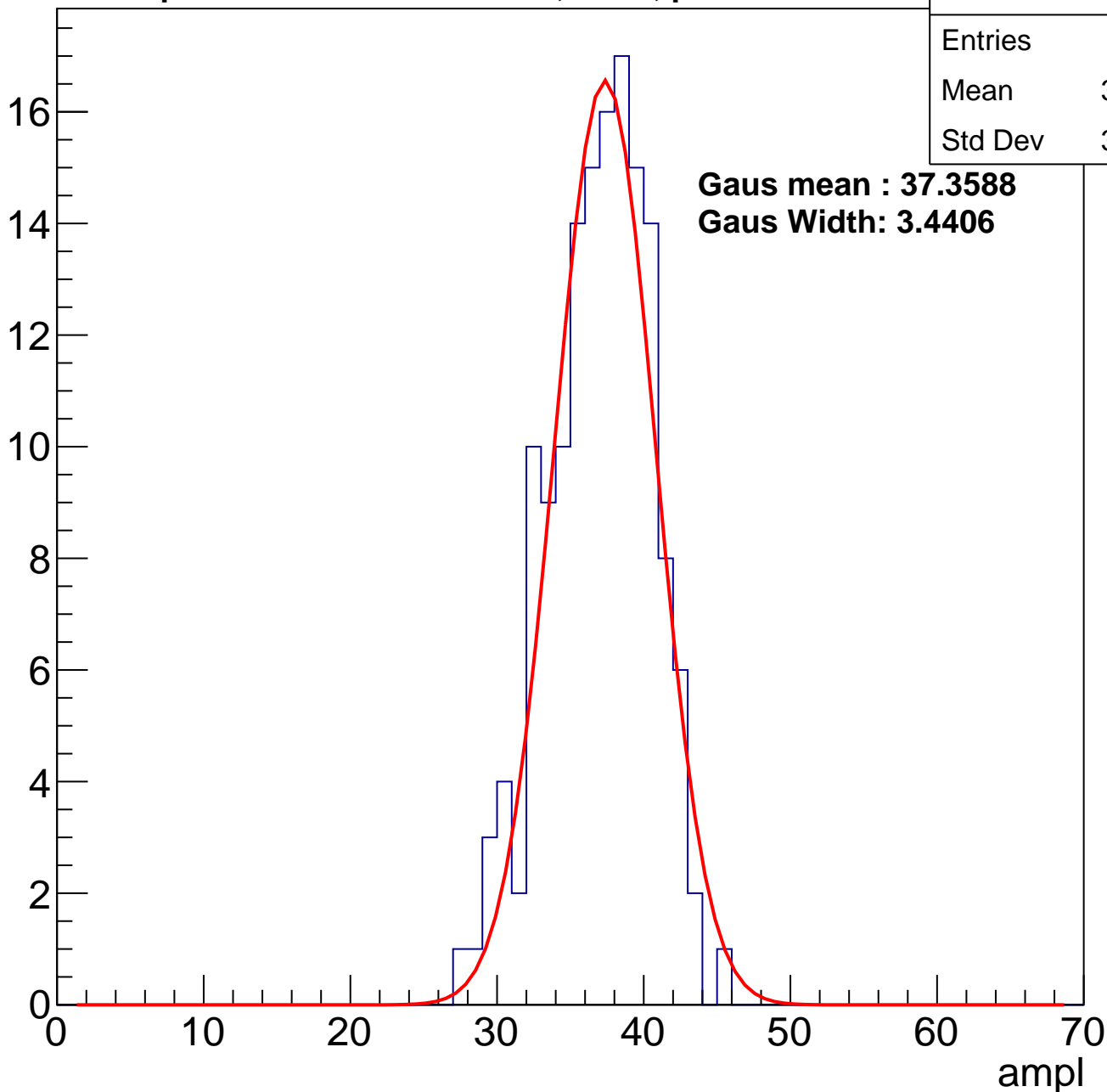
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.52
Std Dev	3.467

Gaus mean : 37.3588

Gaus Width: 3.4406

Entry



B1L001S, U22-ch123, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

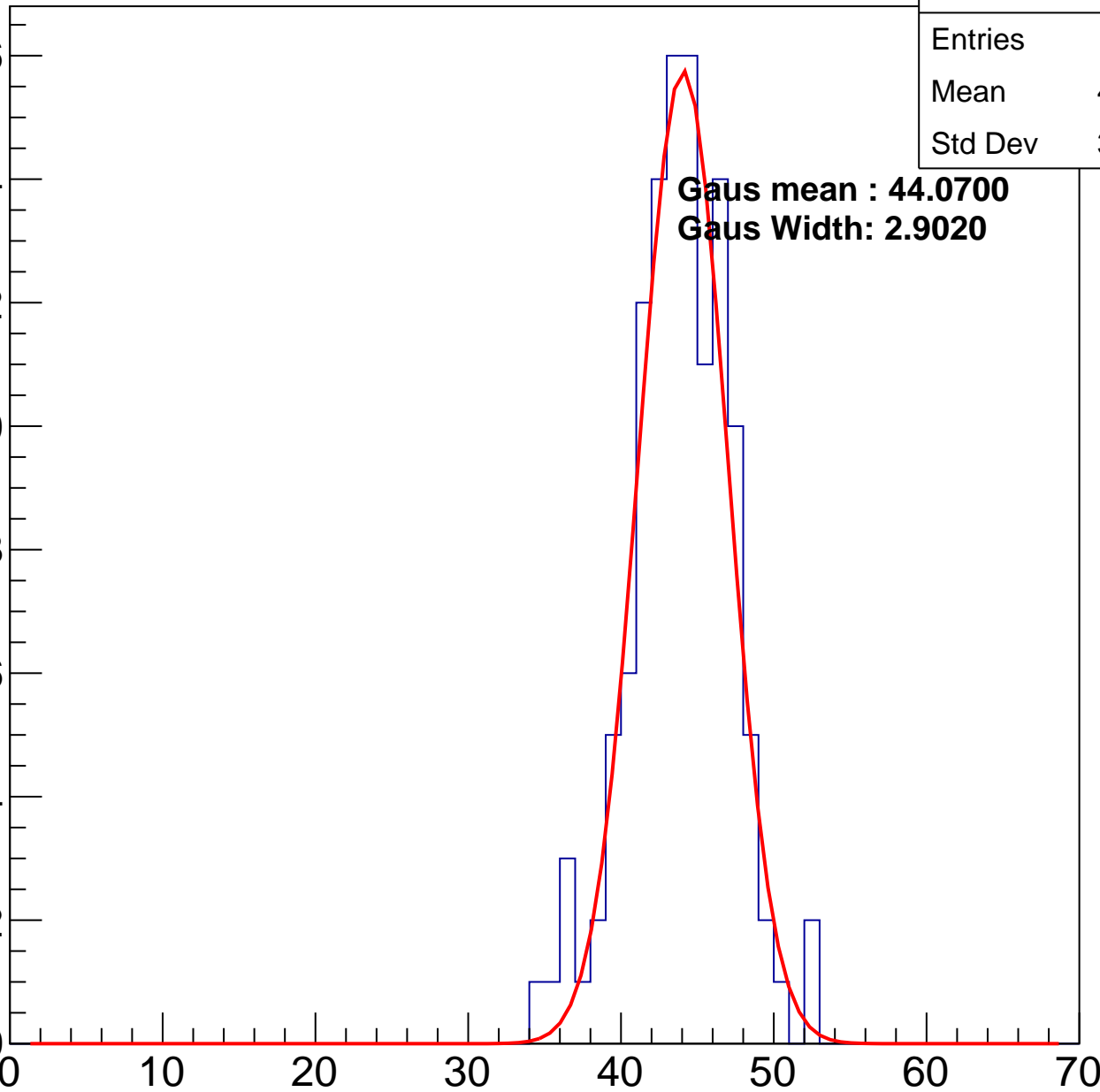
16
14
12
10
8
6
4
2
0

Entries	122
Mean	43.43
Std Dev	3.272

Gaus mean : 44.0700

Gaus Width: 2.9020

ampl

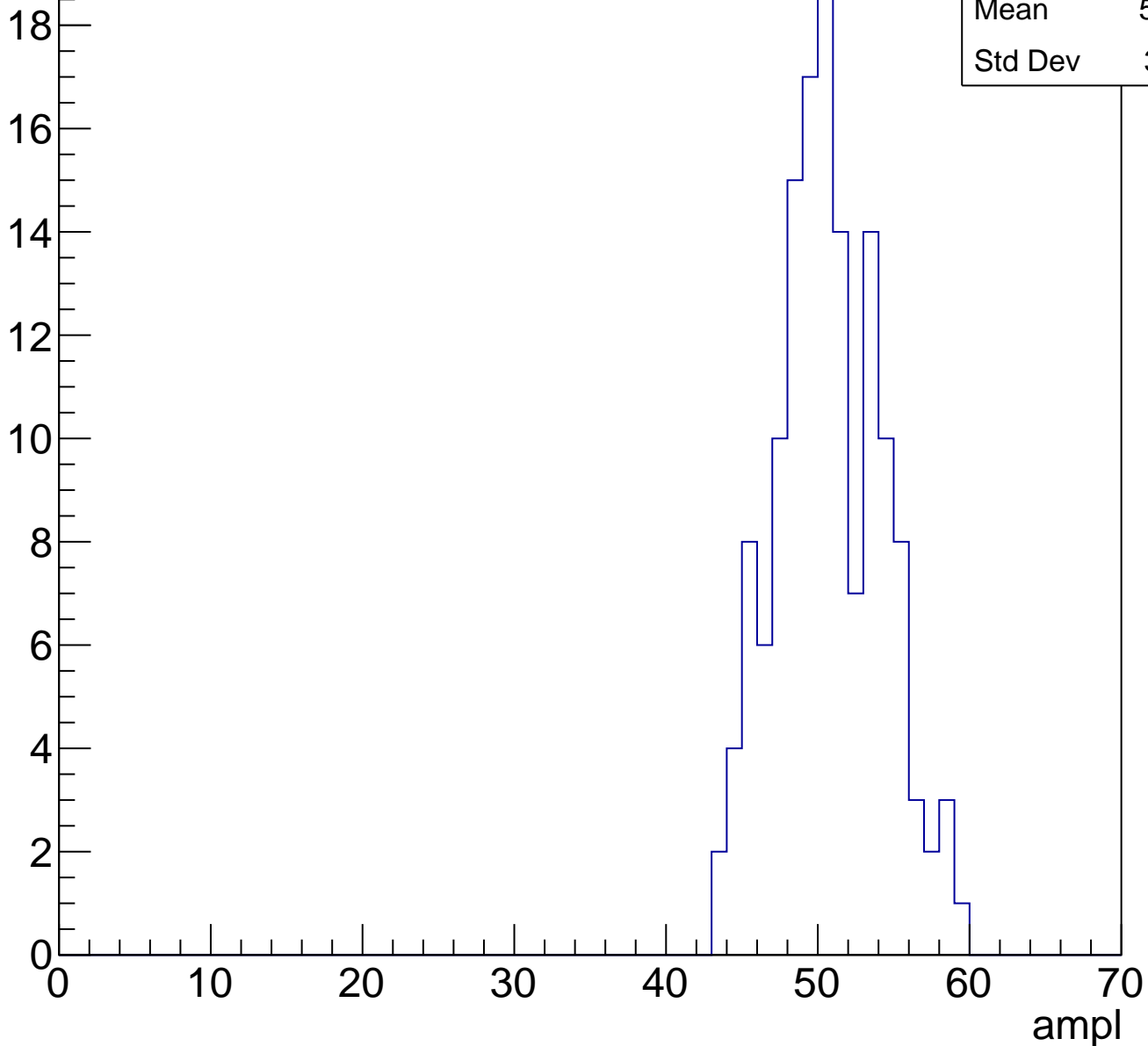


B1L001S, U22-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	50.25
Std Dev	3.451

Entry



B1L001S, U22-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

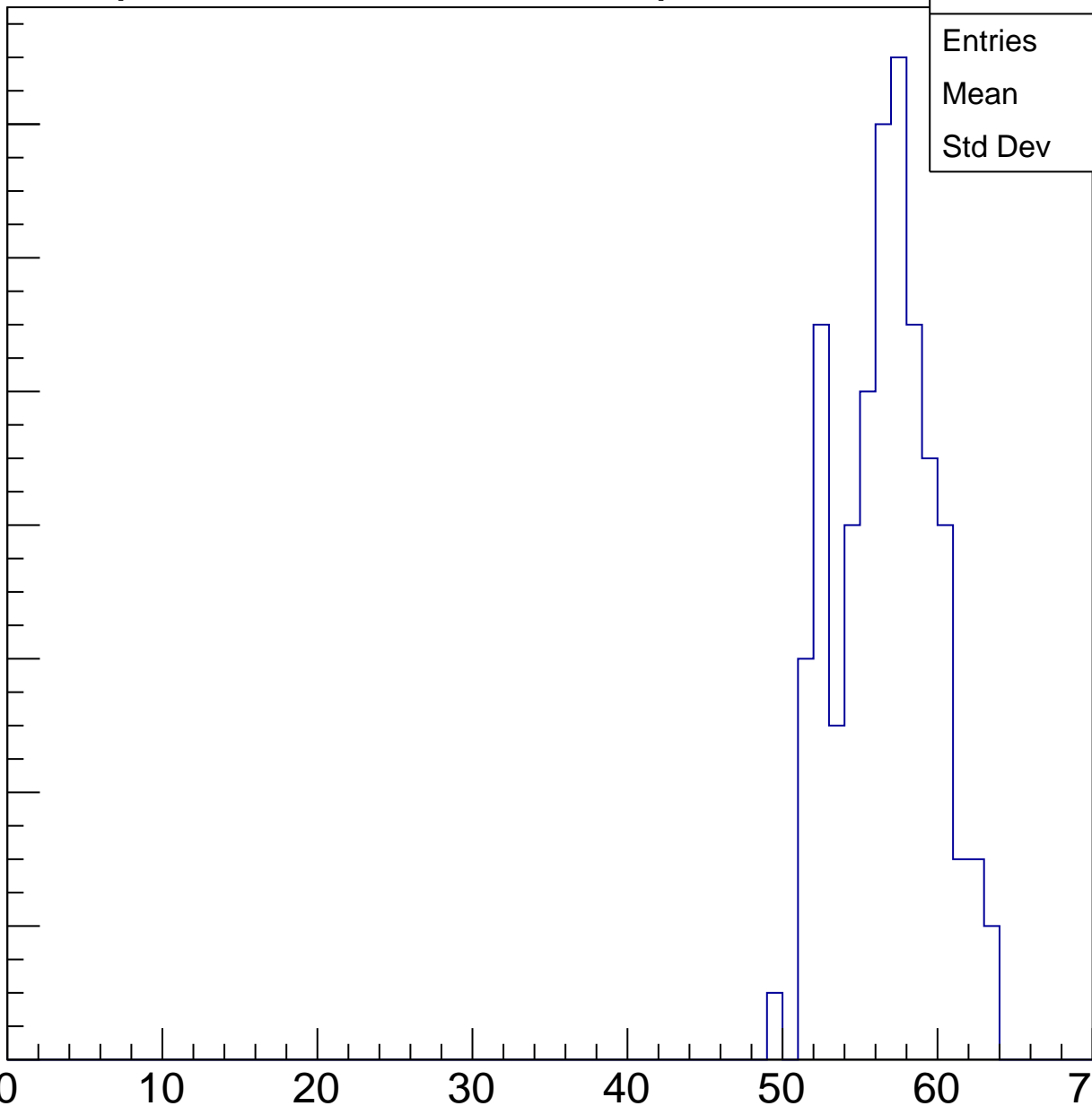
Entries	106
Mean	56.2
Std Dev	3.082

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U22-ch123, adc5

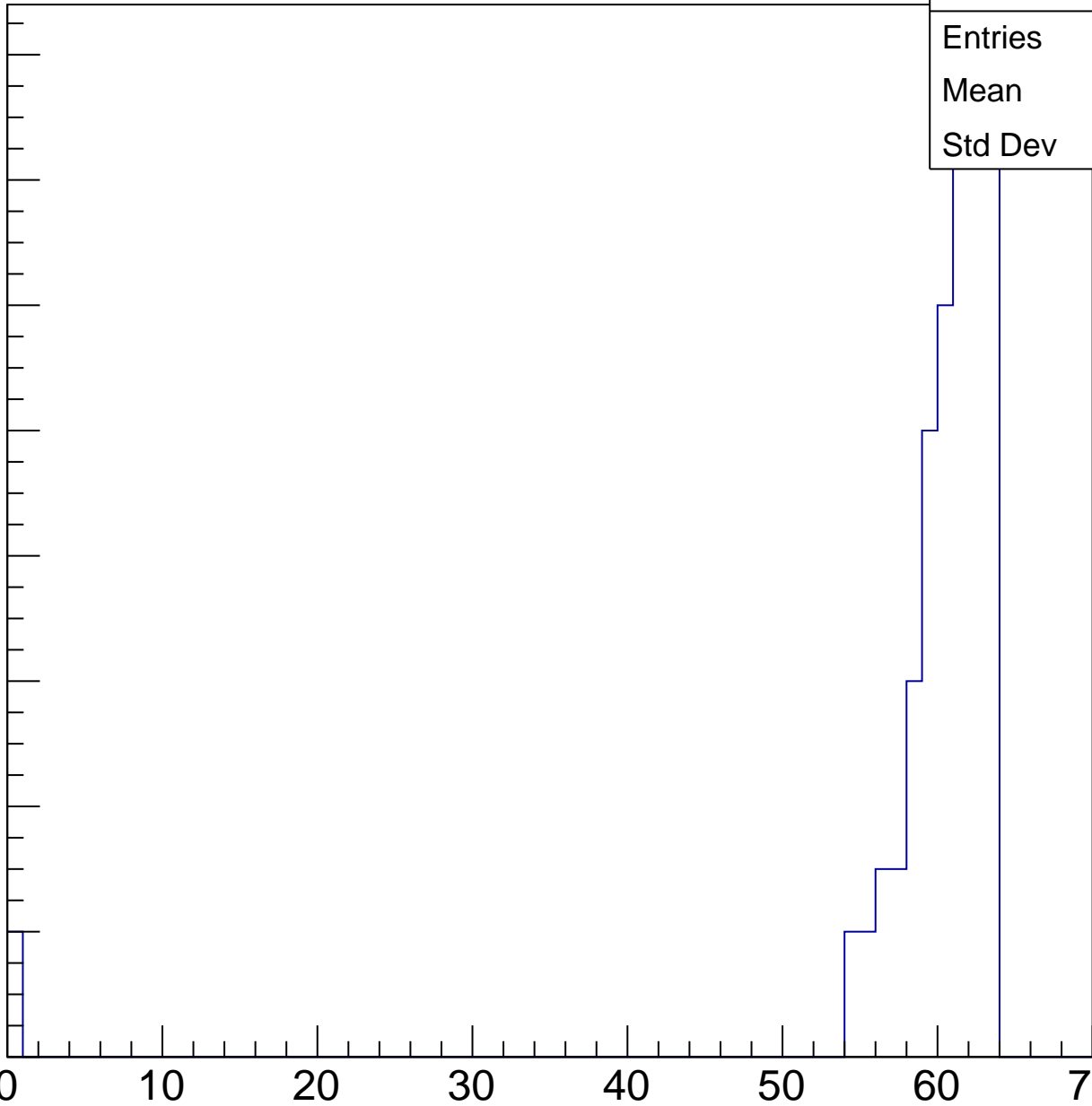
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.93
Std Dev	9.371

ampl



B1L001S, U22-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.71
Std Dev	3.239

0 10 20 30 40 50 60 70

ampl

1

B1L001S, U22-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U22-ch124, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	28.88
Std Dev	3.248

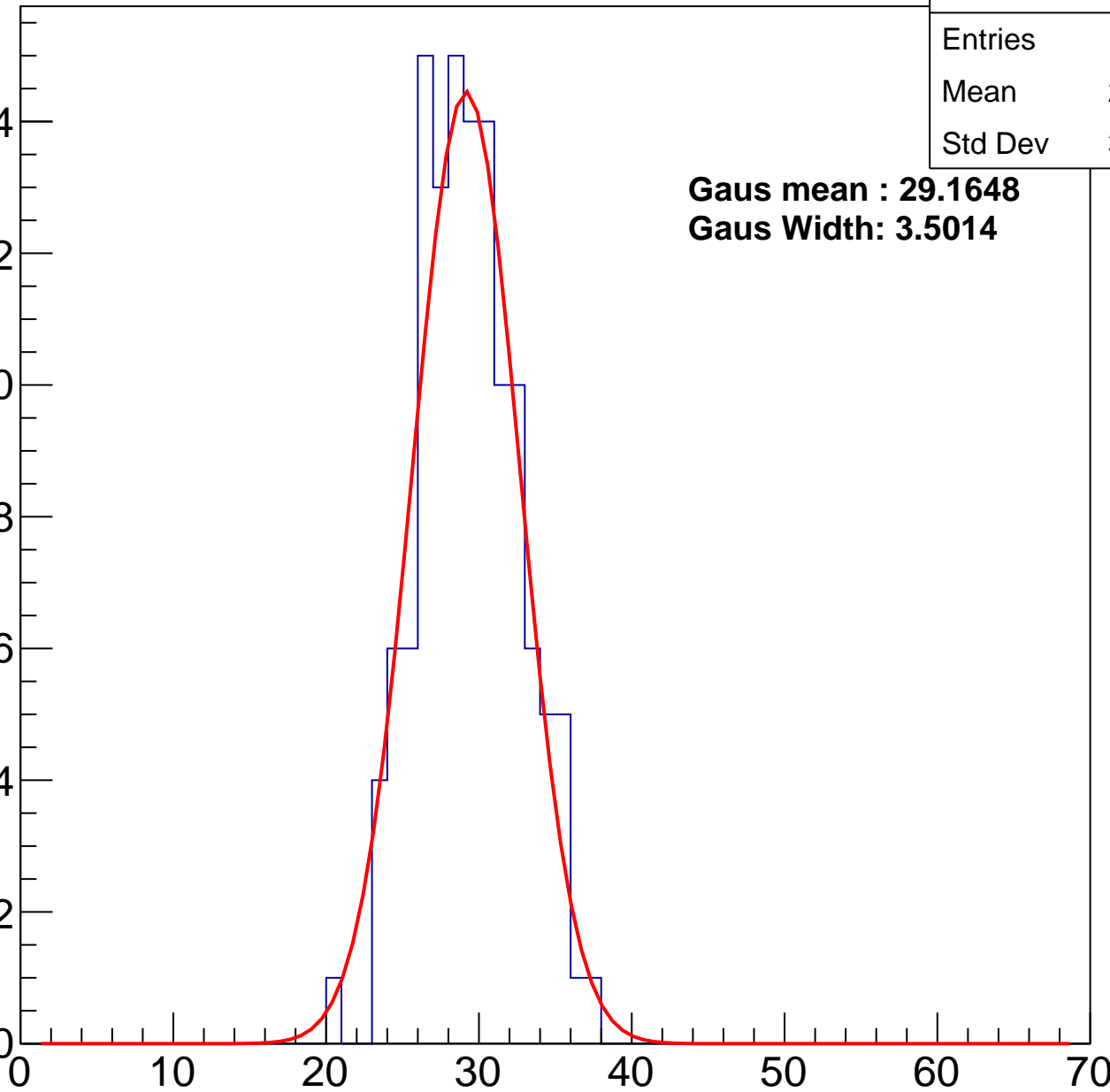
Gaus mean : 29.1648

Gaus Width: 3.5014

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U22-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	35.41
Std Dev	3.343

Gaus mean : 35.8787

Gaus Width: 3.6758

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch124, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	41.96
Std Dev	2.953

Gaus mean : 42.7033

Gaus Width: 3.3278

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

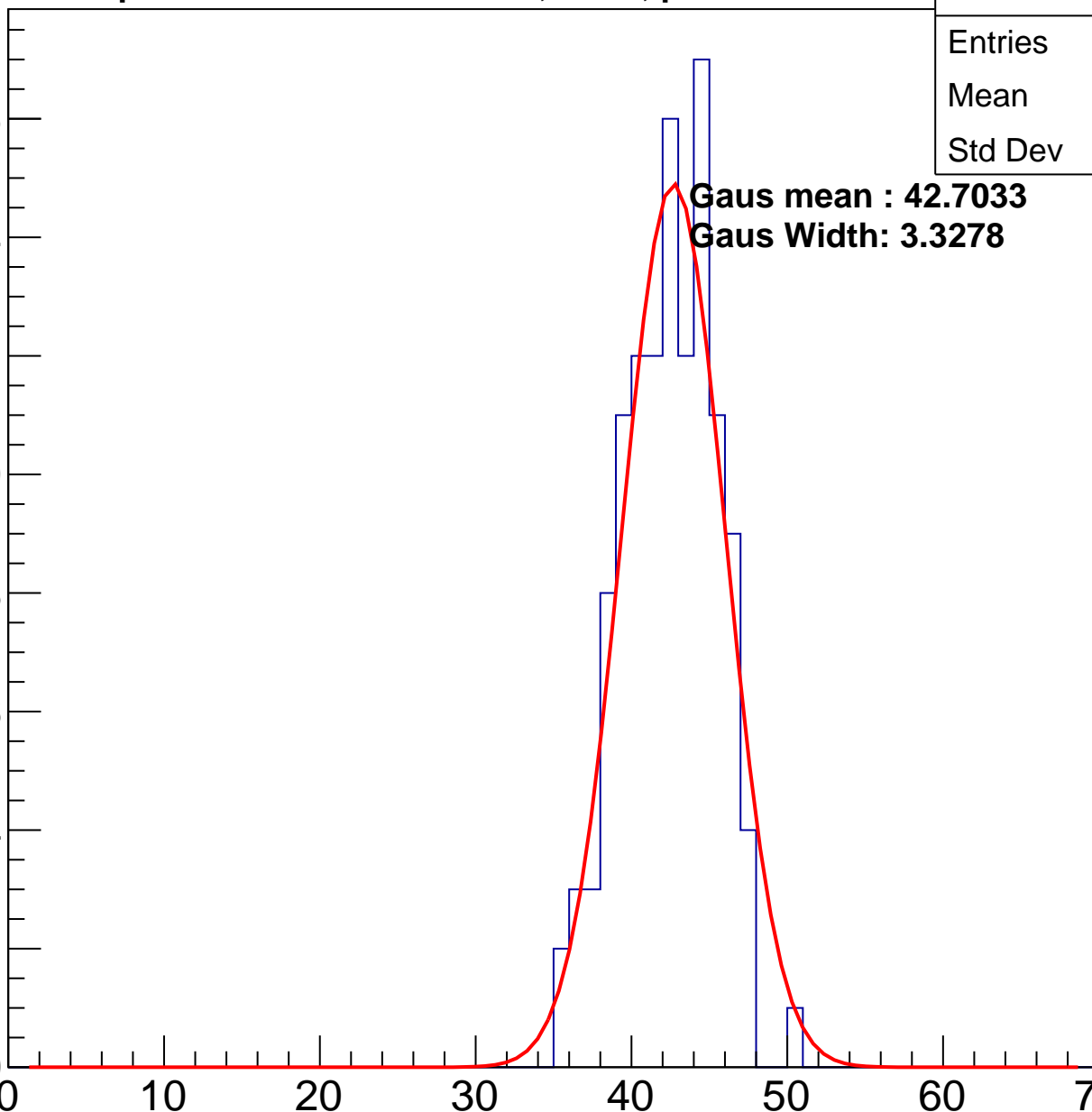
40

50

60

70

ampl



B1L001S, U22-ch124, adc3

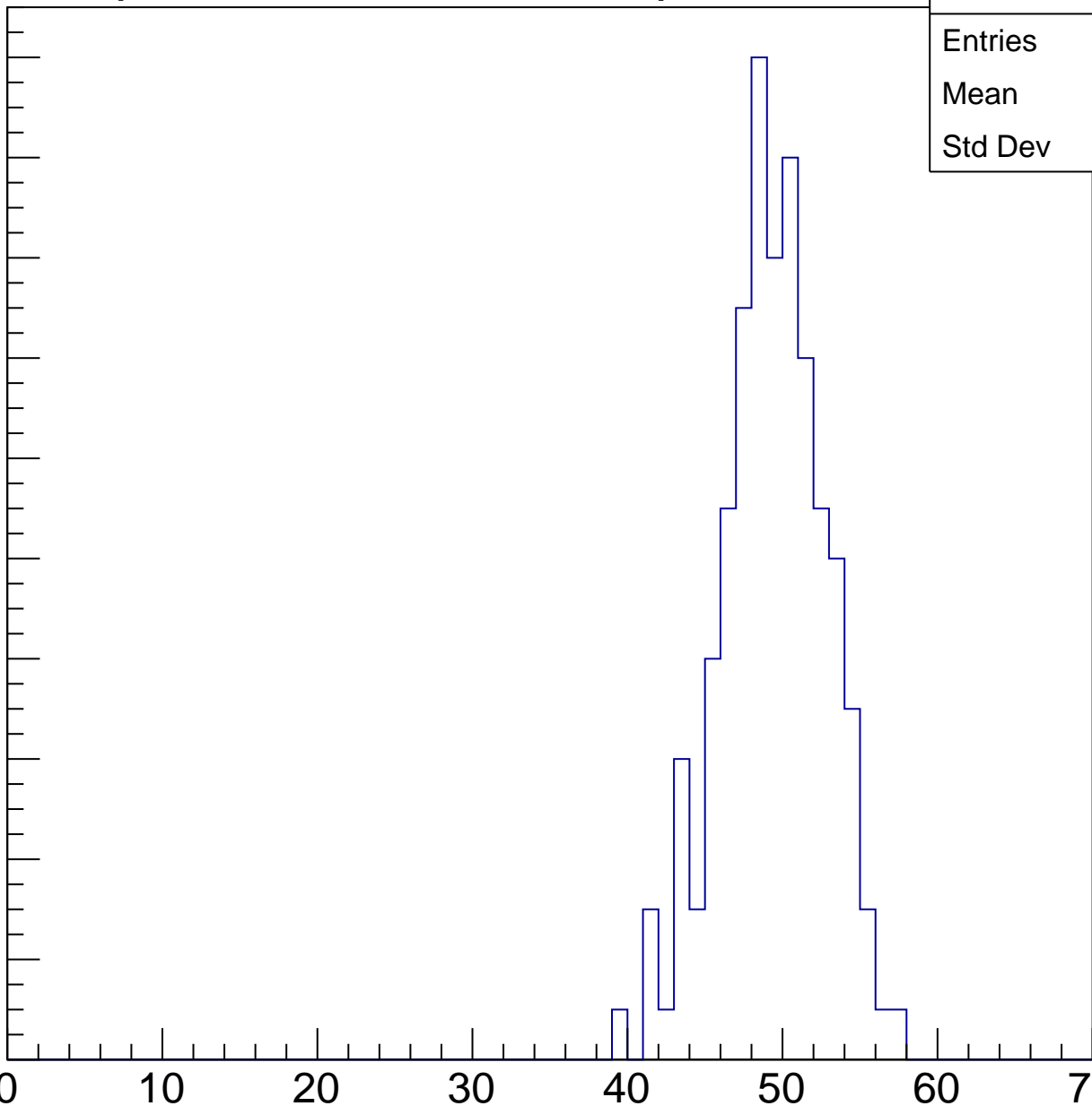
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	149
Mean	48.87
Std Dev	3.343

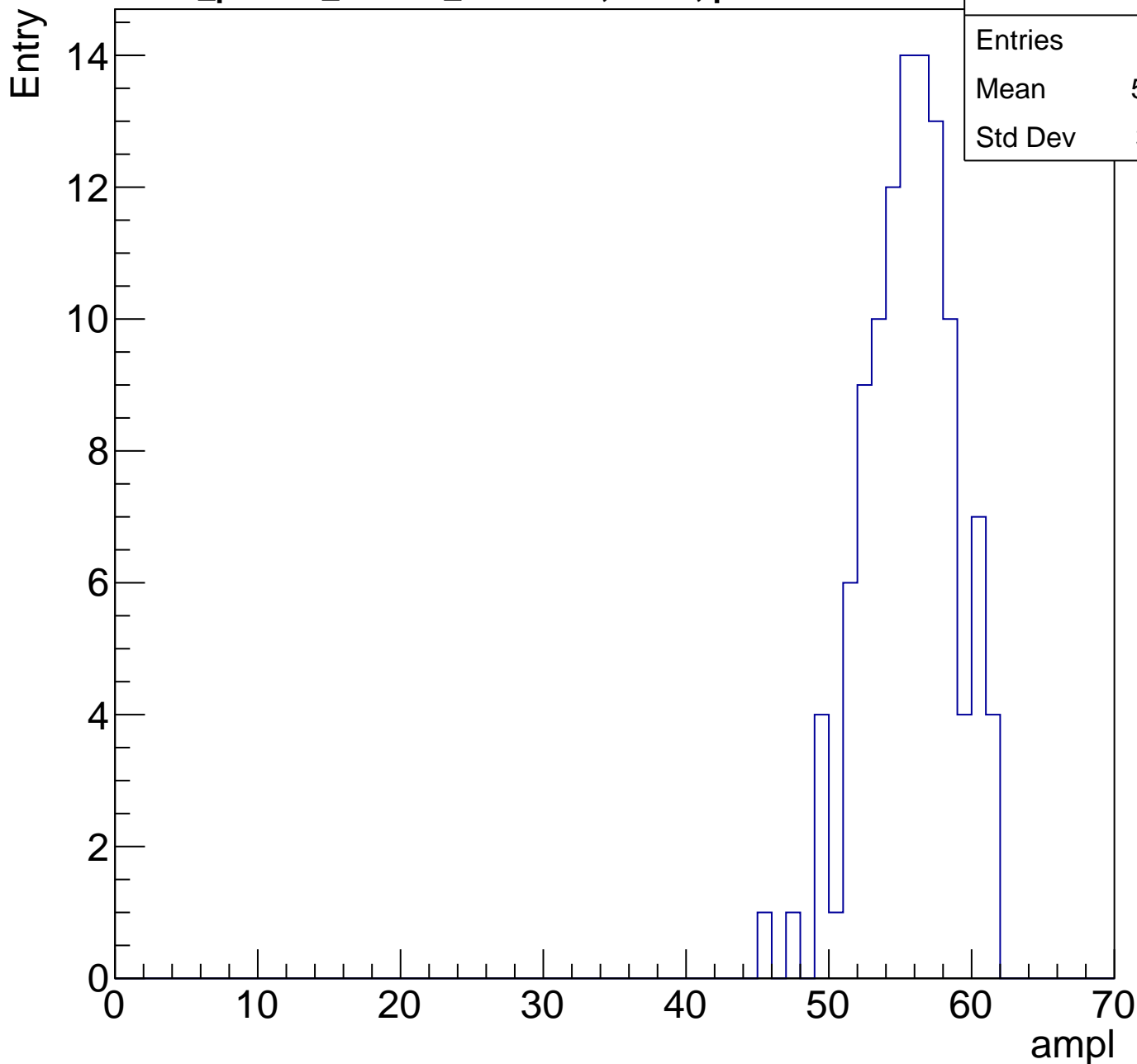
ampl



B1L001S, U22-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	55.14
Std Dev	3.161



B1L001S, U22-ch124, adc5

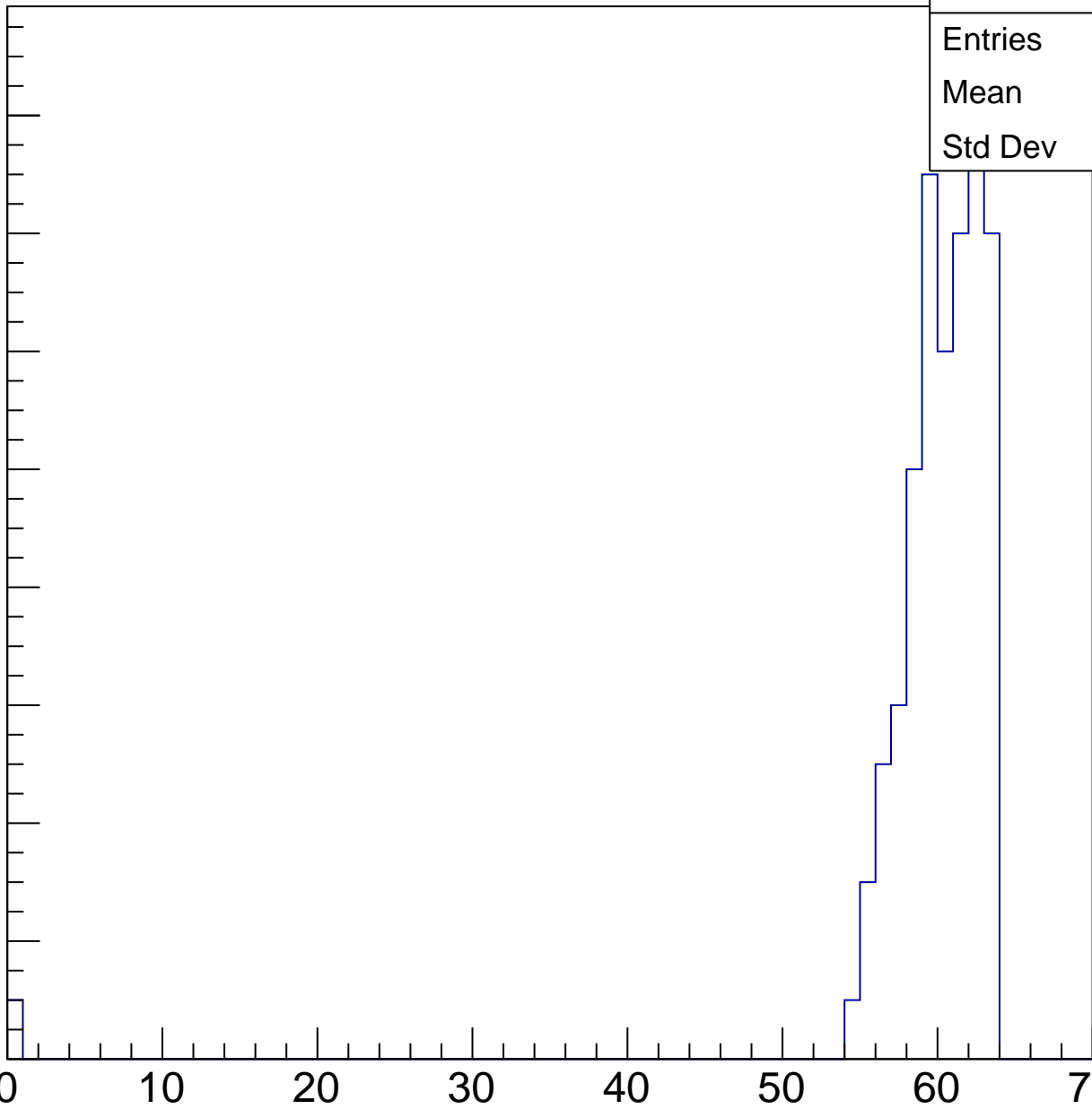
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.35
Std Dev	6.441

ampl

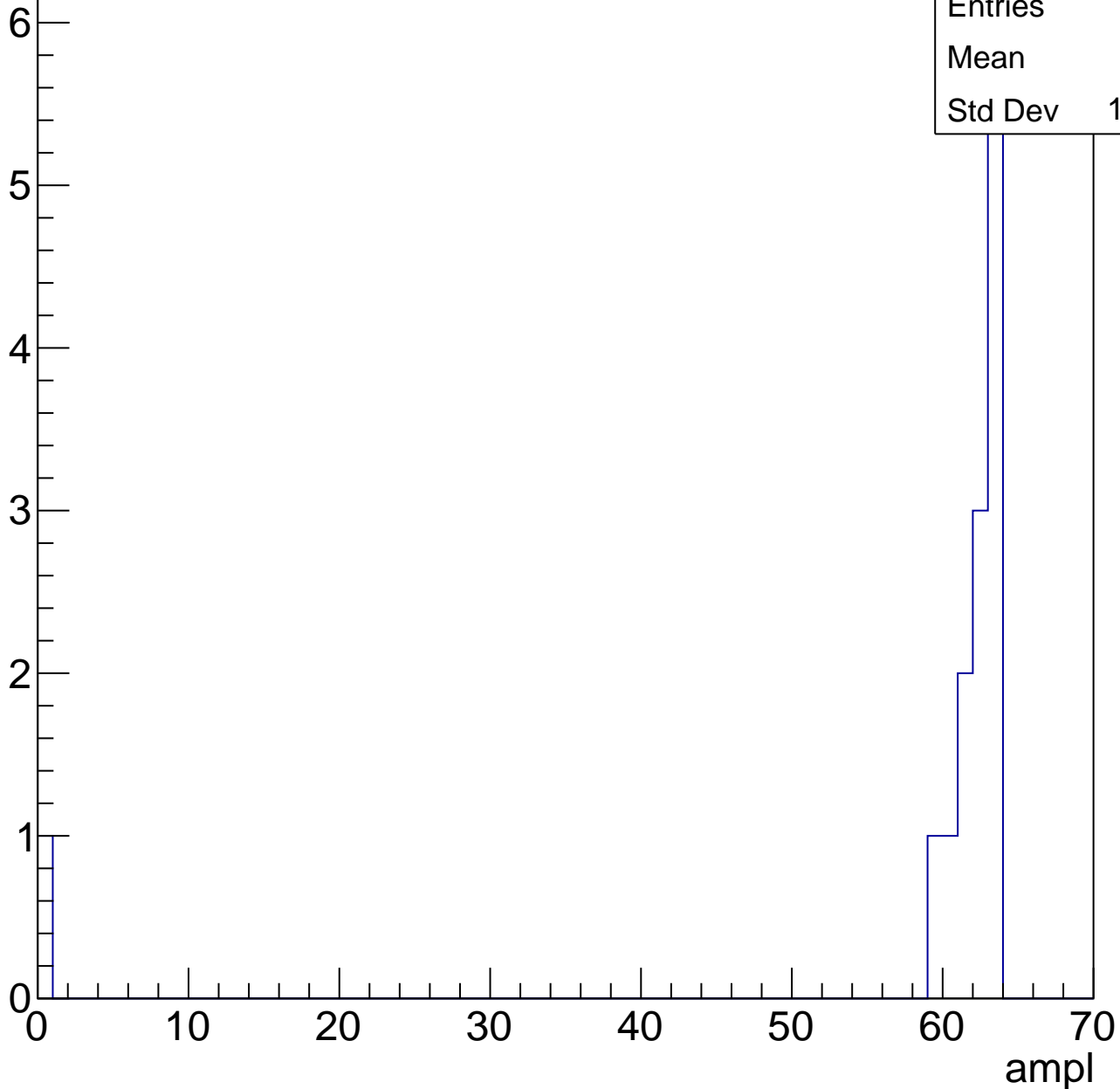


B1L001S, U22-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.5
Std Dev	15.99



B1L001S, U22-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U22-ch125, adc0

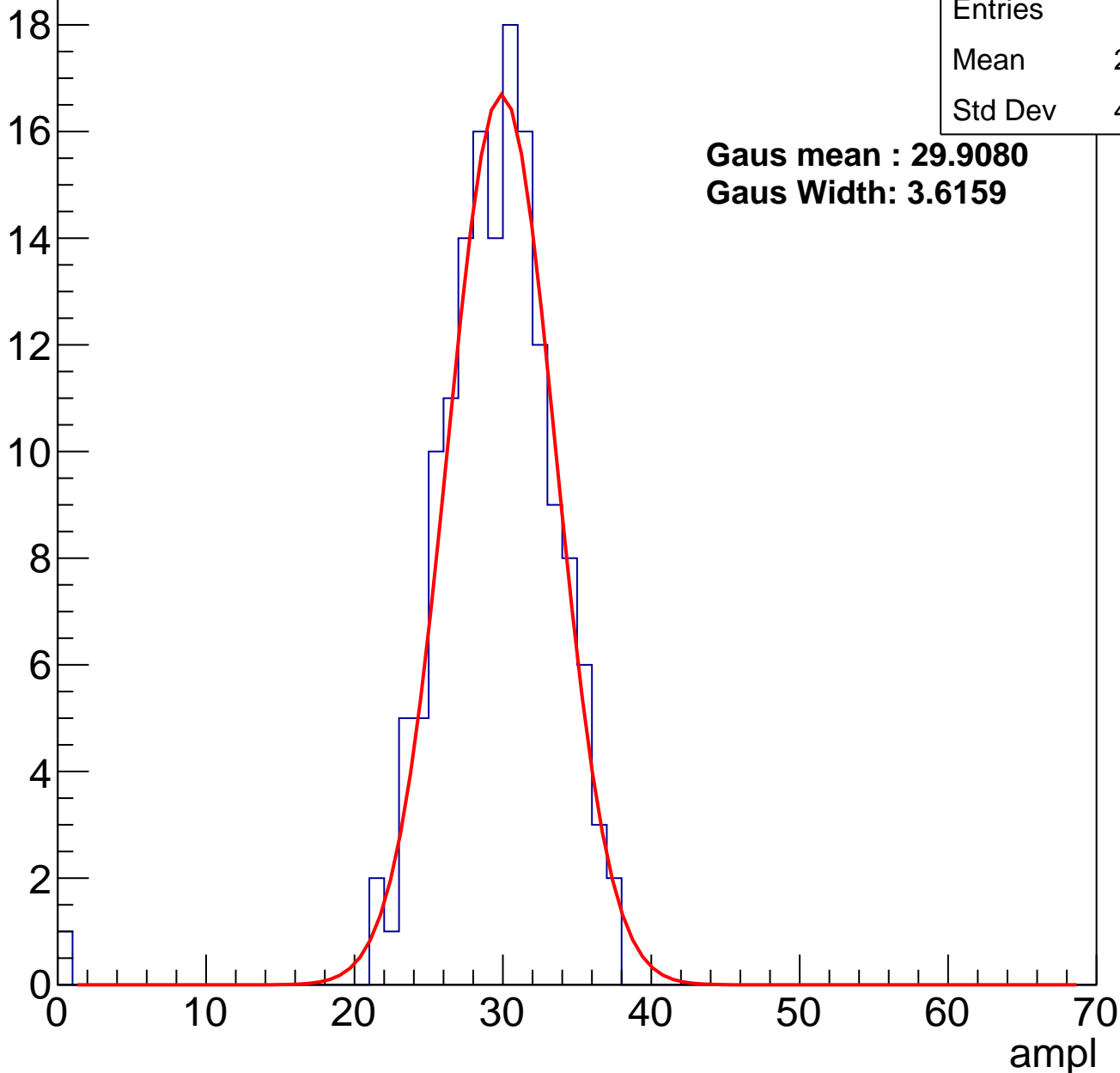
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.07
Std Dev	4.184

Gaus mean : 29.9080

Gaus Width: 3.6159

Entry



B1L001S, U22-ch125, adc1

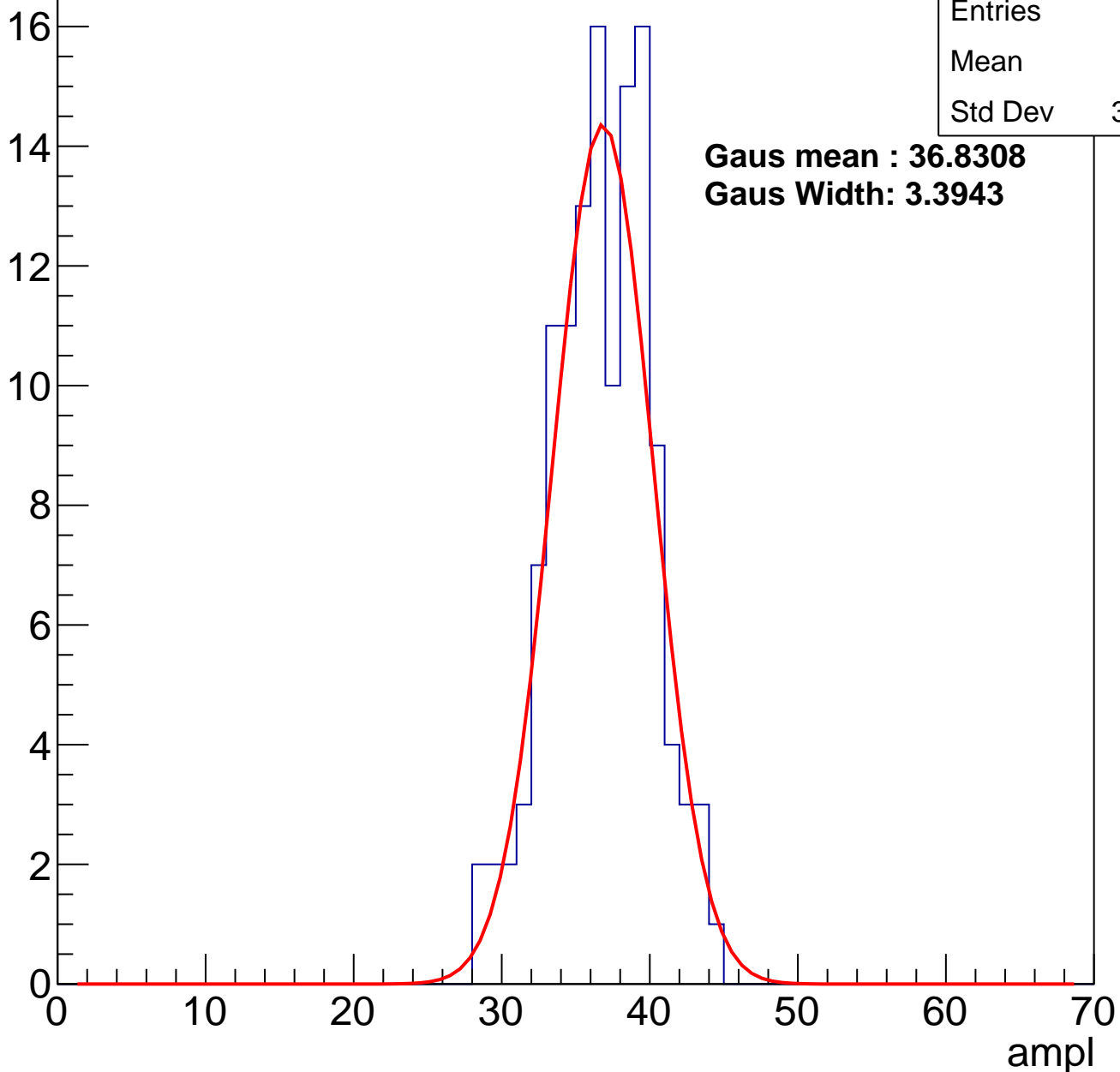
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.3
Std Dev	3.315

Gaus mean : 36.8308

Gaus Width: 3.3943

Entry



B1L001S, U22-ch125, adc2

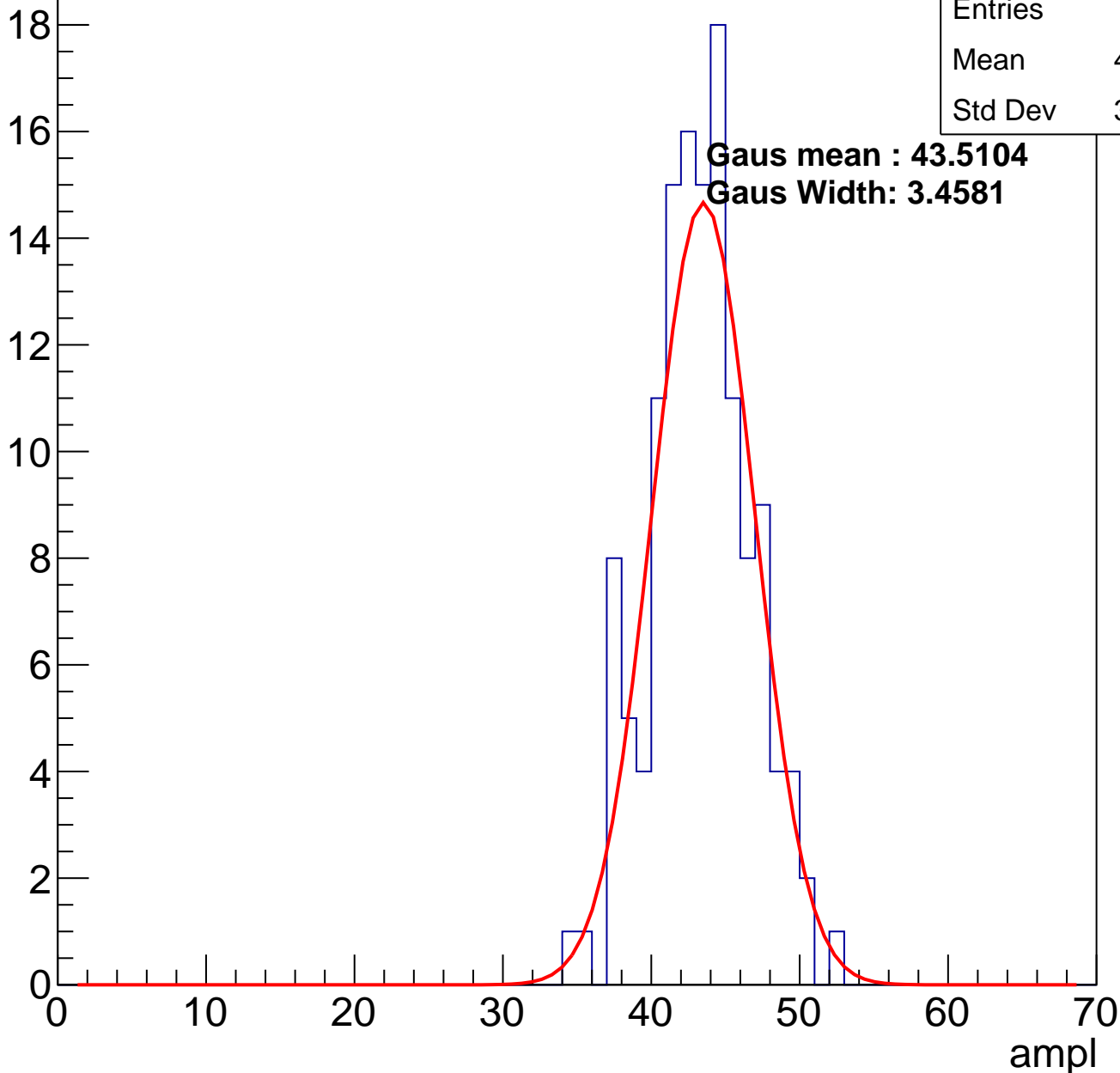
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	133
Mean	42.86
Std Dev	3.353

Gaus mean : 43.5104

Gaus Width: 3.4581



B1L001S, U22-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	49.92
Std Dev	3.449

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

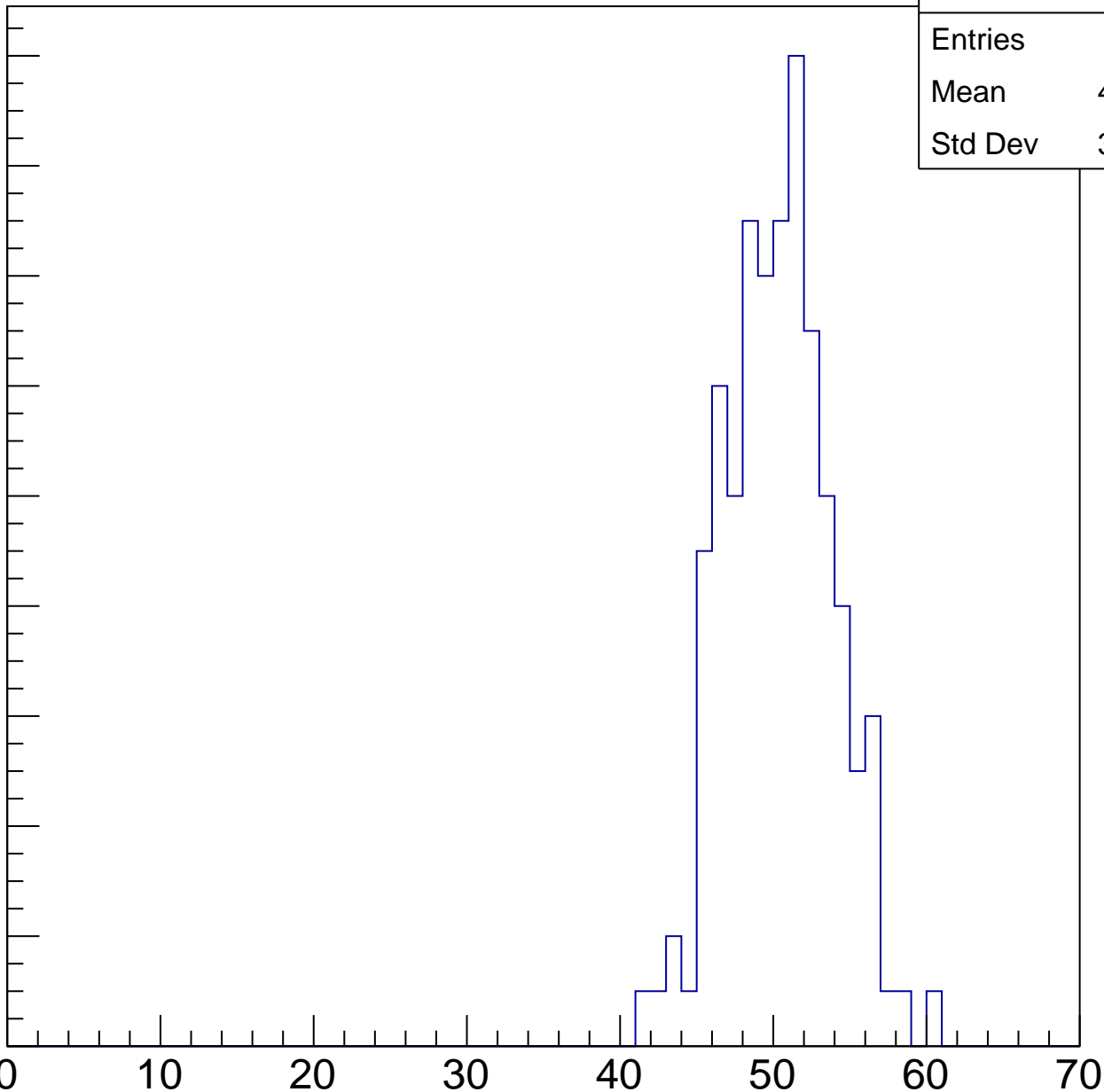
30

40

50

60

ampl



B1L001S, U22-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	56.3
Std Dev	3.234

Entry

14

12

10

8

6

4

2

0

0

10

20

30

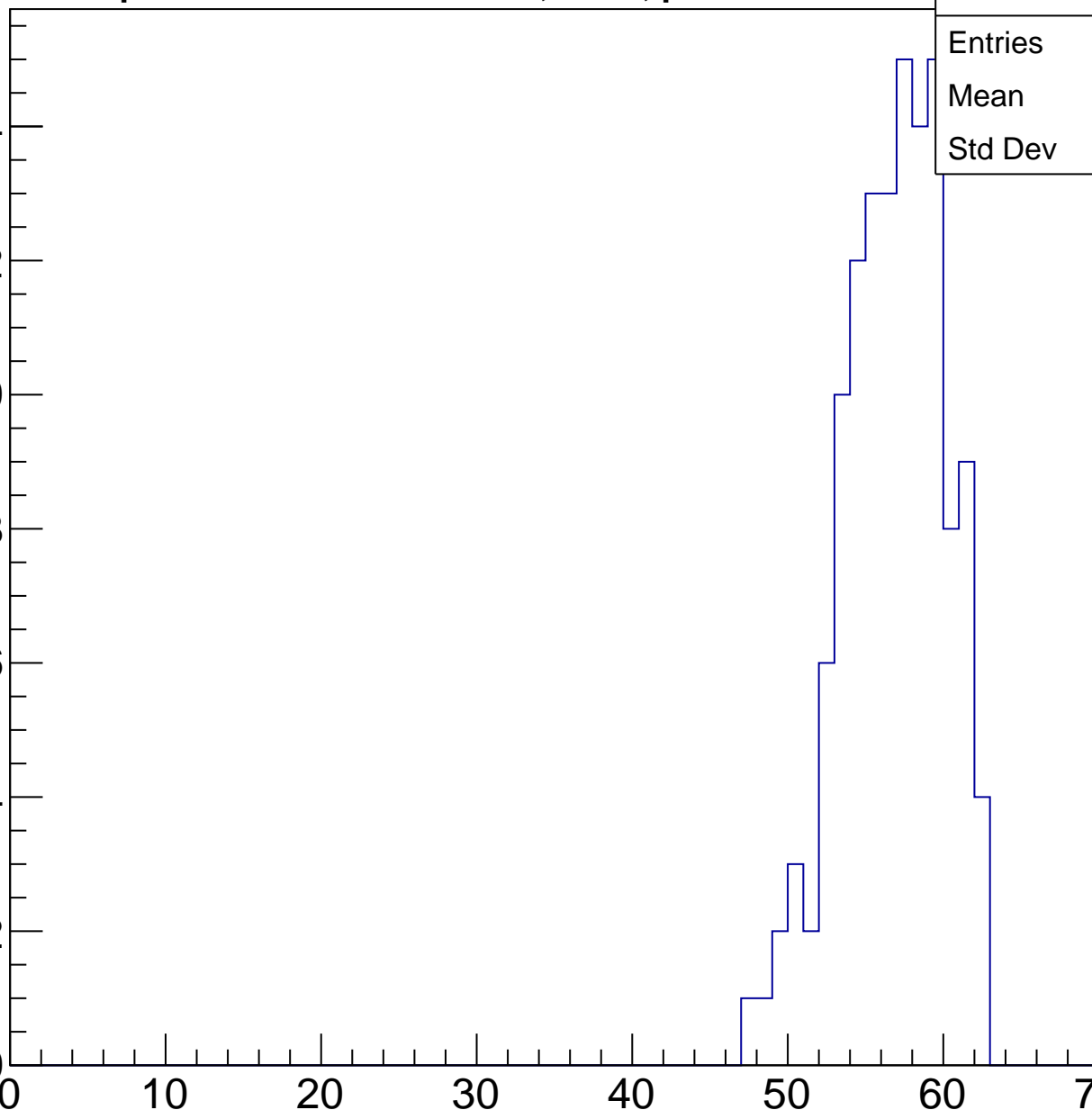
40

50

60

70

ampl



B1L001S, U22-ch125, adc5

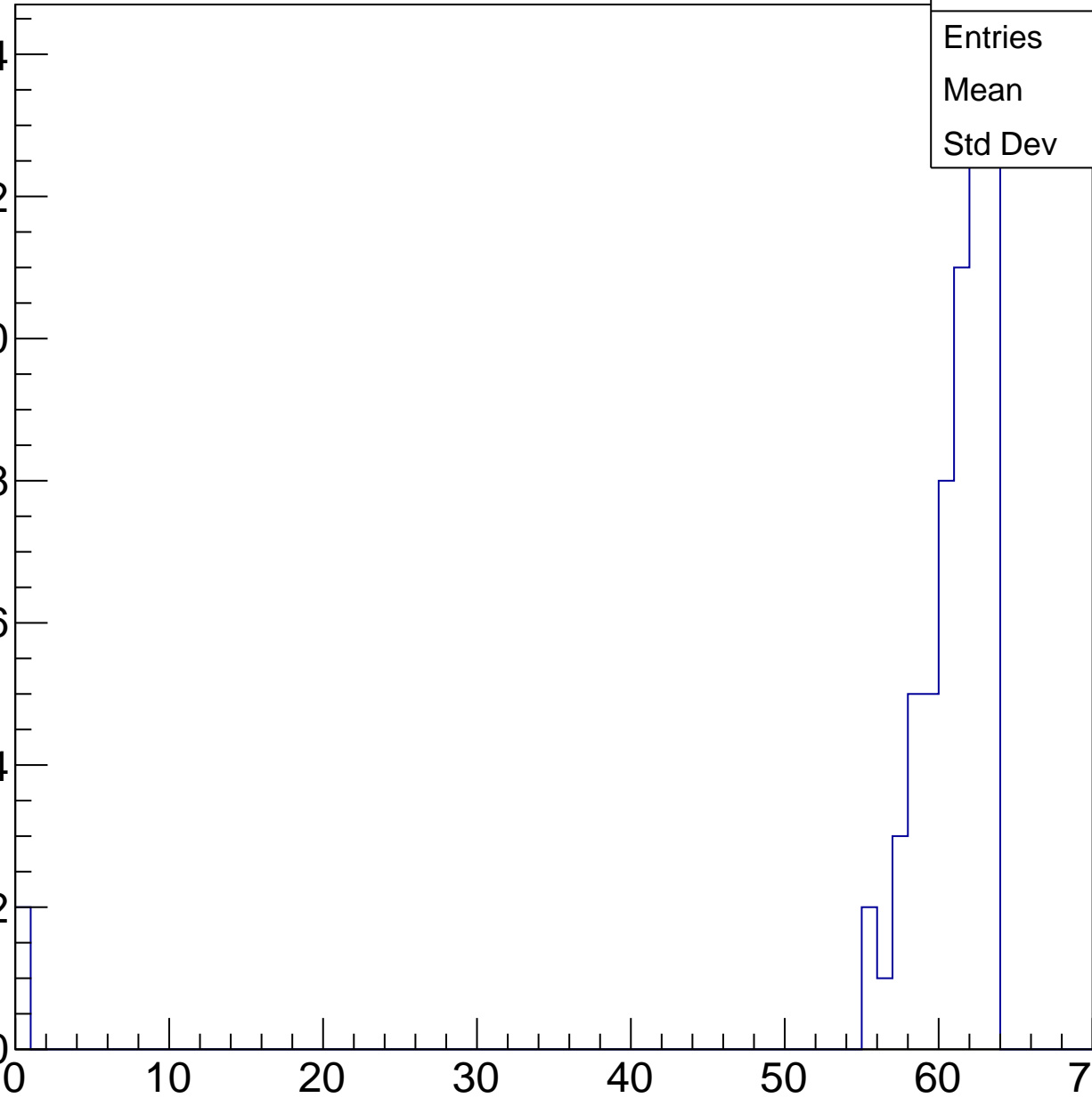
calib_packv5_042523_0143.root, FC#2, port C2

Entries	64
Mean	58.75
Std Dev	10.75

Entry

14
12
10
8
6
4
2
0

ampl

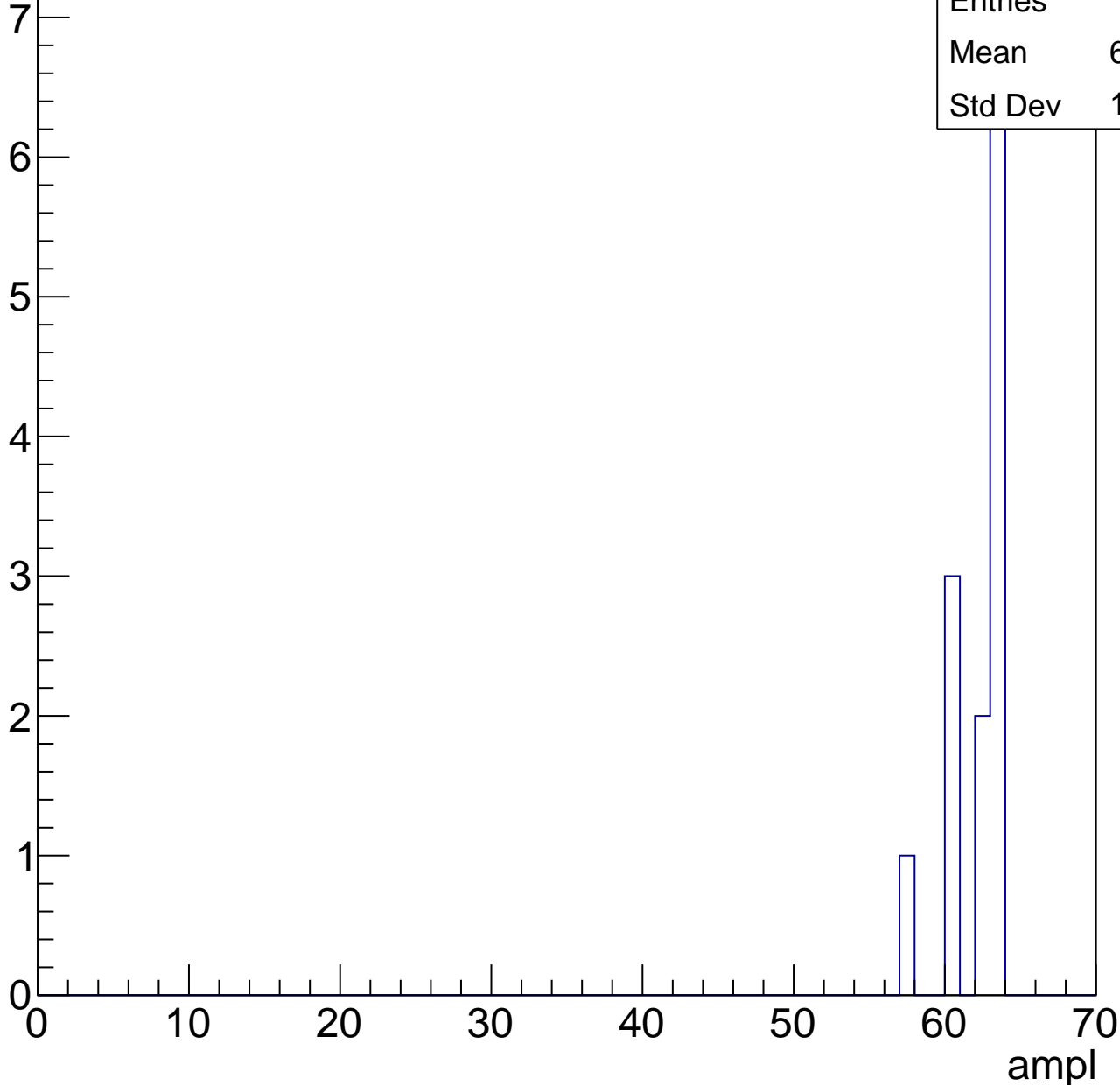


B1L001S, U22-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

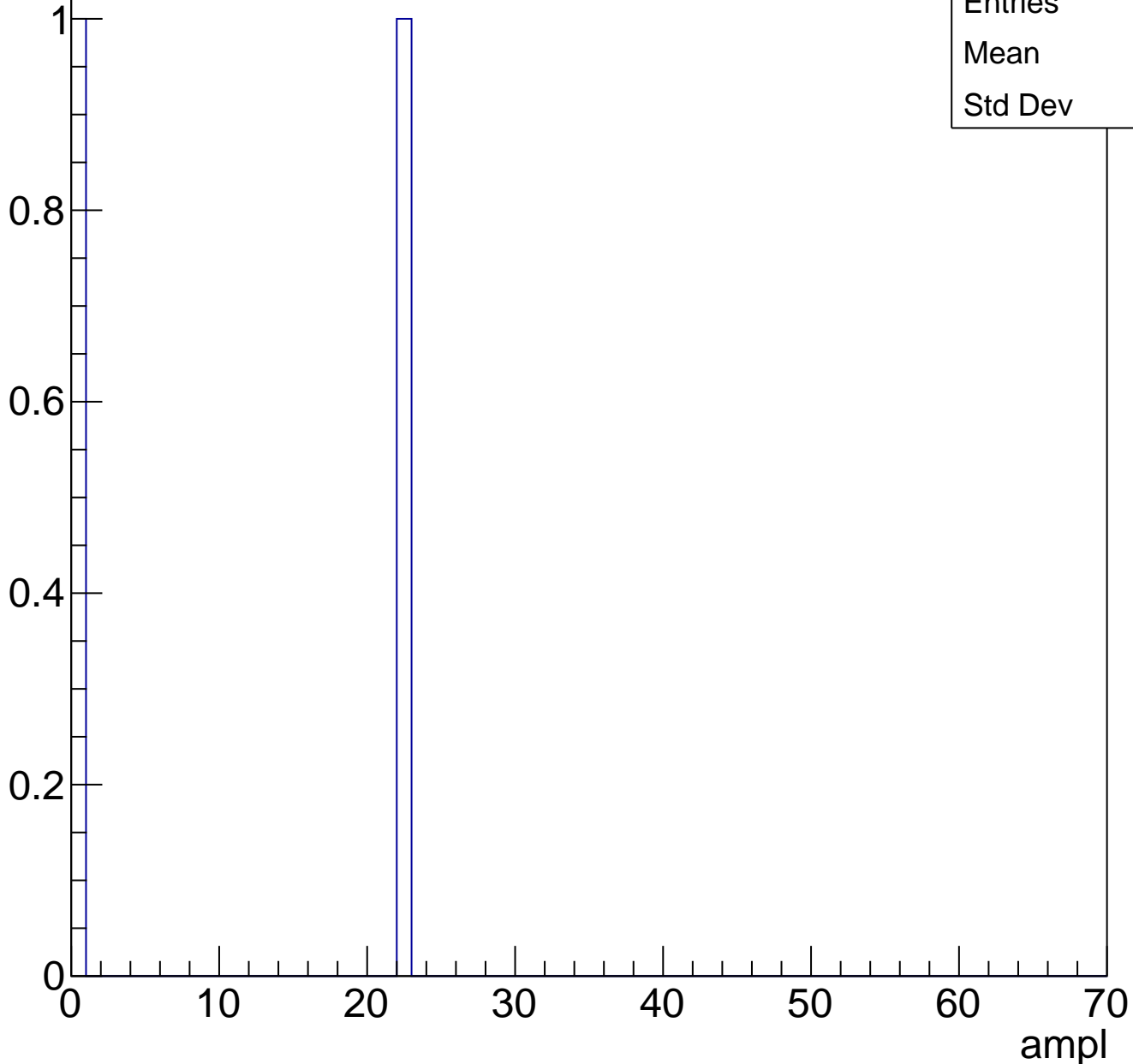
Entries	13
Mean	61.69
Std Dev	1.814



B1L001S, U22-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U22-ch126, adc0

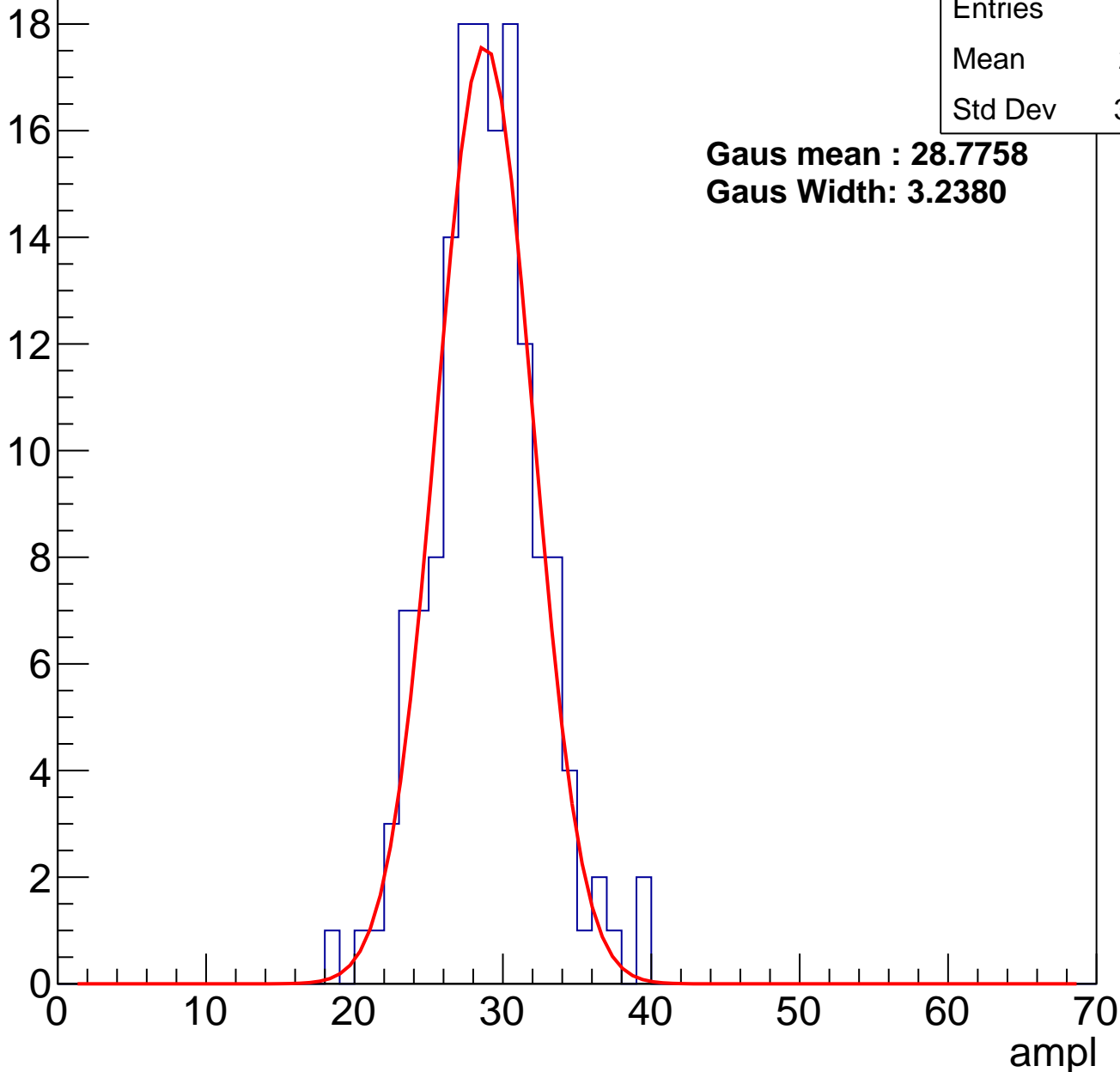
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	28.41
Std Dev	3.556

Gaus mean : 28.7758

Gaus Width: 3.2380

Entry



B1L001S, U22-ch126, adc1

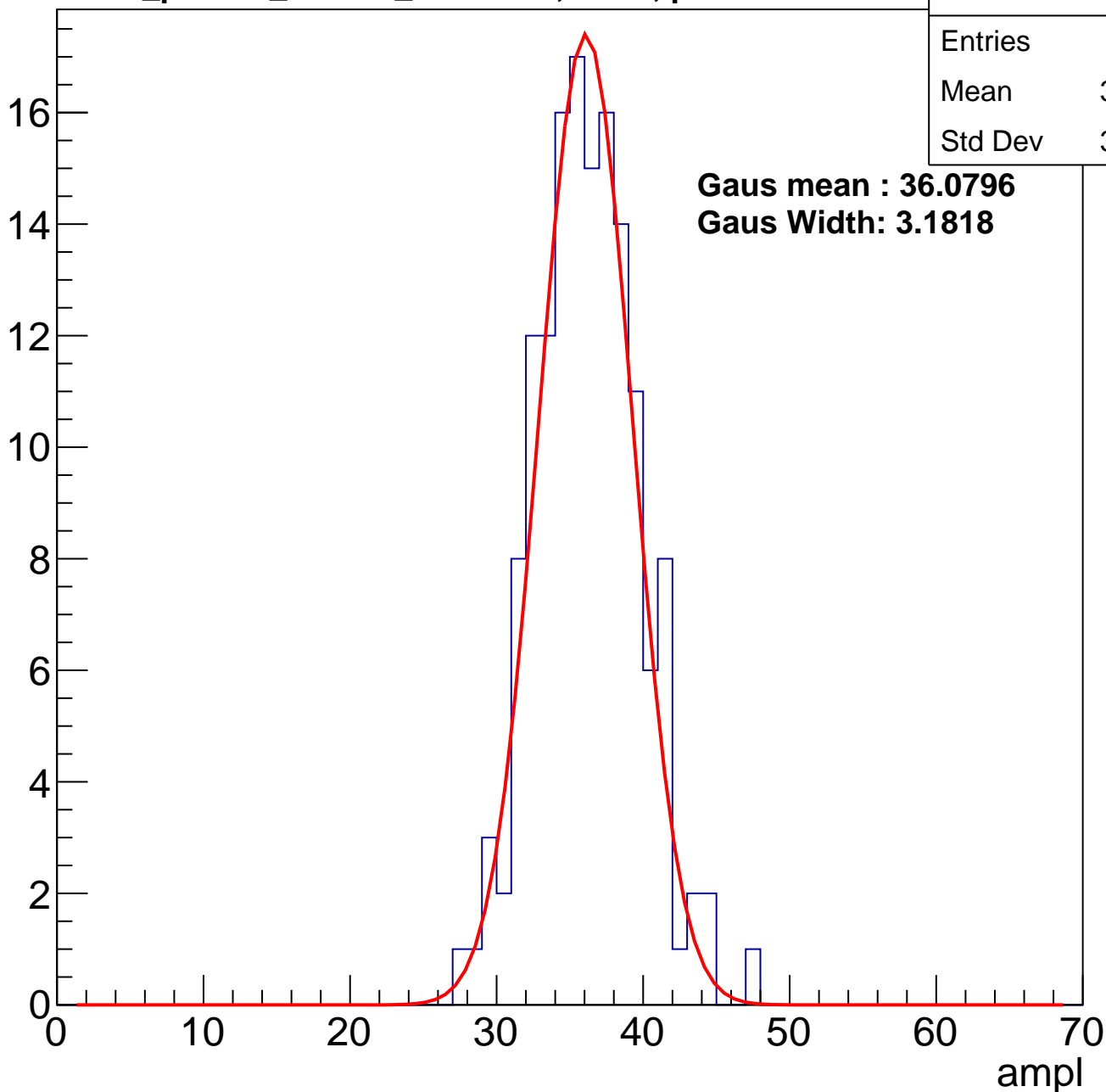
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.76
Std Dev	3.475

Gaus mean : 36.0796

Gaus Width: 3.1818

Entry



B1L001S, U22-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	42.66
Std Dev	3.265

Gaus mean : 43.1258

Gaus Width: 3.2675

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

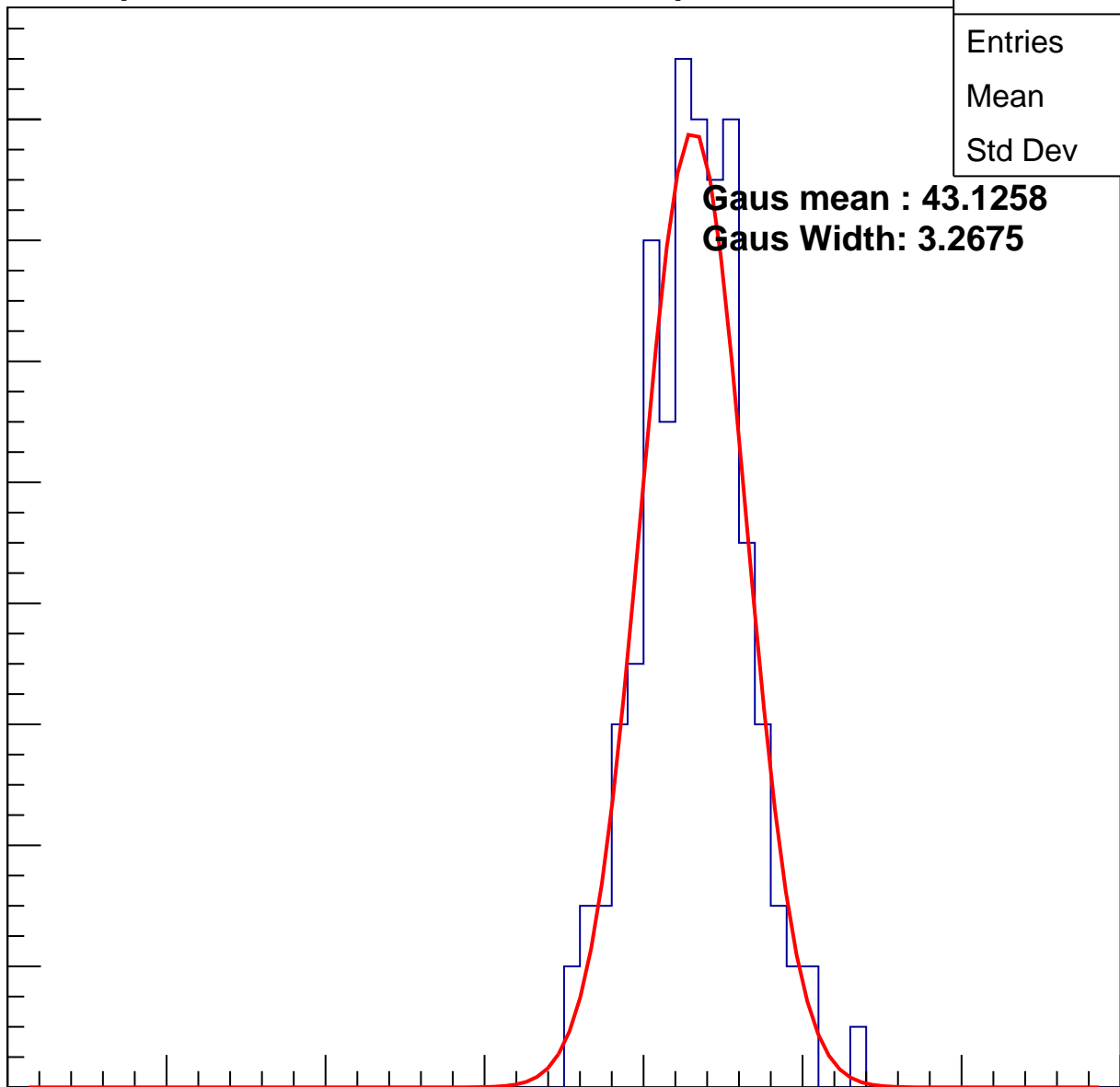
40

50

60

70

ampl



B1L001S, U22-ch126, adc3

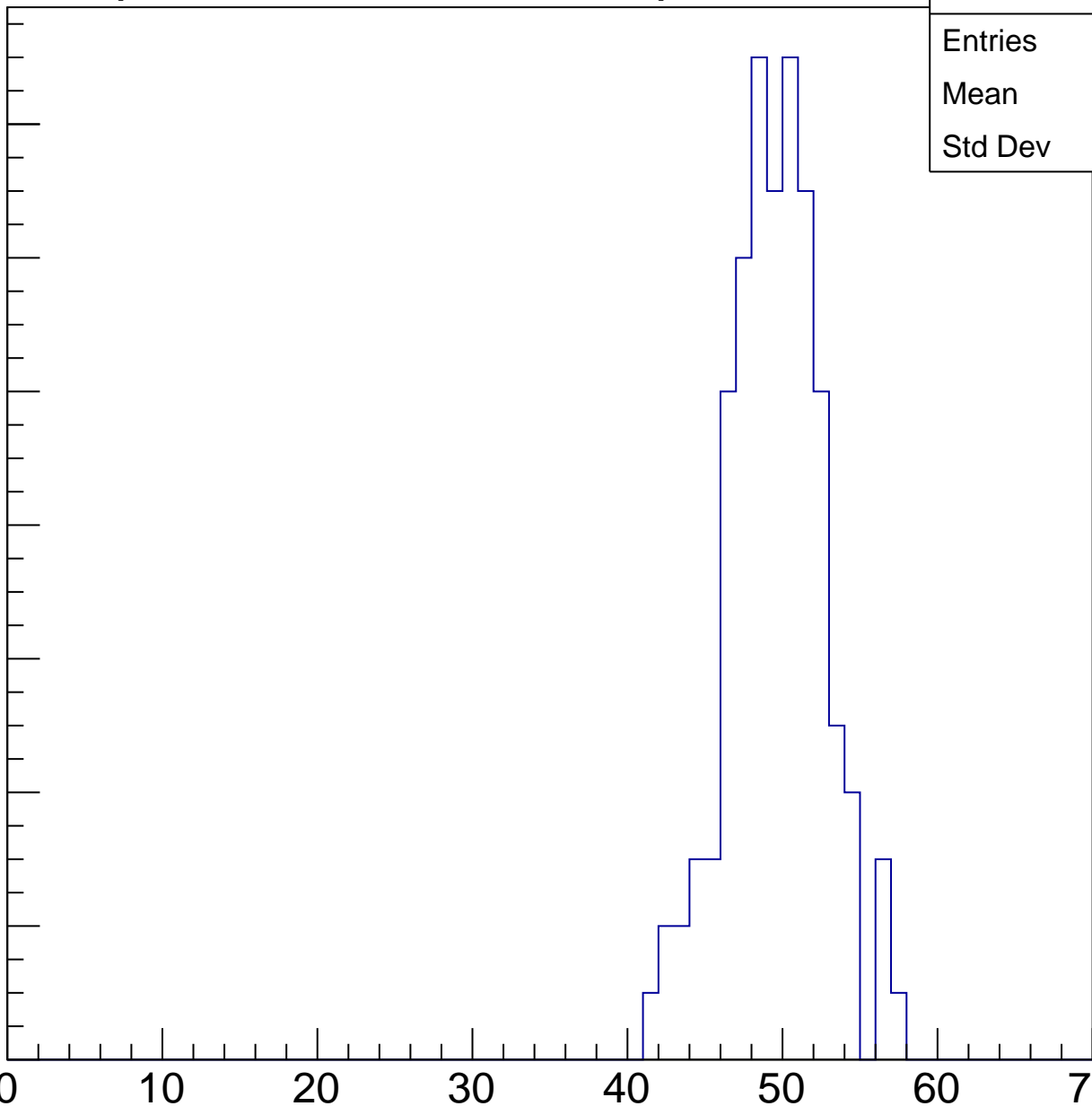
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	49.09
Std Dev	3.072

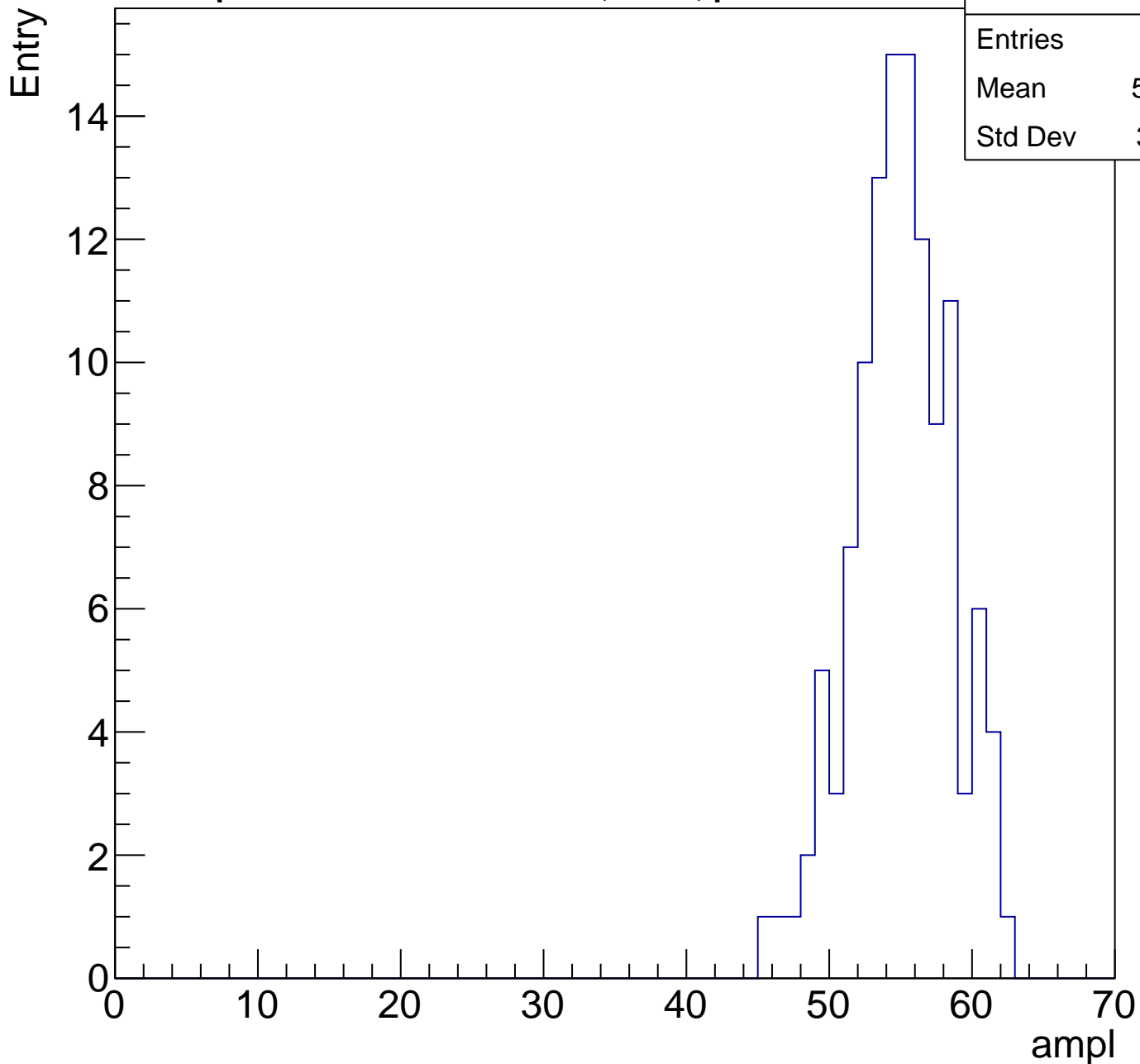
ampl



B1L001S, U22-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.59
Std Dev	3.411



B1L001S, U22-ch126, adc5

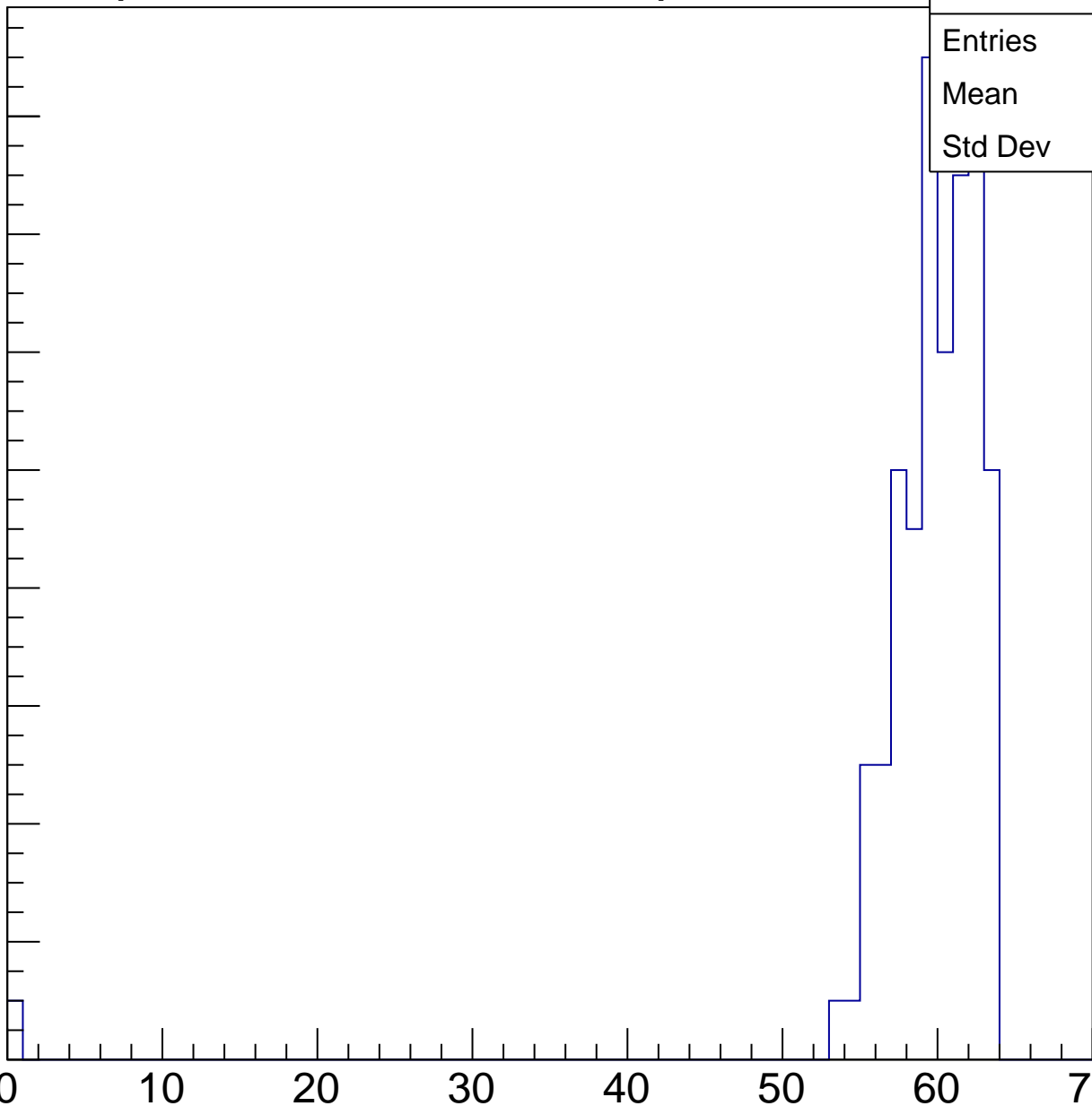
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.96
Std Dev	6.334

ampl



B1L001S, U22-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

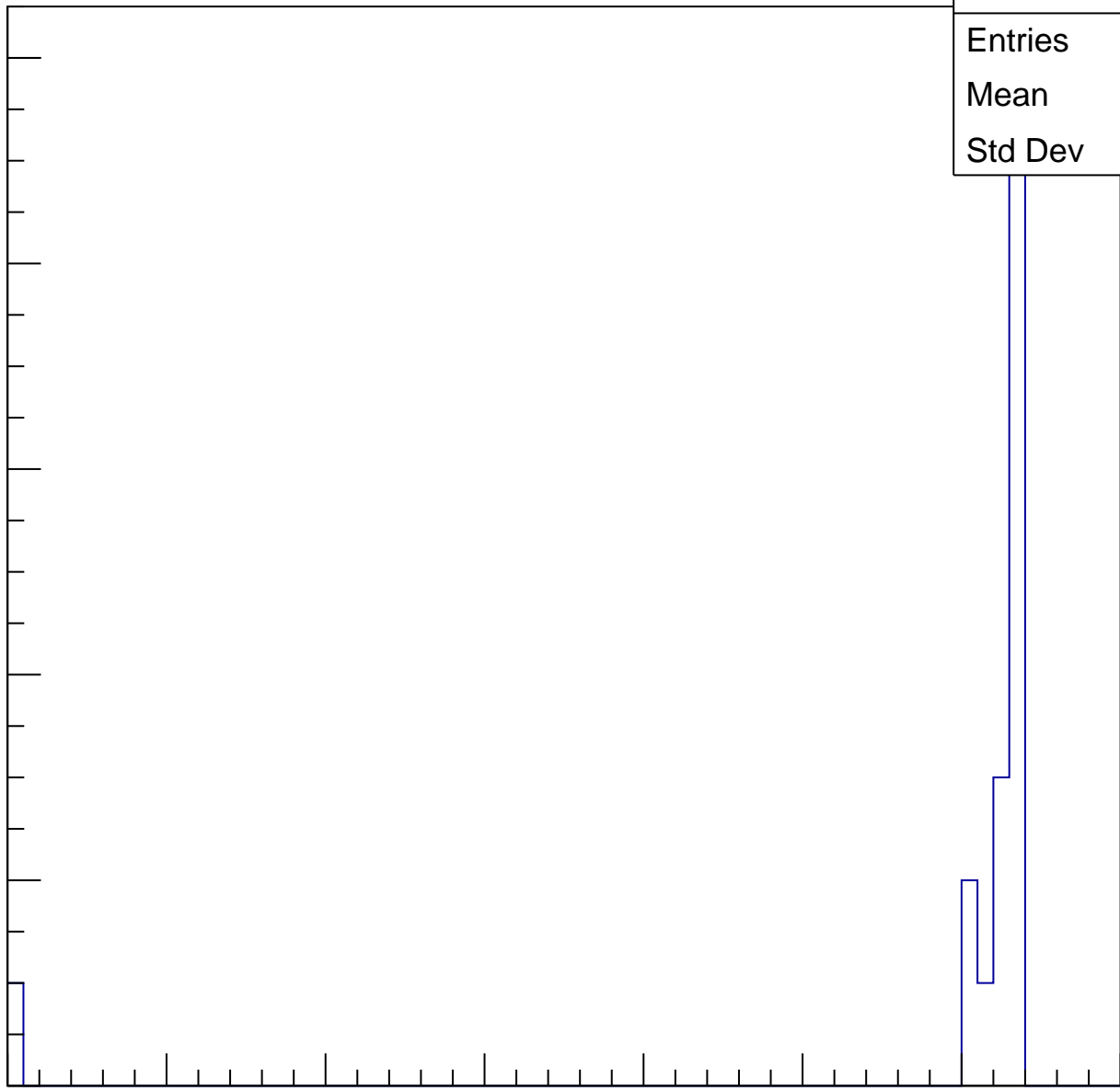
Entries	17
Mean	58.65
Std Dev	14.7

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

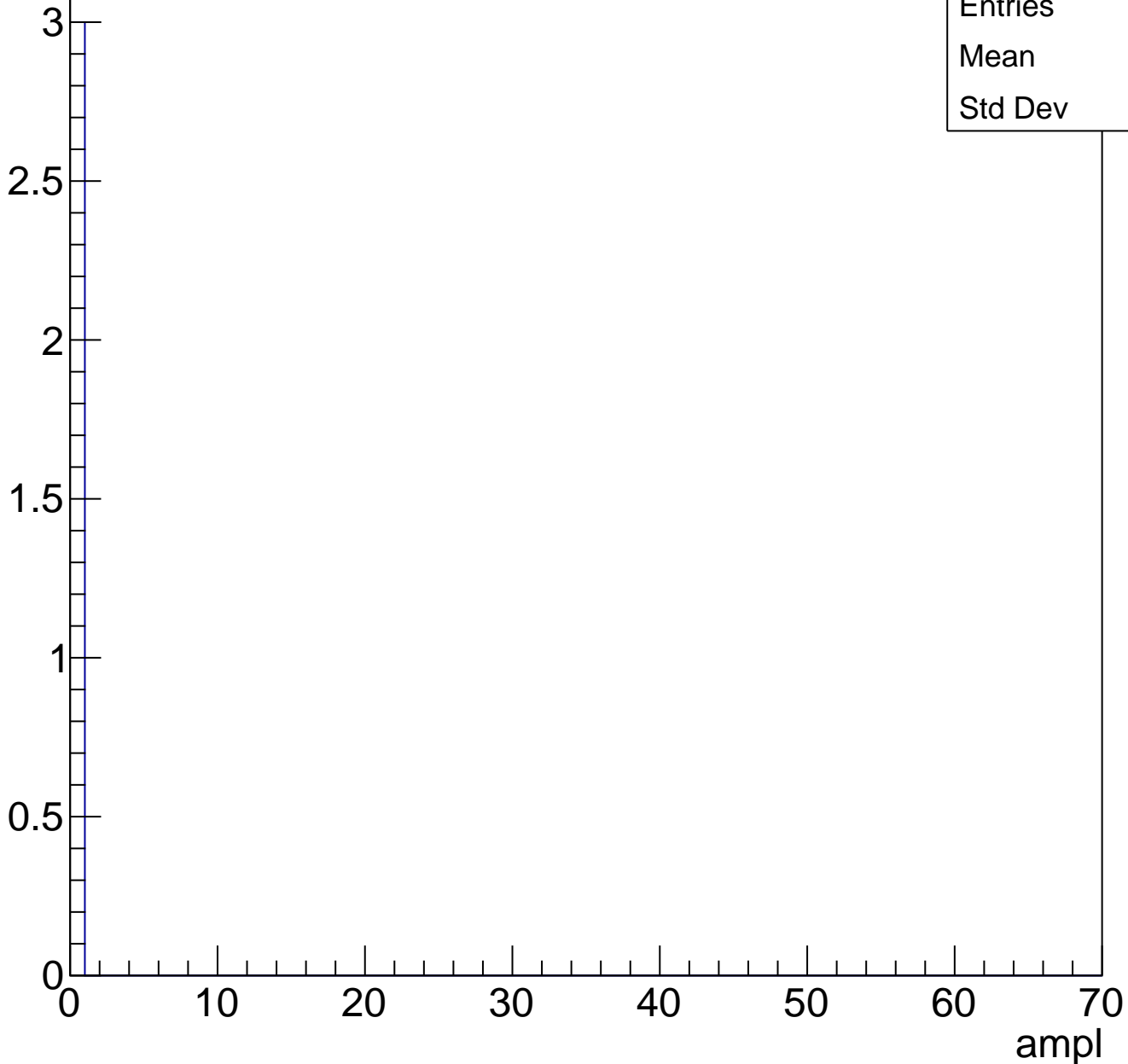
ampl



B1L001S, U22-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L001S, U22-ch127, adc0

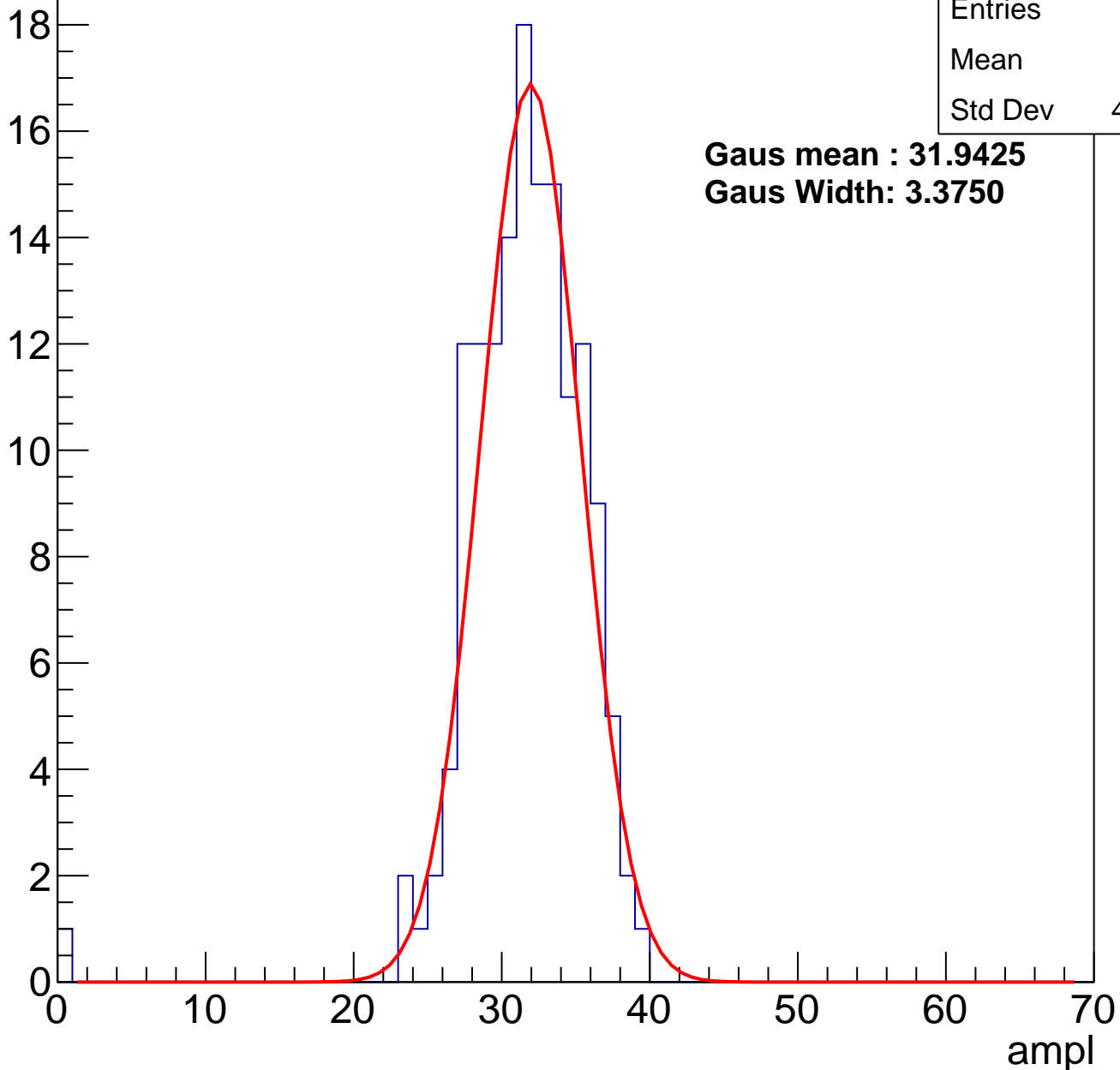
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	31.1
Std Dev	4.194

Gaus mean : 31.9425

Gaus Width: 3.3750

Entry



B1L001S, U22-ch127, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	38.4
Std Dev	3.478

Gaus mean : 38.6334

Gaus Width: 3.6338

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

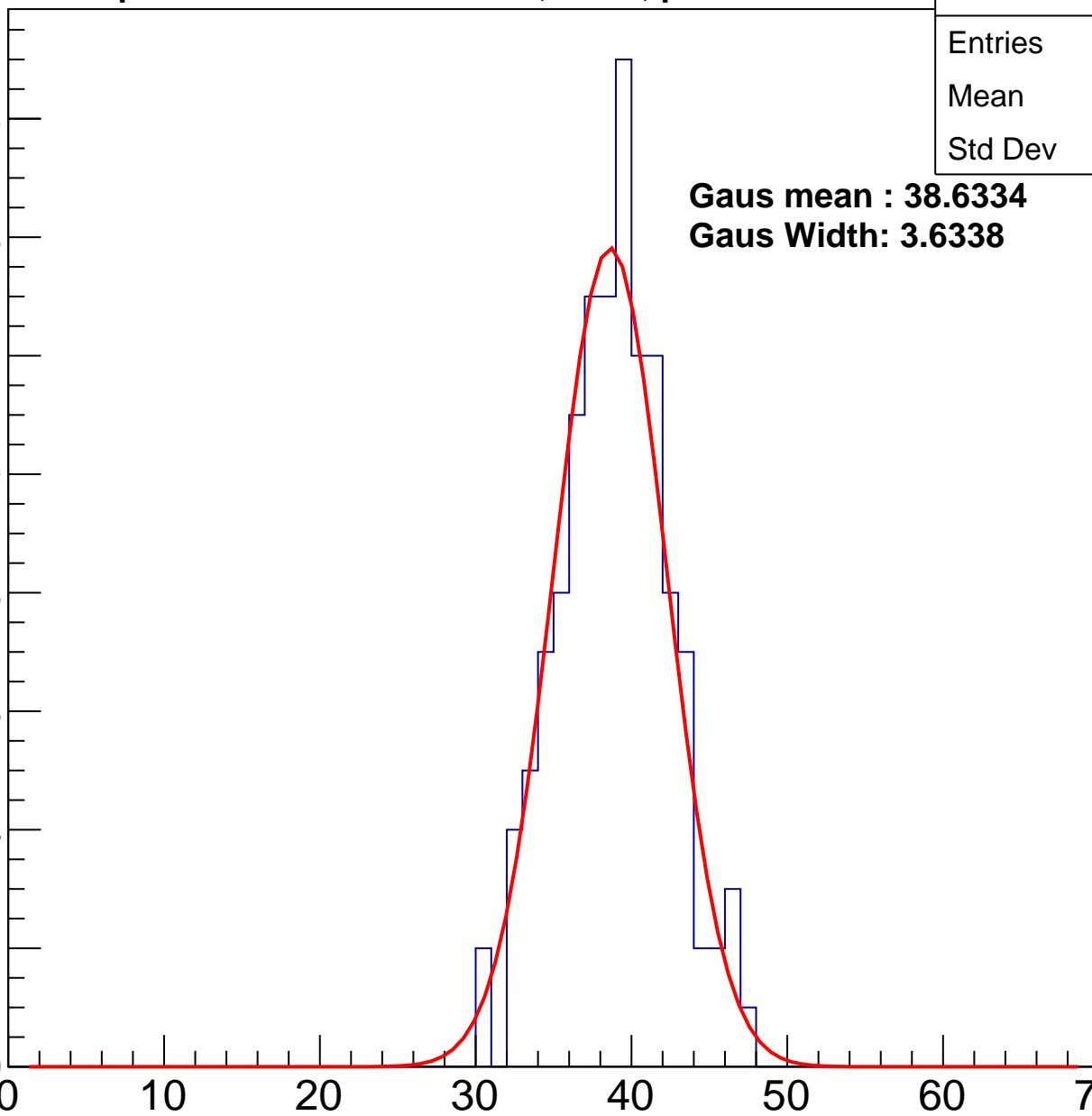
40

50

60

70

ampl



B1L001S, U22-ch127, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	44.58
Std Dev	3.524

Gaus mean : 45.0111

Gaus Width: 3.2842

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

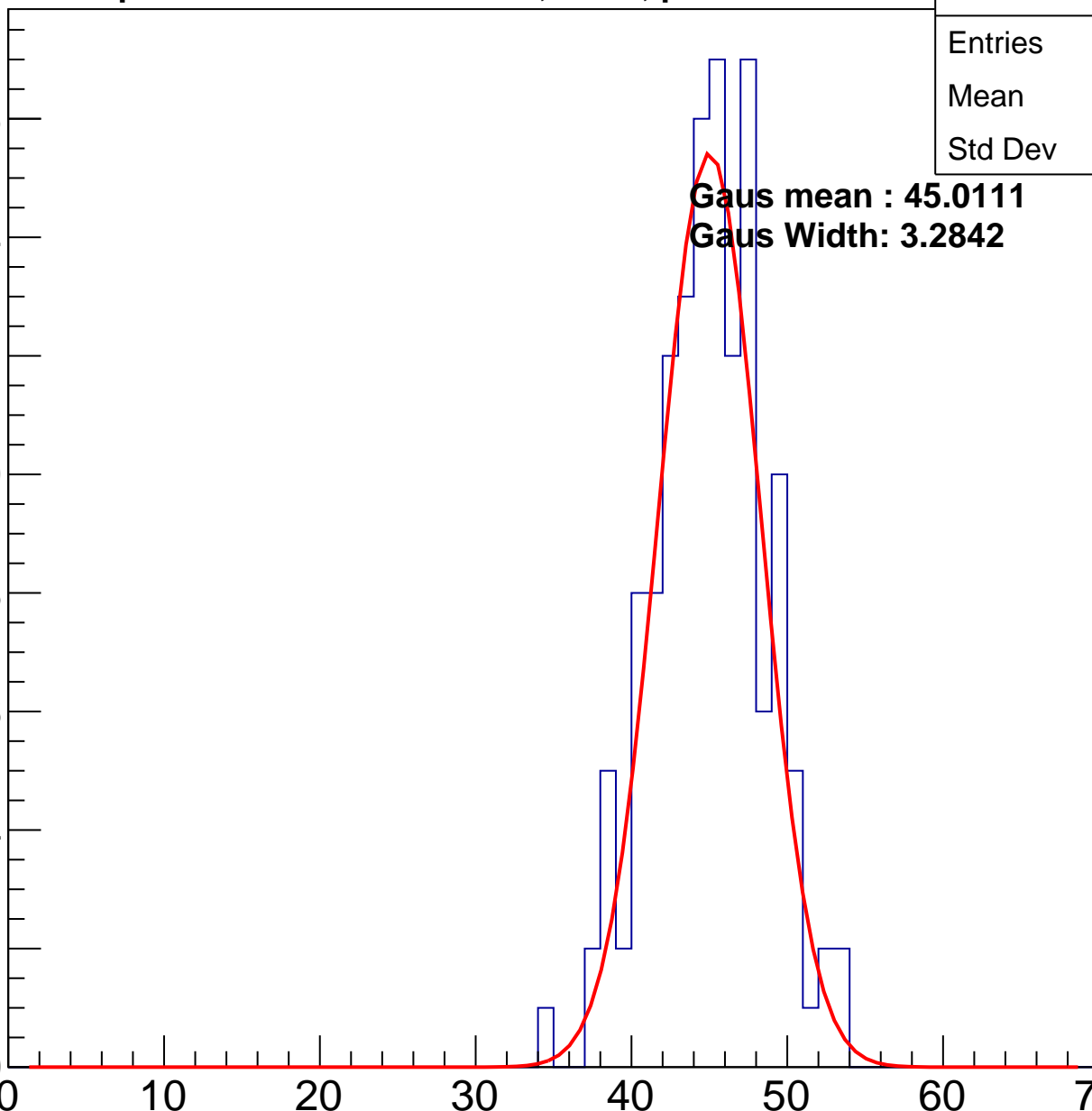
40

50

60

70

ampl

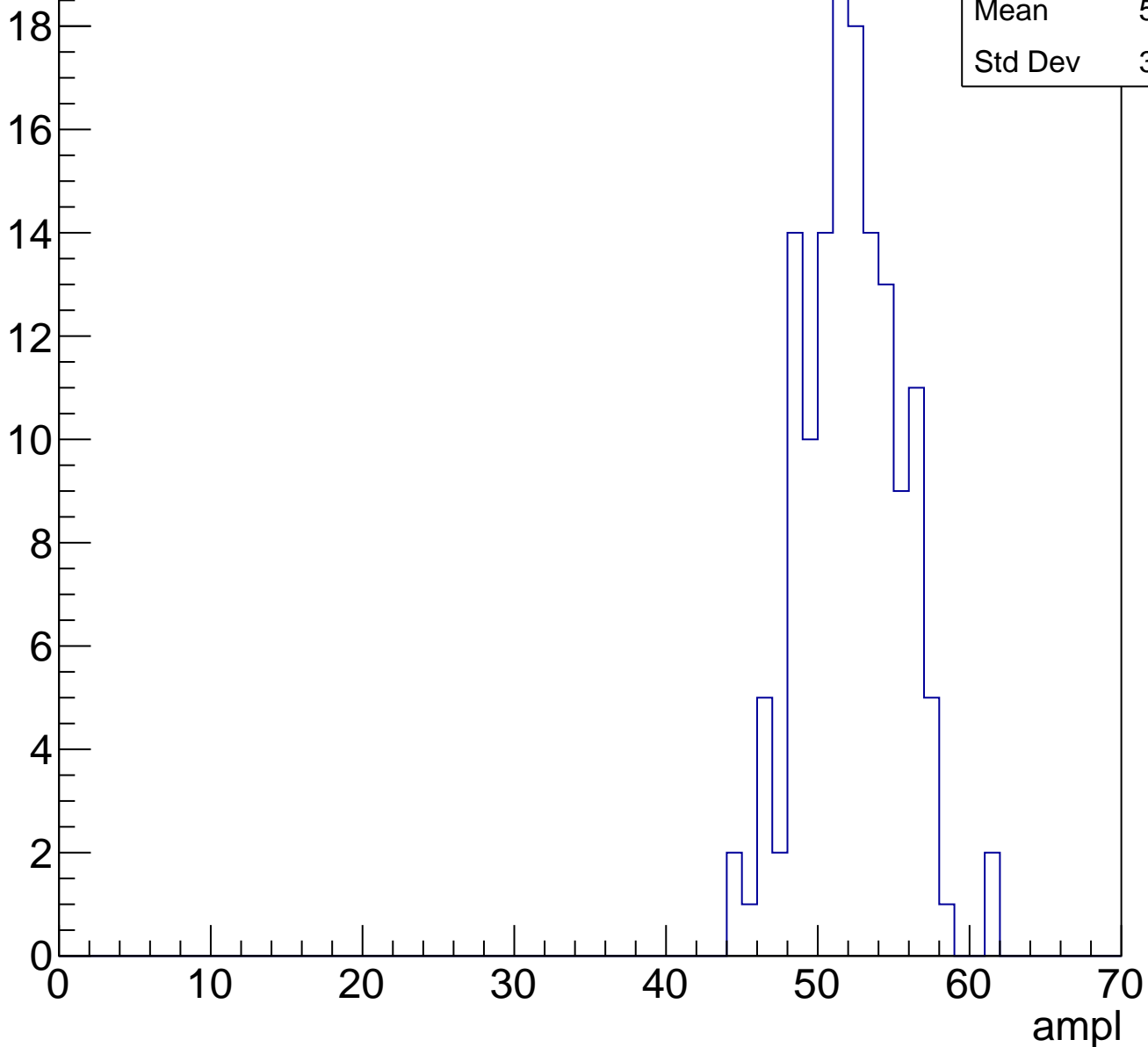


B1L001S, U22-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	51.74
Std Dev	3.199

Entry



B1L001S, U22-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	58.26
Std Dev	2.764

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

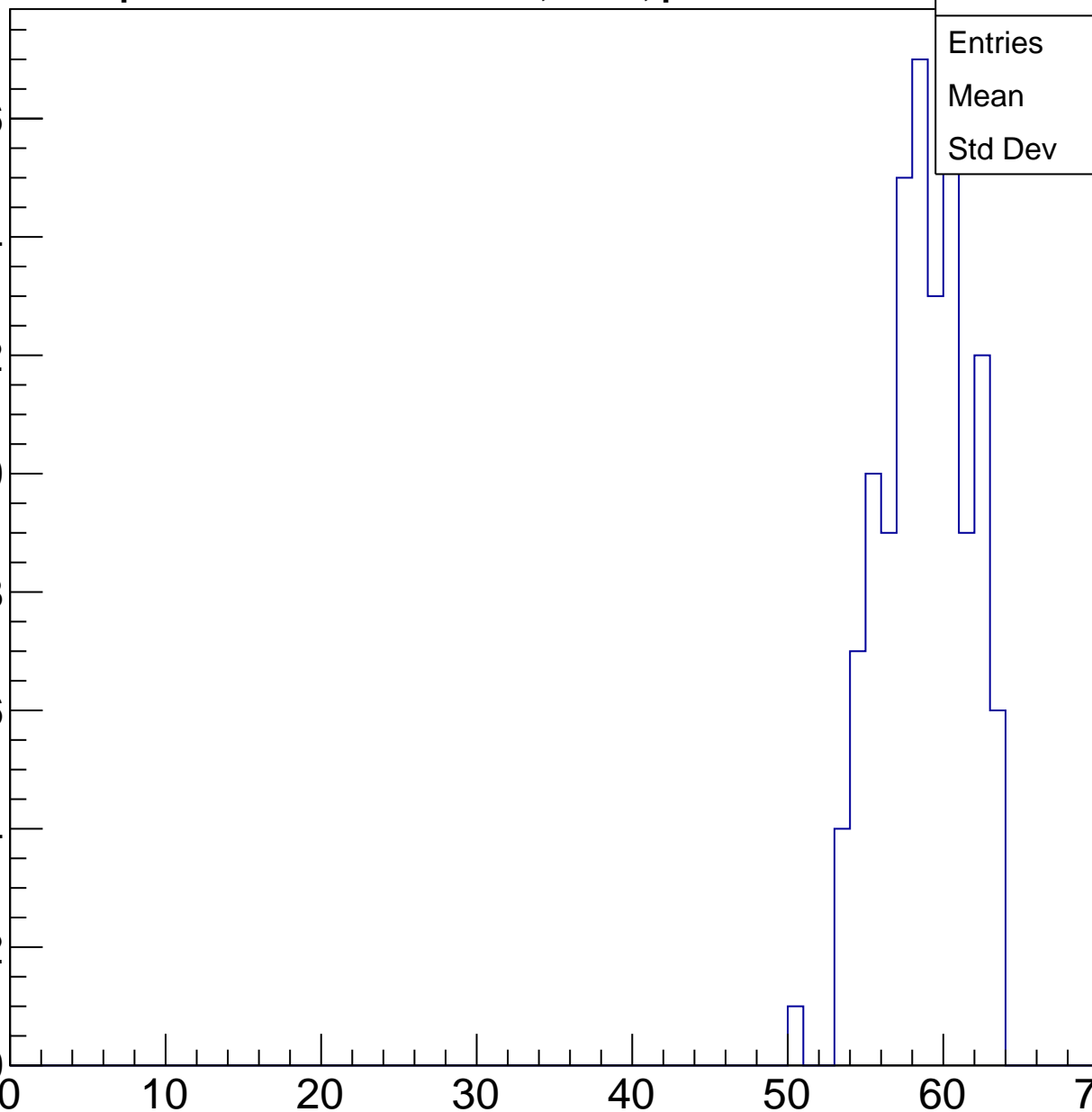
40

50

60

70

ampl



B1L001S, U22-ch127, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	45
Mean	58.33
Std Dev	12.71

ampl

0

10

20

30

40

50

60

70

B1L001S, U22-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

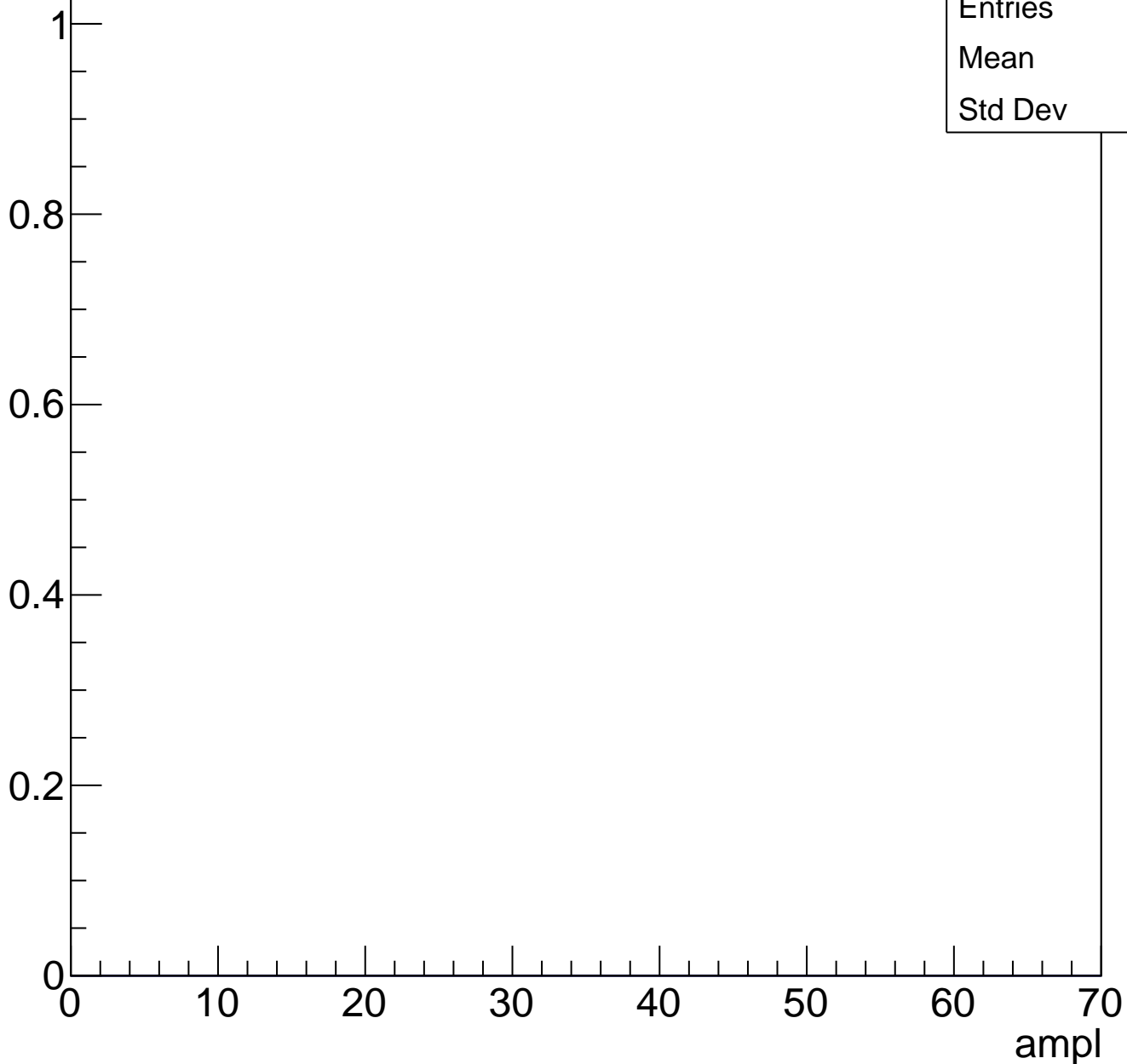


Entries	0
Mean	0
Std Dev	0

B1L001S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0