B0L100S, U13-ch0, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch0, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	2
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
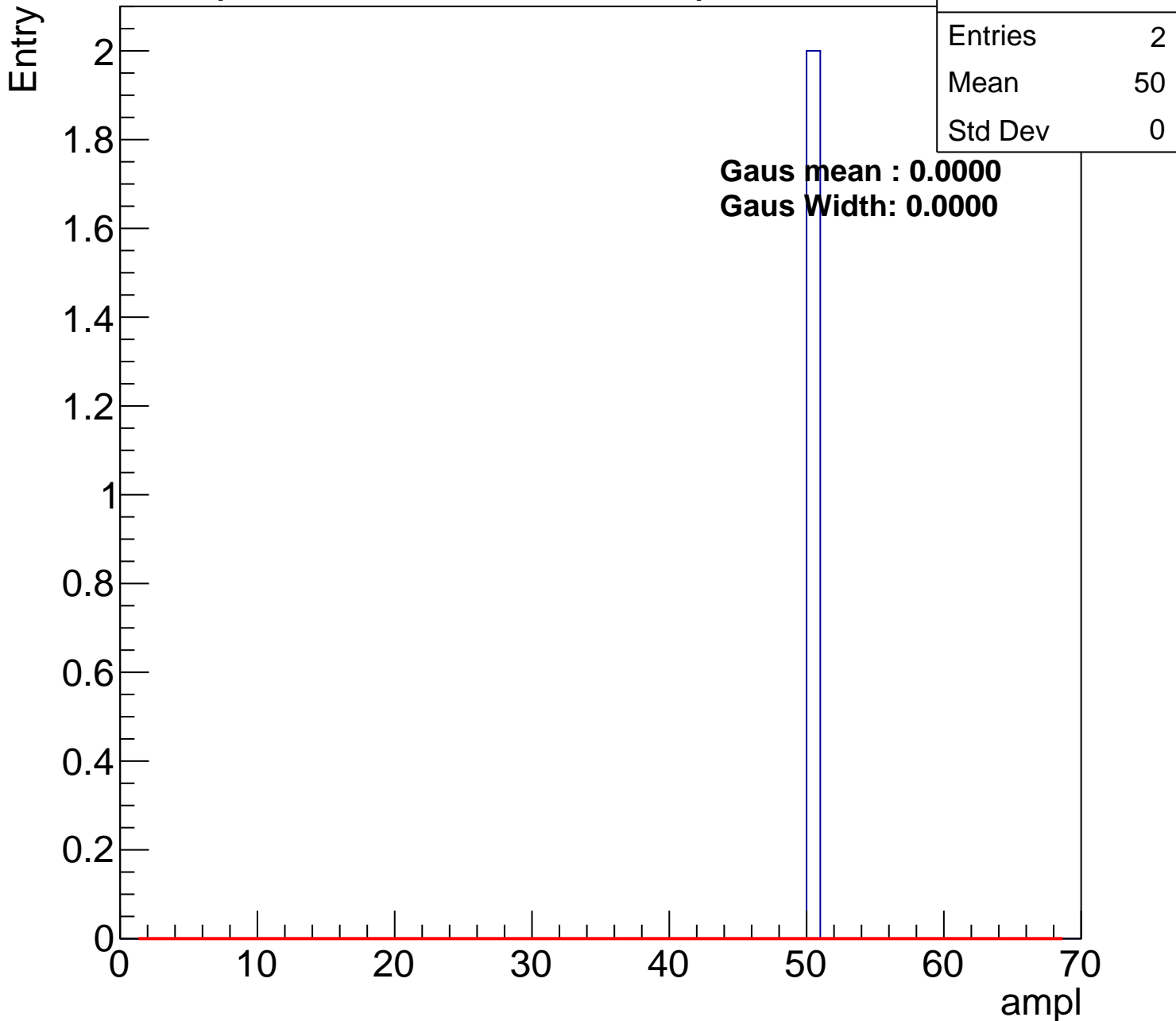
B0L100S, U13-ch0, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch0, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	50
Std Dev	0

Entry

ampl

B0L100S, U13-ch0, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch0, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch0, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch0, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch1, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	45
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
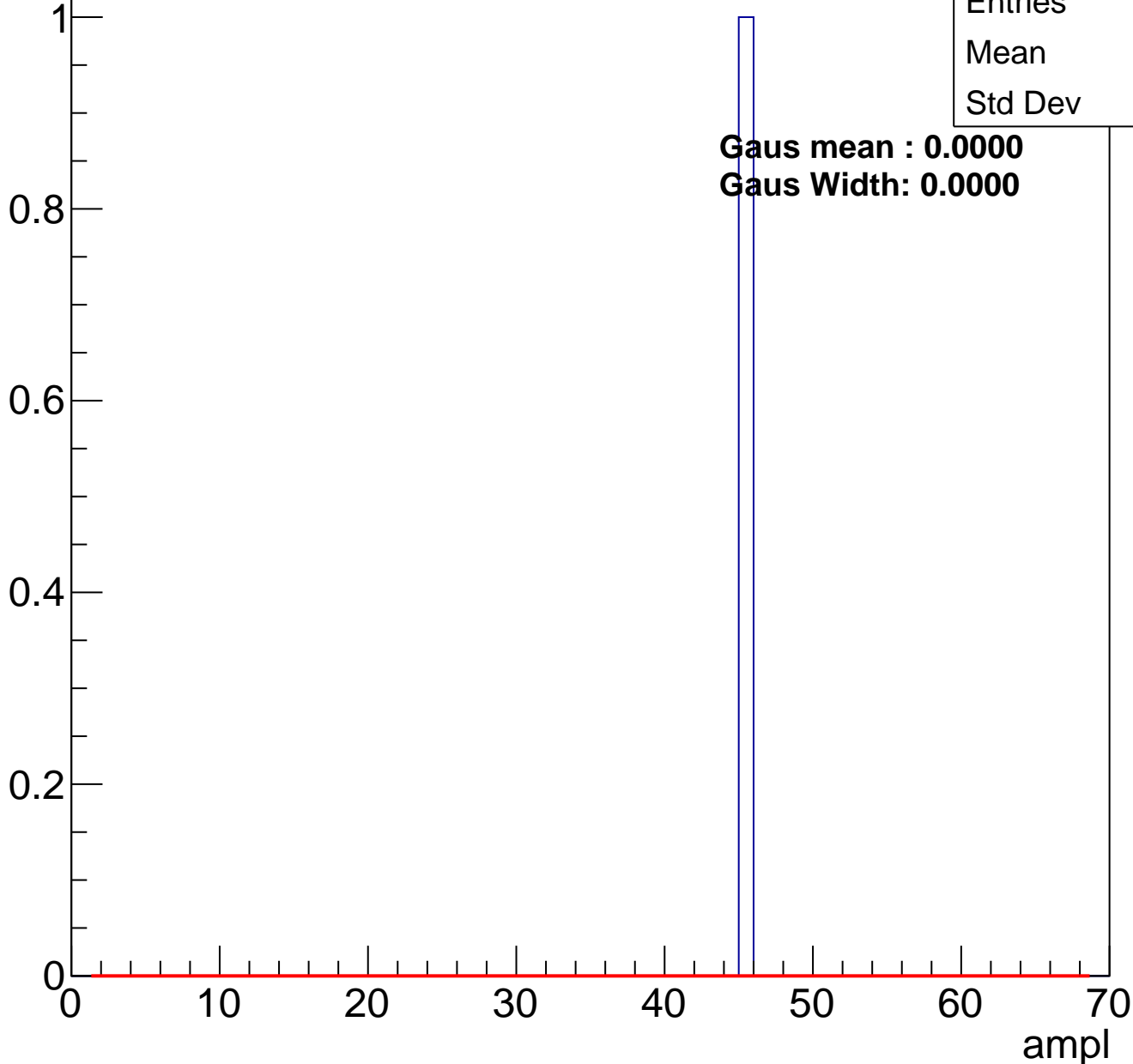
B0L100S, U13-ch1, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	44.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000
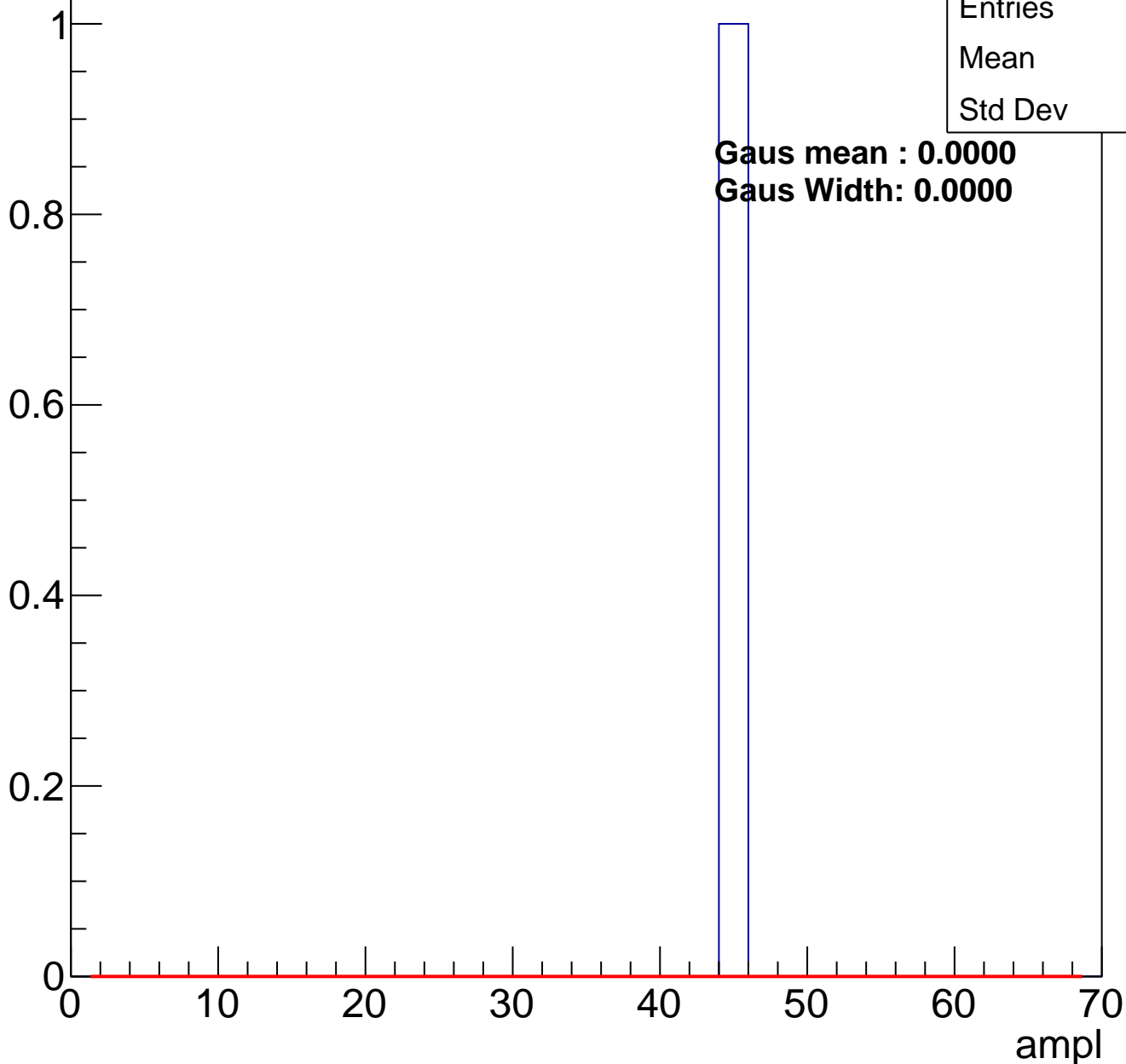
Entry

ampl

B0L100S, U13-ch1, adc2

calib_packv5_042523_0143.root, FC#6, port A1
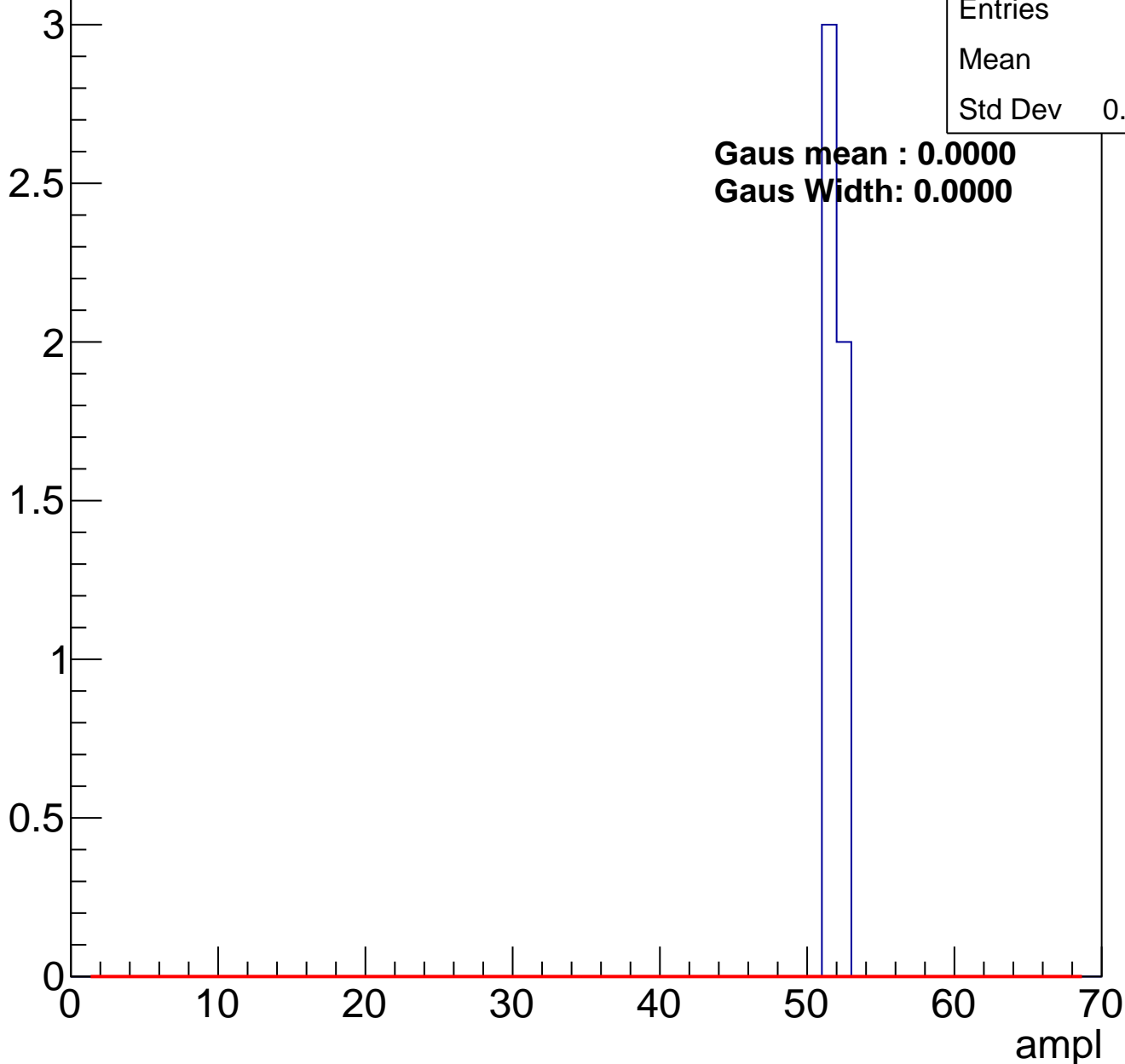
Entries	5
Mean	51.4
Std Dev	0.4899

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch1, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	51
Std Dev	0

Entry

ampl
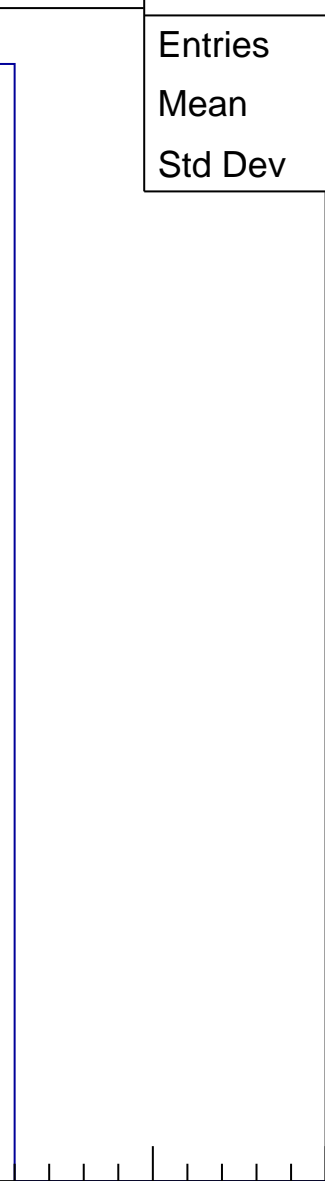
B0L100S, U13-ch1, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch1, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch1, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch1, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch2, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	10
Mean	33.6
Std Dev	0.8

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
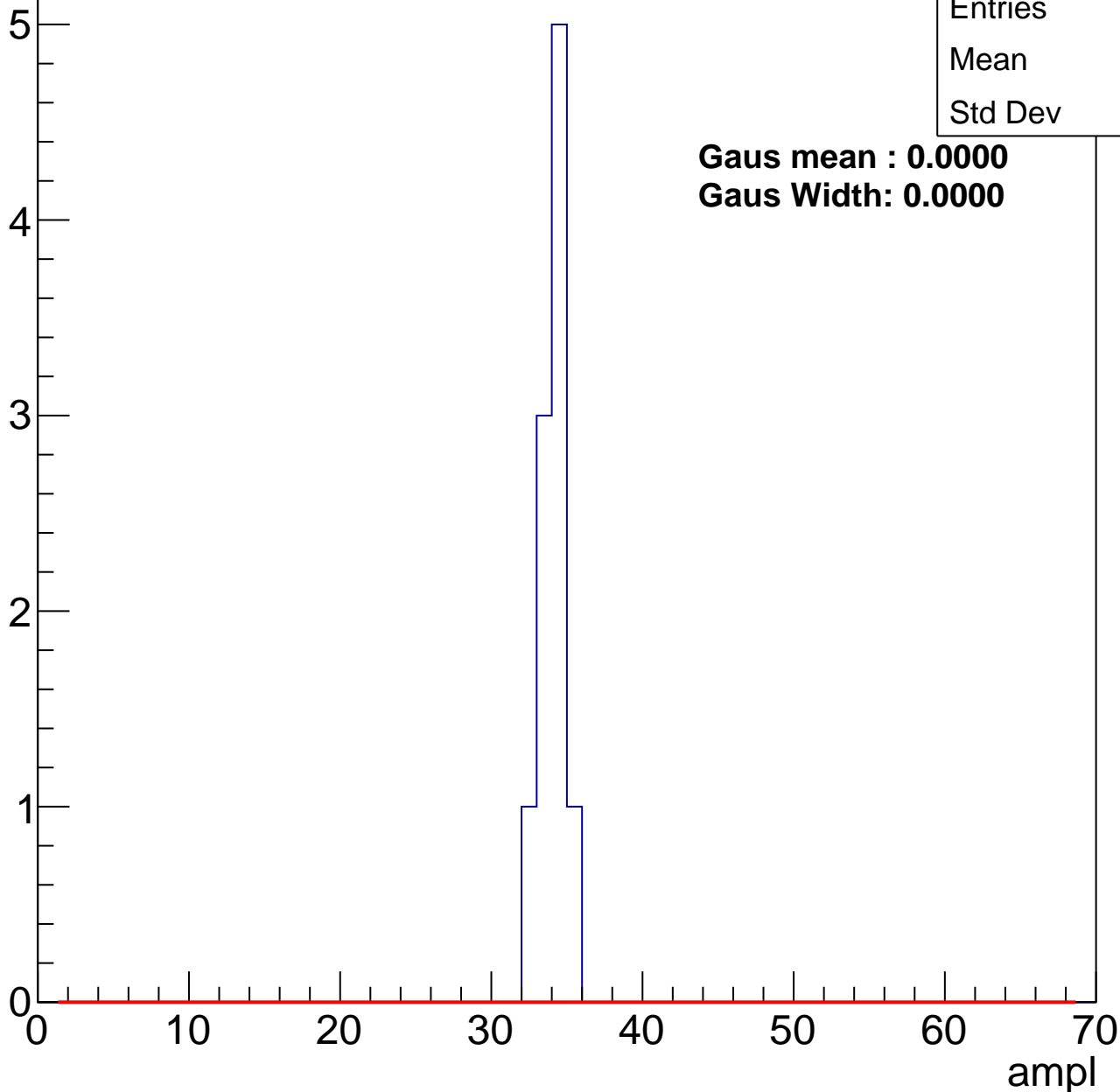
B0L100S, U13-ch2, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	35
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
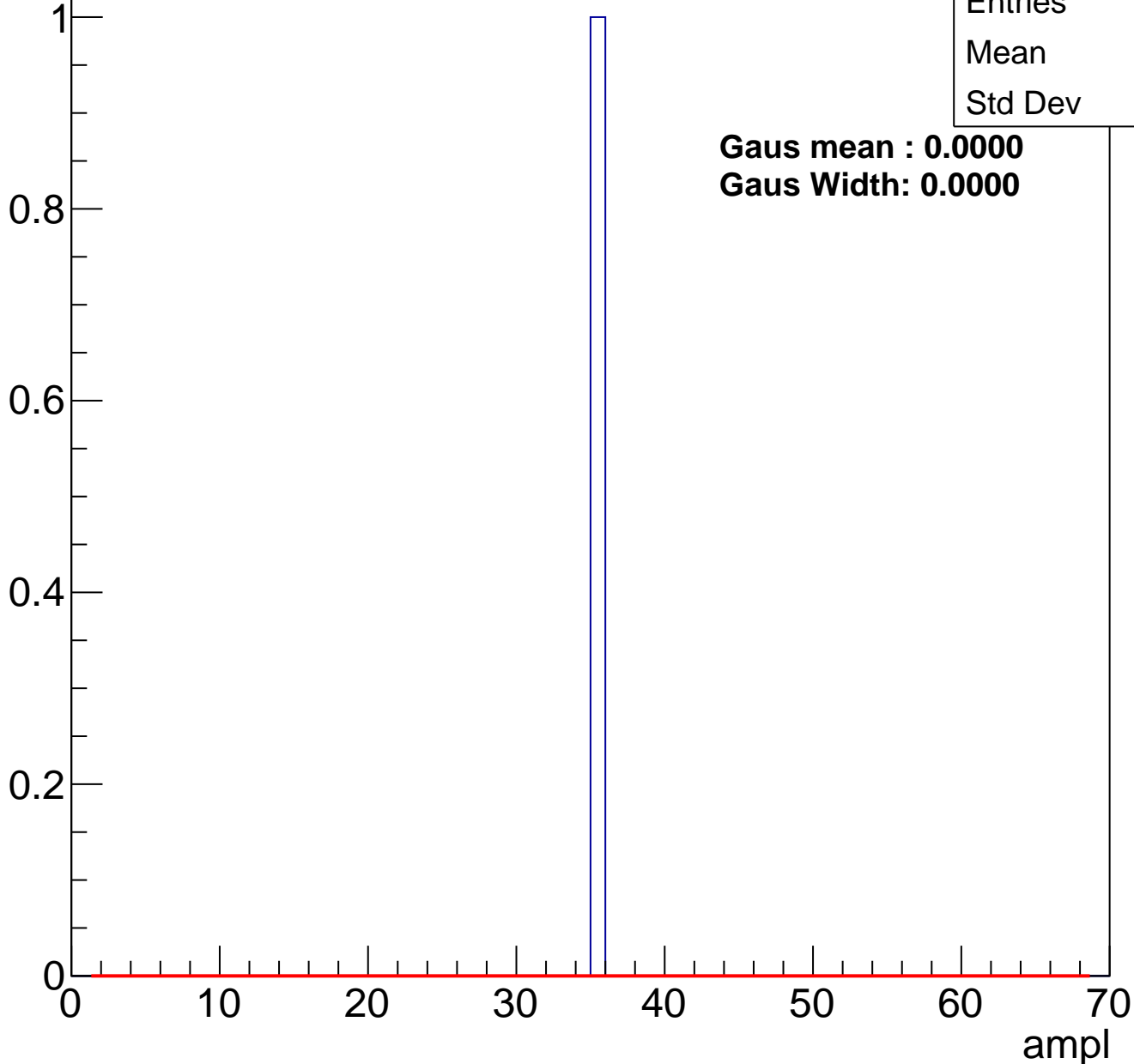
B0L100S, U13-ch2, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	49.4
Std Dev	0.4899

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch2, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	52.5
Std Dev	3.5

Entry

ampl
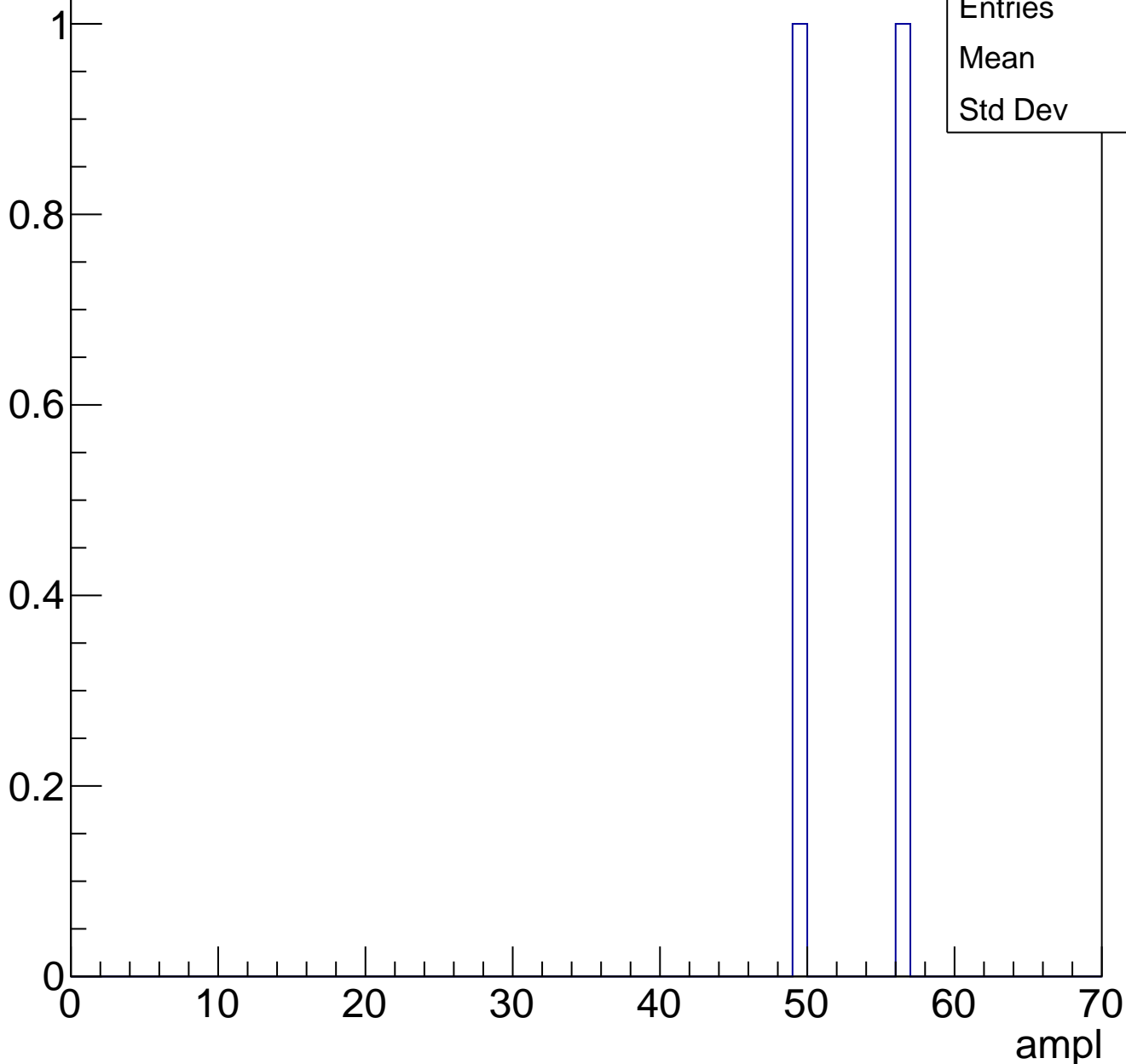
B0L100S, U13-ch2, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch2, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch2, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch2, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch3, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch3, adc1
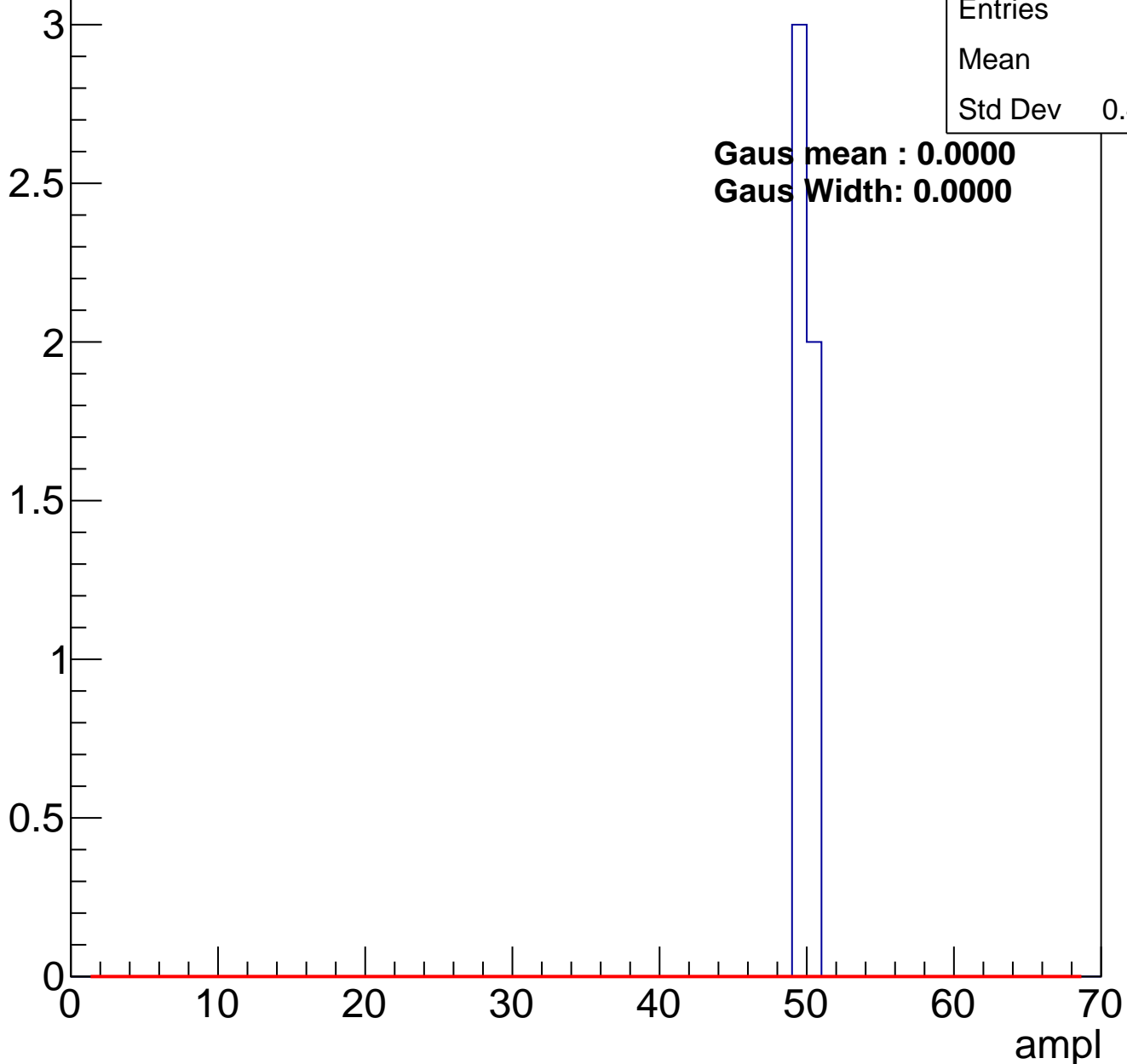
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch3, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch3, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch3, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch3, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch3, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch3, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch4, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch4, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	44.5
Std Dev	2.092

Gaus mean : 38.7890
Gaus Width: 7.5952
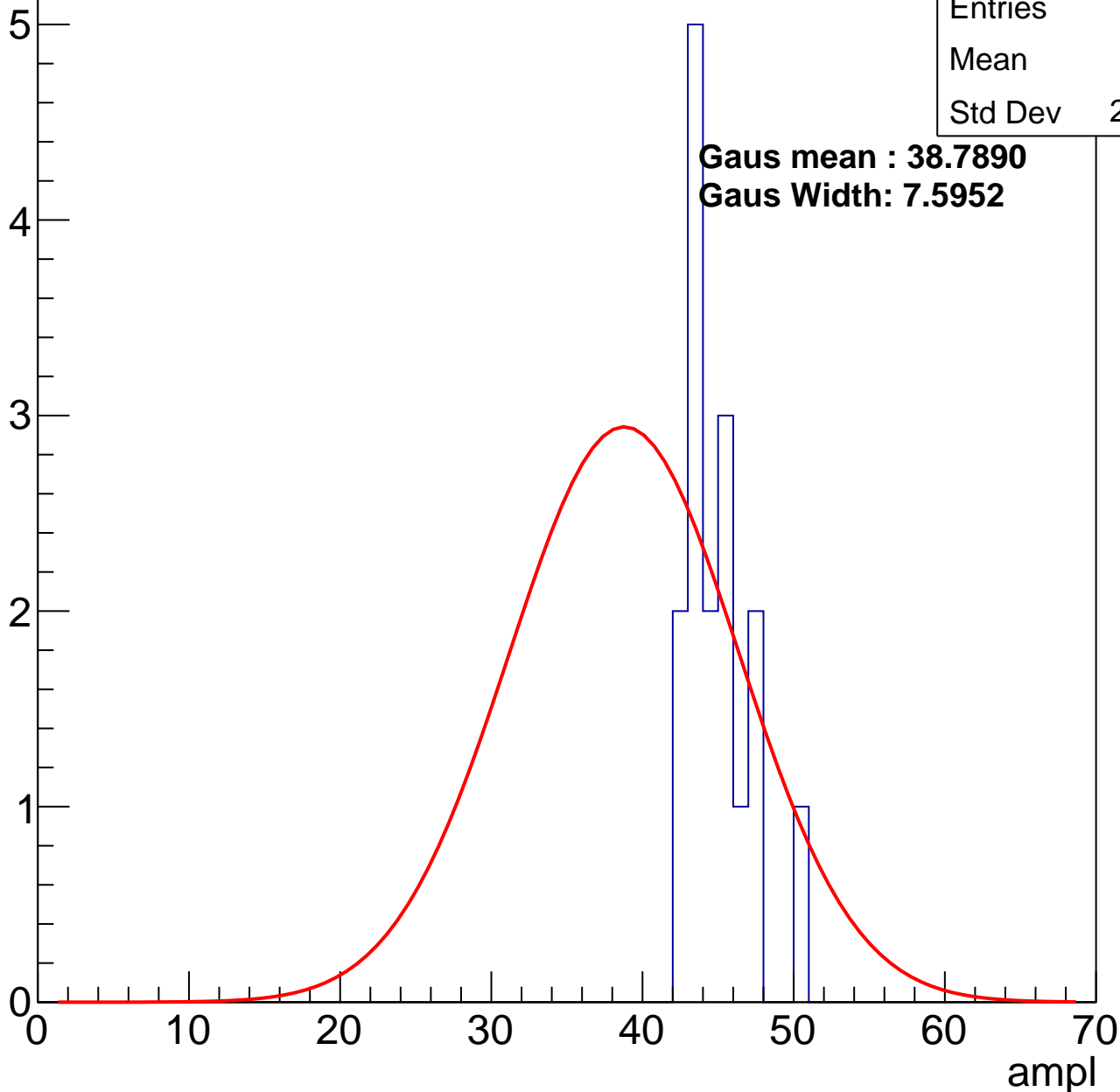
B0L100S, U13-ch4, adc2

calib_packv5_042523_0143.root, FC#6, port A1
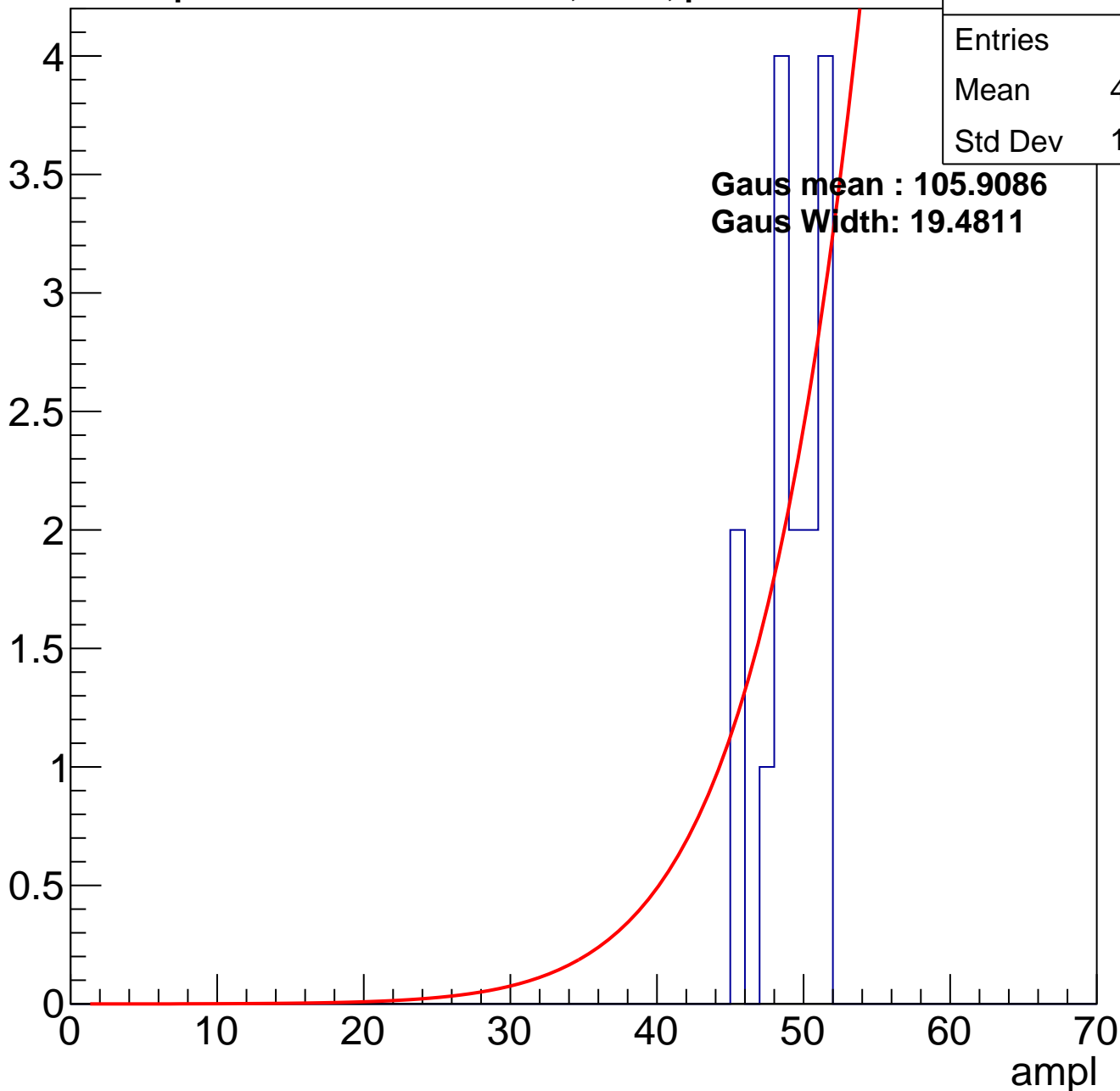
Entries	15
Mean	48.73
Std Dev	1.948

Gaus mean : 105.9086
Gaus Width: 19.4811

Entry

ampl
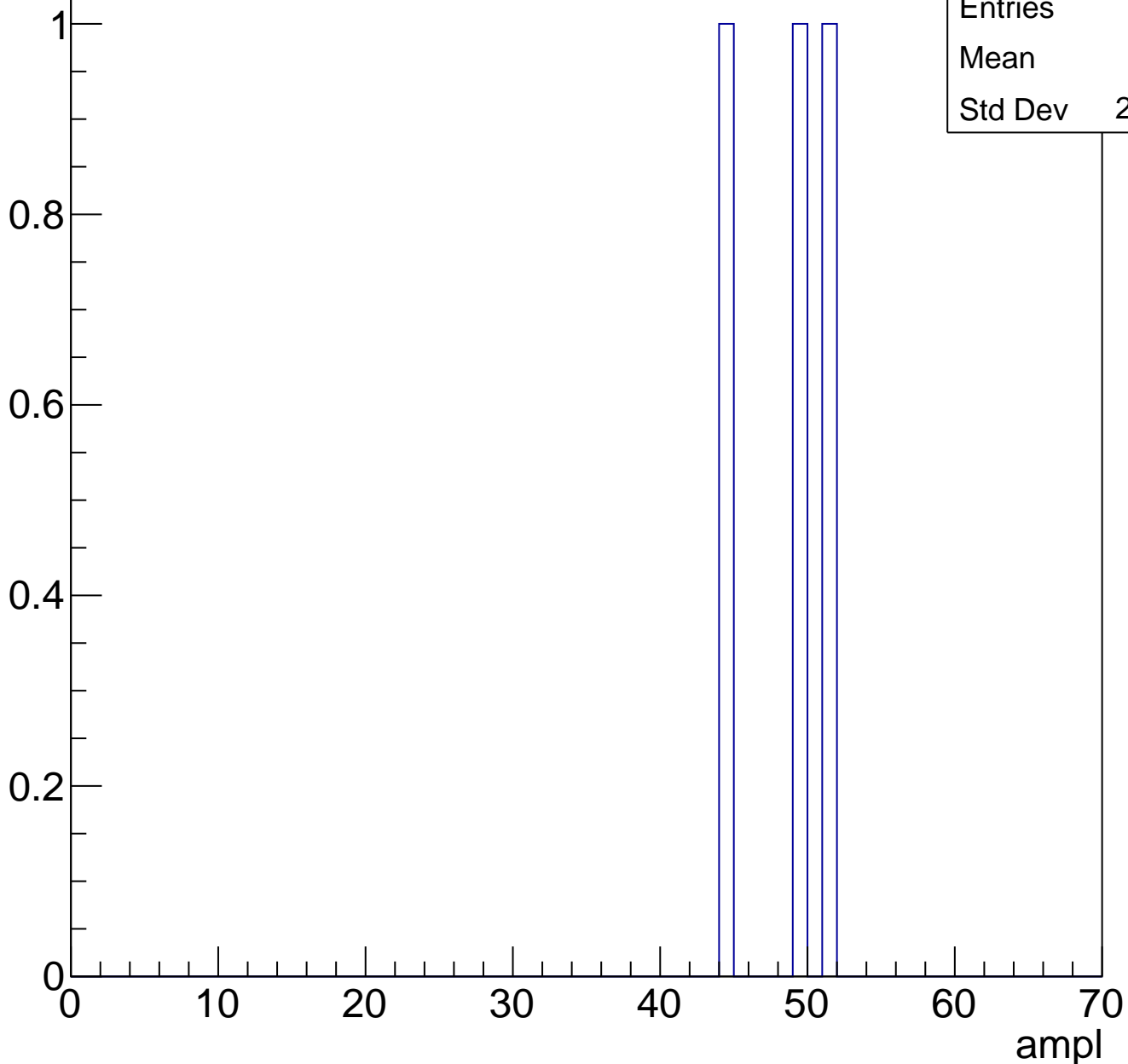
B0L100S, U13-ch4, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch4, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch4, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch4, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch5, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch5, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch5, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch5, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Entry

ampl

B0L100S, U13-ch5, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch5, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch5, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch5, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch6, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch6, adc1
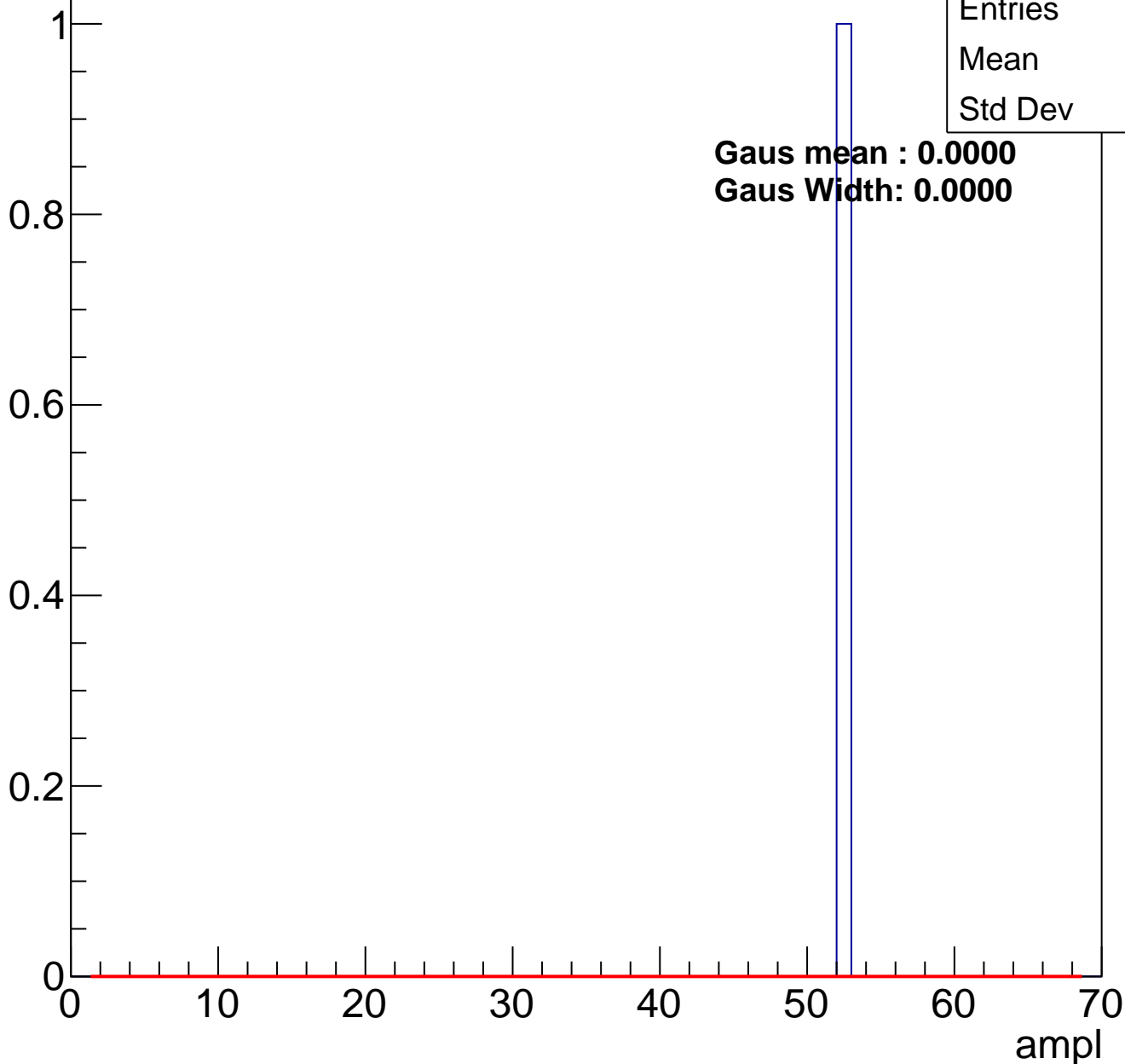
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch6, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch6, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch6, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch6, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch6, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch6, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch7, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch7, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch7, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch7, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch7, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch7, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch7, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch7, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch8, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch8, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch8, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch8, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch8, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch8, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch8, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch8, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch9, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch9, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
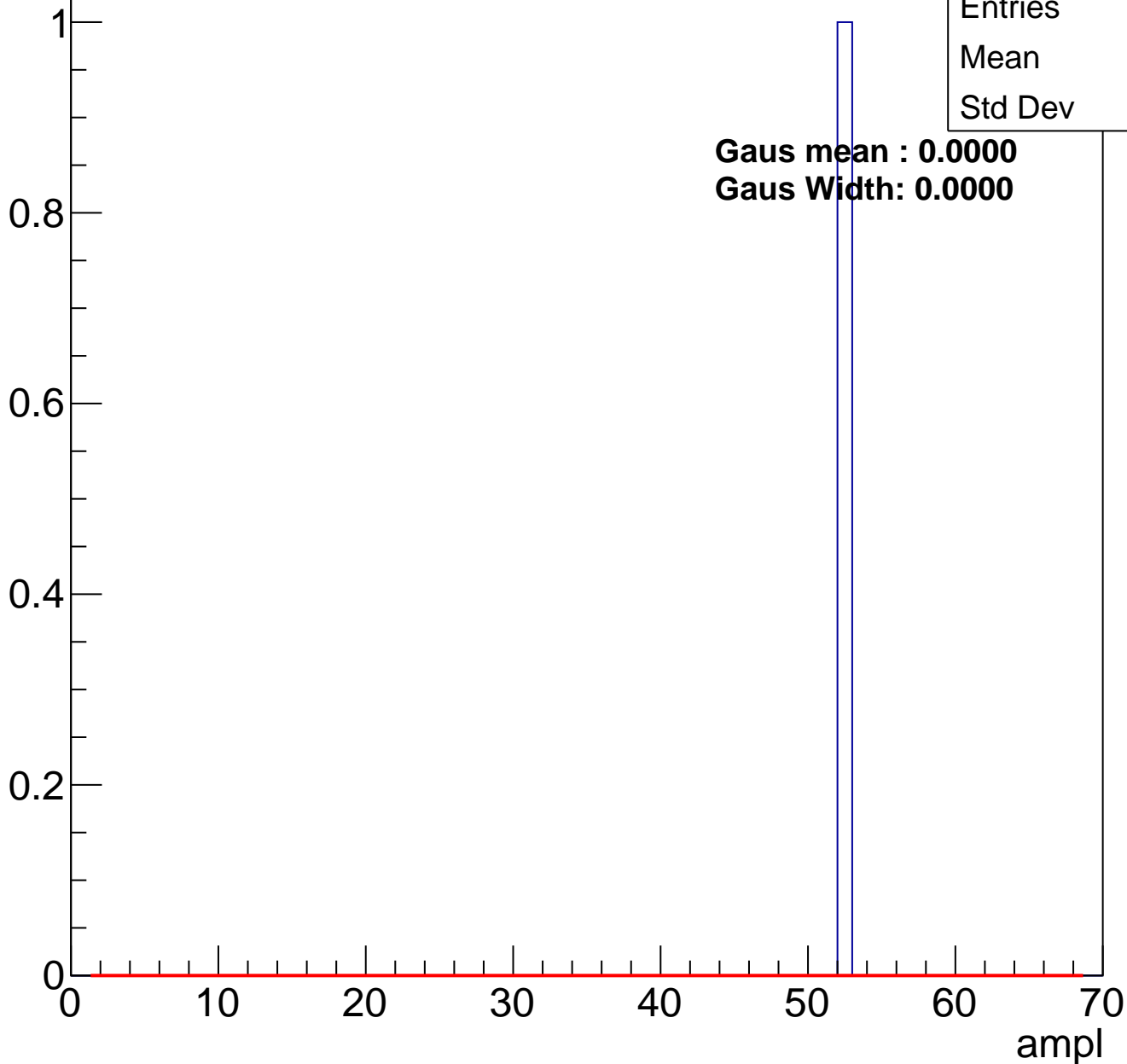
B0L100S, U13-ch9, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50
Std Dev	2.16

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
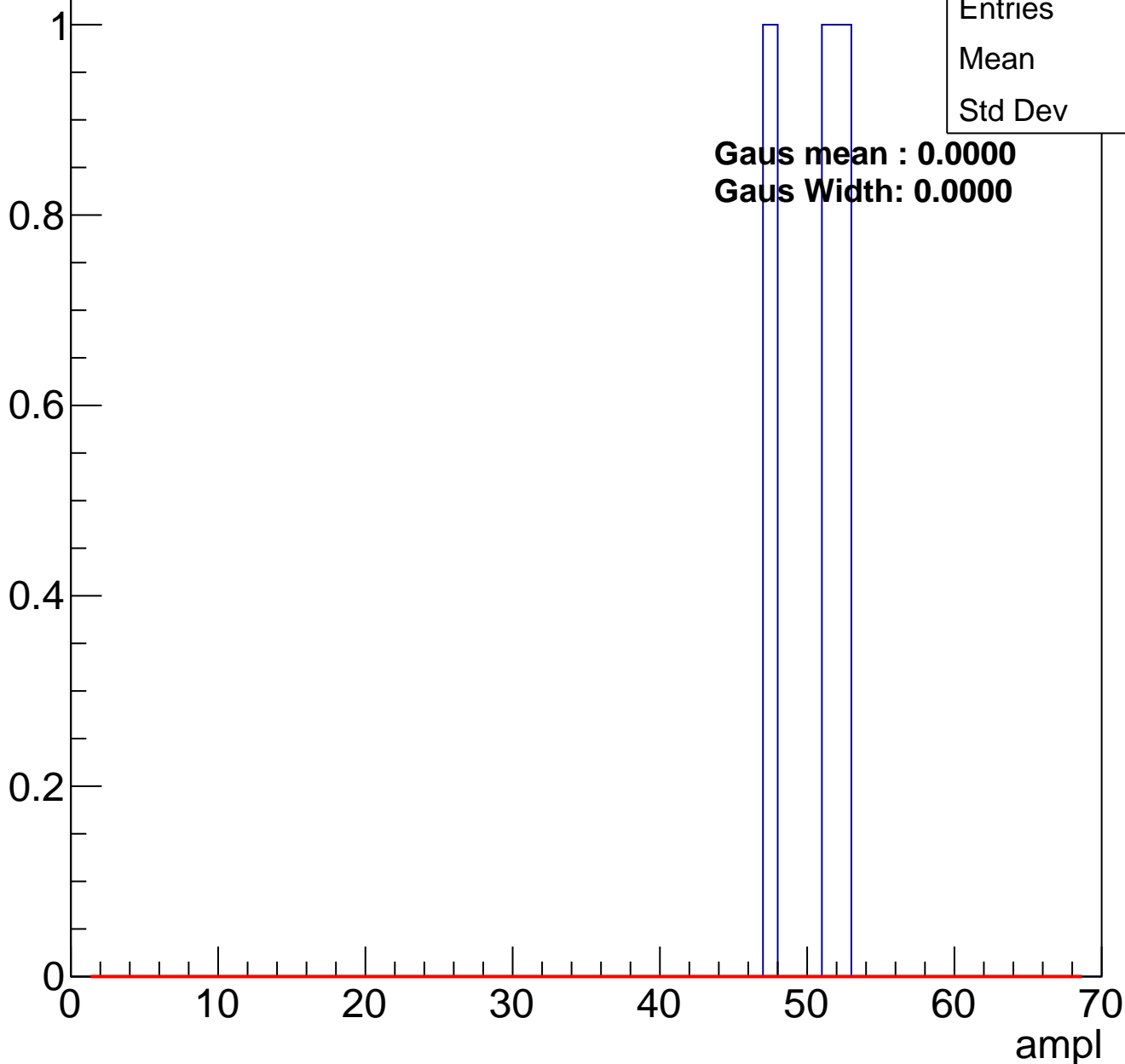
B0L100S, U13-ch9, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	51
Std Dev	0

ampl
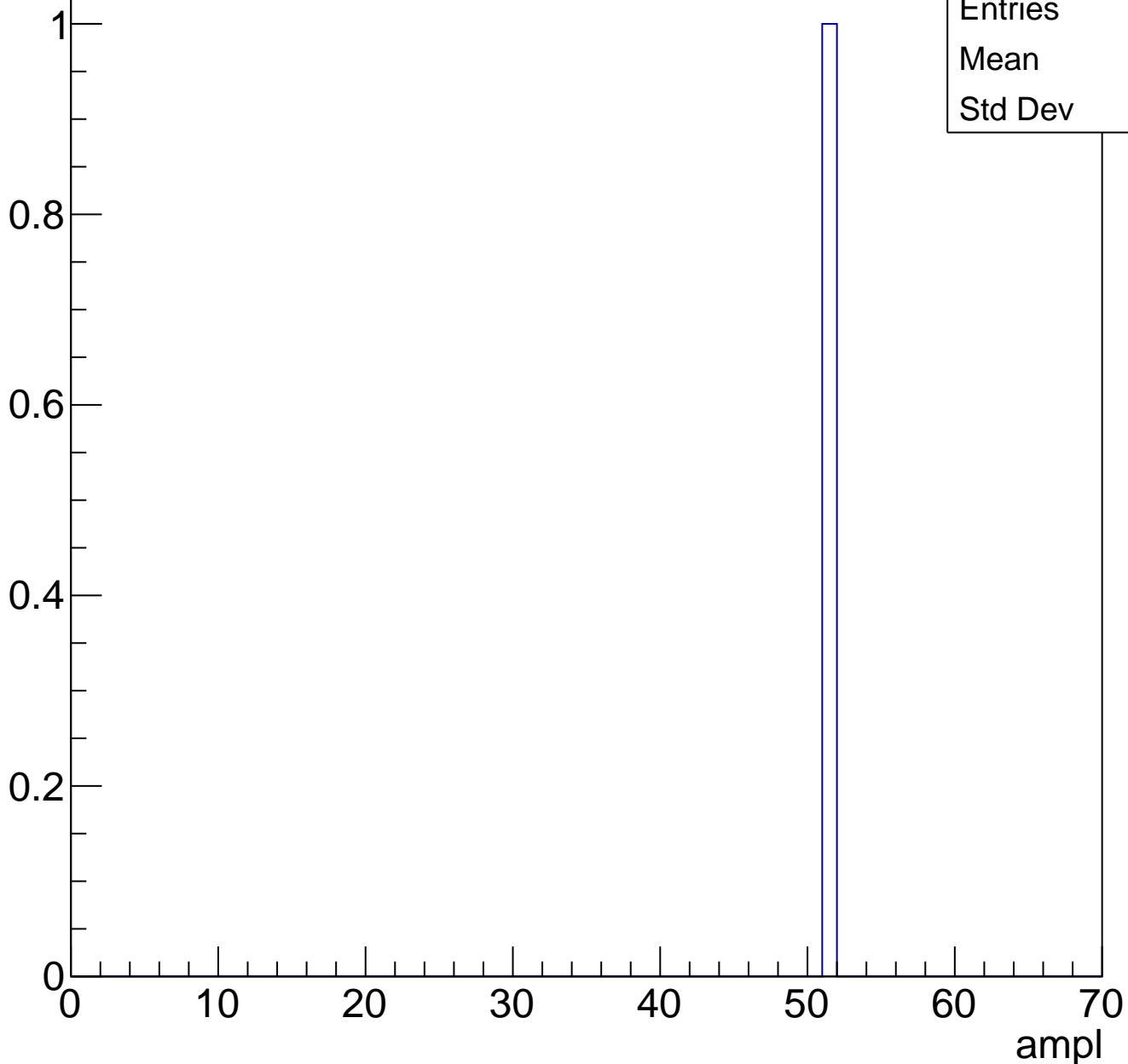
B0L100S, U13-ch9, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

ampl
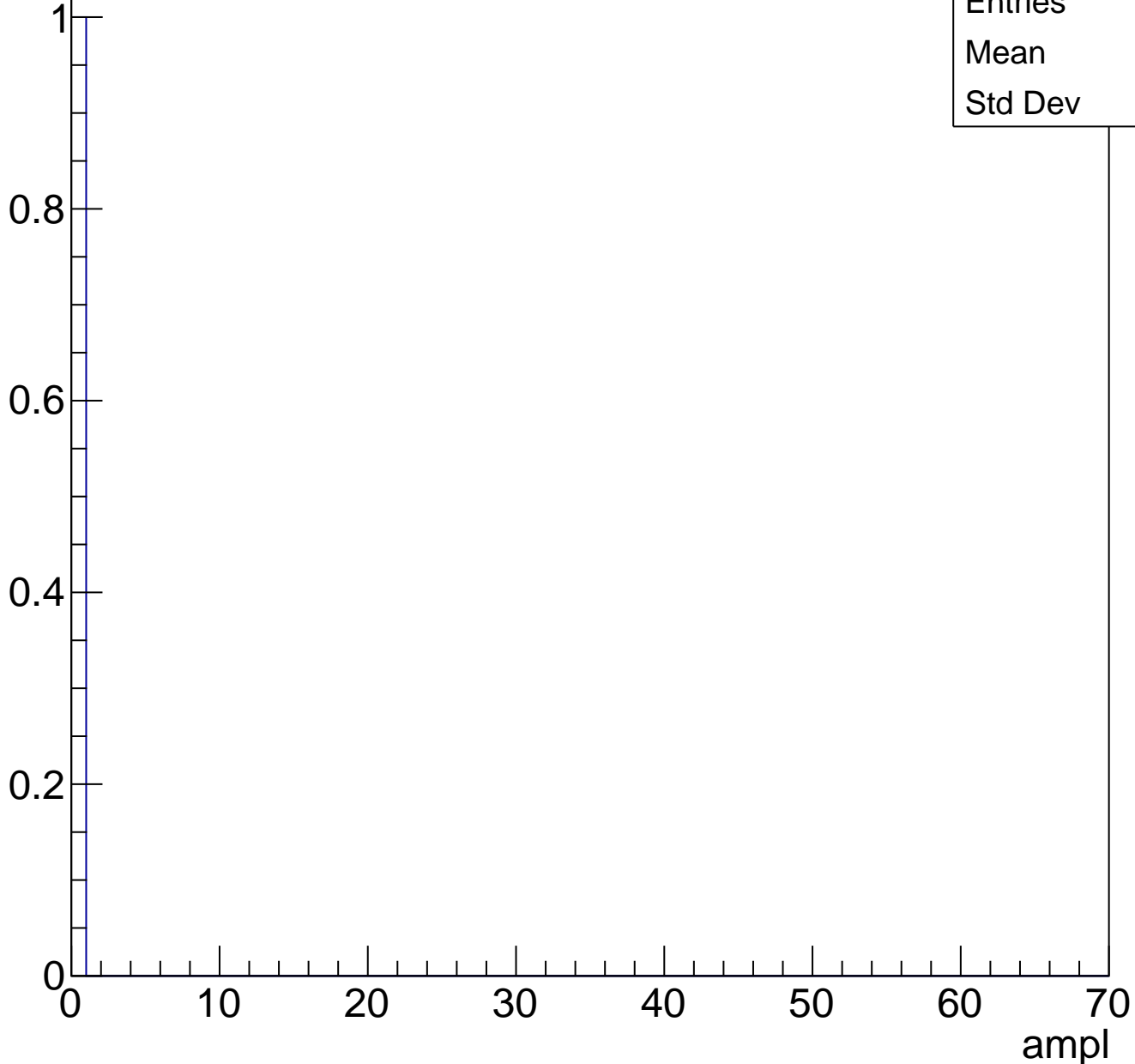
B0L100S, U13-ch9, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch9, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch9, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch10, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch10, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	47
Std Dev	0
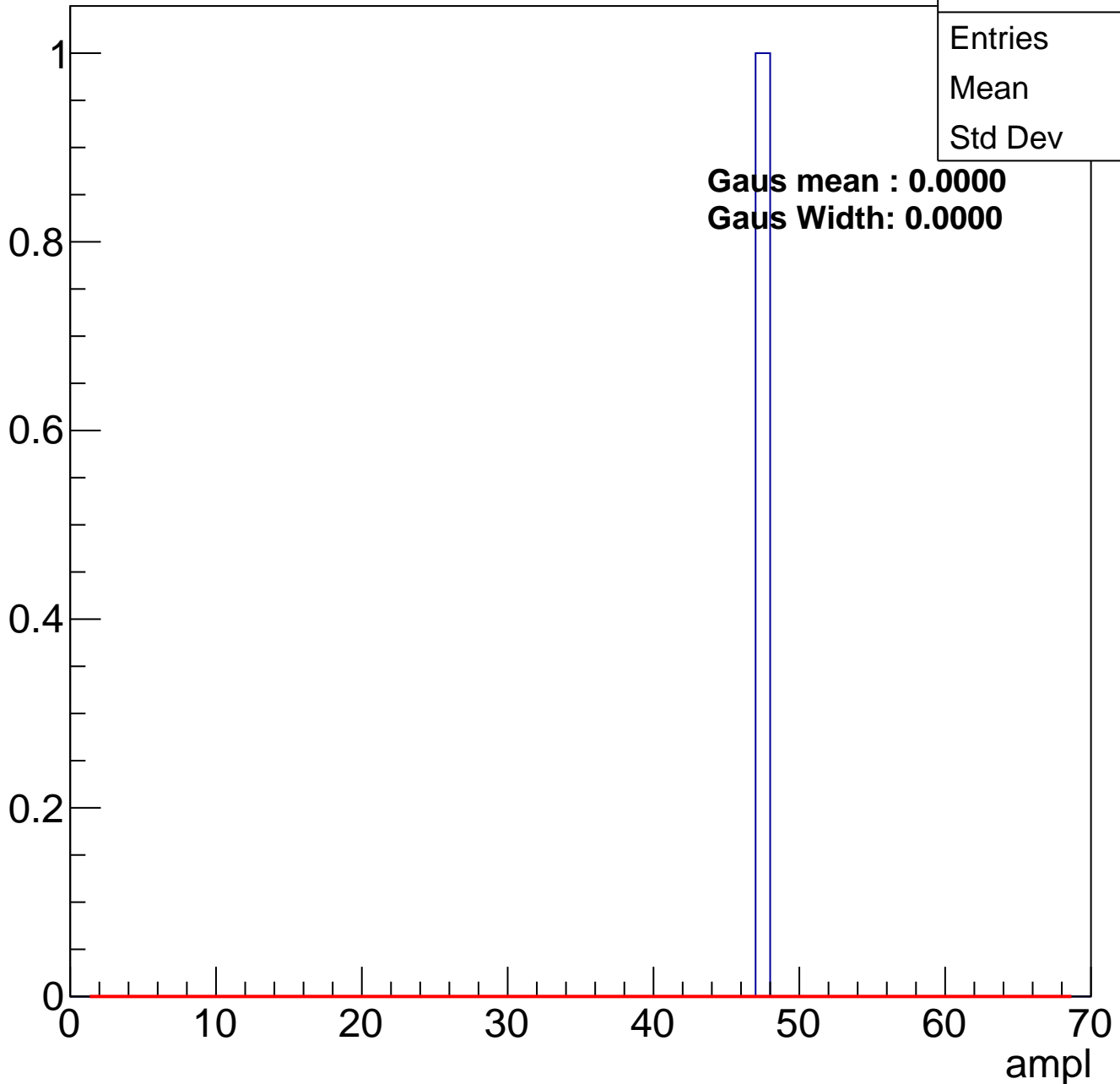
Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch10, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	48.25
Std Dev	1.785

Gaus mean : 0.0000
Gaus Width: 0.0000
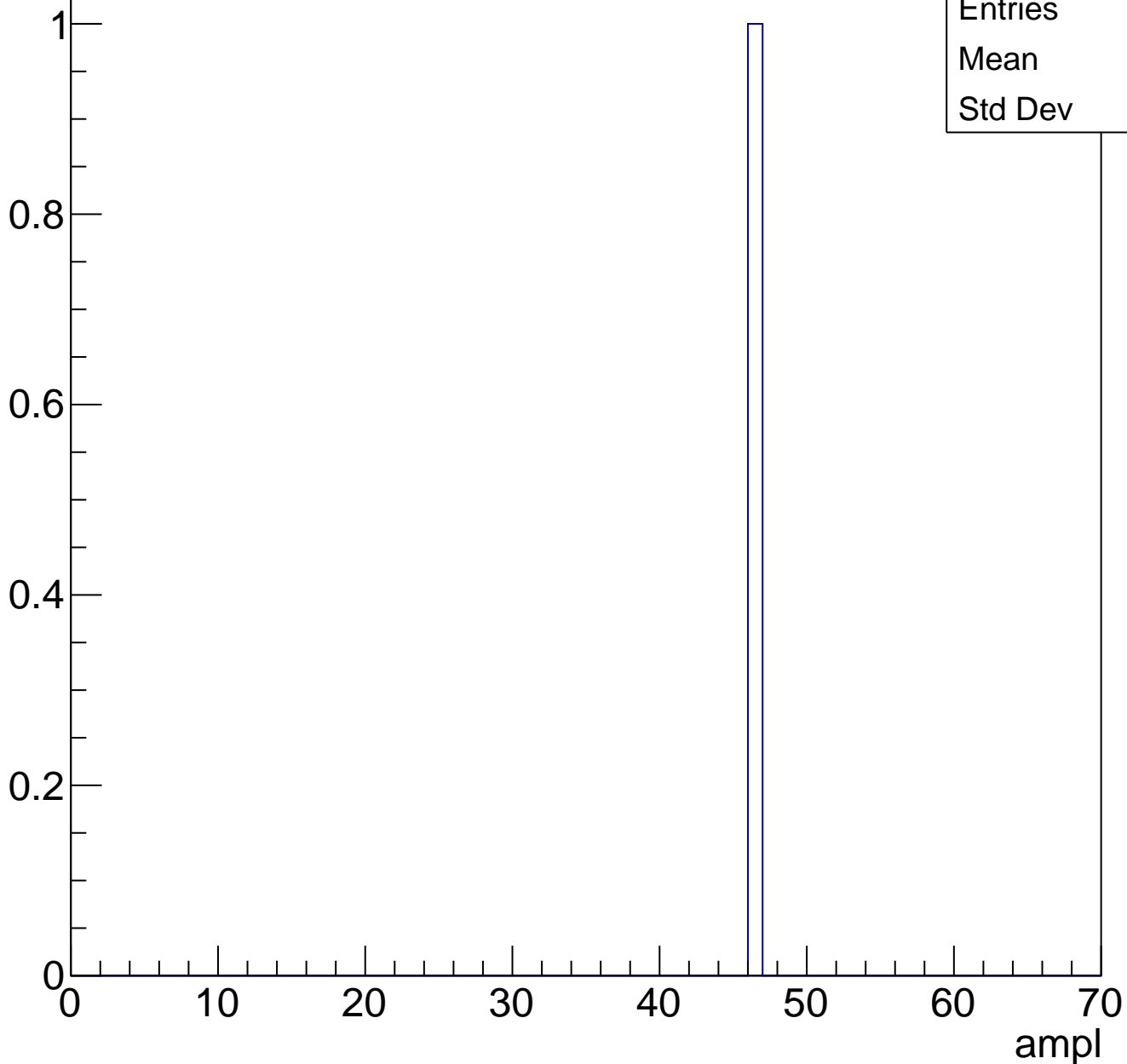
Entry

ampl
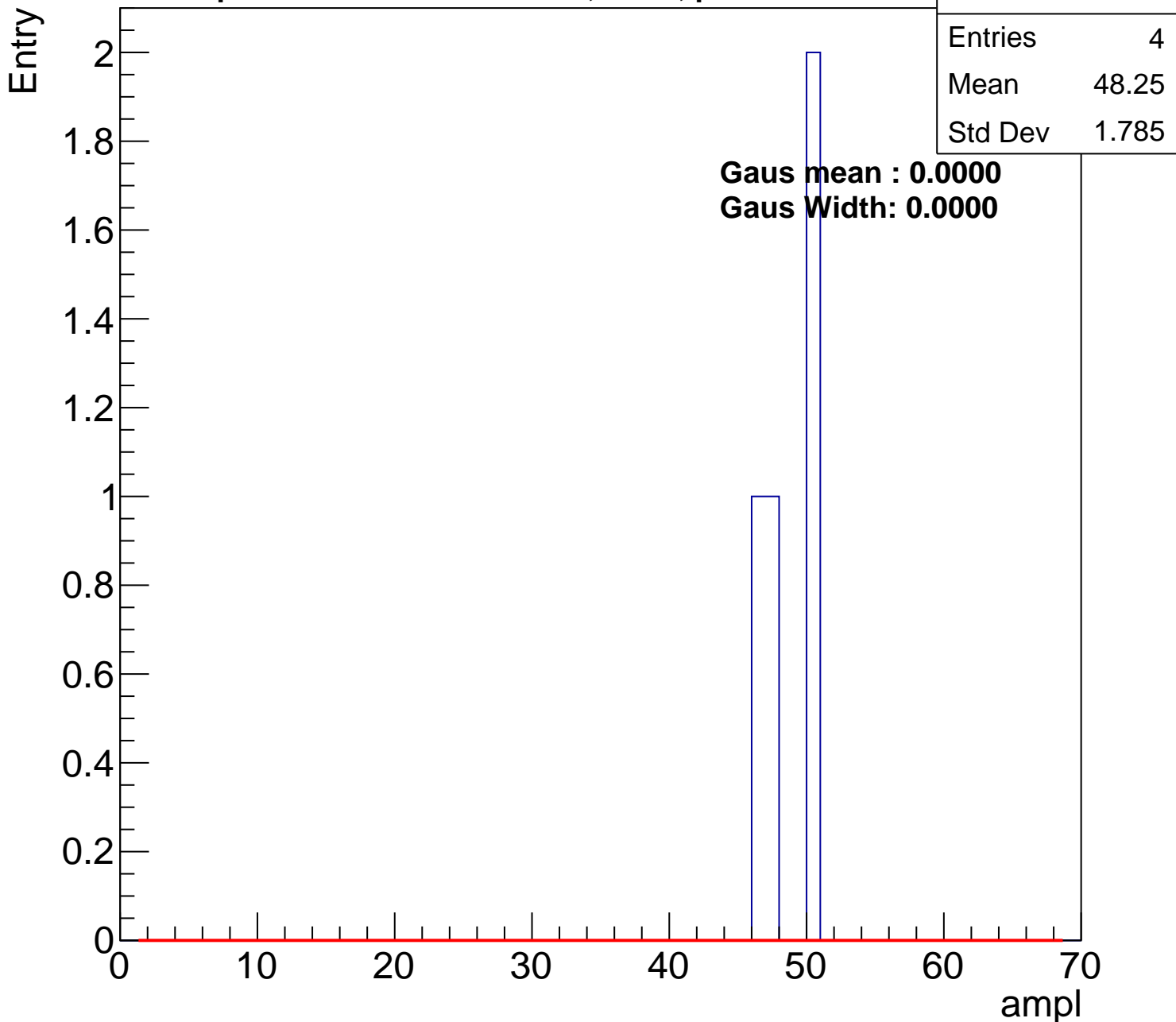
B0L100S, U13-ch10, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	46
Std Dev	0

Entry

ampl

B0L100S, U13-ch10, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch10, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch10, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch10, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch11, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch11, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch11, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
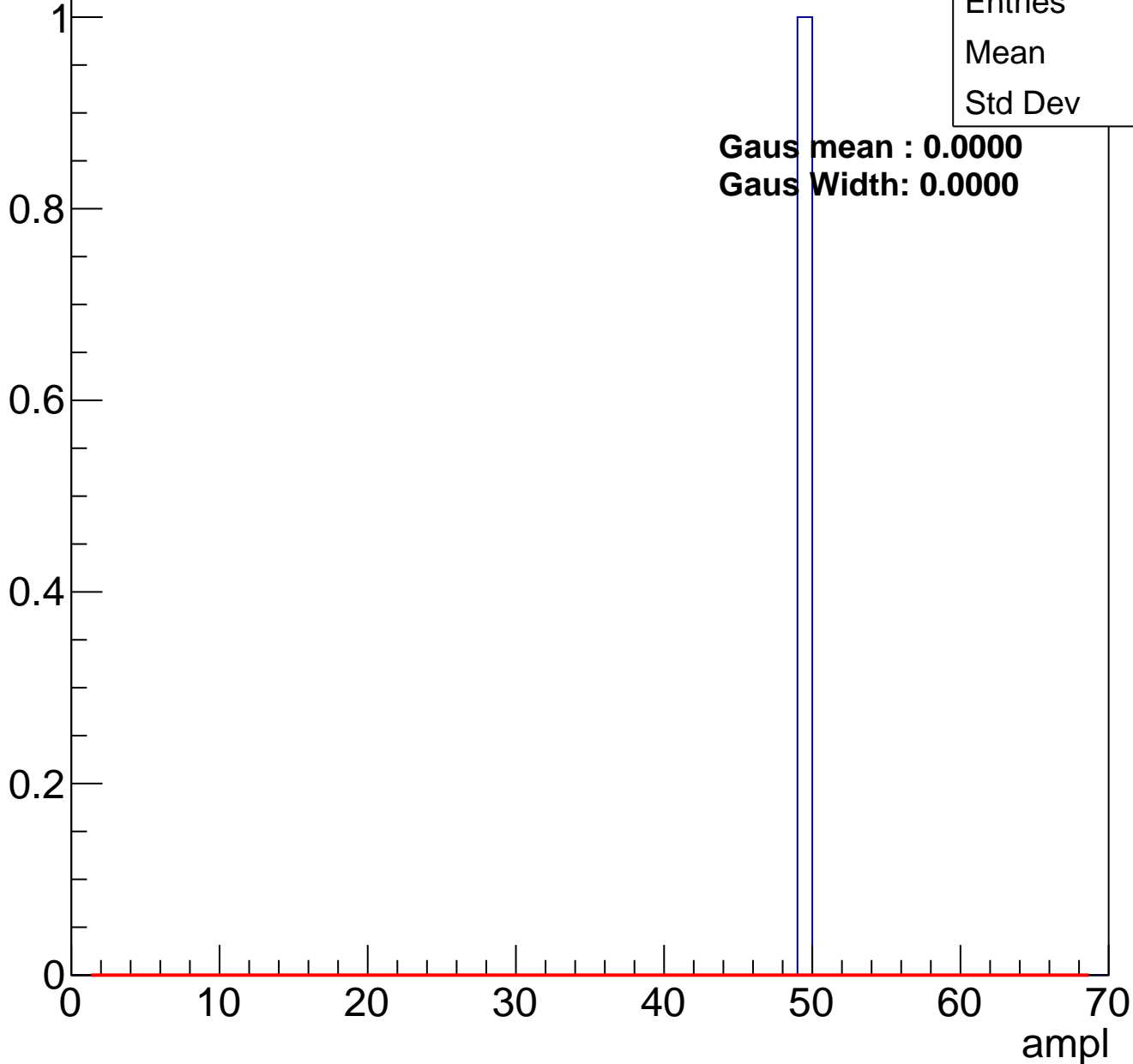
B0L100S, U13-ch11, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch11, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch11, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch11, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch11, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch12, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch12, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch12, adc2

calib_packv5_042523_0143.root, FC#6, port A1
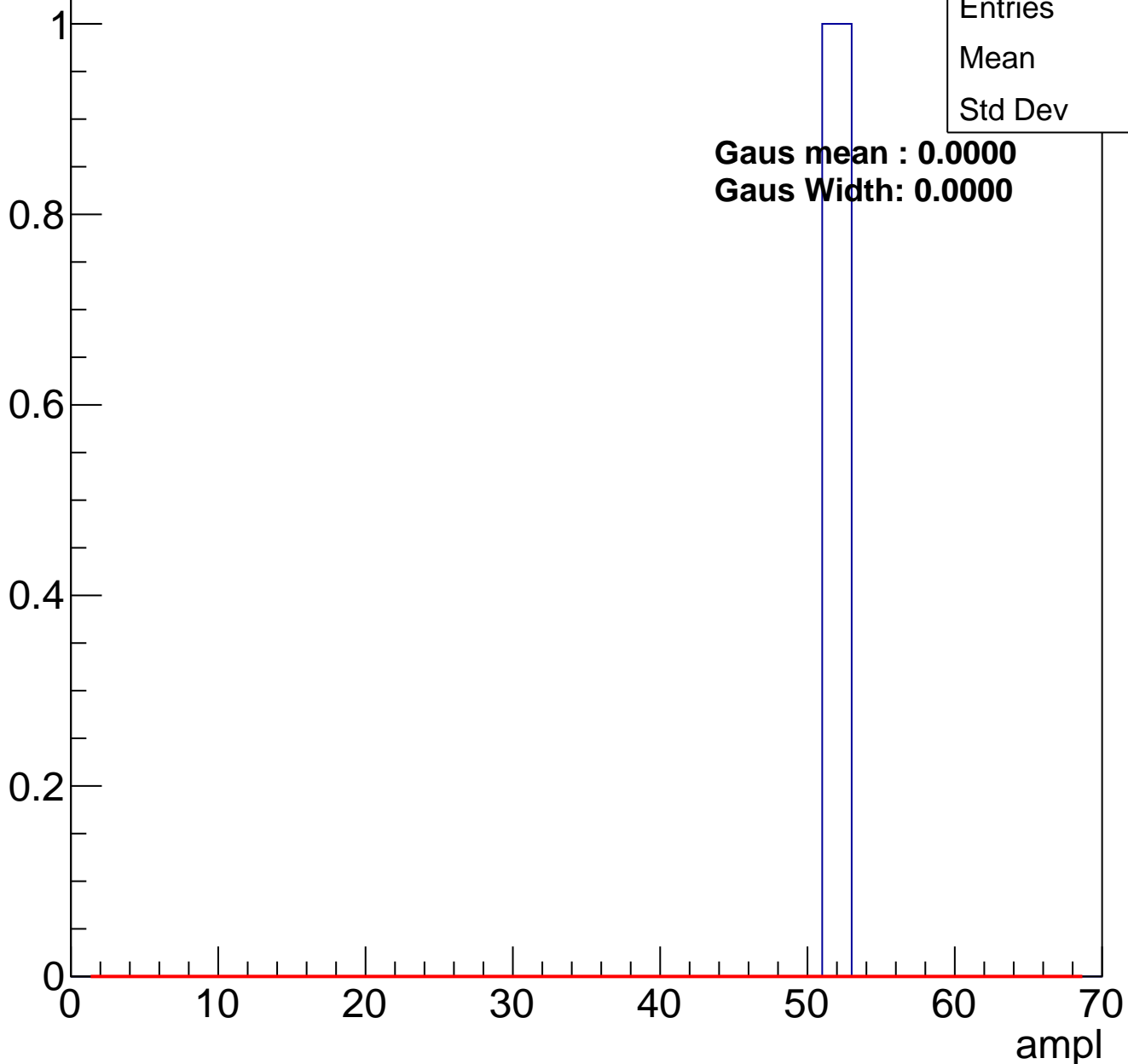
Entries	2
Mean	51.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch12, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	52
Std Dev	0.5774

Entry

ampl
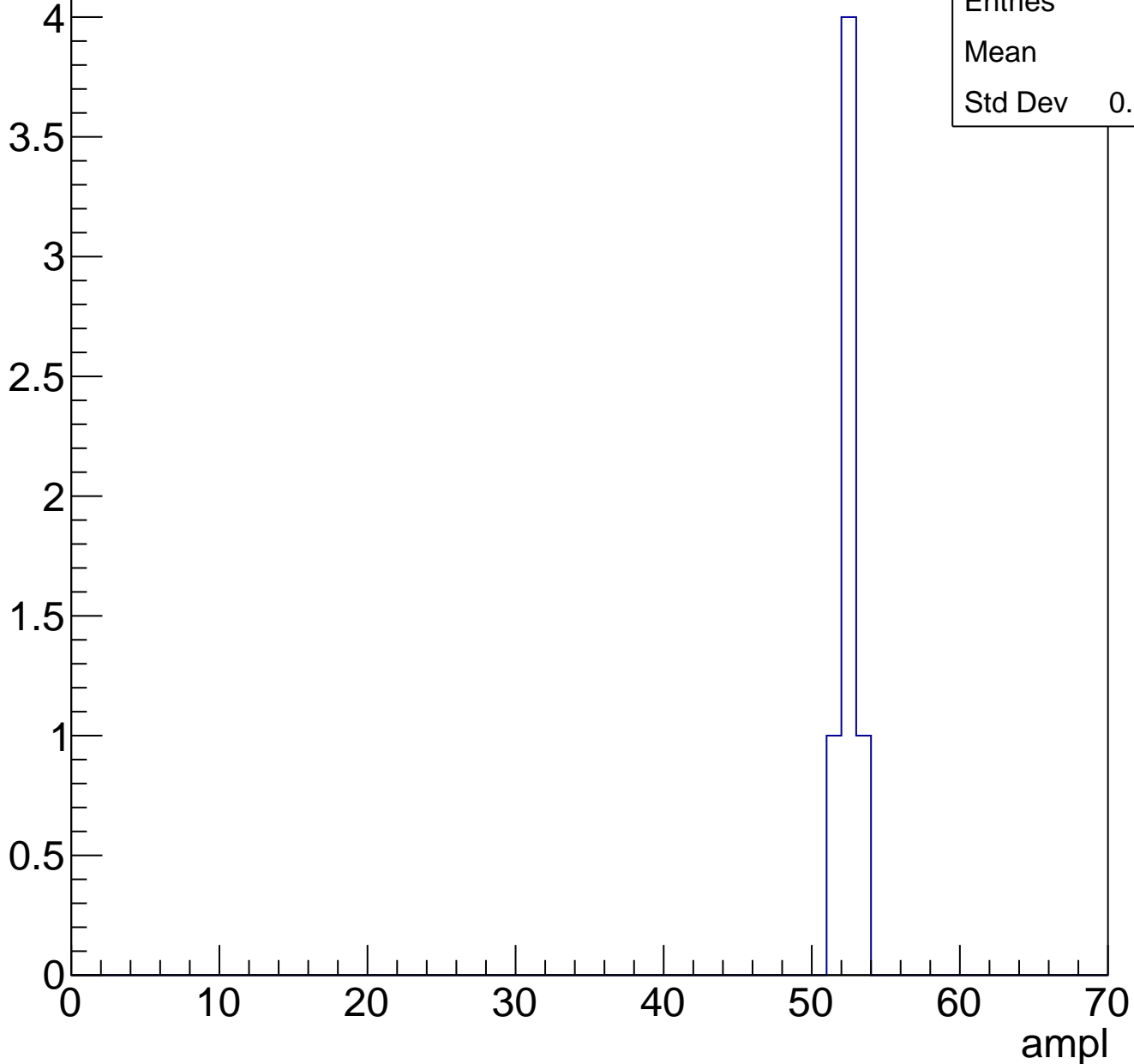
B0L100S, U13-ch12, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch12, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch12, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch12, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch13, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch13, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch13, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch13, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch13, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch13, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch13, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch13, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch14, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch14, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch14, adc2

calib_packv5_042523_0143.root, FC#6, port A1
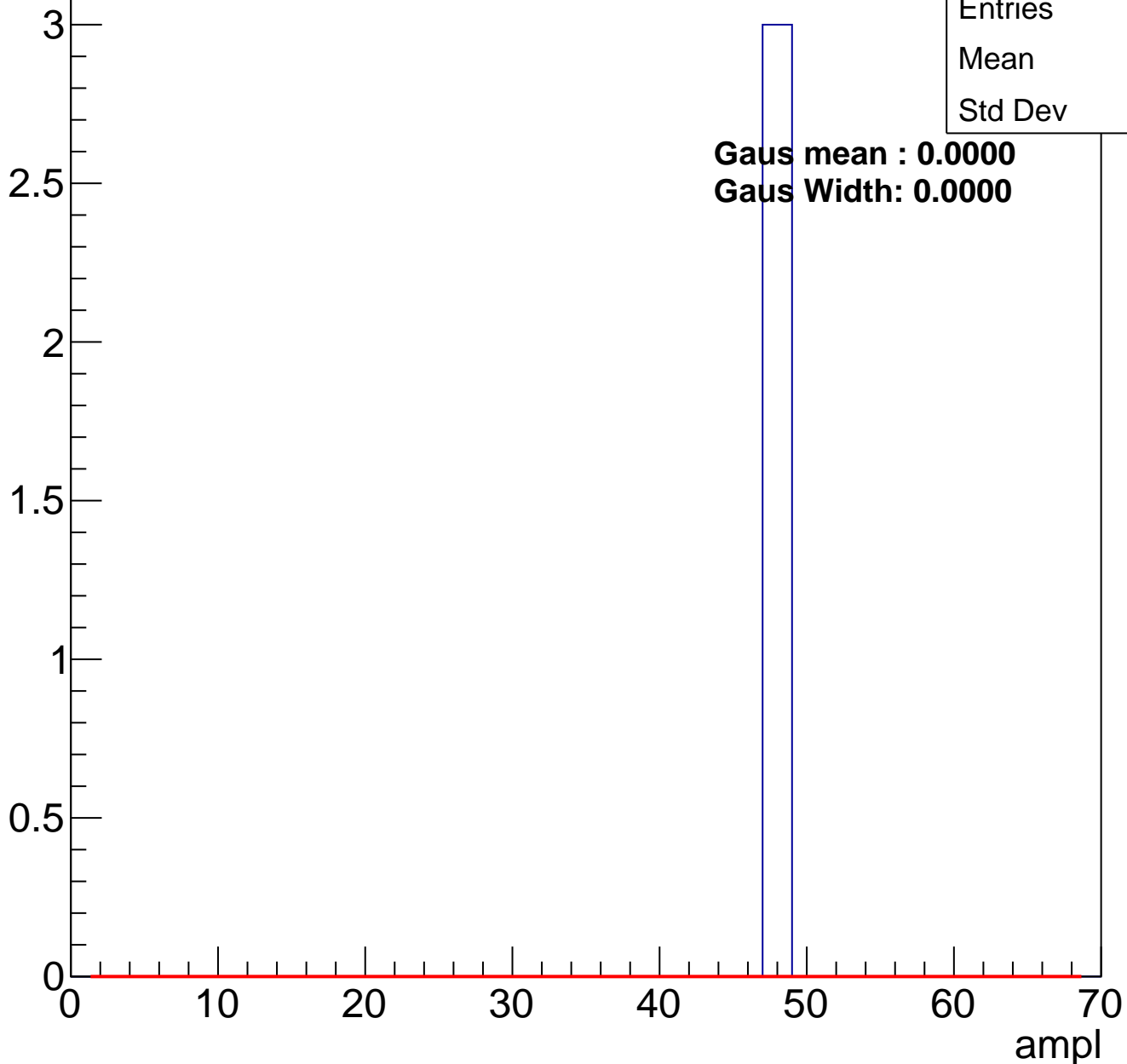
Entry

Entries	6
Mean	47.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch14, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	47
Std Dev	0

Entry

ampl

B0L100S, U13-ch14, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	56
Std Dev	0

Entry

ampl

B0L100S, U13-ch14, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch14, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch14, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch15, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	45
Std Dev	0

ampl

B0L100S, U13-ch15, adc1

calib_packv5_042523_0143.root, FC#6, port A1
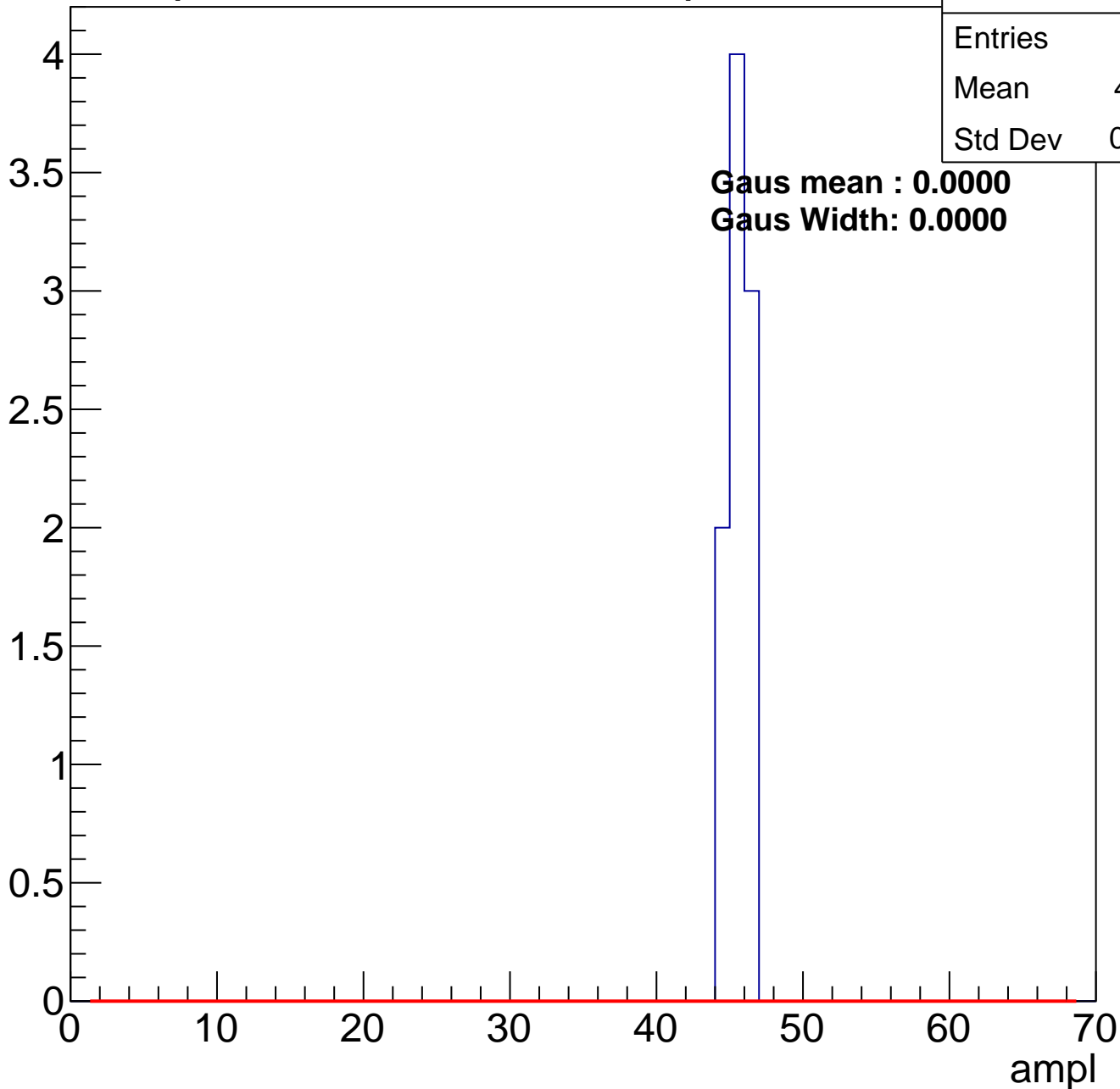
Entries	9
Mean	45.11
Std Dev	0.737

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch15, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	10
Mean	49.9
Std Dev	3.081
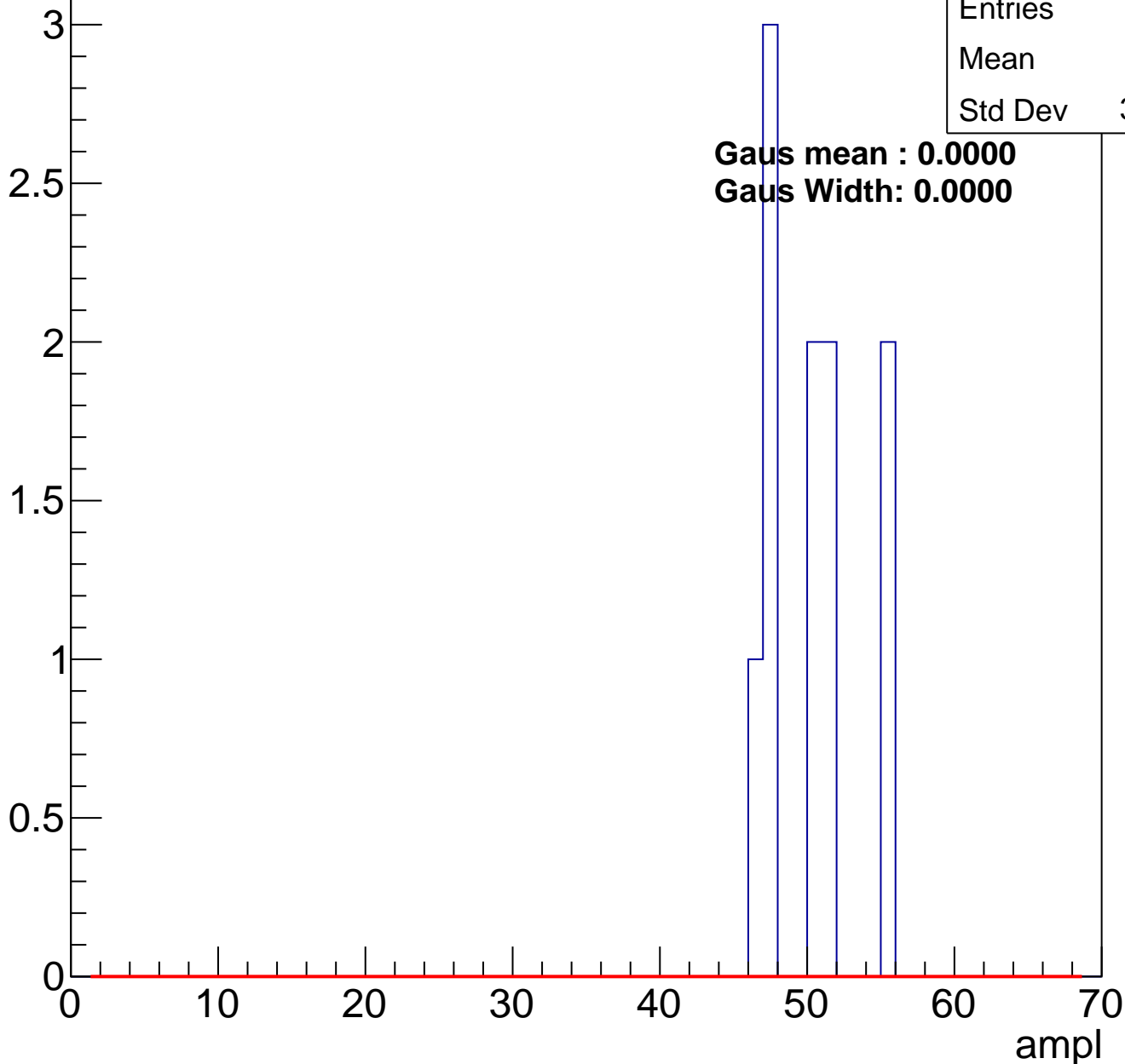
Entry

ampl

B0L100S, U13-ch15, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	55
Std Dev	0

ampl

B0L100S, U13-ch15, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch15, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch15, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch15, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch16, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch16, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch16, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch16, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch16, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch16, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch16, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch16, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch17, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
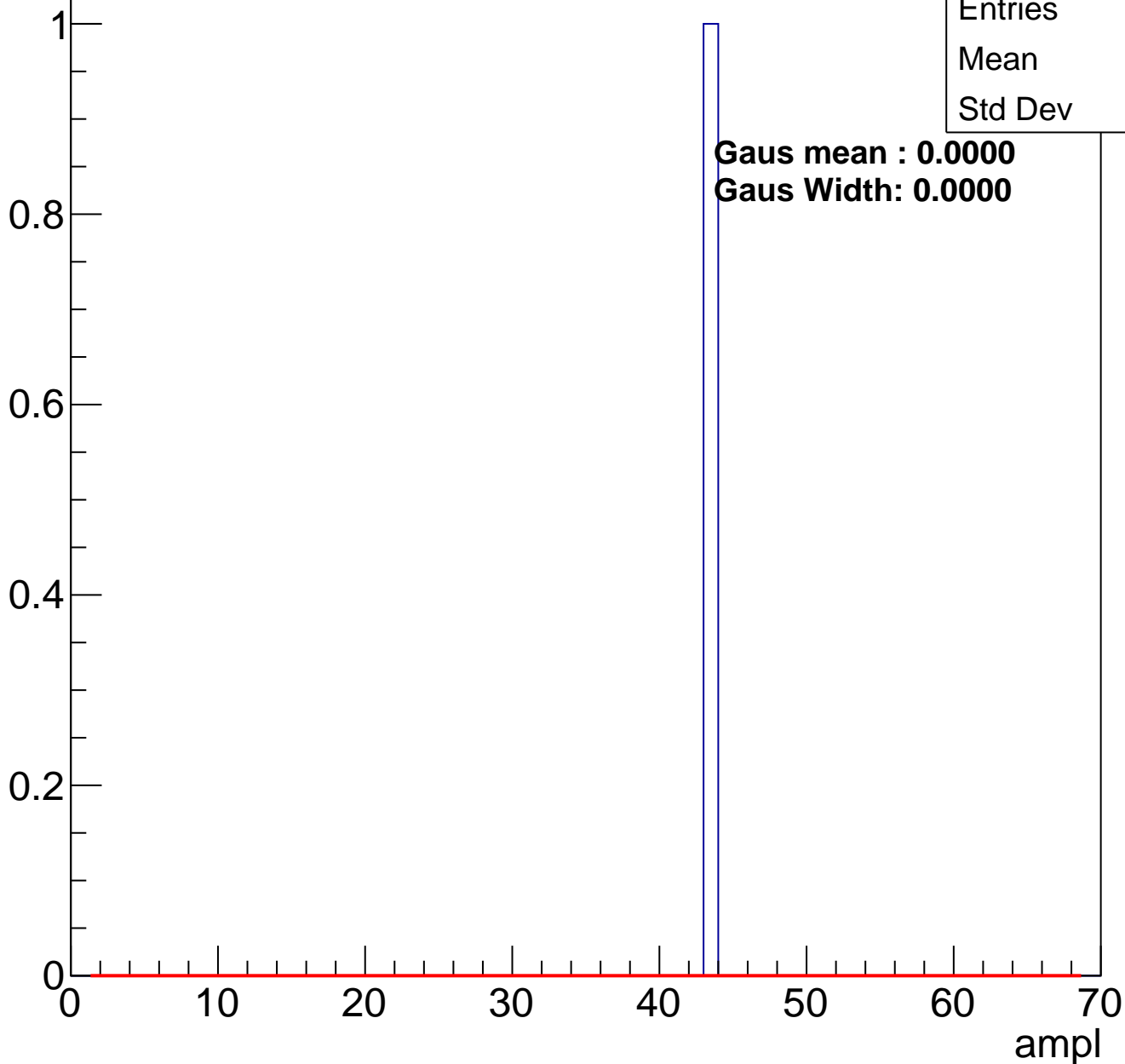
B0L100S, U13-ch17, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	13
Mean	45.77
Std Dev	2.224

Gaus mean : 45.0061
Gaus Width: 3.1293

Entry

ampl
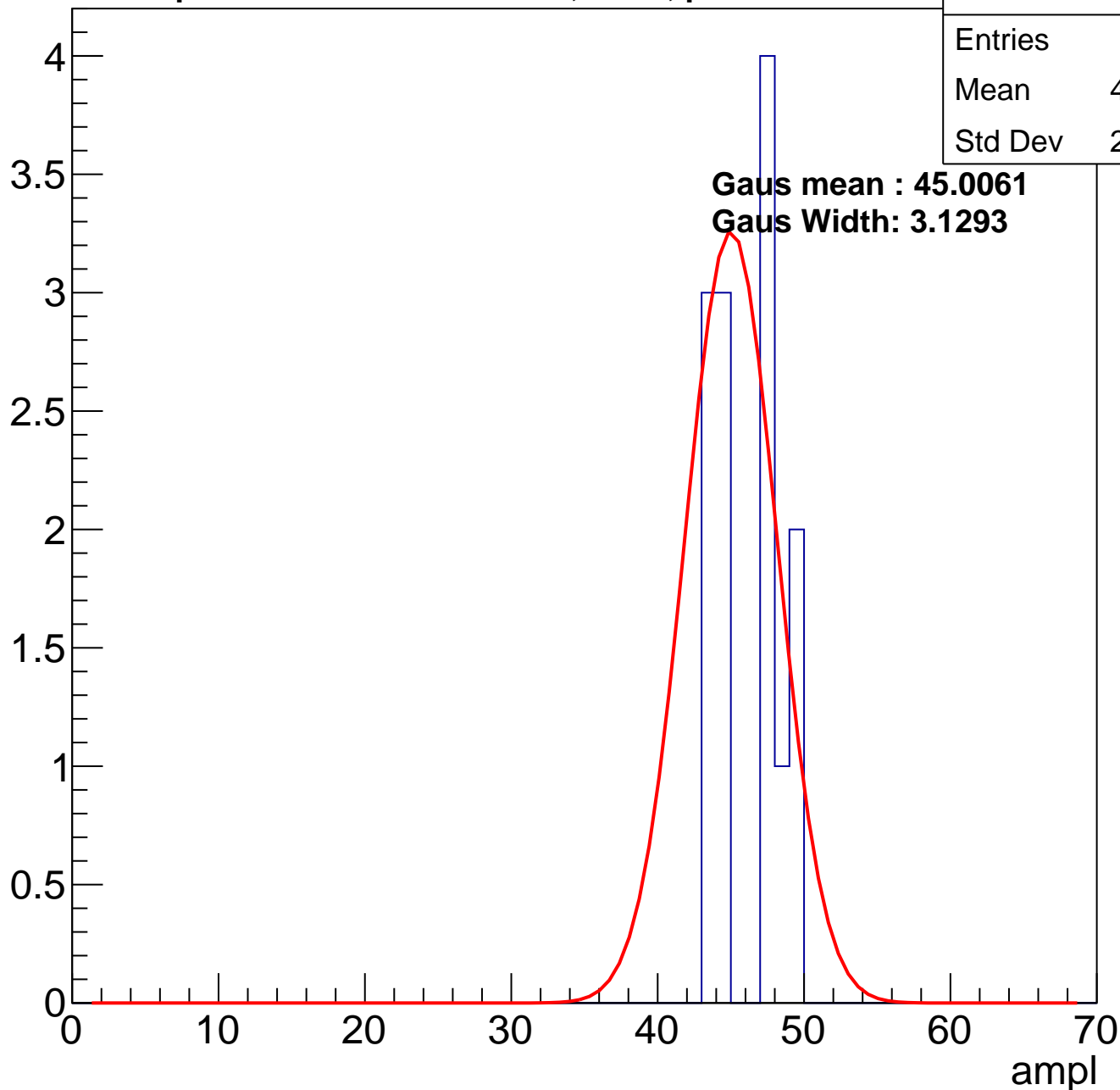
B0L100S, U13-ch17, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 88.7802
Gaus Width: 23.9739

Entries	20
Mean	47.55
Std Dev	2.397

Entry

ampl
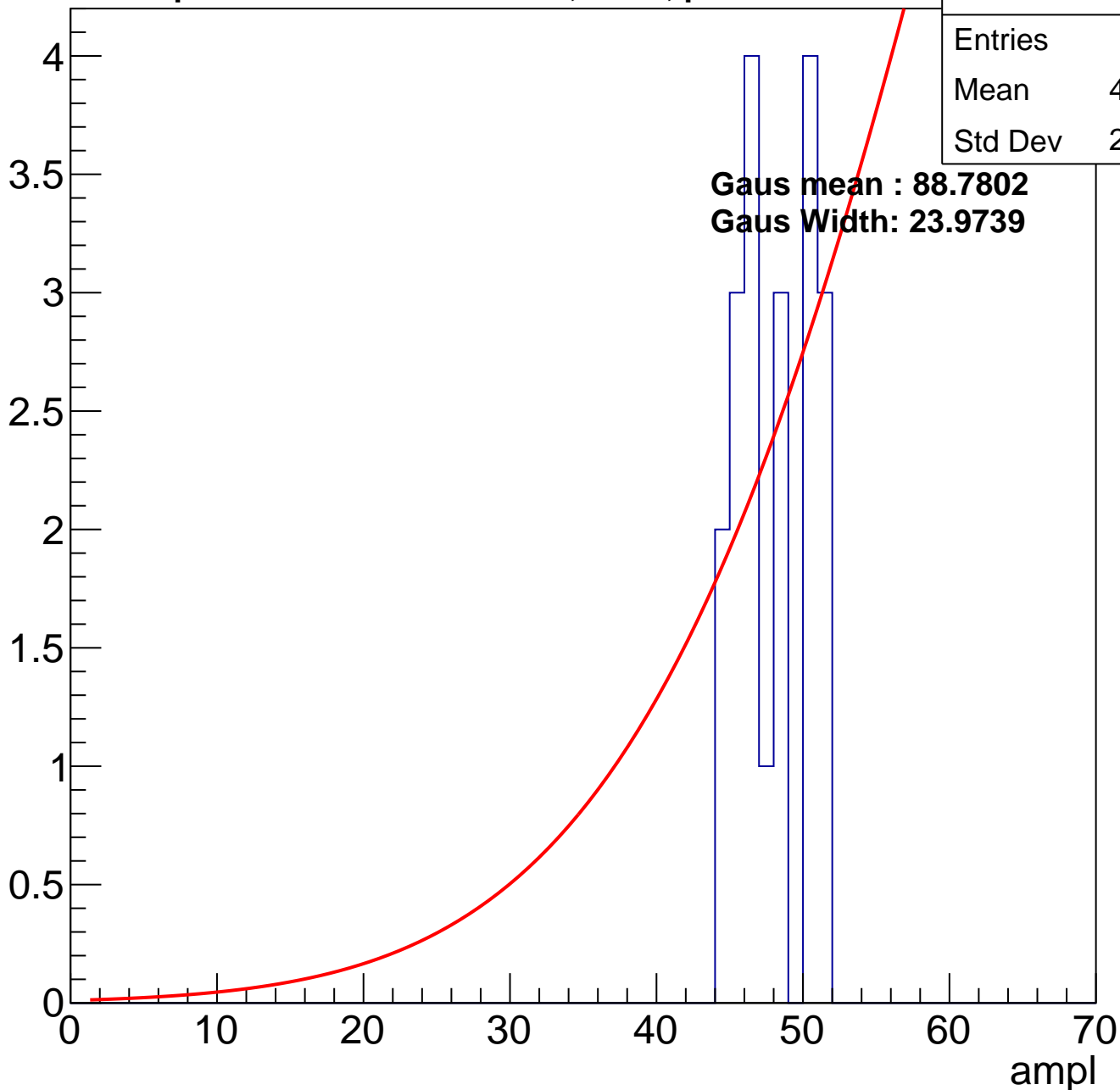
B0L100S, U13-ch17, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	50.67
Std Dev	1.374

ampl
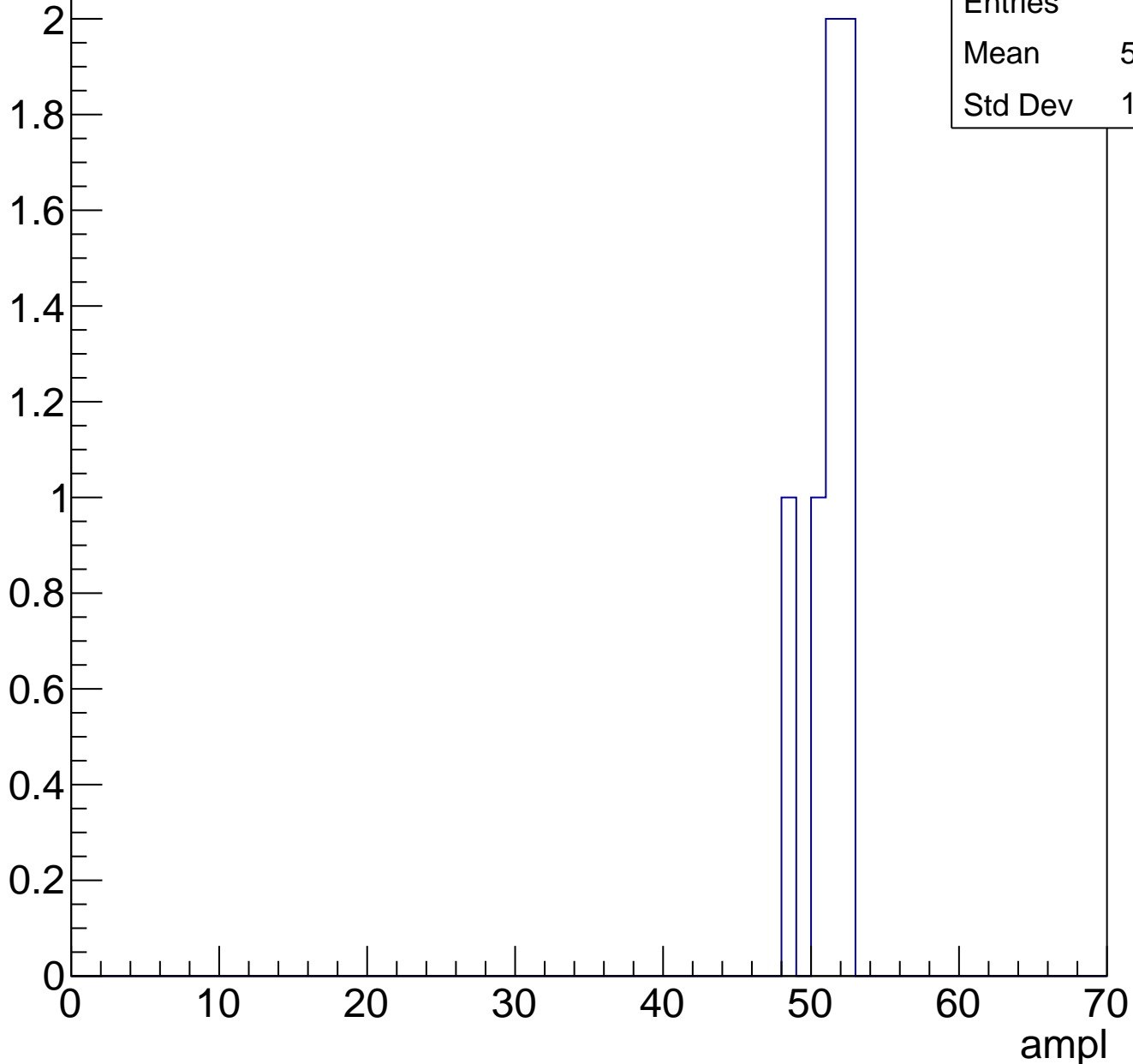
B0L100S, U13-ch17, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch17, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch17, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch17, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch18, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43.5
Std Dev	3.5

Gaus mean : 0.0000
Gaus Width: 0.0000
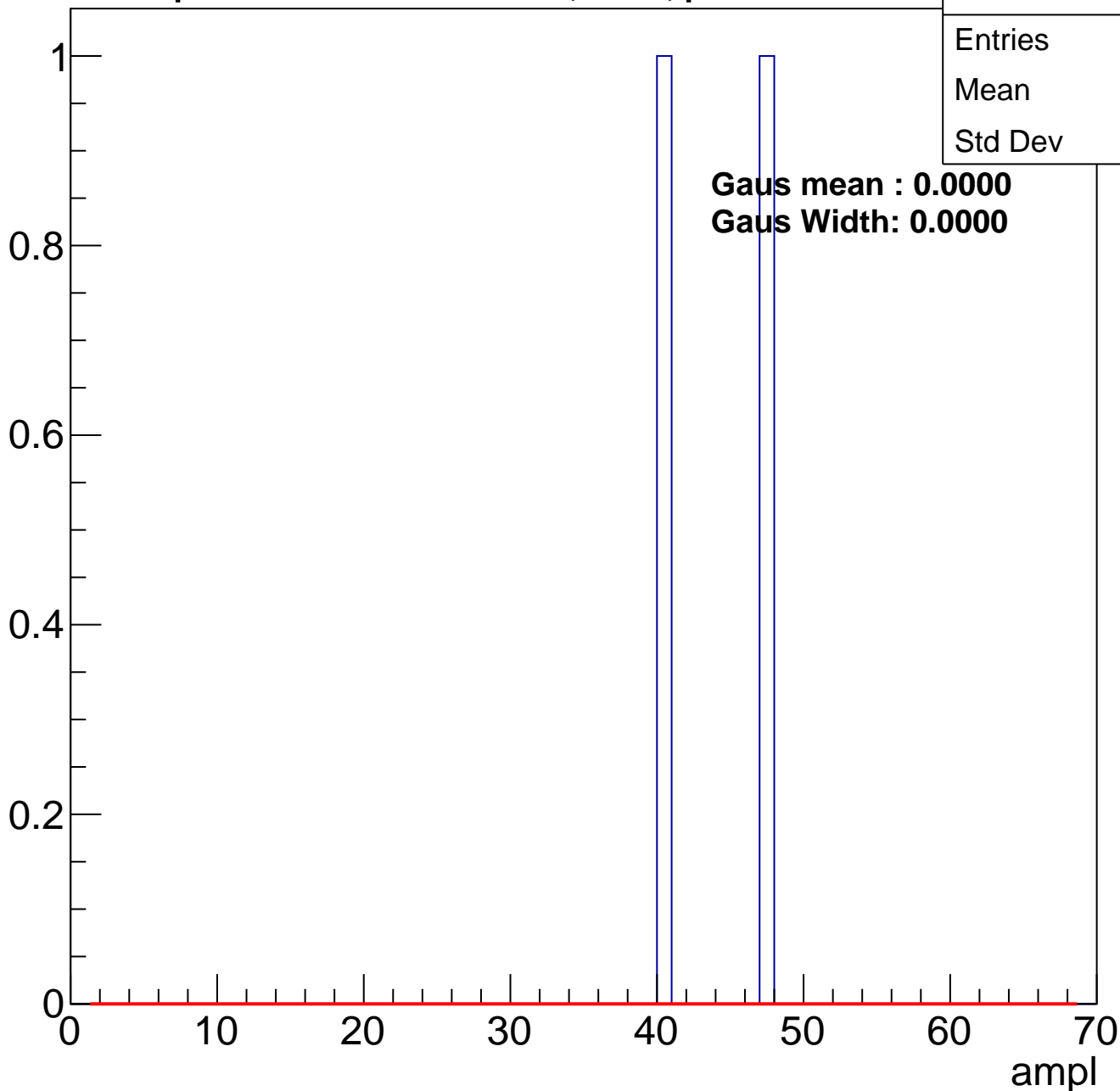
B0L100S, U13-ch18, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	43.07
Std Dev	2.186

Gaus mean : -30.6457
Gaus Width: 17.8130

Entry

ampl
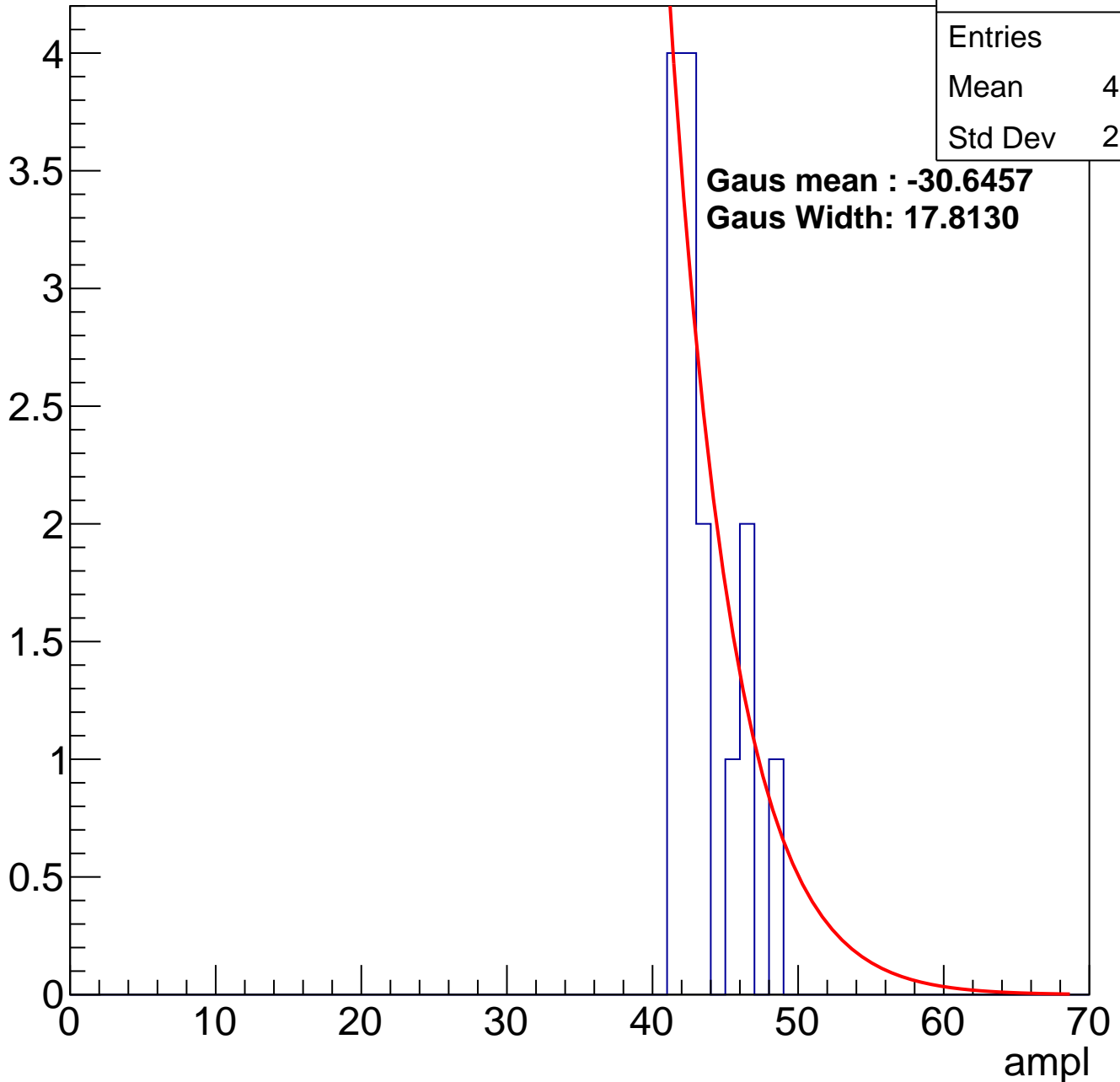
B0L100S, U13-ch18, adc2

calib_packv5_042523_0143.root, FC#6, port A1
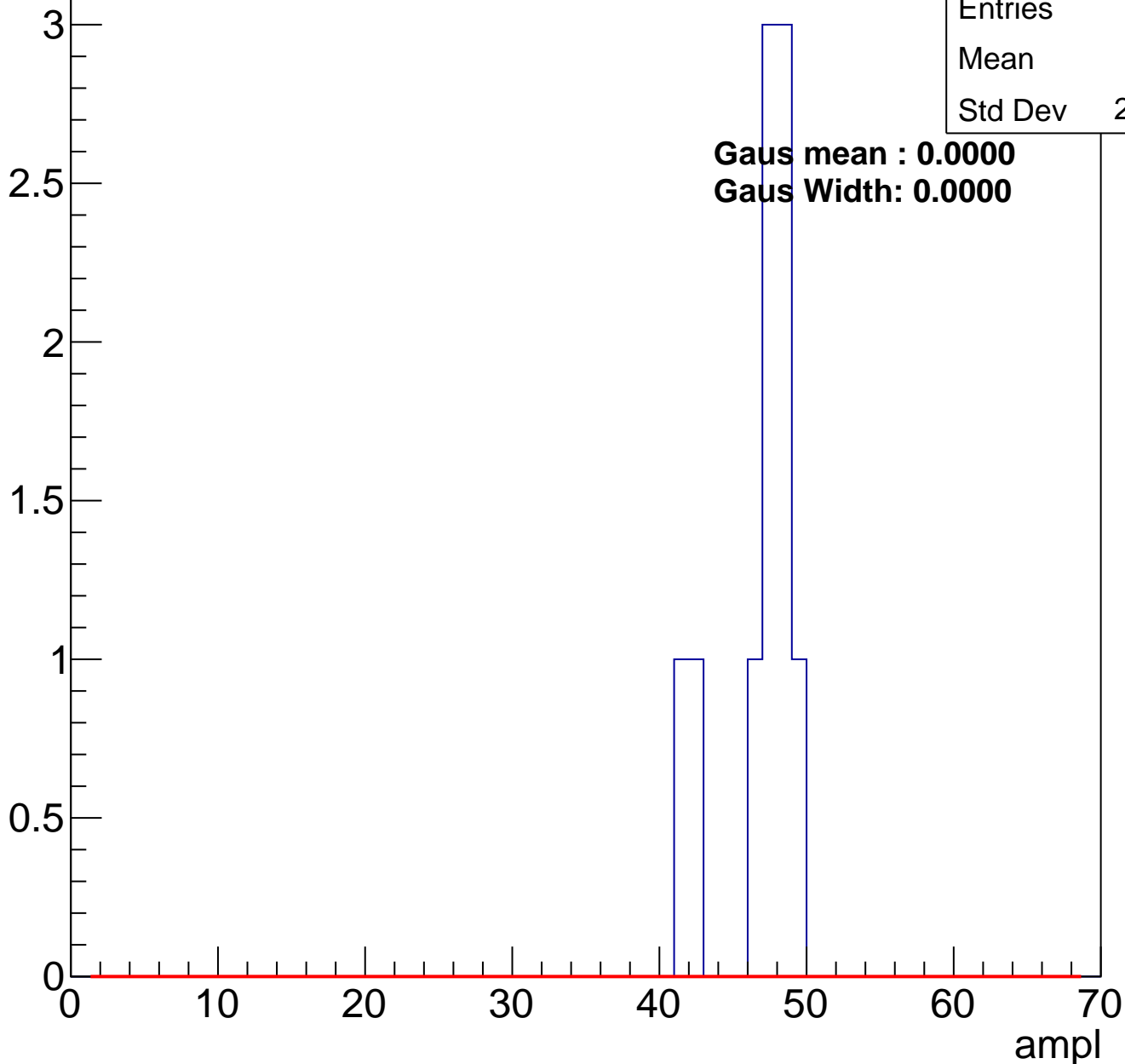
Entries	10
Mean	46.3
Std Dev	2.532

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch18, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch18, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch18, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch18, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch18, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch19, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch19, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch19, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	49.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000
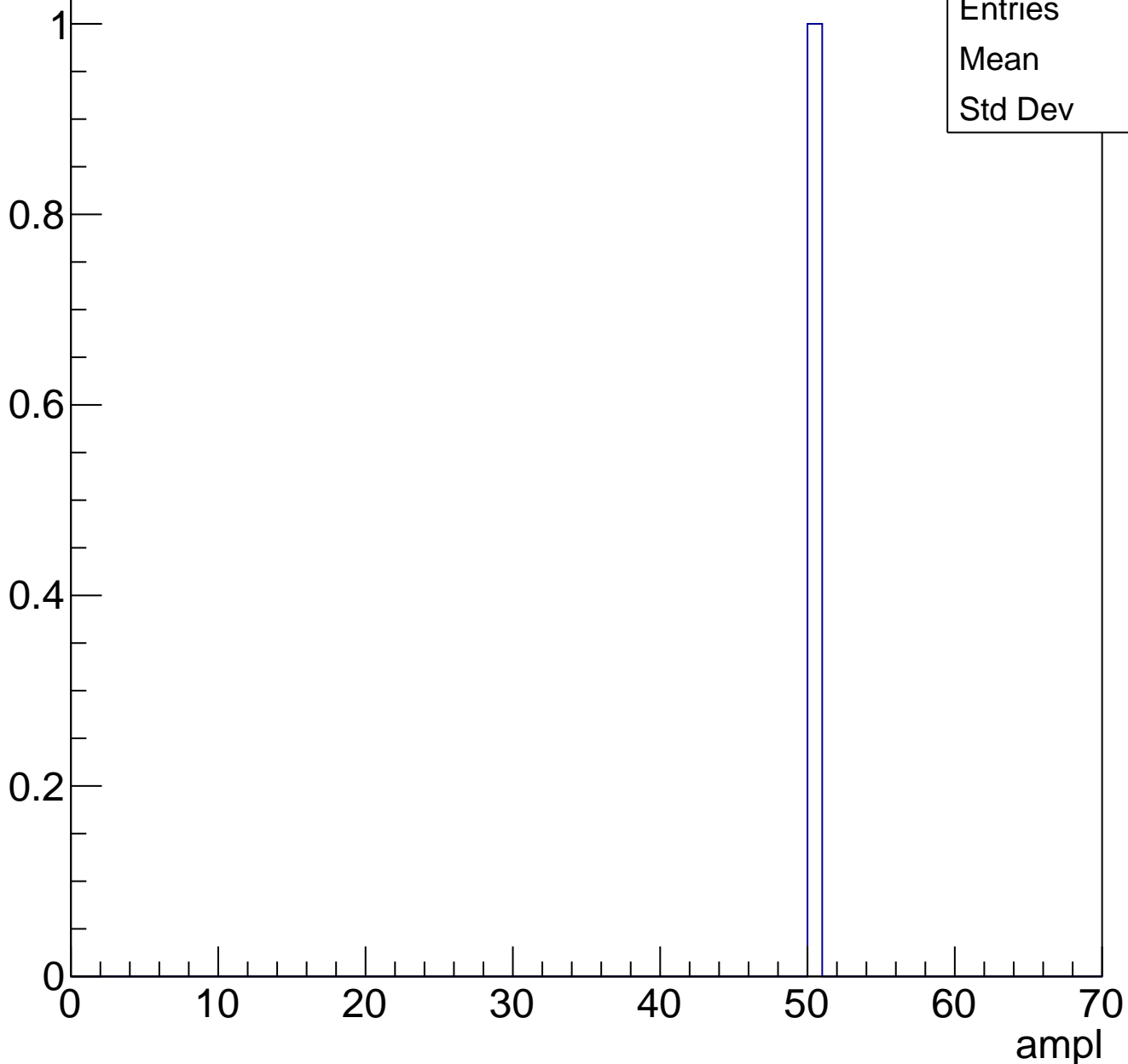
Entry

ampl

B0L100S, U13-ch19, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Entry

ampl

B0L100S, U13-ch19, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch19, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch19, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch19, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch20, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch20, adc1

calib_packv5_042523_0143.root, FC#6, port A1
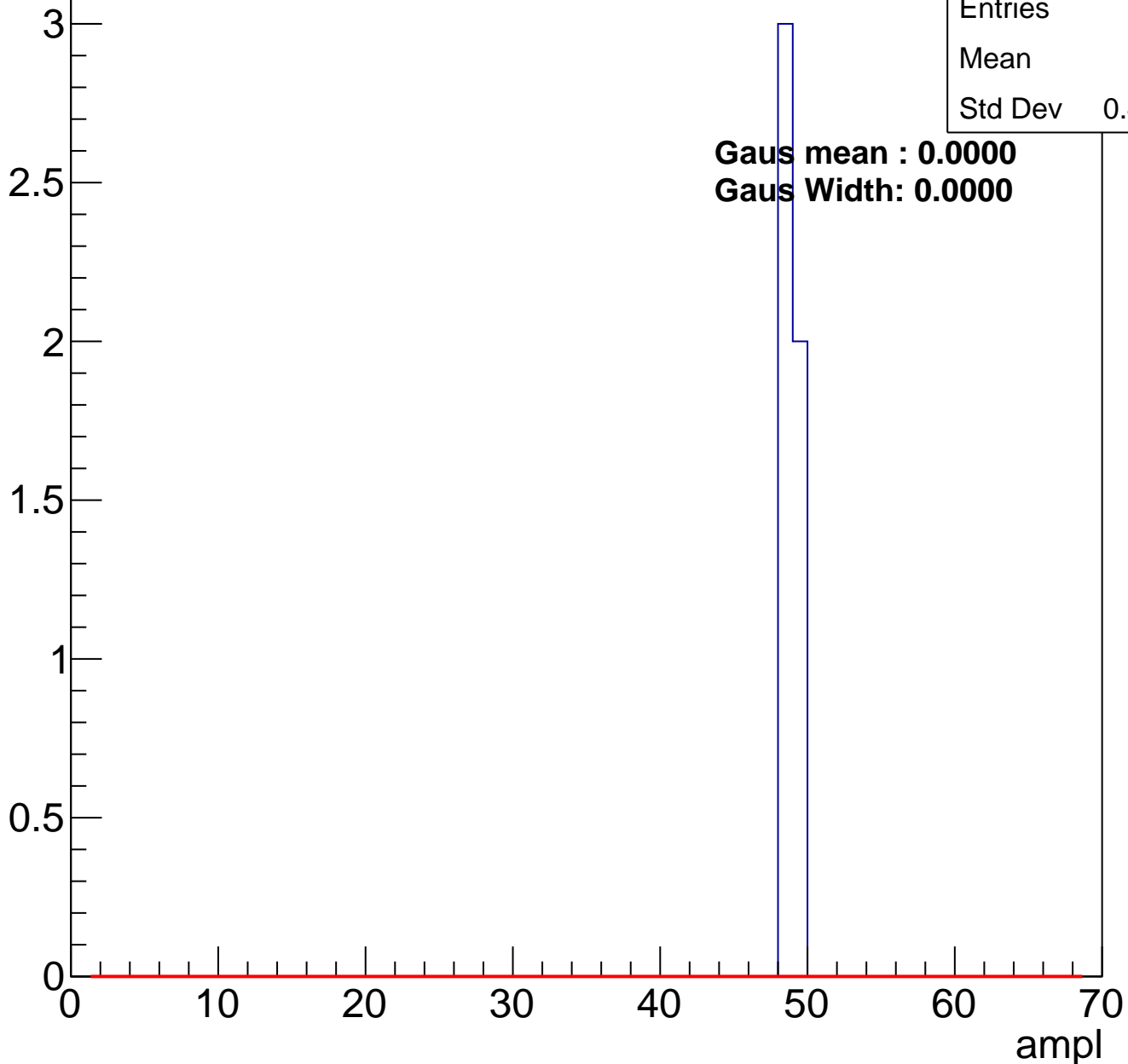
Entries		5
Mean		48.4
Std Dev		0.4899

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch20, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	22
Mean	49.77
Std Dev	2.173

Gaus mean : 51.4943
Gaus Width: 3.8600

Entry

ampl
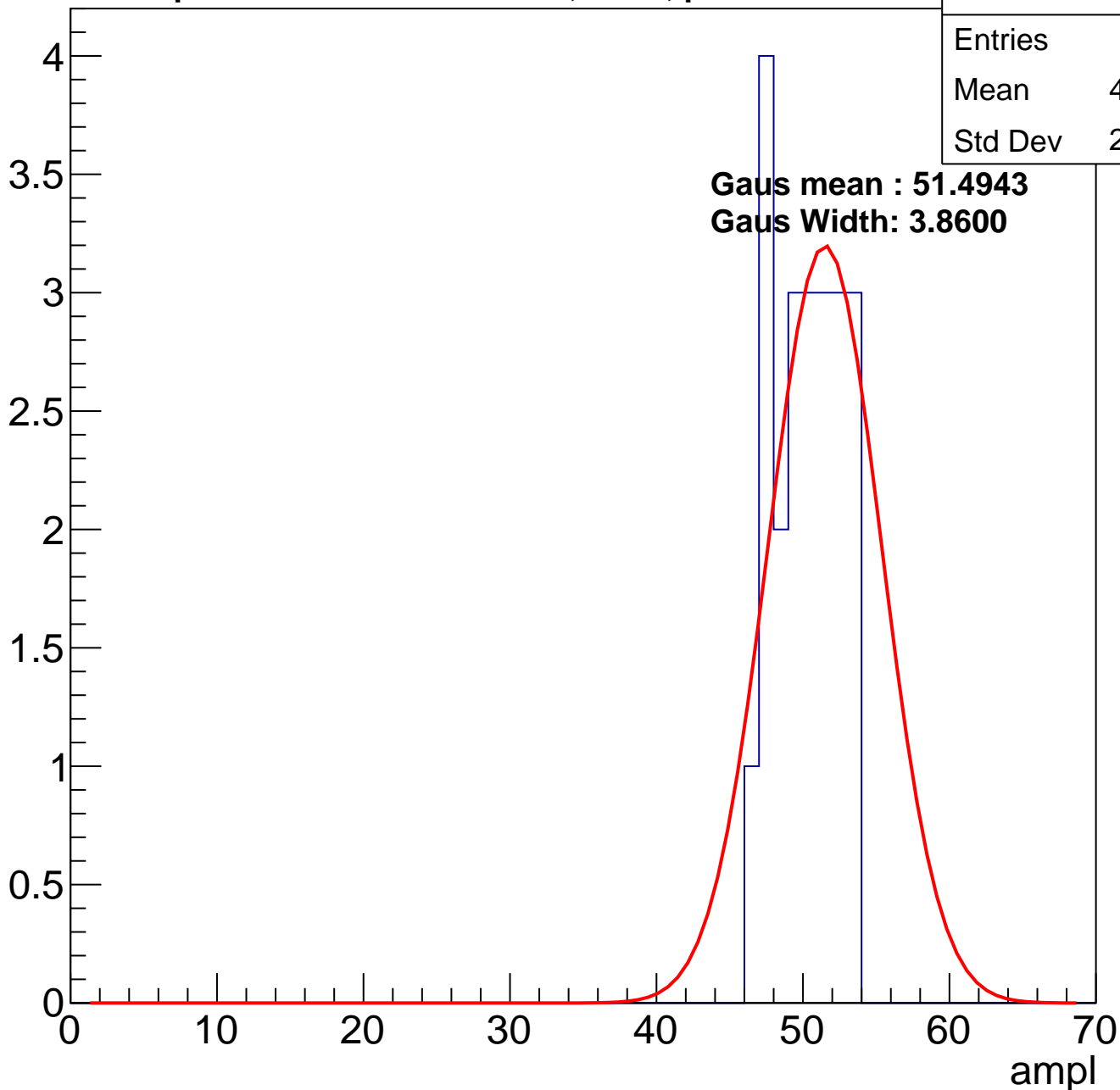
B0L100S, U13-ch20, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	49.5
Std Dev	2.291

Entry

ampl
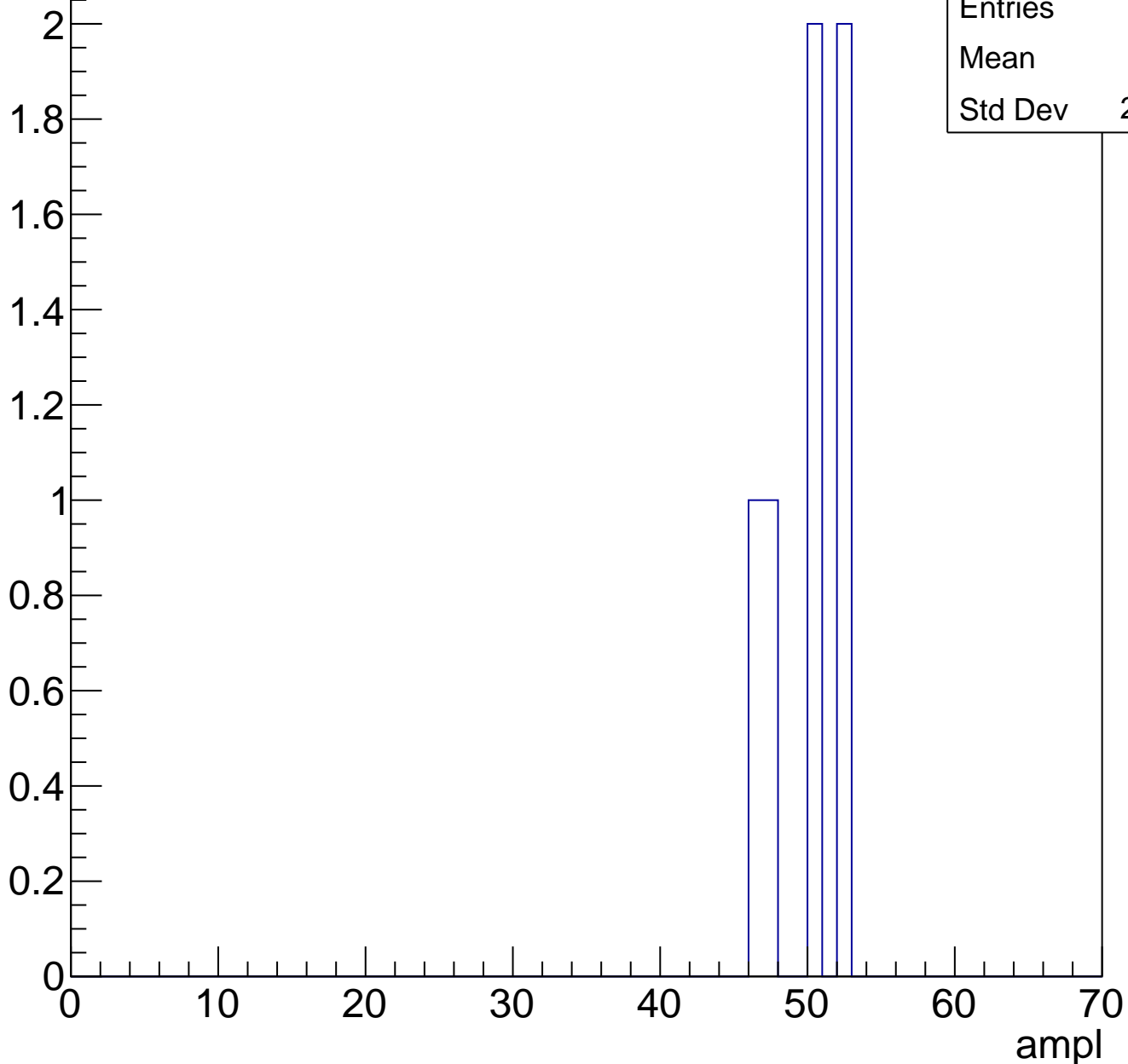
B0L100S, U13-ch20, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch20, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch20, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch20, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch21, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
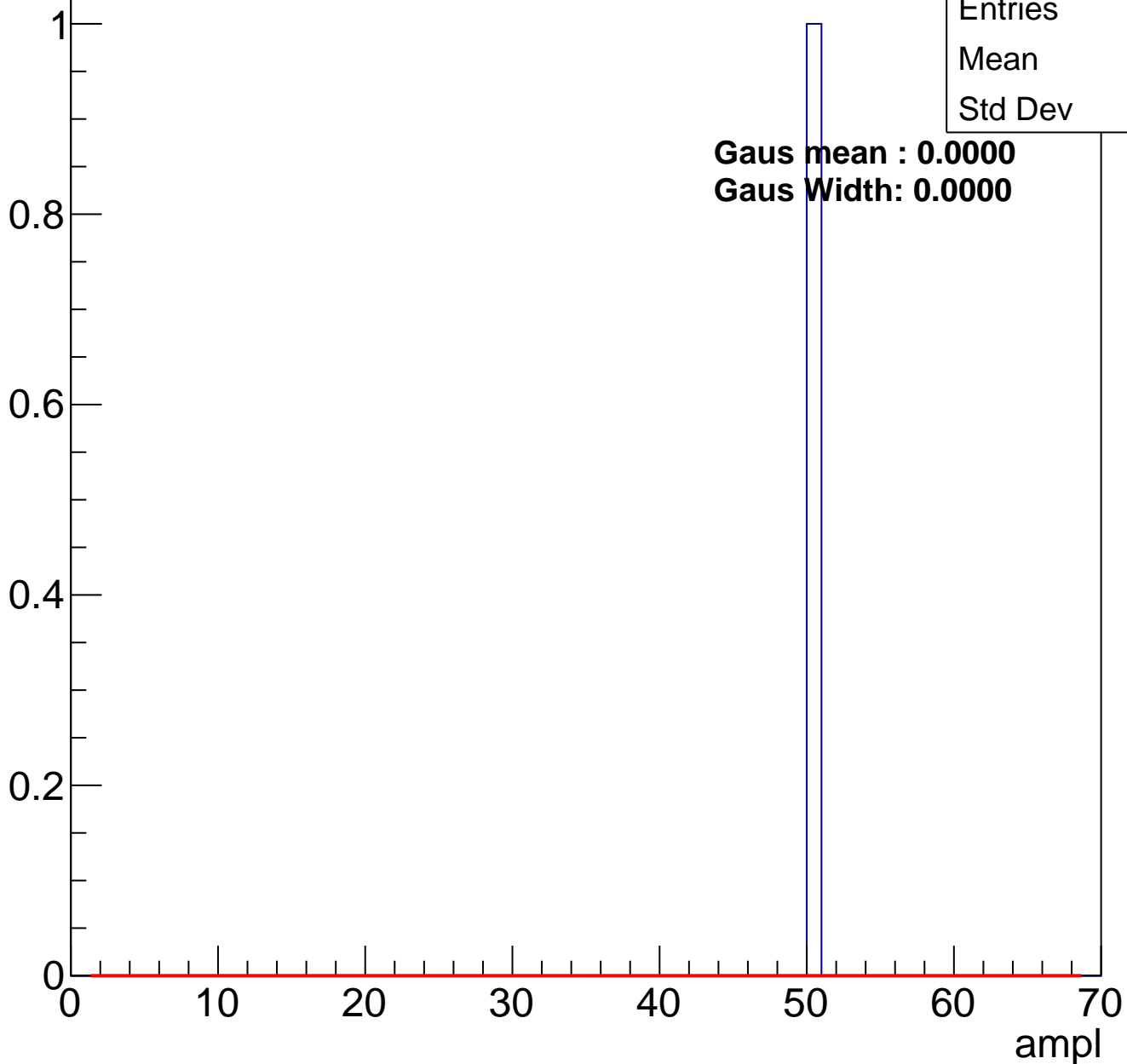
B0L100S, U13-ch21, adc2

calib_packv5_042523_0143.root, FC#6, port A1
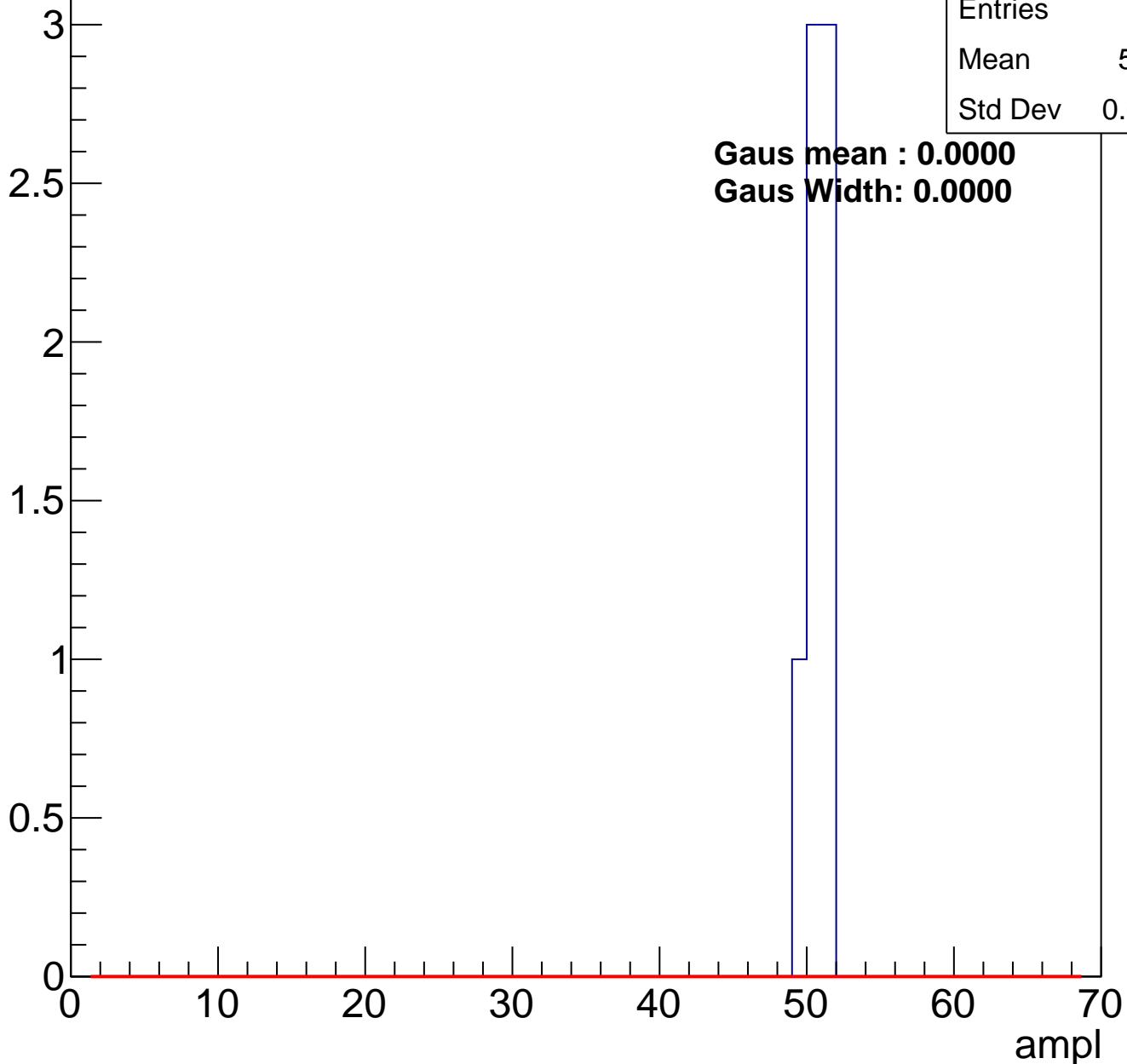
Entries	7
Mean	50.29
Std Dev	0.6999

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch21, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	1
Mean	50
Std Dev	0
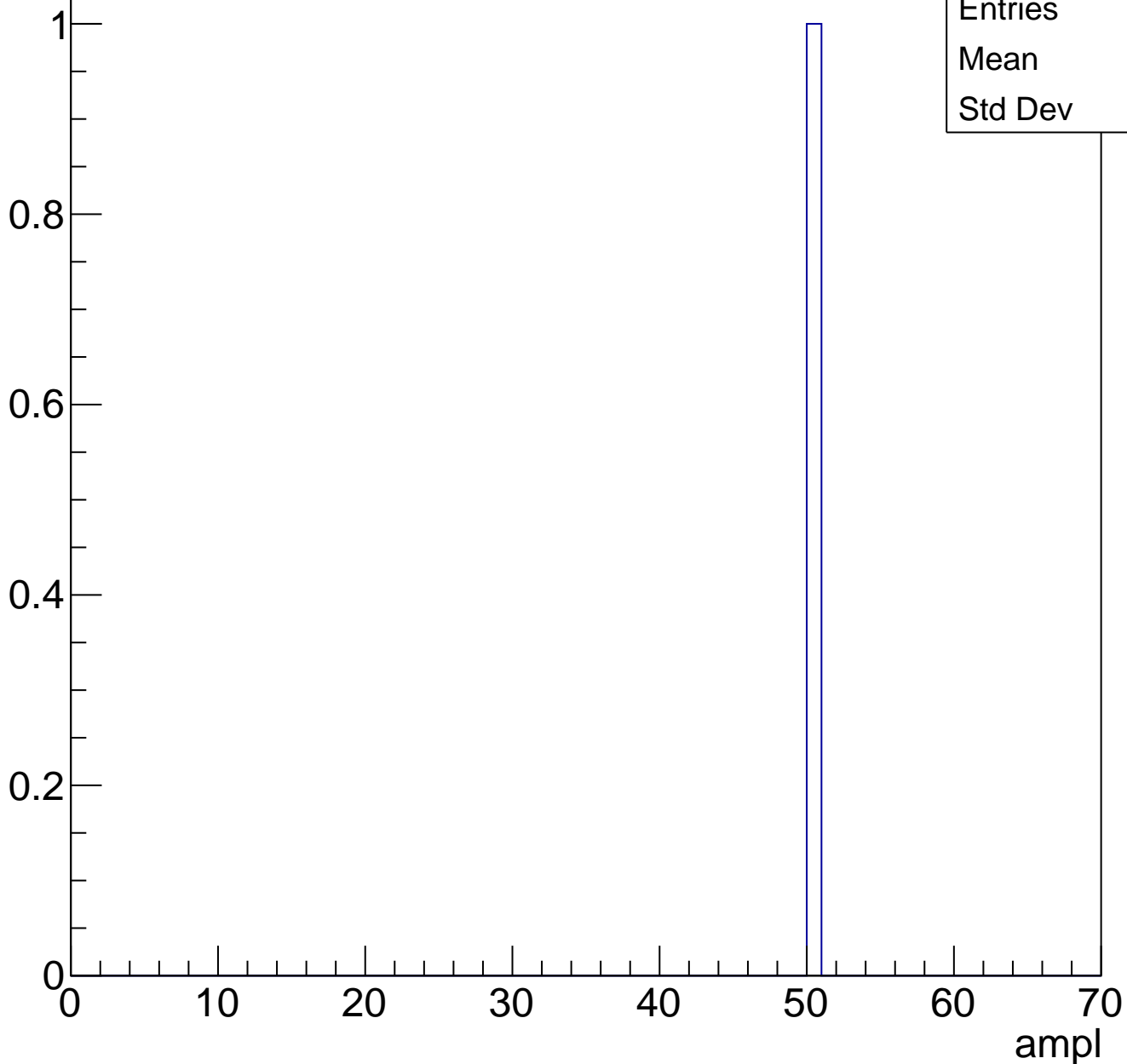
B0L100S, U13-ch21, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch21, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch21, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch21, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch22, adc0

calib_packv5_042523_0143.root, FC#6, port A1
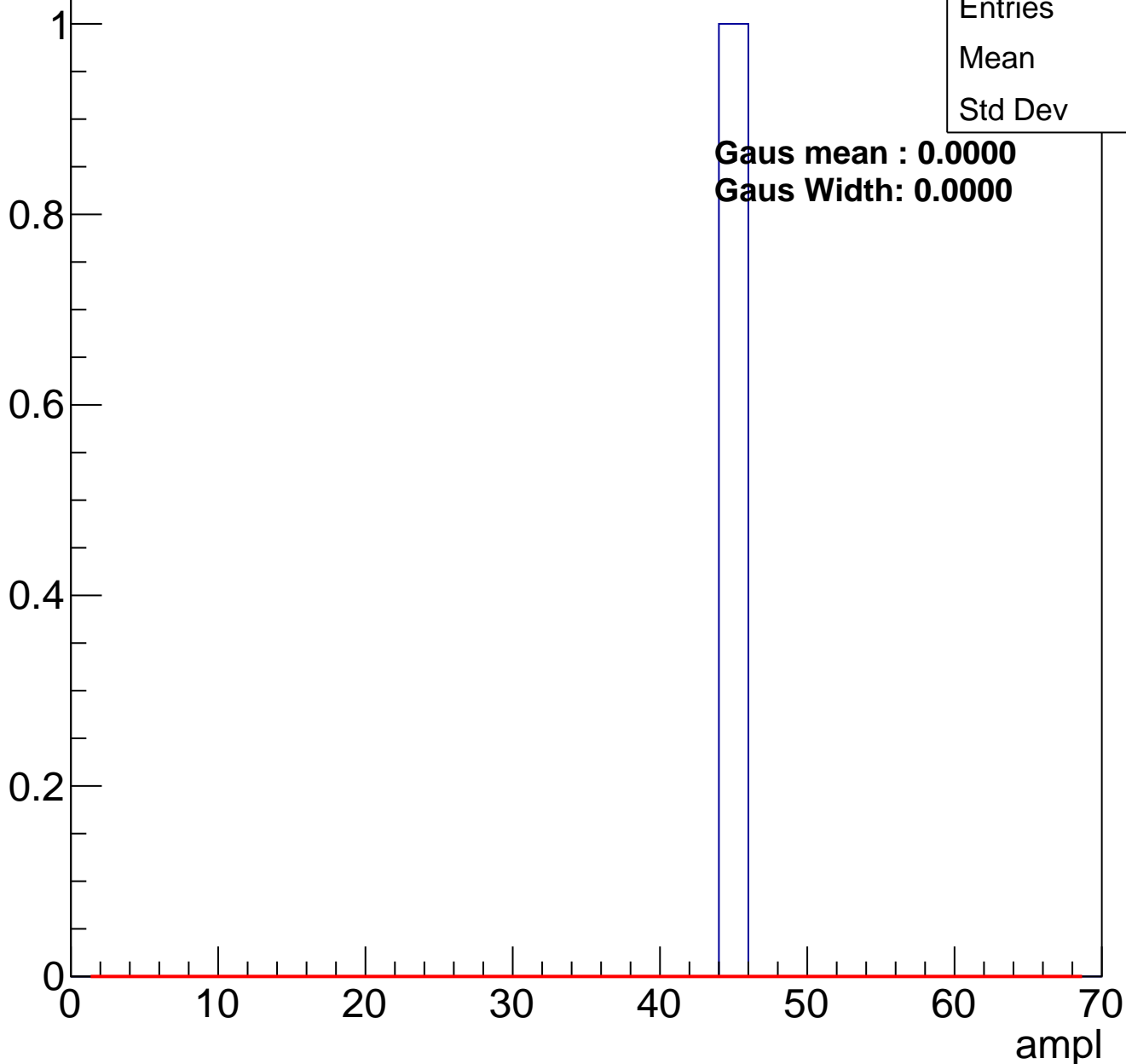
Entries	2
Mean	44.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
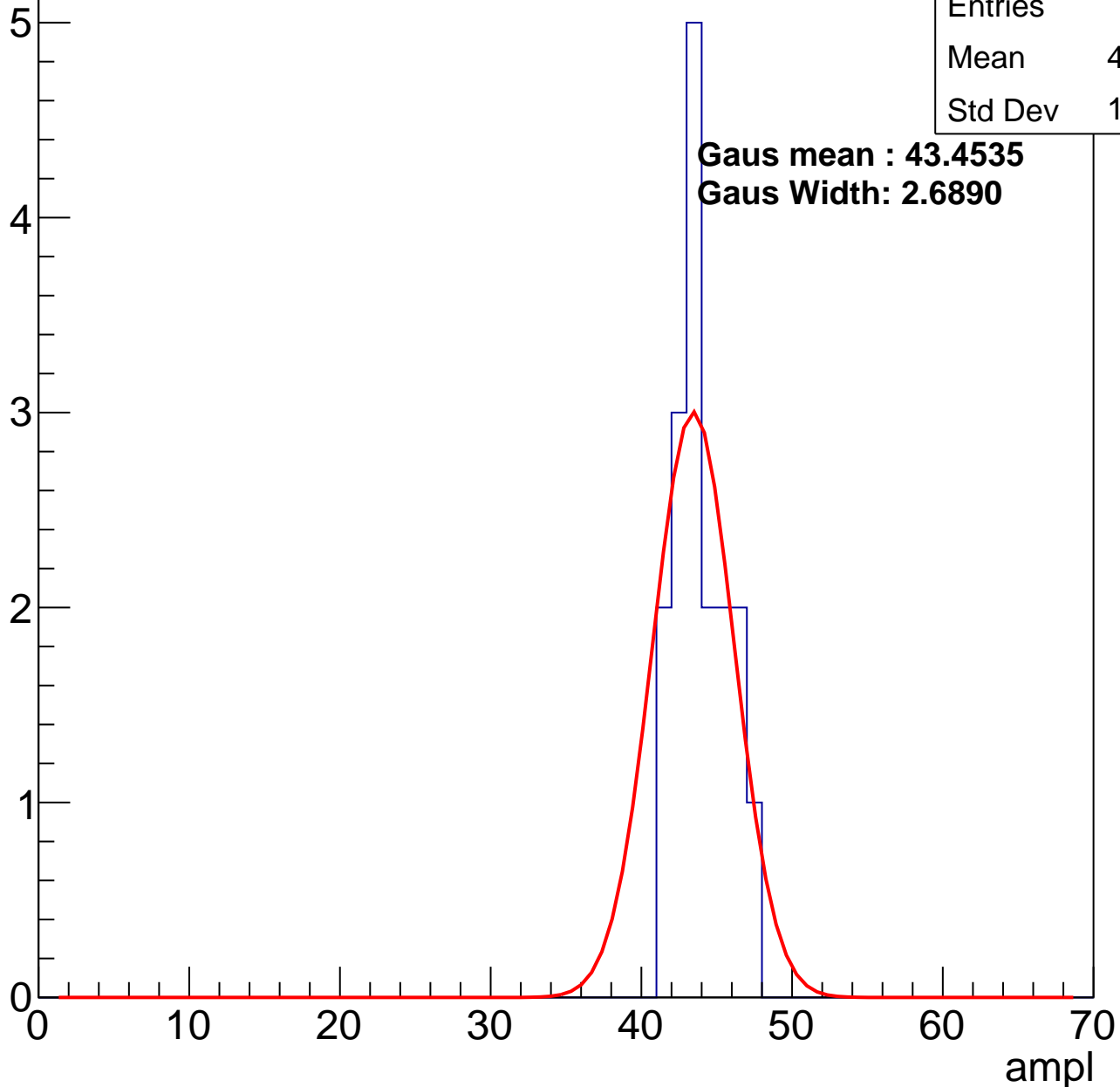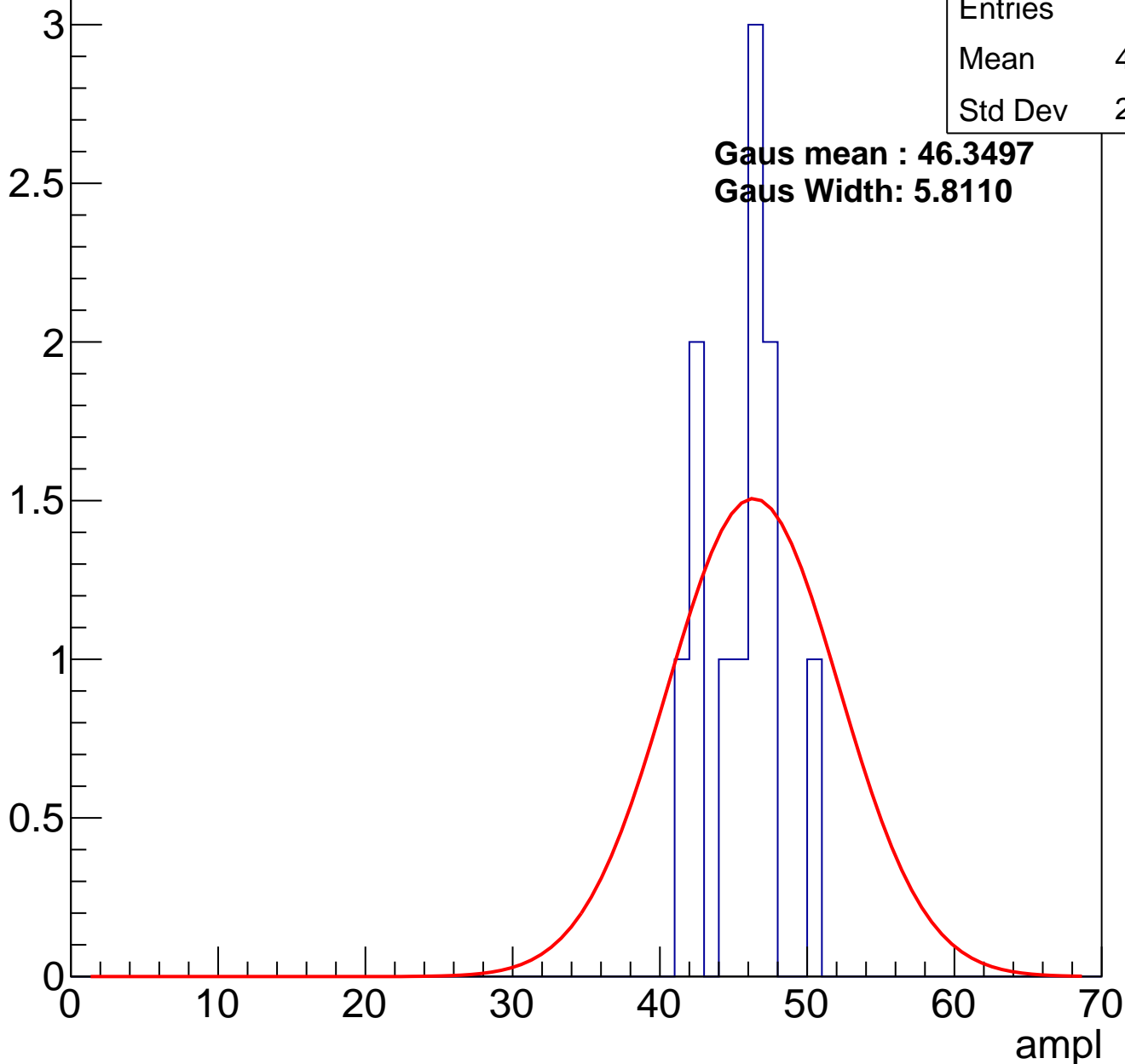
B0L100S, U13-ch22, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch22, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch22, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch22, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch22, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch23, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch23, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	45.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch23, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	46
Std Dev	0.7071

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
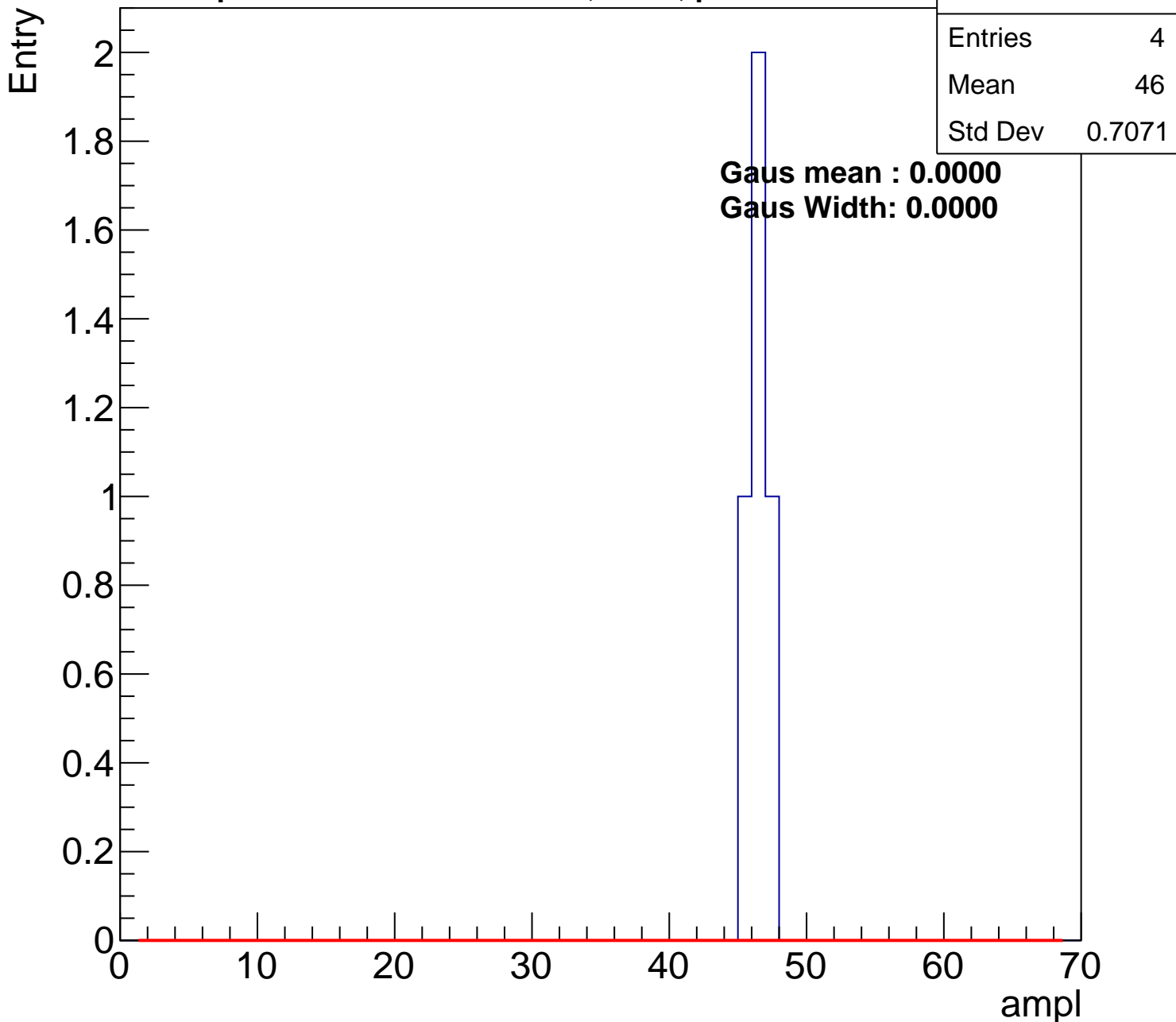
B0L100S, U13-ch23, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	46
Std Dev	0

Entry

ampl

B0L100S, U13-ch23, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch23, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch23, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch23, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch24, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	30
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
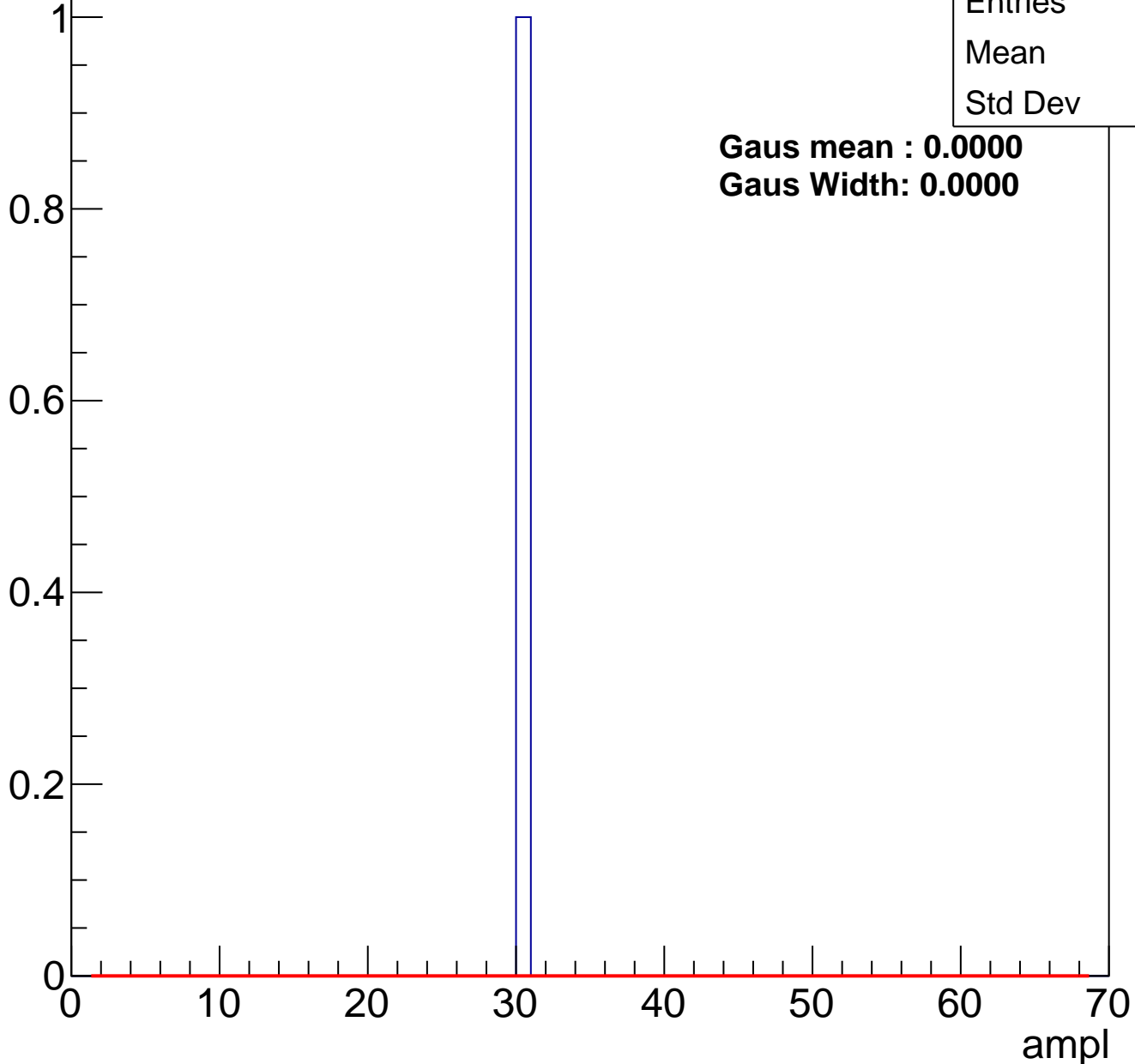
B0L100S, U13-ch24, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	12
Mean	44.83
Std Dev	2.64

Gaus mean : 12.2236
Gaus Width: 15.5782
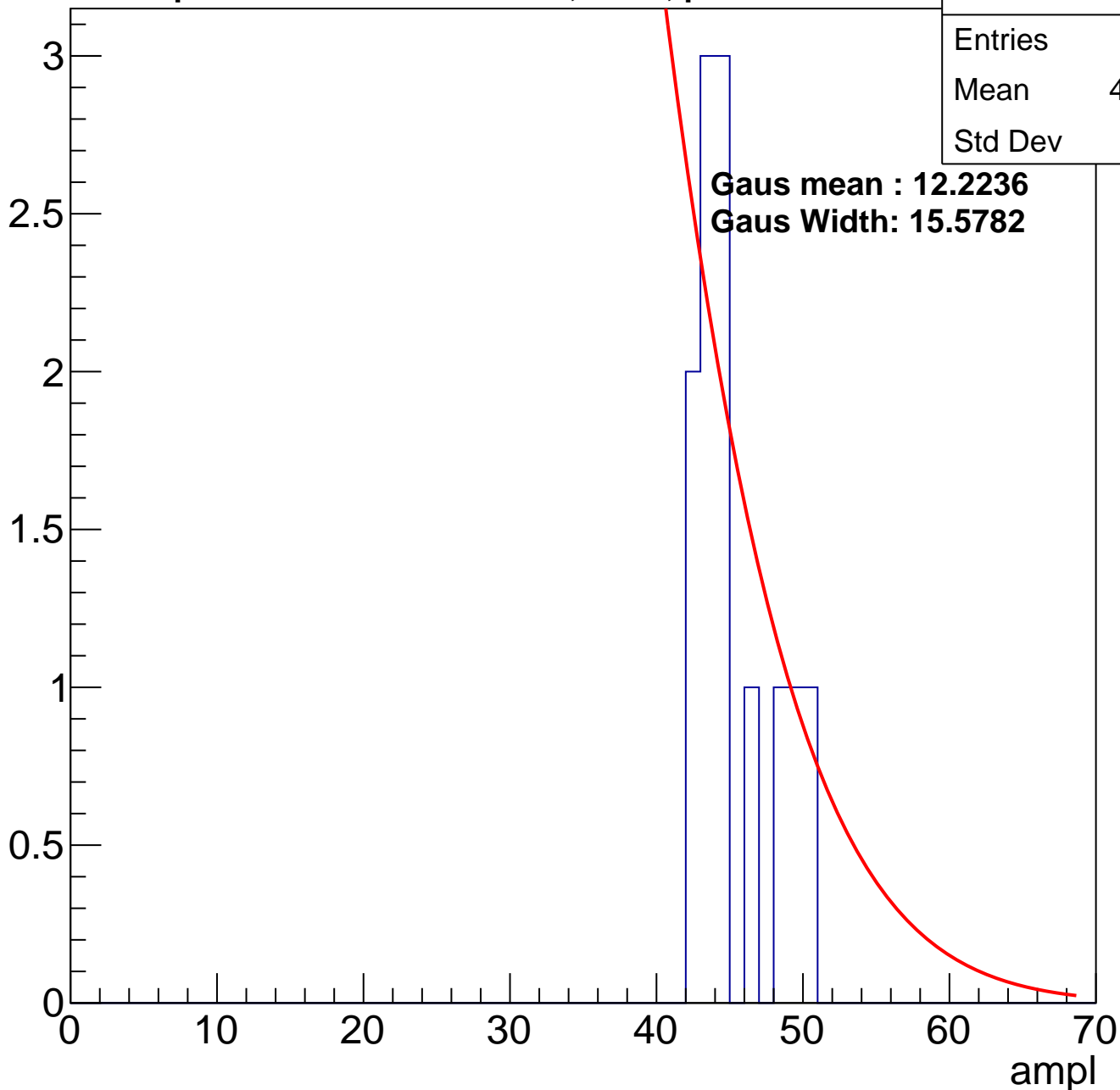
B0L100S, U13-ch24, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	20
Mean	45.2
Std Dev	2.249

Gaus mean : 1.9502
Gaus Width: 22.4892
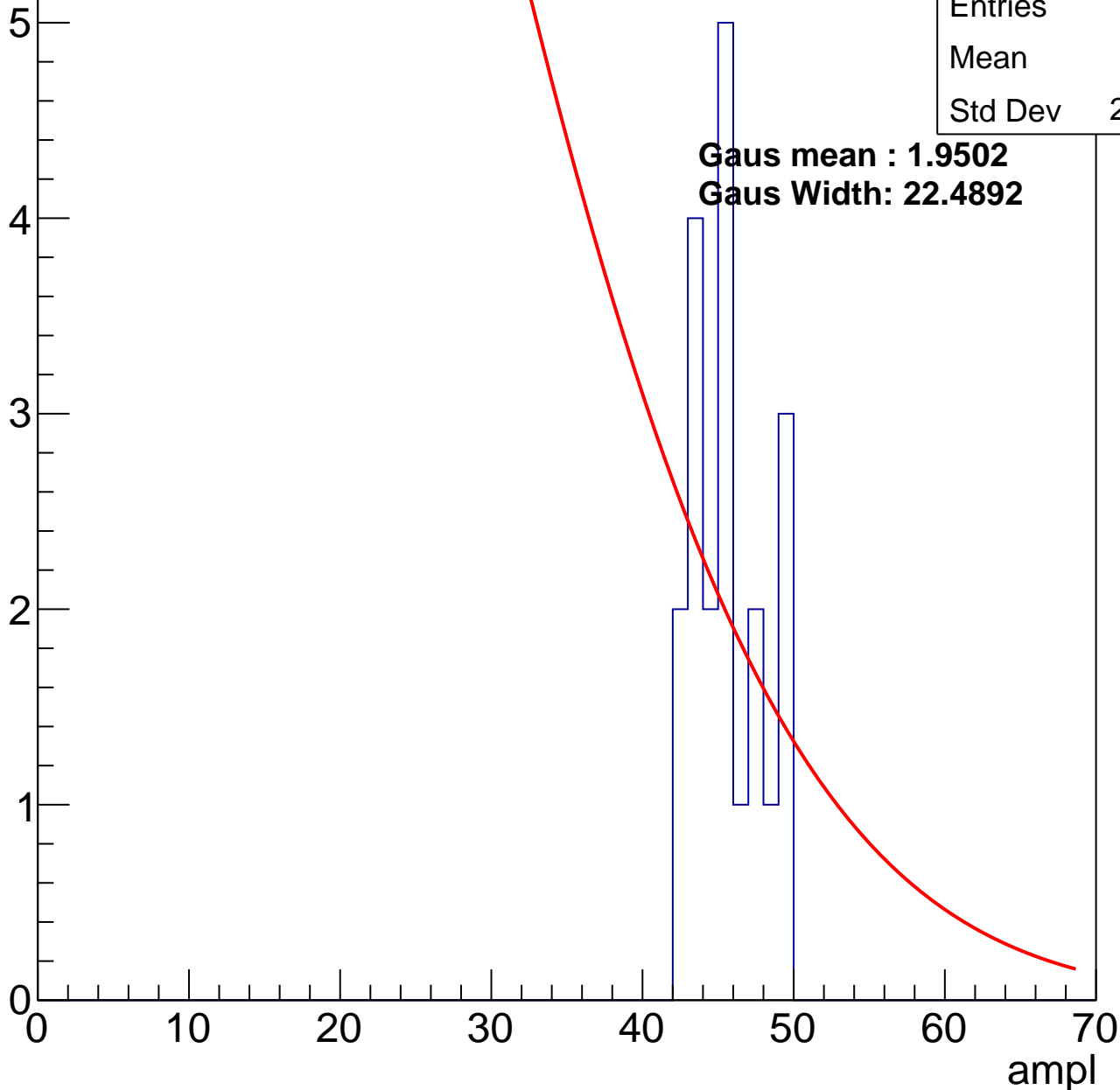
B0L100S, U13-ch24, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	48.25
Std Dev	0.433

Entry

ampl
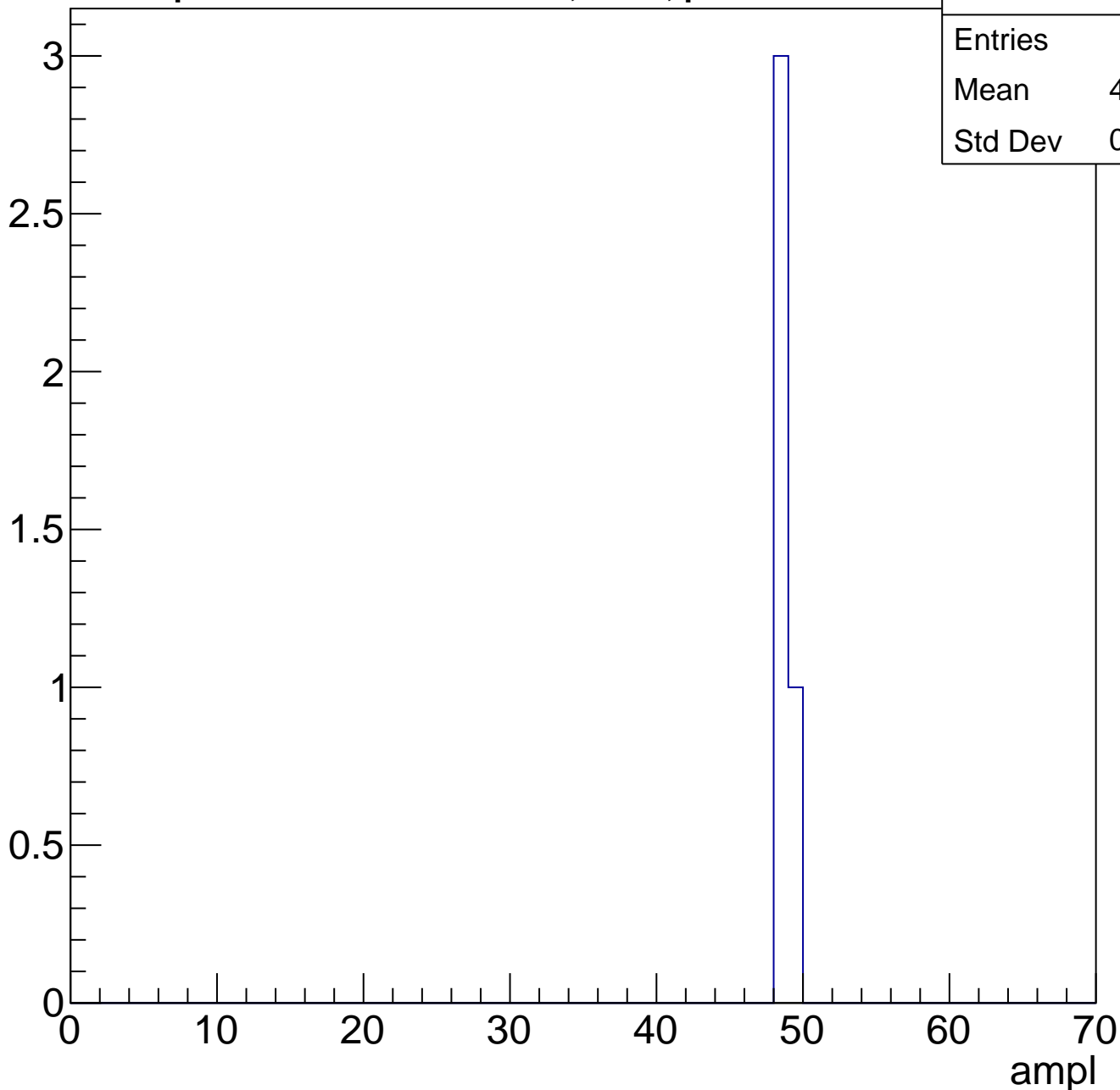
B0L100S, U13-ch24, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch24, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch24, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch24, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch25, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch25, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch25, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch25, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch25, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch25, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch25, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch25, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch26, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch26, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
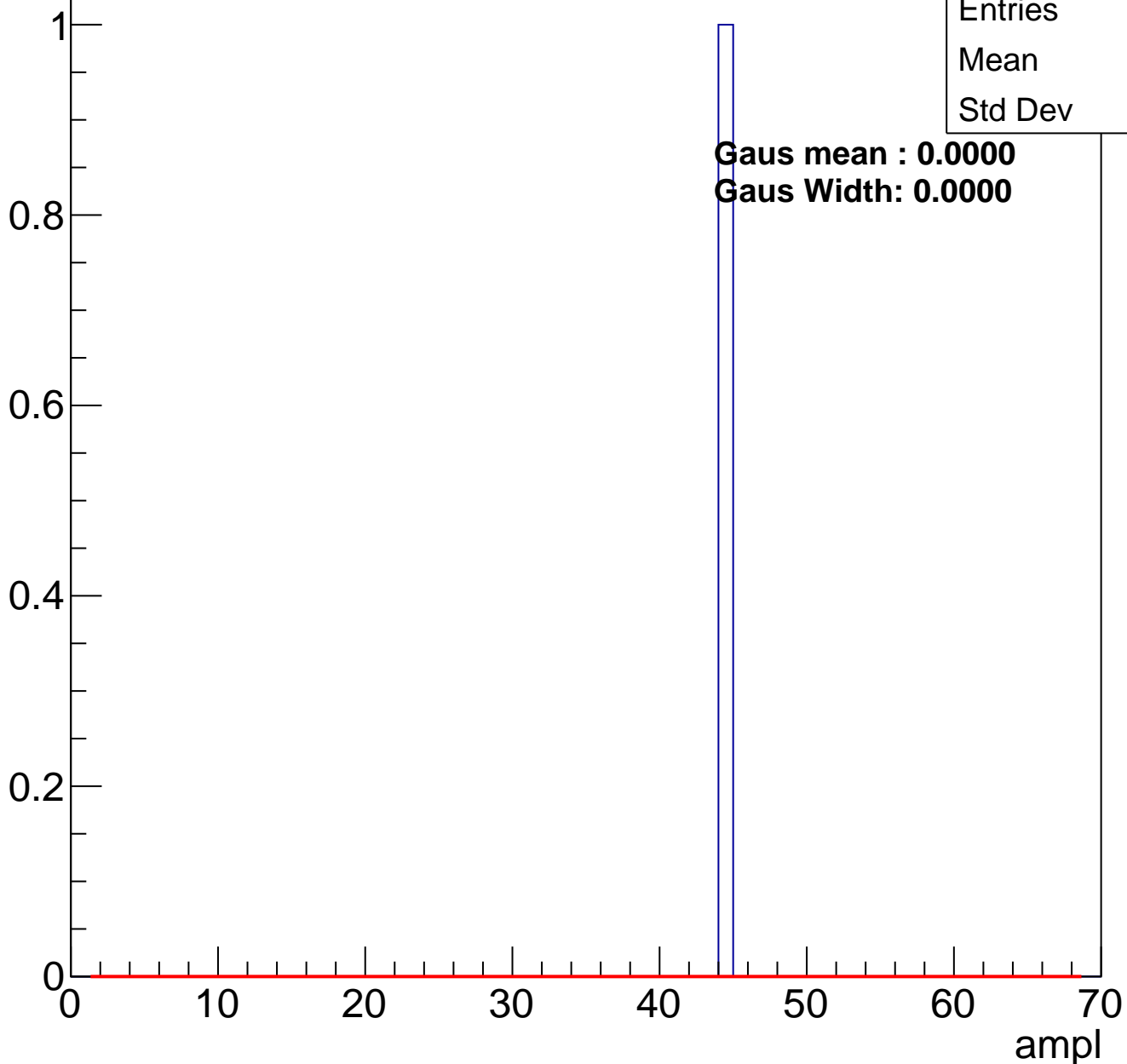
B0L100S, U13-ch26, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch26, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch26, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch26, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch26, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch26, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch27, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch27, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	45.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000
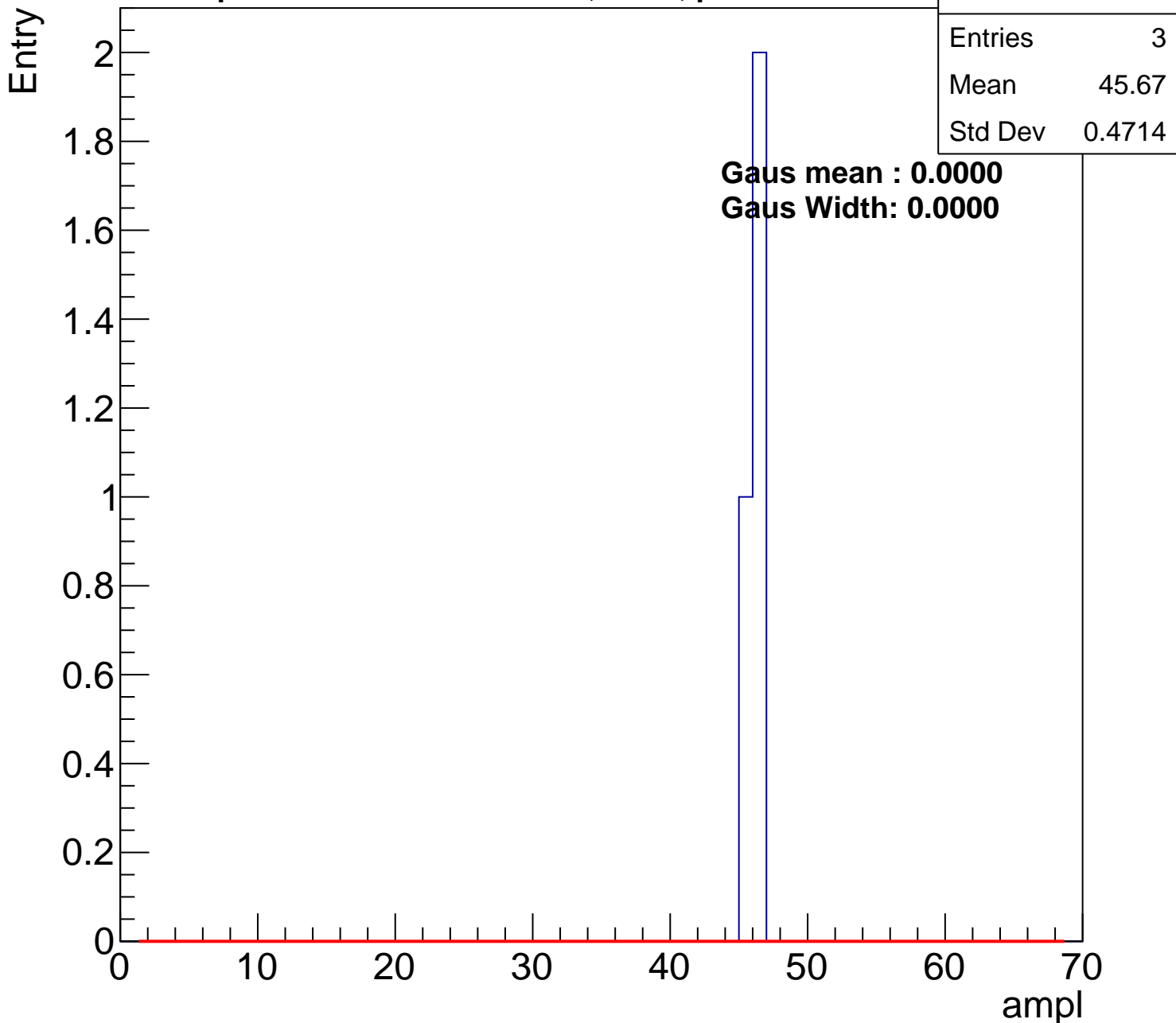
B0L100S, U13-ch27, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	45.8
Std Dev	0.7483

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
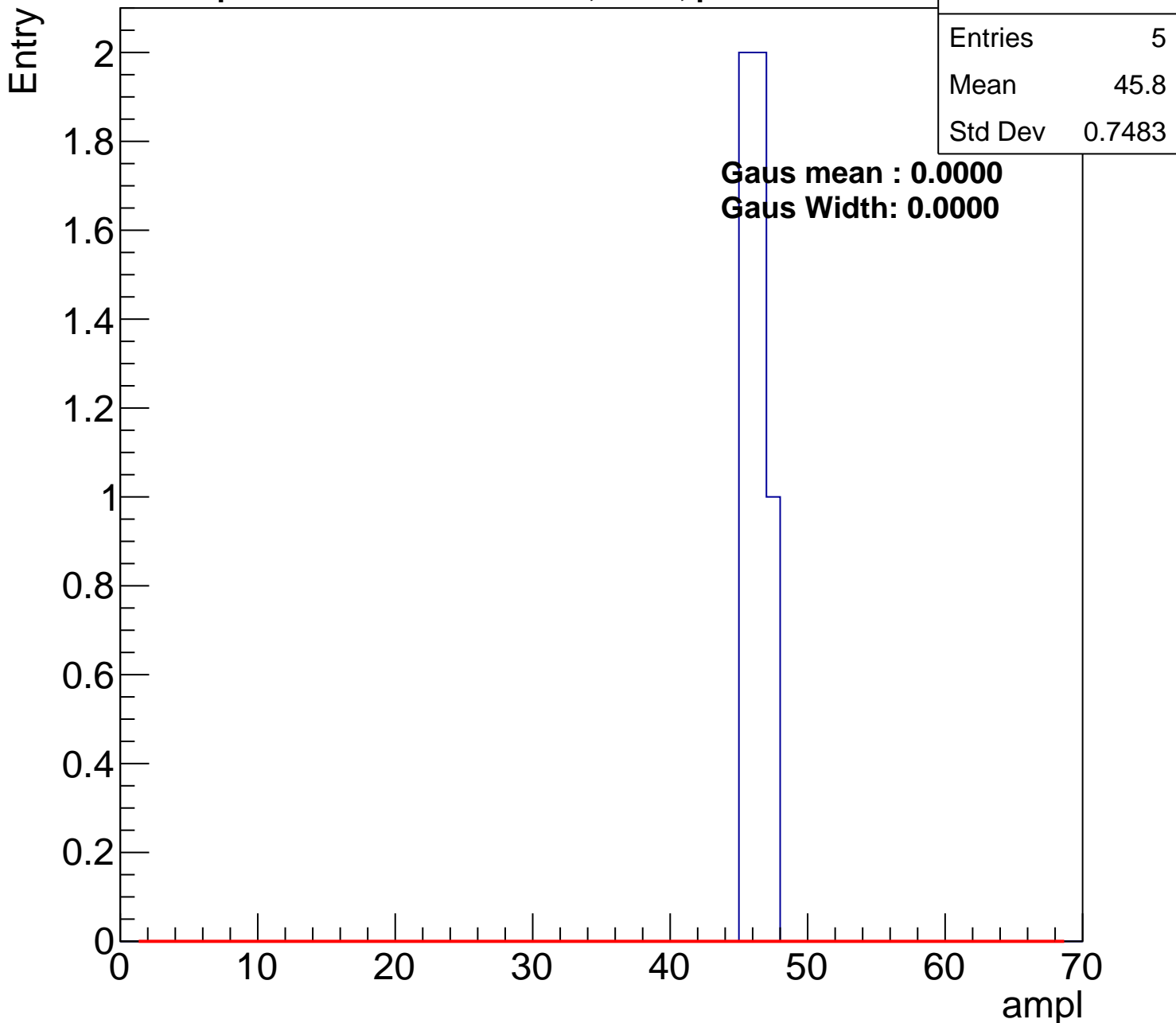
B0L100S, U13-ch27, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch27, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch27, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch27, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch27, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch28, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	48
Std Dev	0
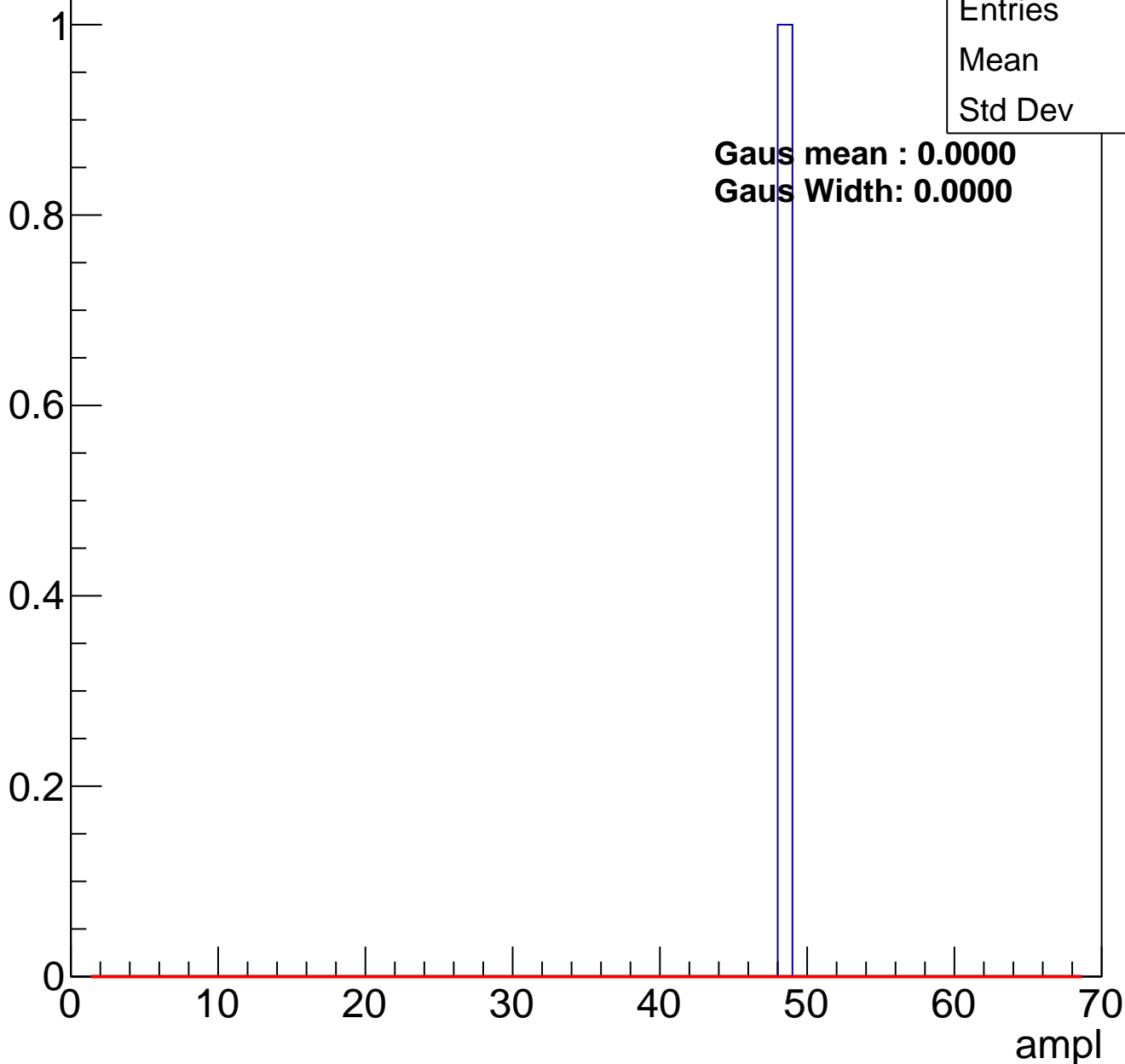
Entry

ampl

B0L100S, U13-ch28, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	5
Mean	46
Std Dev	1.095

Entry

ampl

B0L100S, U13-ch28, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	49.06
Std Dev	2.657

Gaus mean : 30.9432
Gaus Width: 26.5680

Entry

ampl
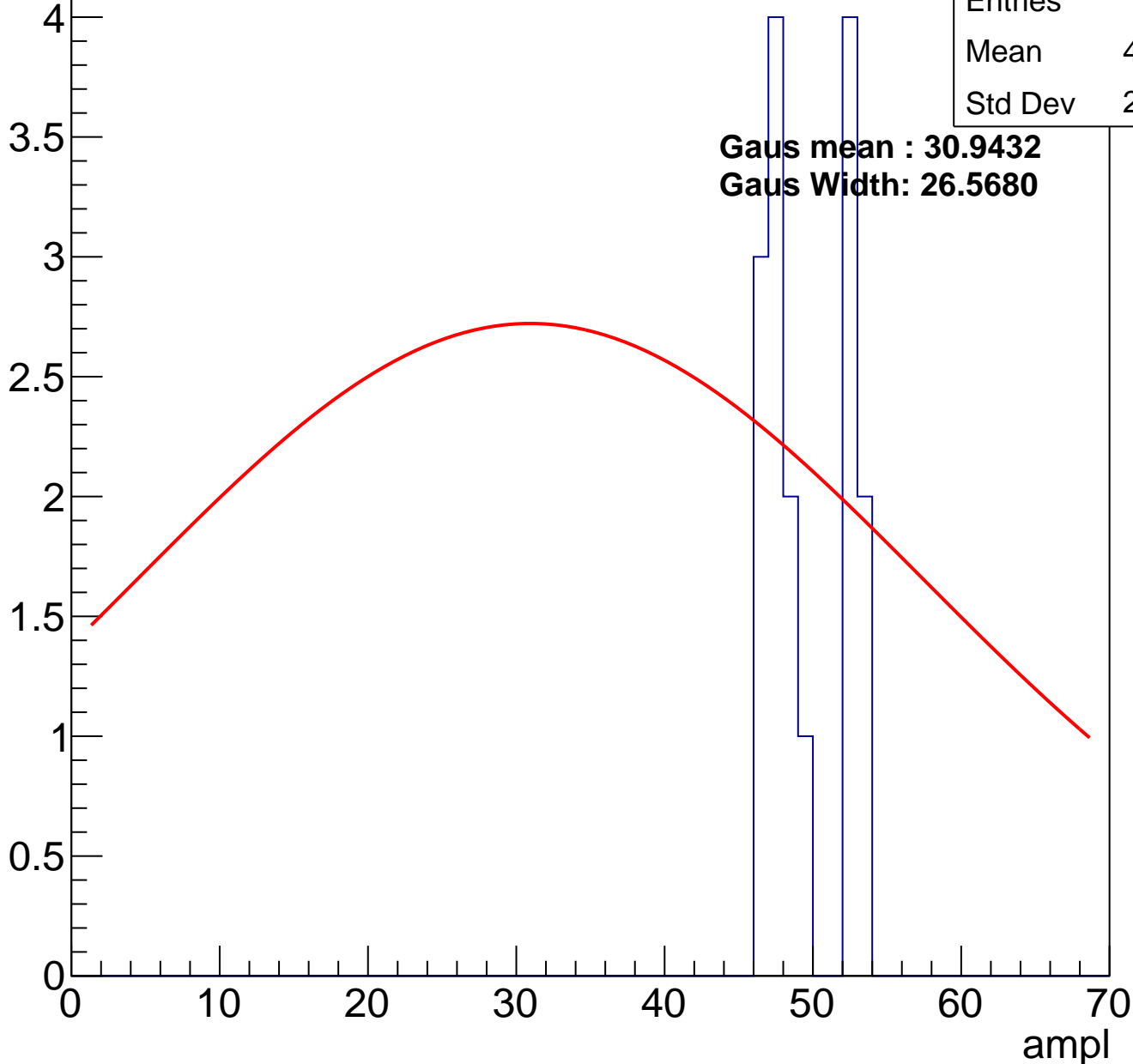
B0L100S, U13-ch28, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch28, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch28, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch28, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch29, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch29, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch29, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	48.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
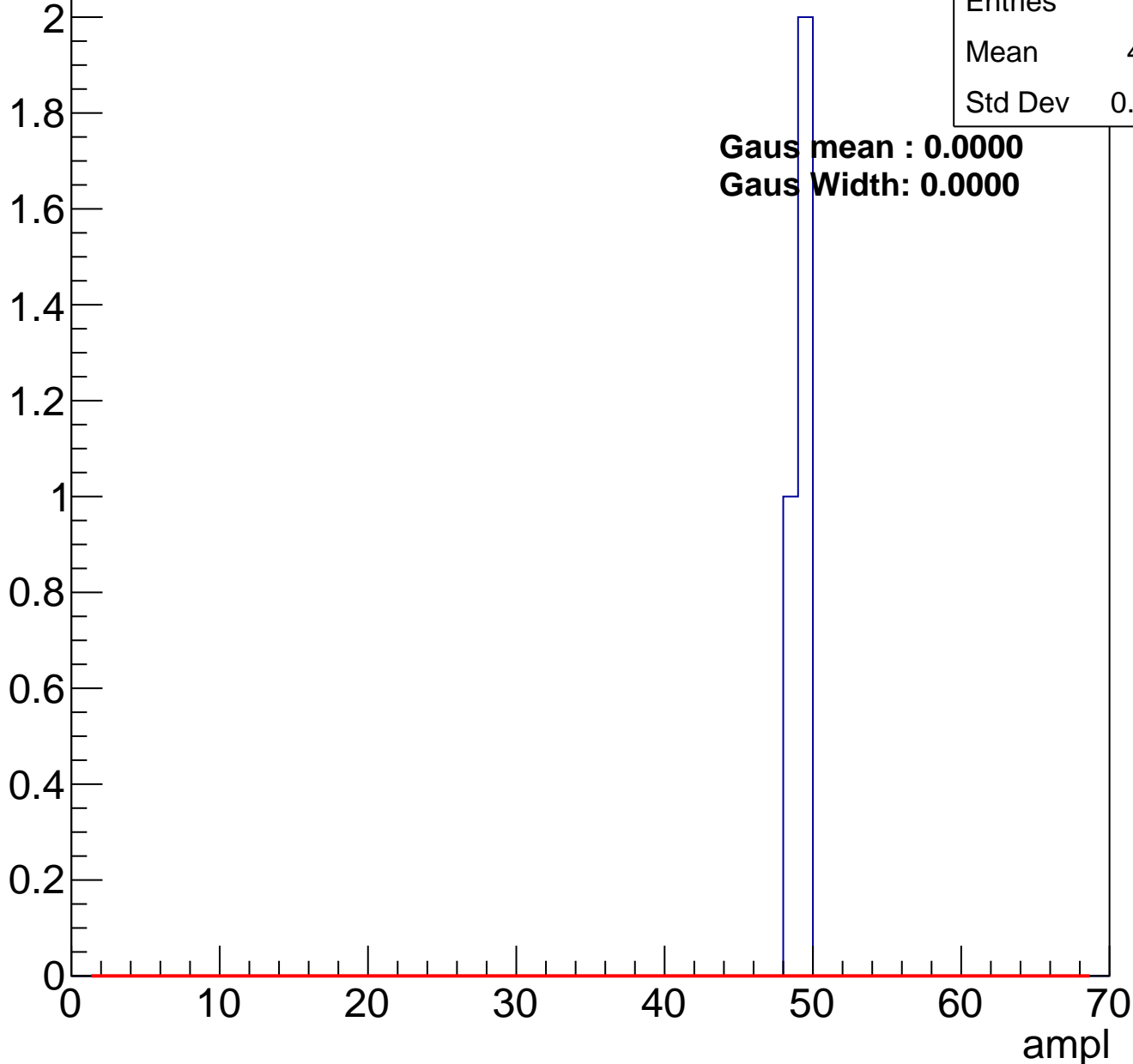
B0L100S, U13-ch29, adc3

calib_packv5_042523_0143.root, FC#6, port A1

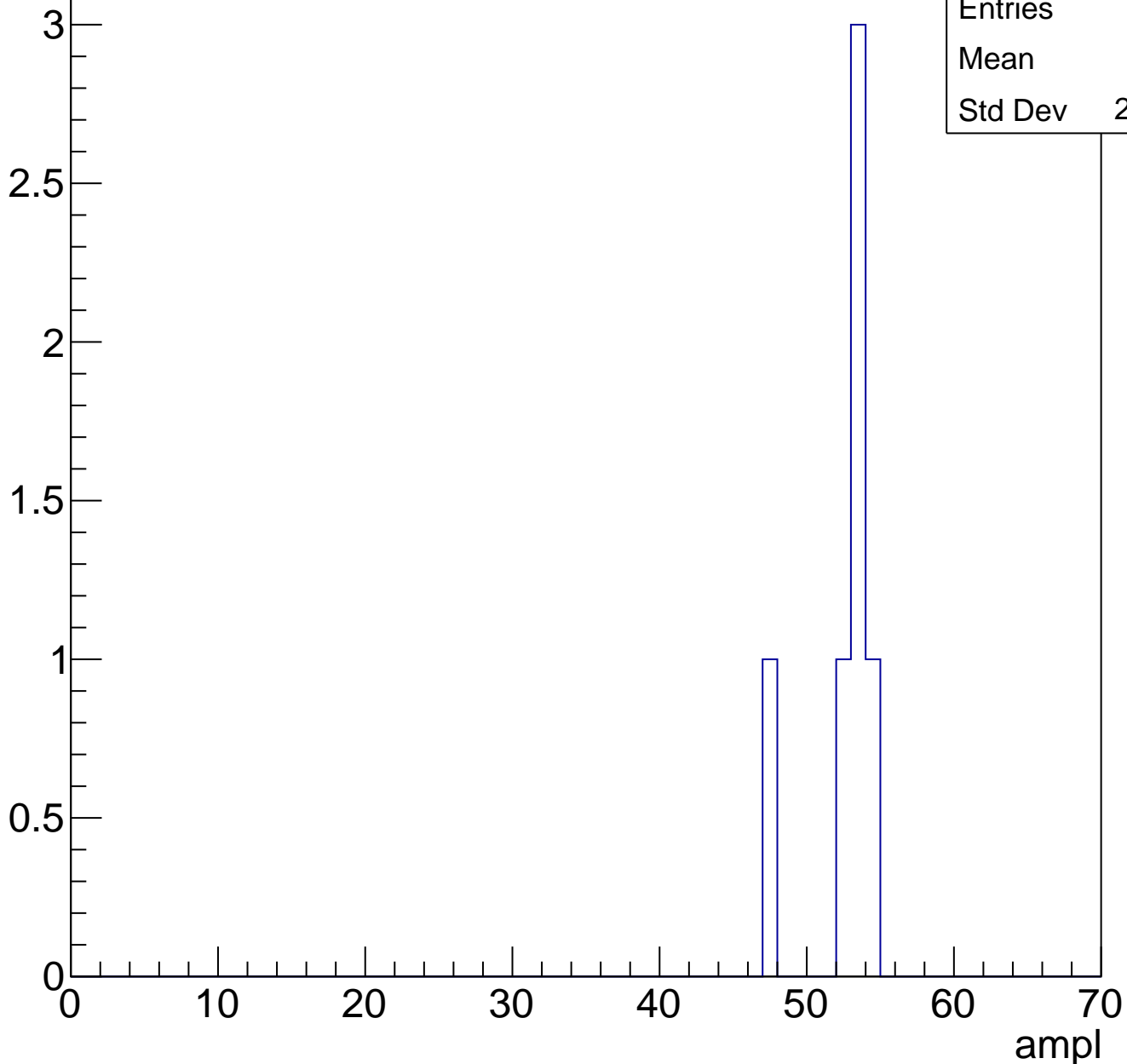|	
Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch29, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch29, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch29, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch29, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch30, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch30, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch30, adc2

calib_packv5_042523_0143.root, FC#6, port A1
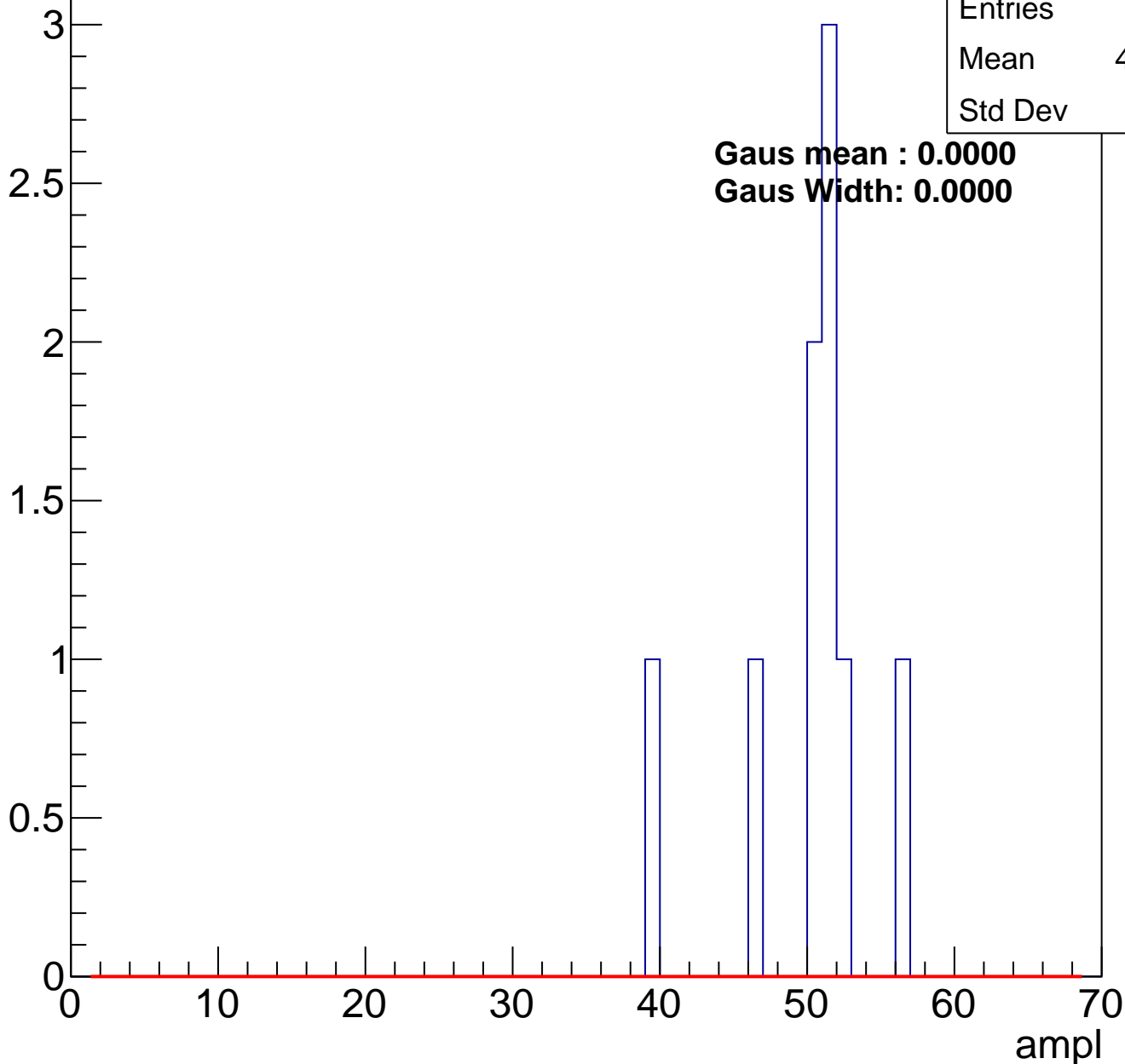
Entries	9
Mean	49.56
Std Dev	4.45

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch30, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	53.17
Std Dev	2.544

Entry

ampl

B0L100S, U13-ch30, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch30, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch30, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch30, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch31, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch31, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch31, adc2
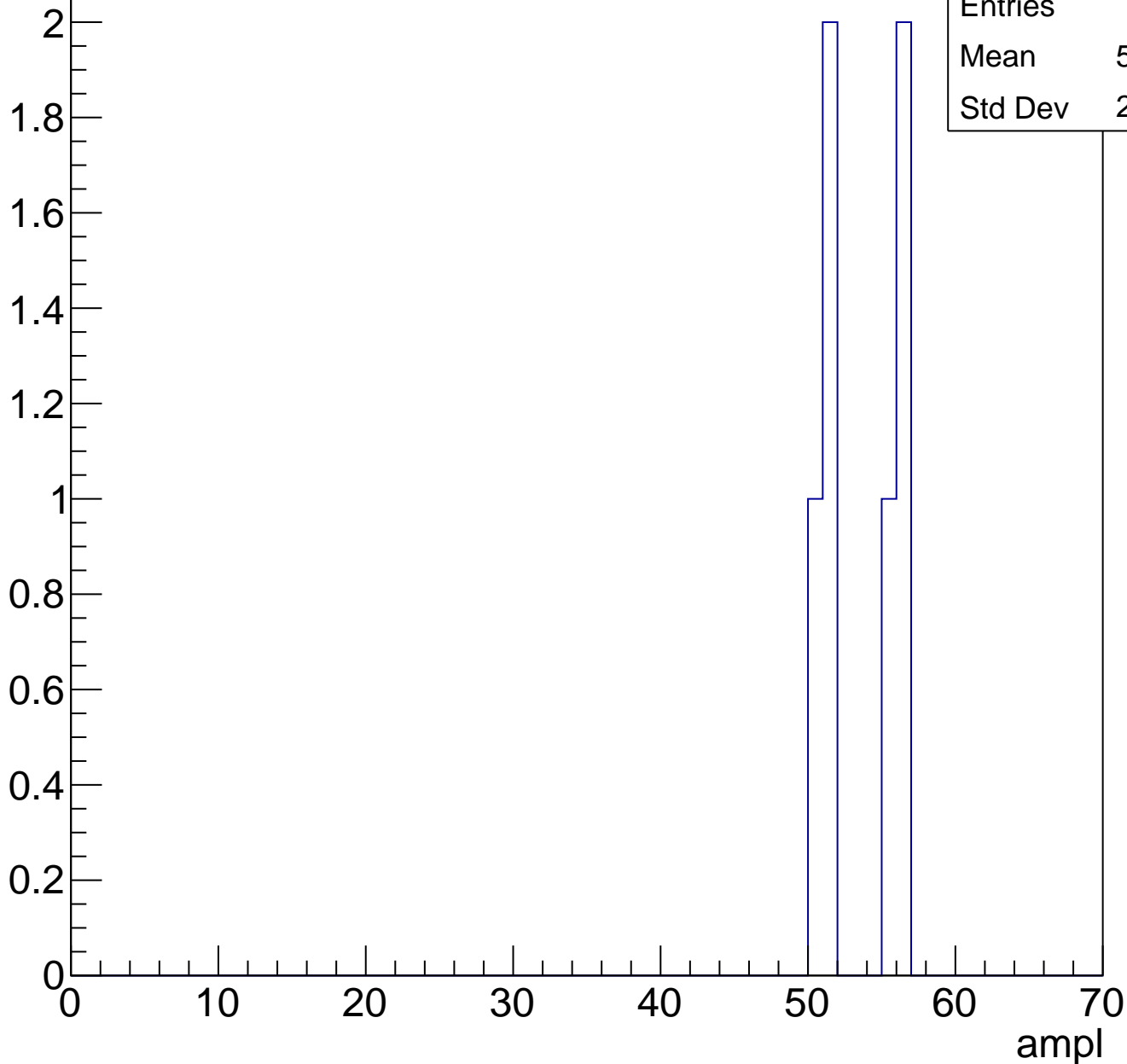
calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch31, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch31, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch31, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch31, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch31, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch32, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch32, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	44.86
Std Dev	1.884

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
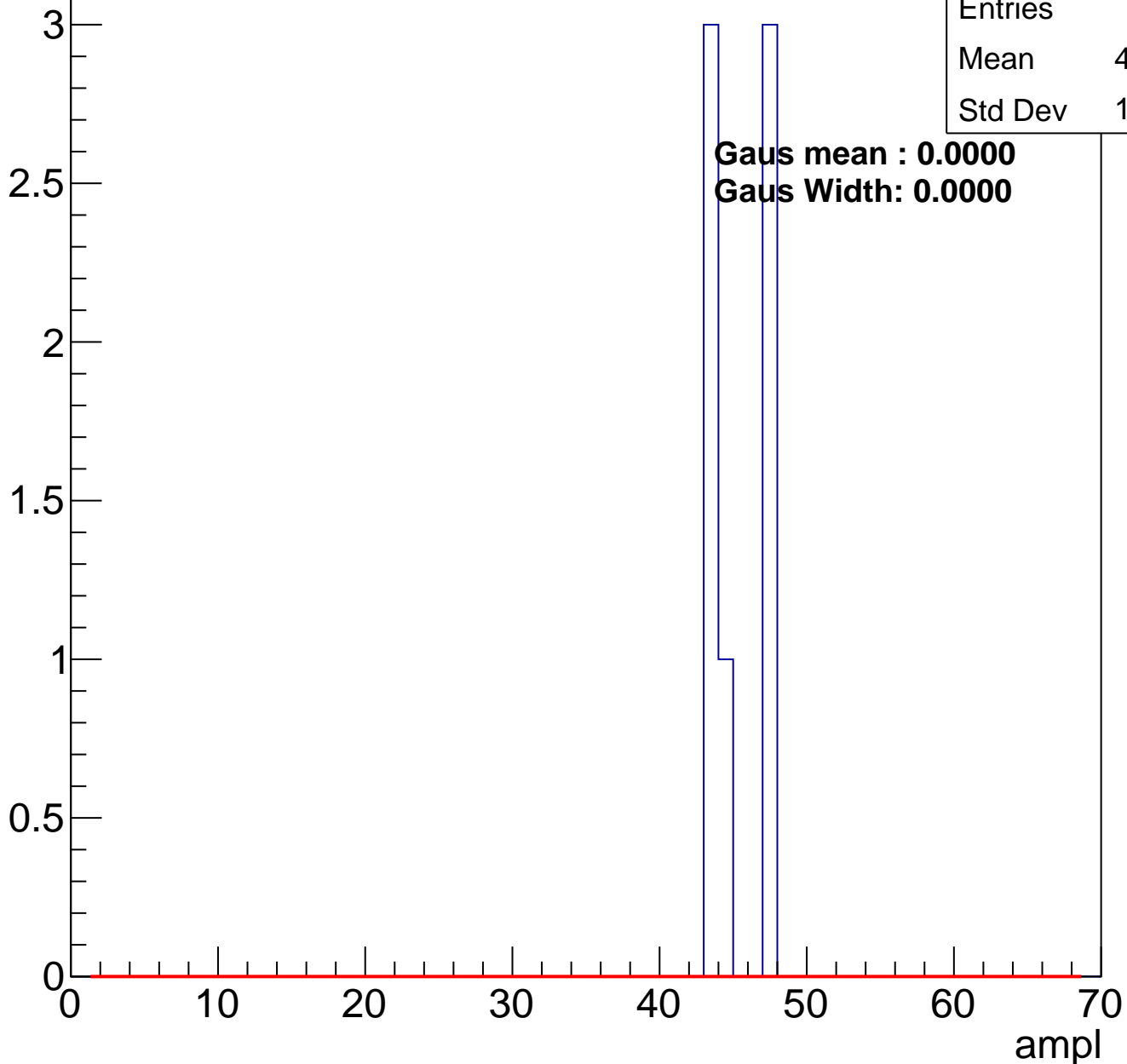
B0L100S, U13-ch32, adc2

calib_packv5_042523_0143.root, FC#6, port A1
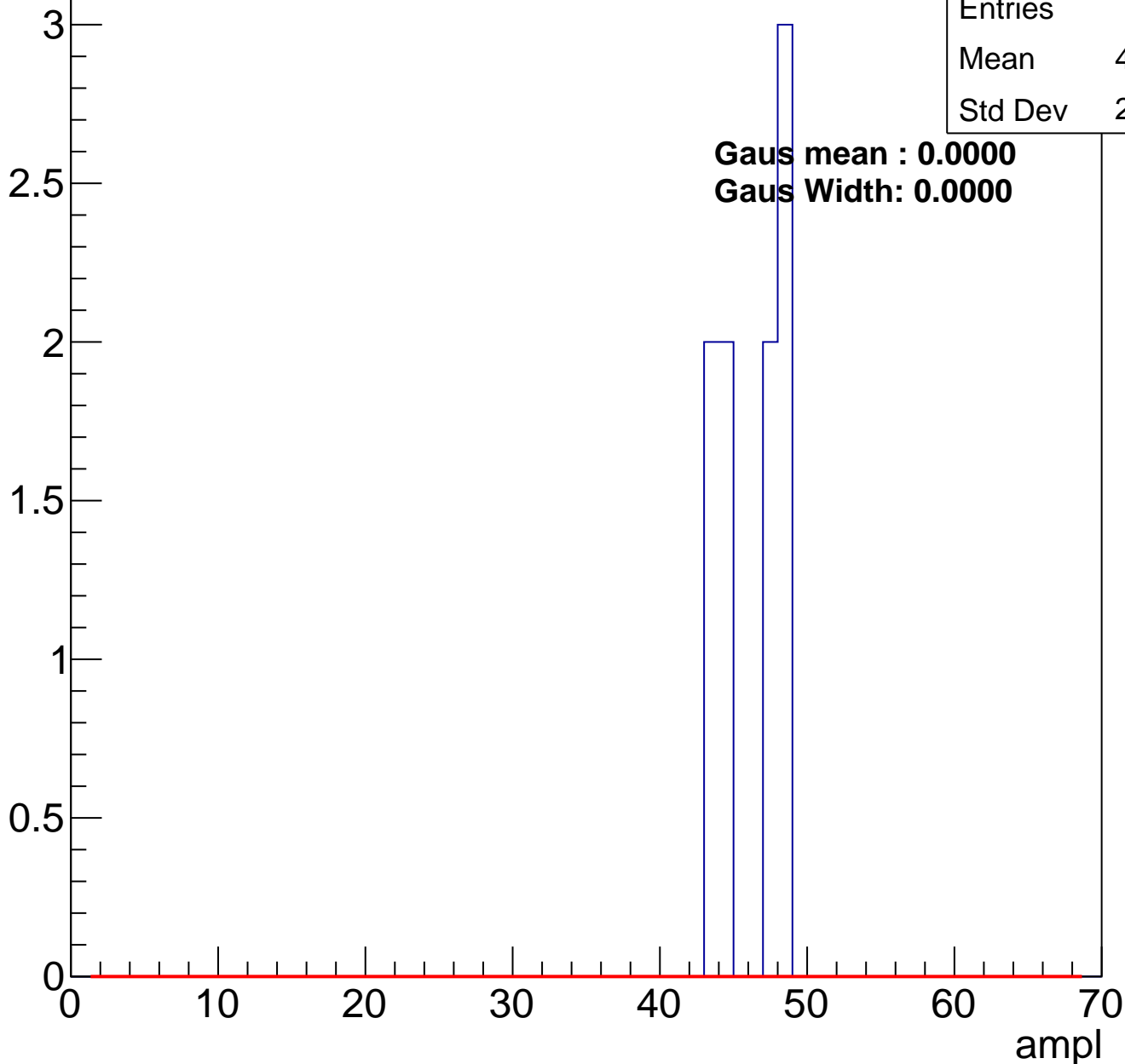
Entry

Entries	9
Mean	45.78
Std Dev	2.096

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch32, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch32, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch32, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch32, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch32, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch33, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch33, adc1

calib_packv5_042523_0143.root, FC#6, port A1
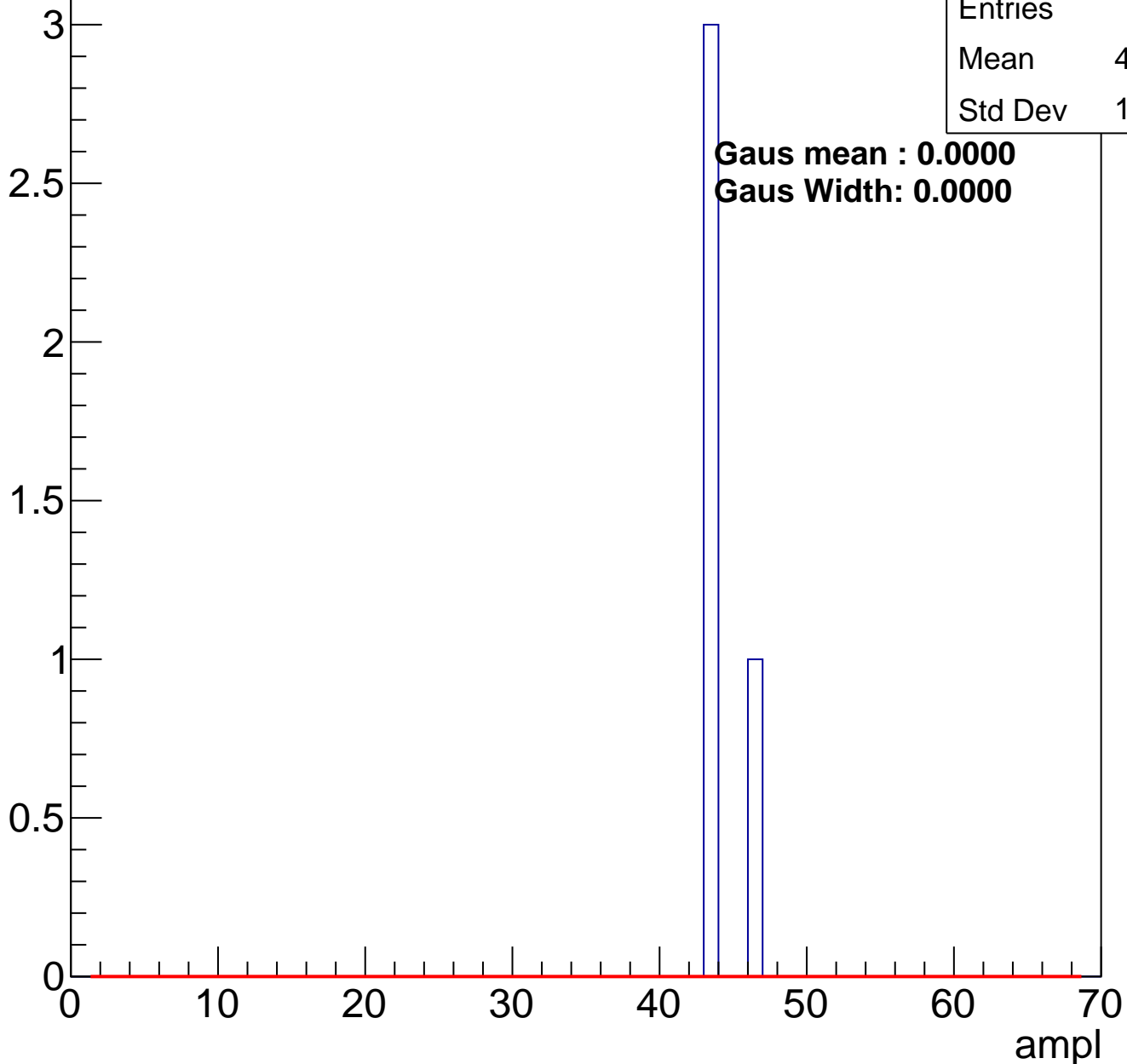
Entries	4
Mean	43.75
Std Dev	1.299

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch33, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
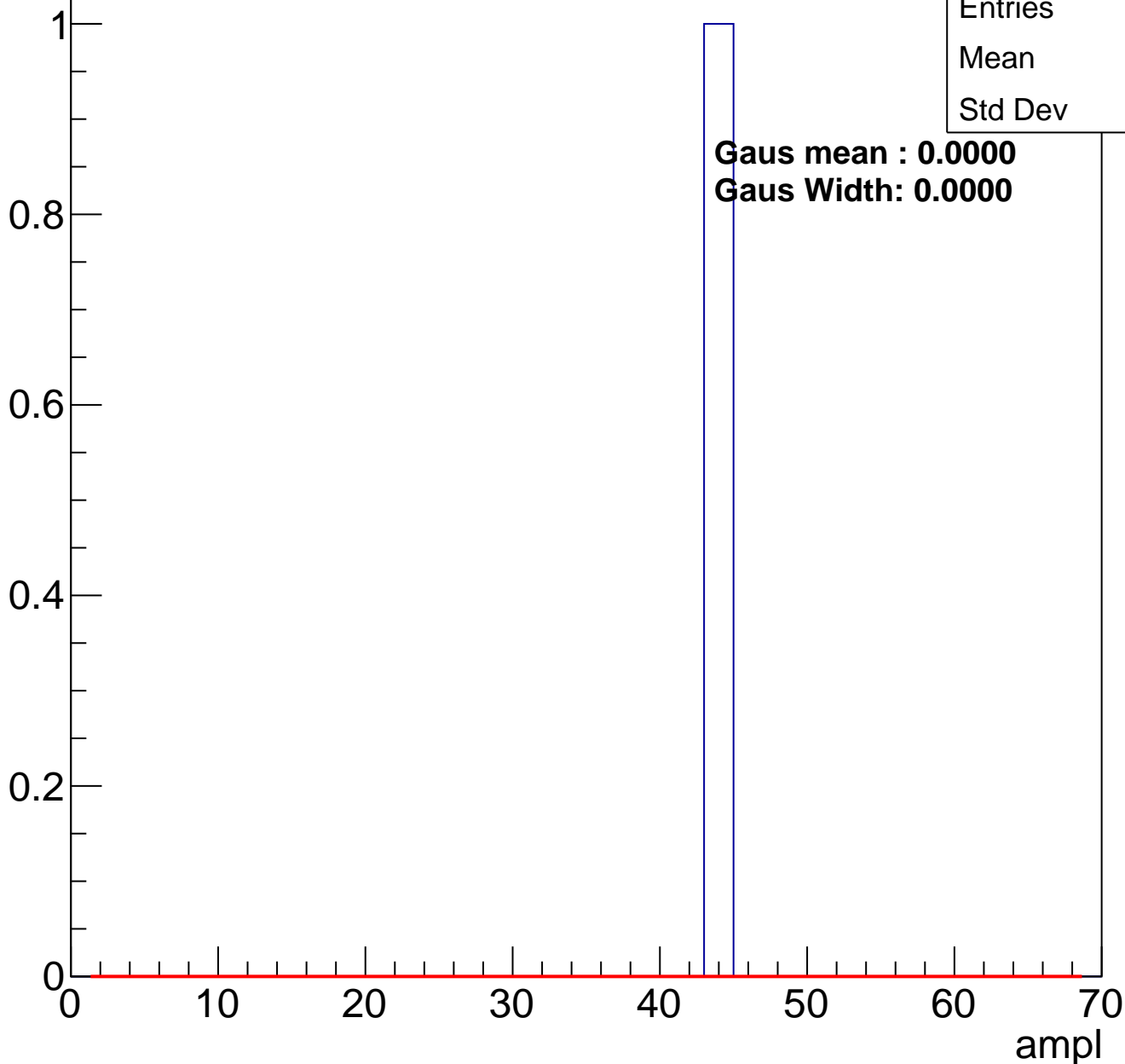
B0L100S, U13-ch33, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch33, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch33, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch33, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch33, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch34, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	41.6
Std Dev	1.02

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
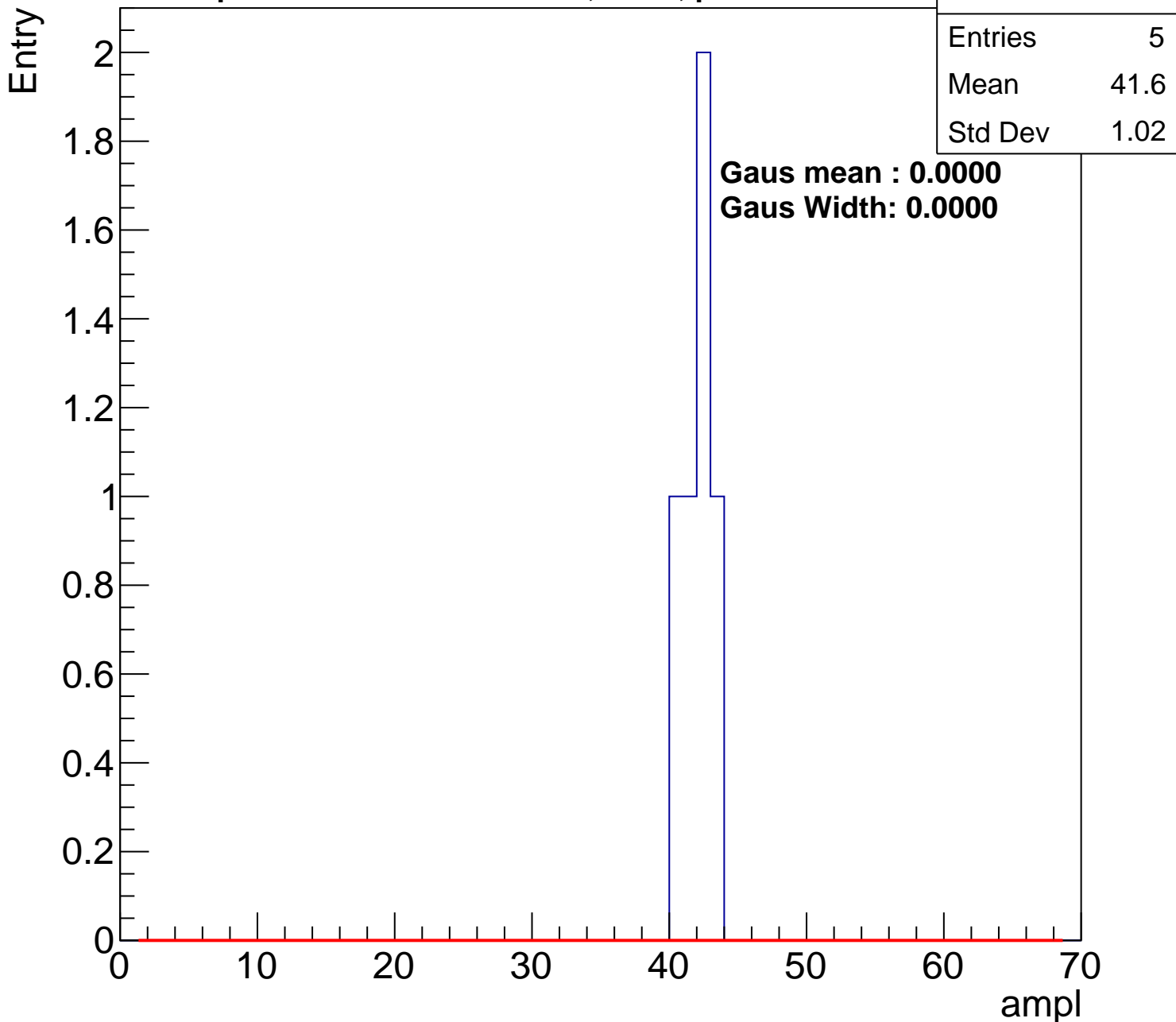
B0L100S, U13-ch34, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	15
Mean	43.33
Std Dev	2.413

Gaus mean : 43.2167
Gaus Width: 3.4332
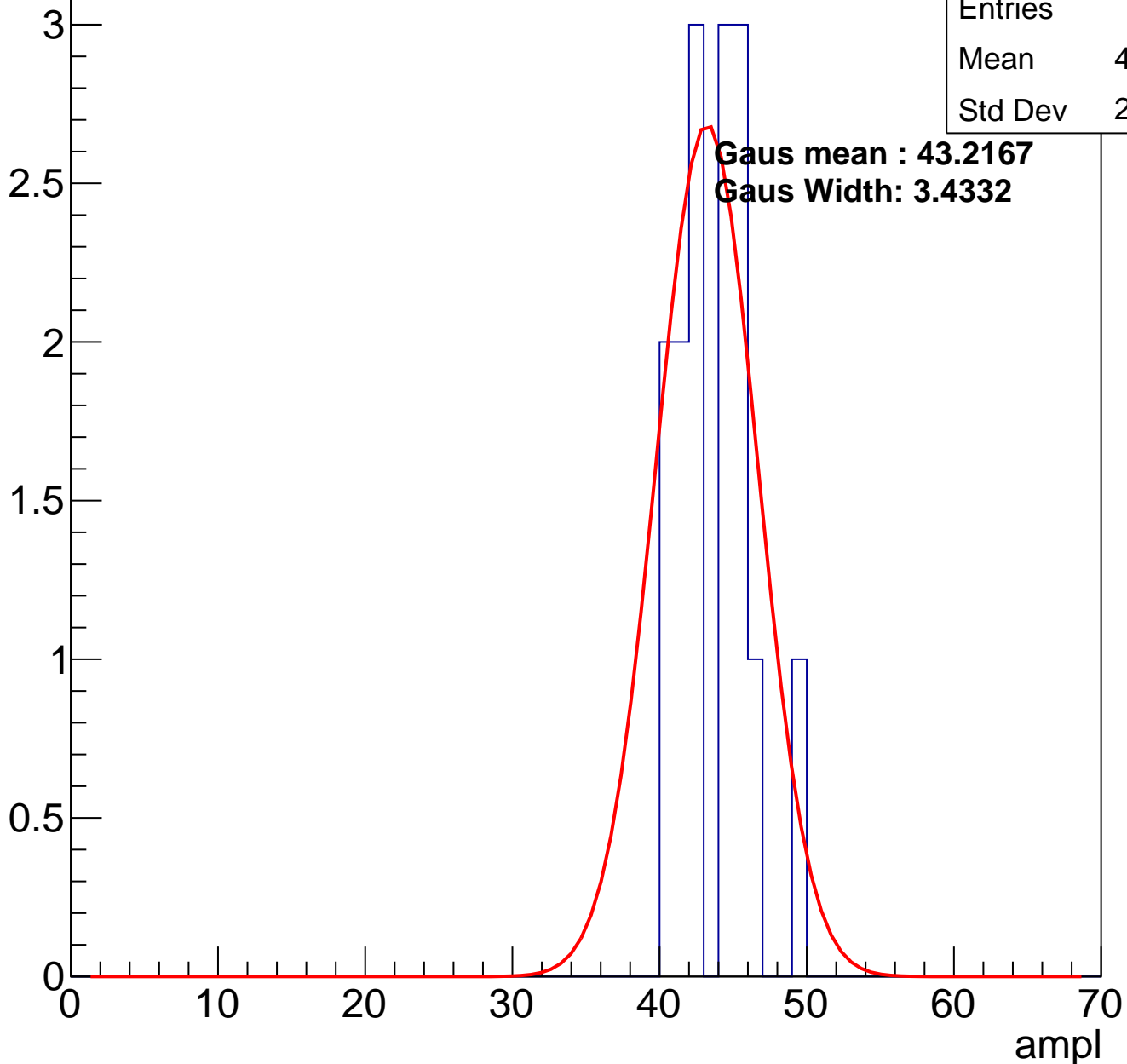
B0L100S, U13-ch34, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	12
Mean	44.67
Std Dev	2.392

Gaus mean : 44.2091
Gaus Width: 6.7000

ampl

B0L100S, U13-ch34, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Entry

ampl
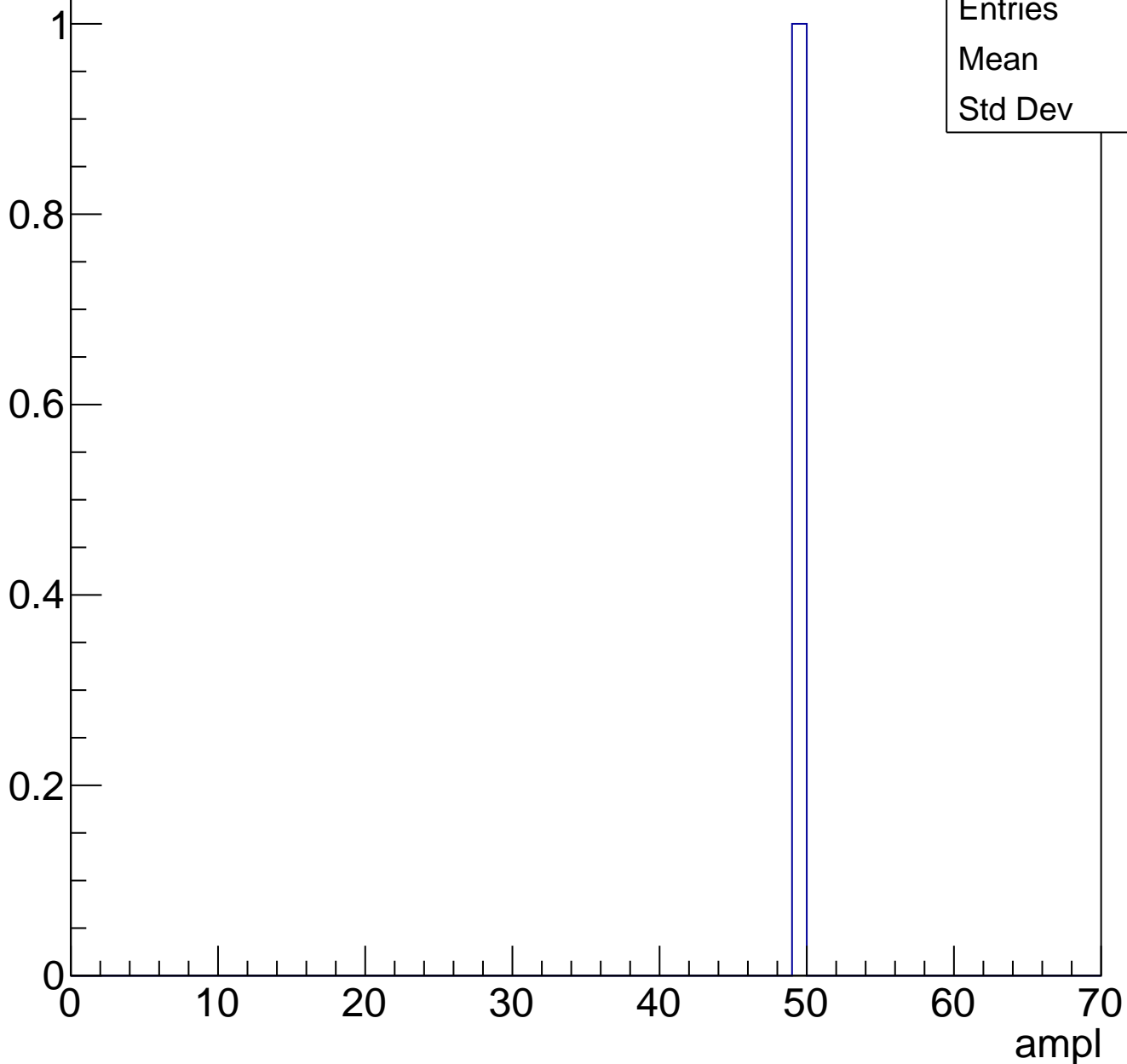
B0L100S, U13-ch34, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch34, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch34, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch34, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch35, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch35, adc1

calib_packv5_042523_0143.root, FC#6, port A1

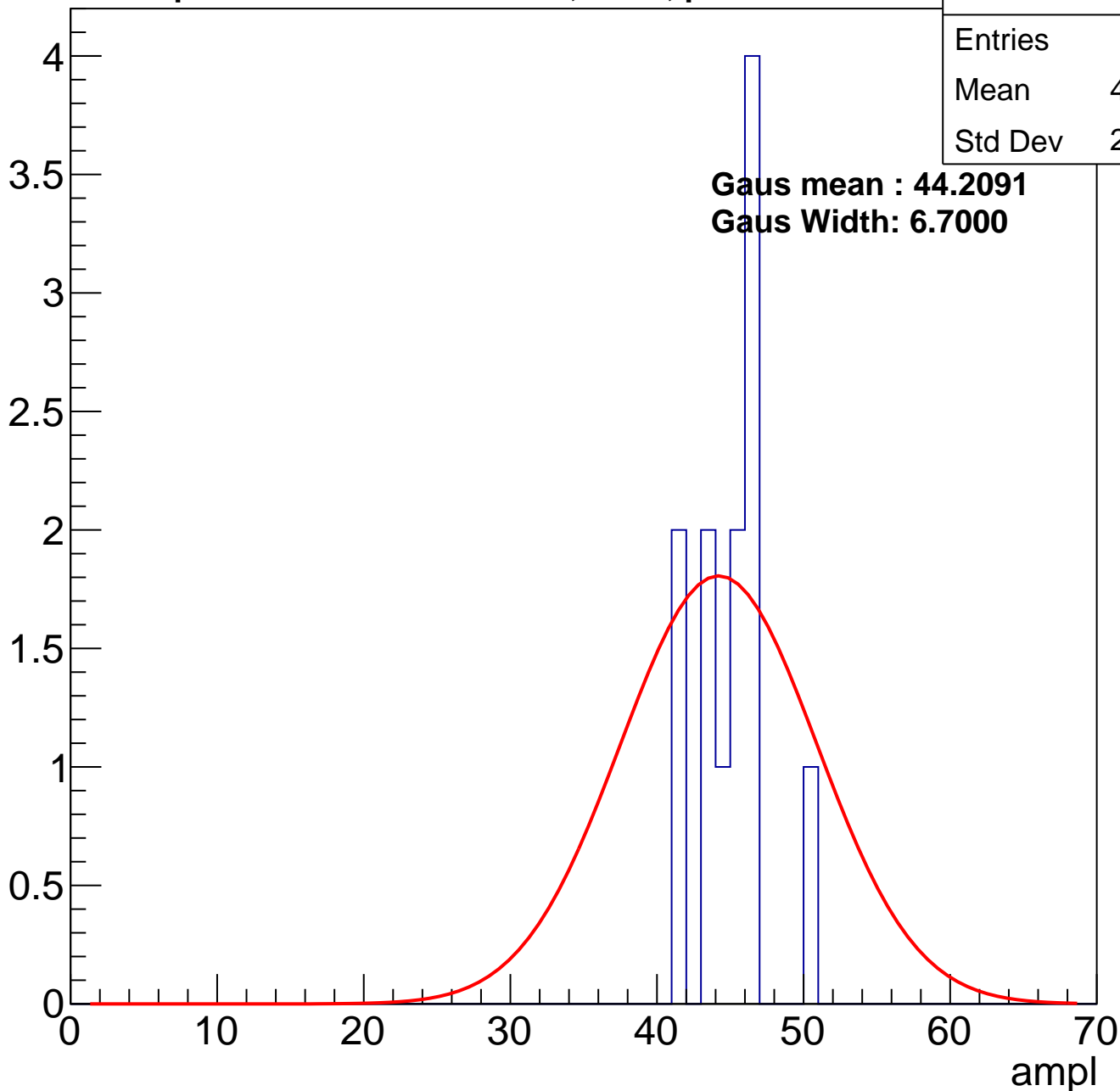|	
Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch35, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch35, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch35, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch35, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch35, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch35, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch36, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch36, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	7
Mean	44.14
Std Dev	2.231

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch36, adc2

calib_packv5_042523_0143.root, FC#6, port A1
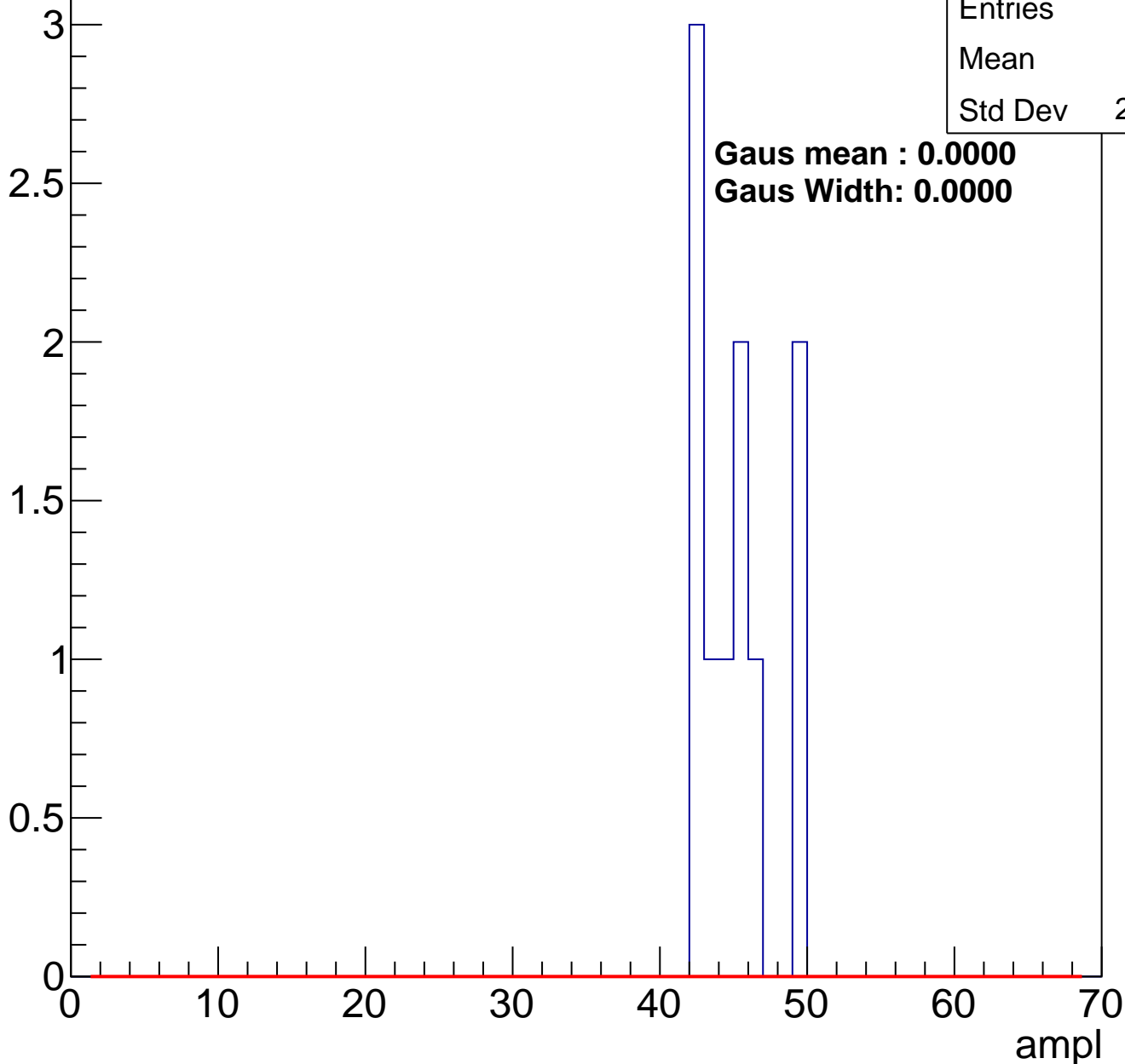
Entry

Entries	10
Mean	44.7
Std Dev	2.532

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch36, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch36, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch36, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch36, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch36, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch37, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
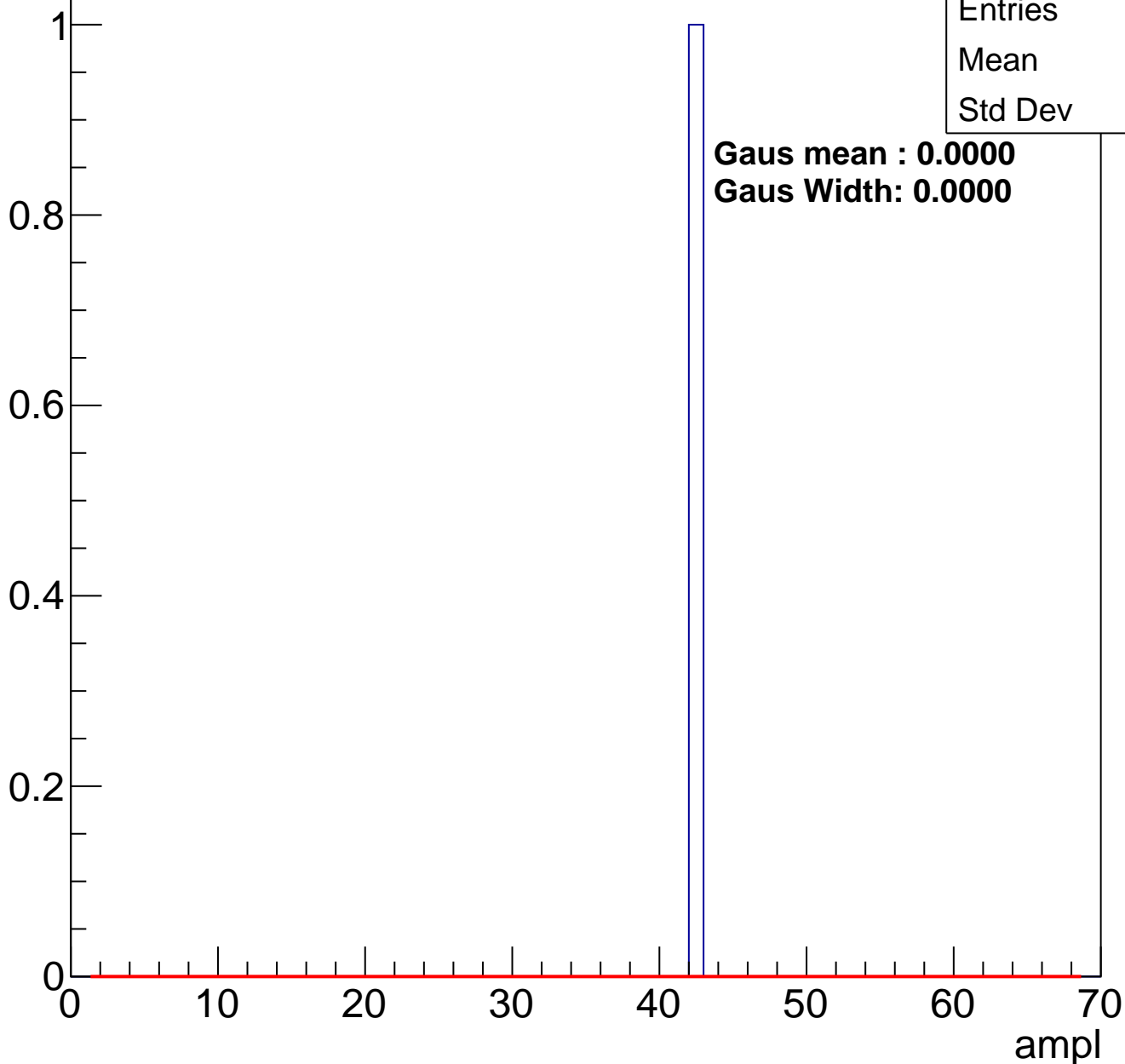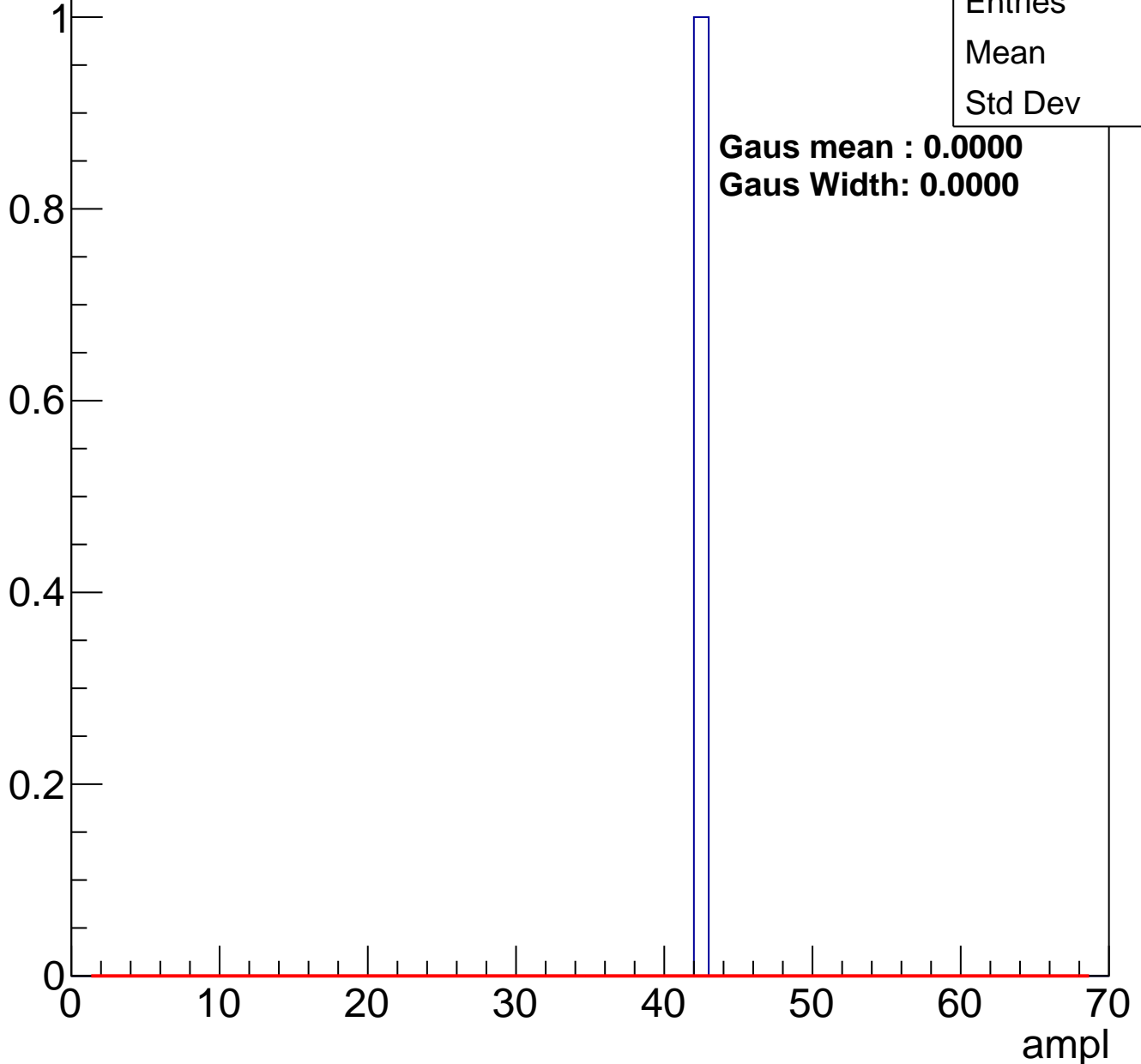
B0L100S, U13-ch37, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch37, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	4
Mean	52
Std Dev	5.788

Gaus mean : 0.0000
Gaus Width: 0.0000
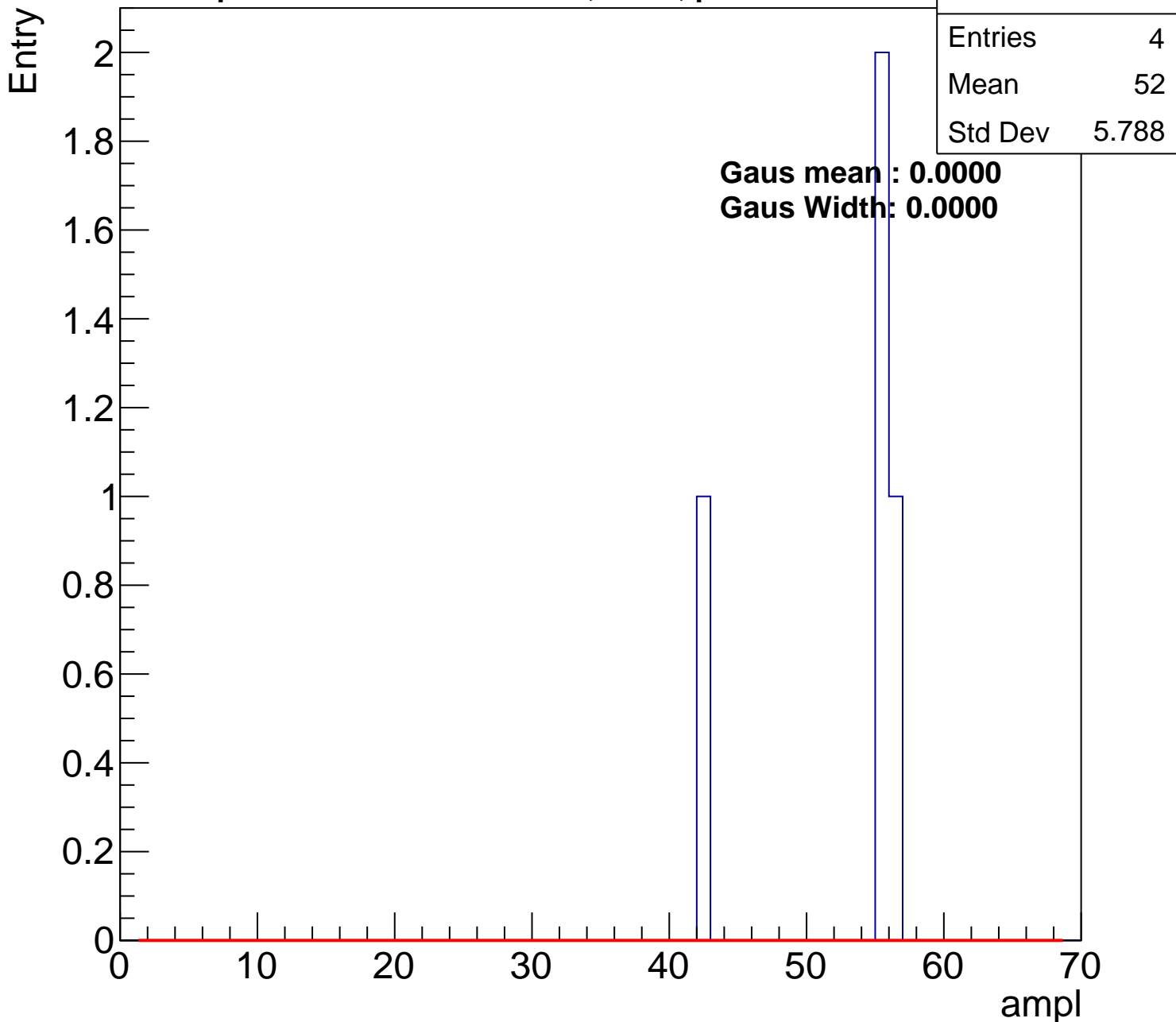
B0L100S, U13-ch37, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	55.75
Std Dev	0.433
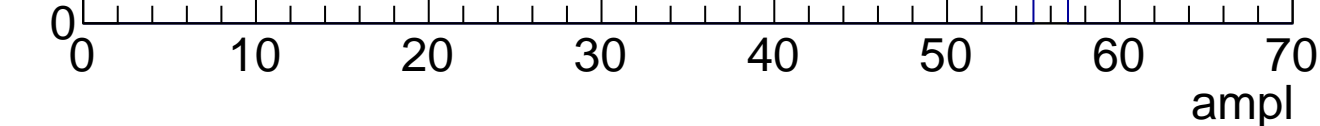
Entry

ampl

B0L100S, U13-ch37, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	55
Std Dev	0

Entry

ampl

B0L100S, U13-ch37, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch37, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch37, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch38, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch38, adc1

calib_packv5_042523_0143.root, FC#6, port A1
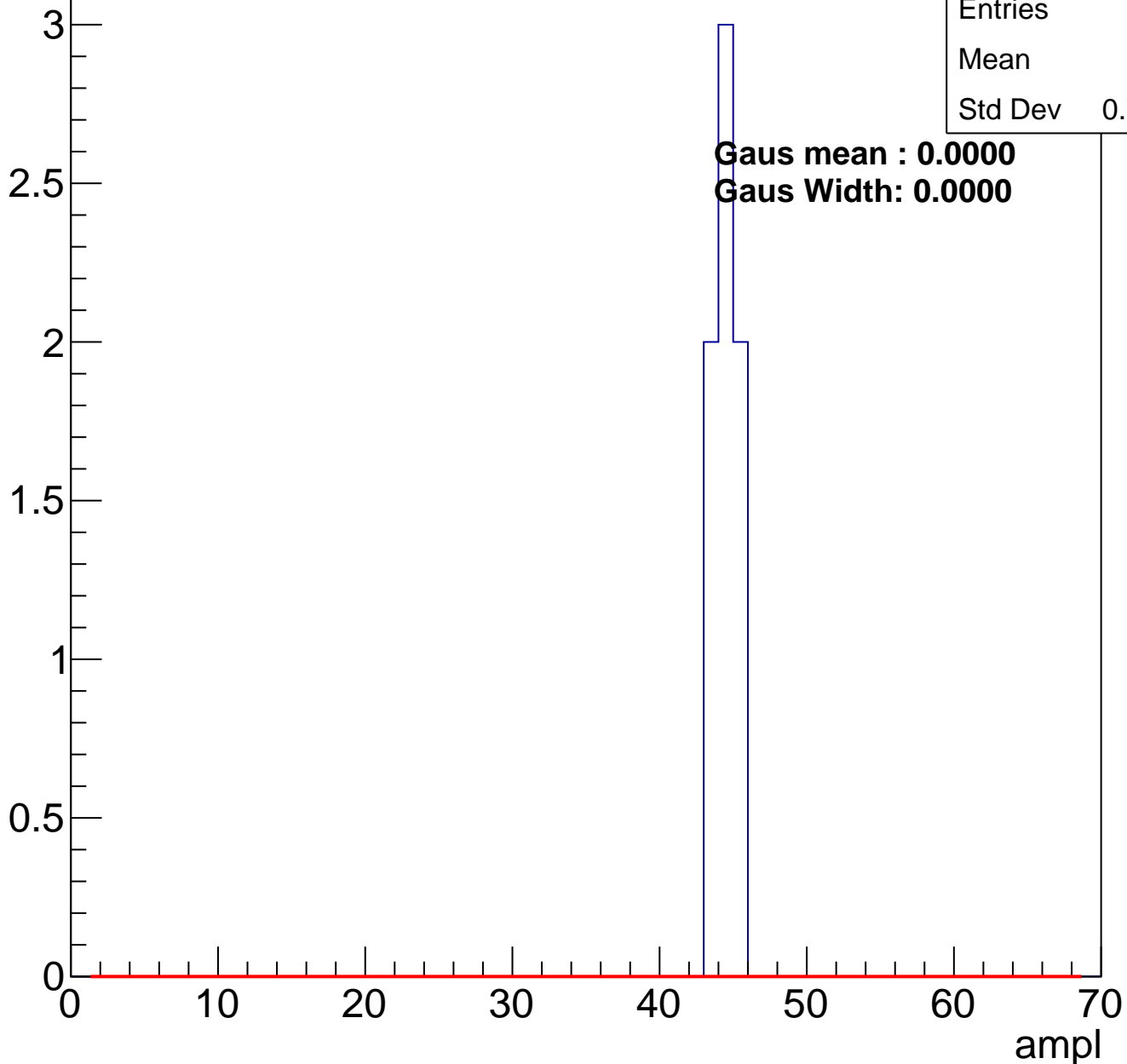
Entries	7
Mean	44
Std Dev	0.7559

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
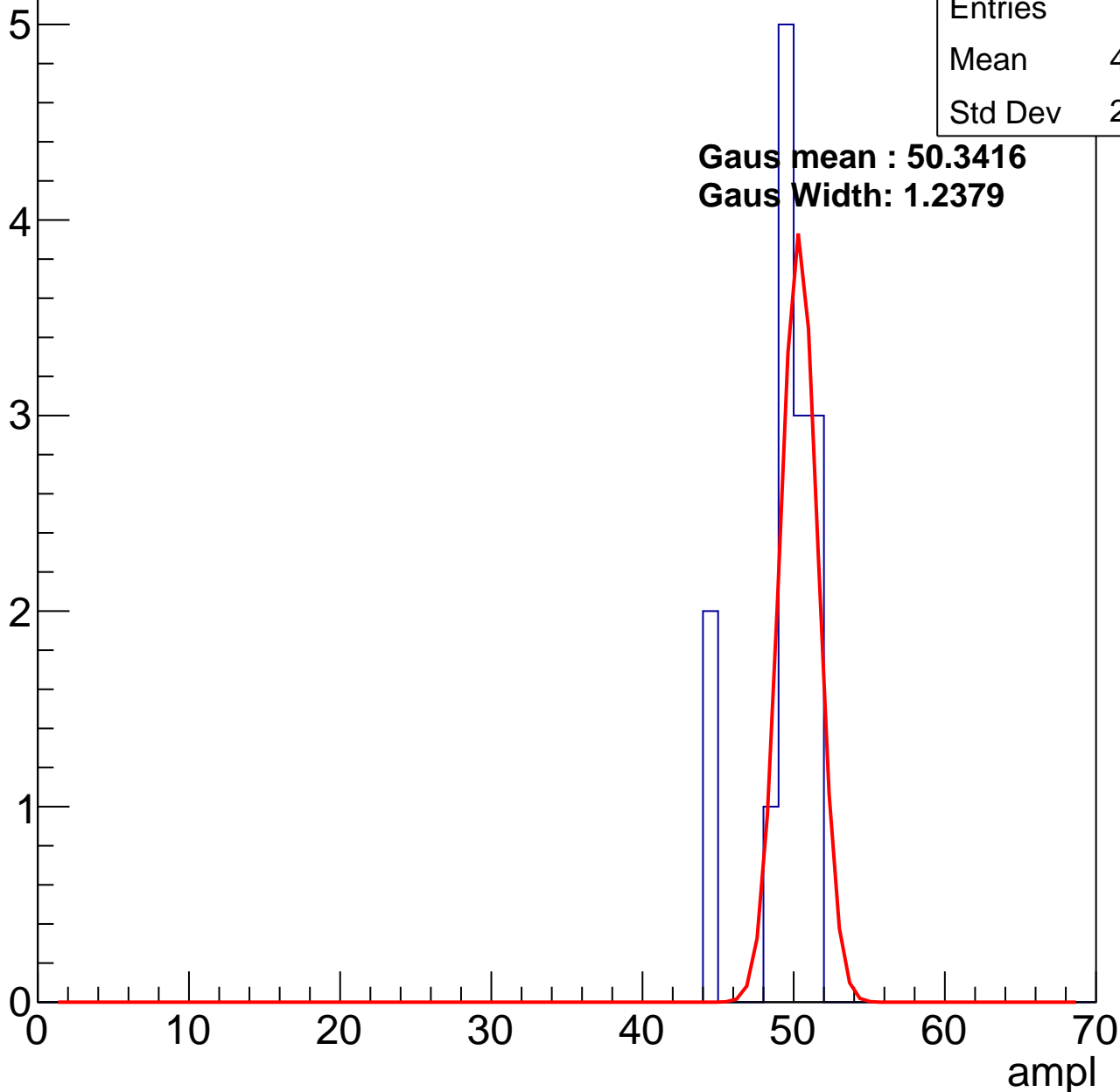
B0L100S, U13-ch38, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	49
Std Dev	1

ampl
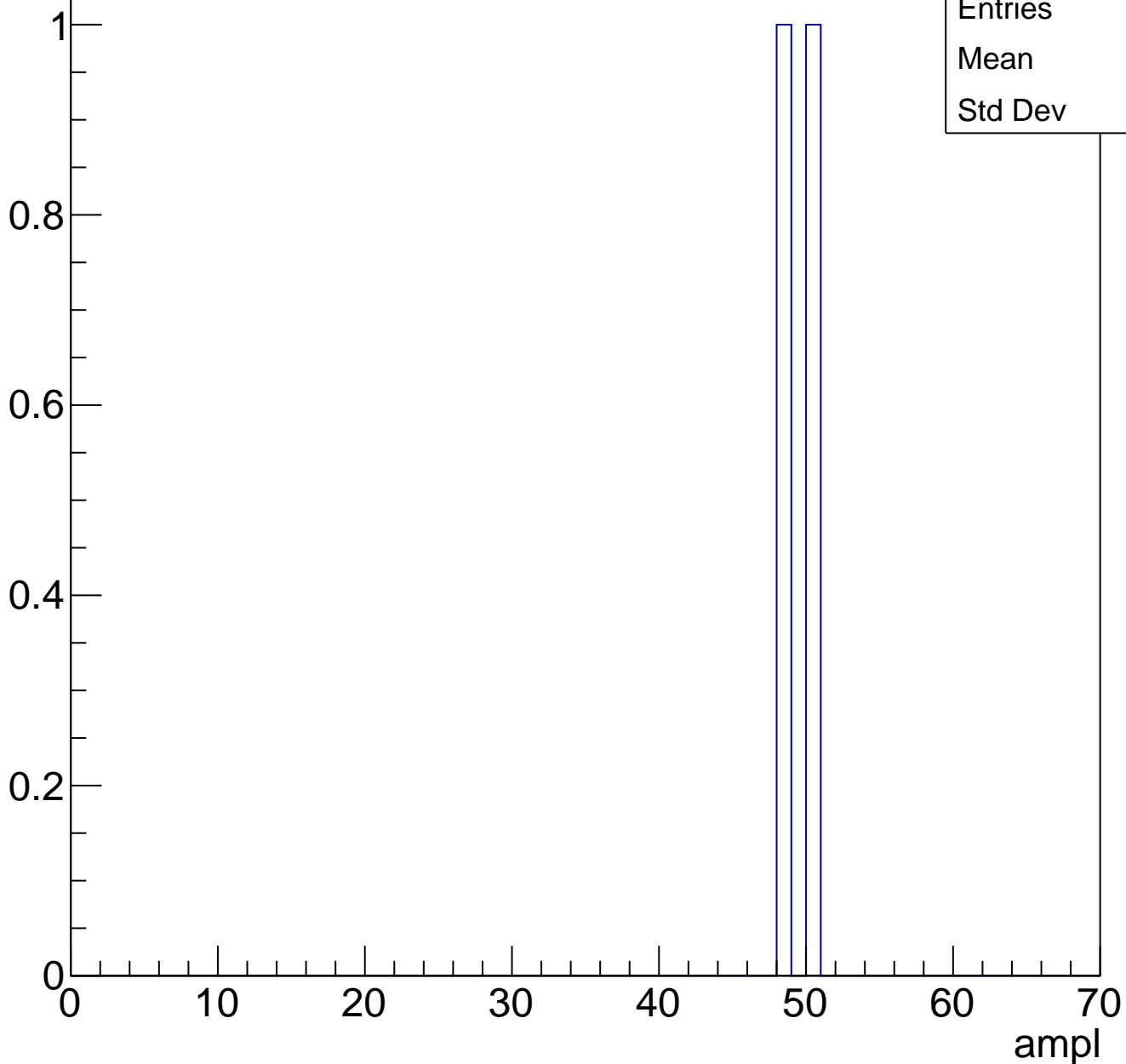
B0L100S, U13-ch38, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch38, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch38, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch38, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch39, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch39, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch39, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch39, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch39, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch39, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch39, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch39, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch40, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch40, adc1

calib_packv5_042523_0143.root, FC#6, port A1
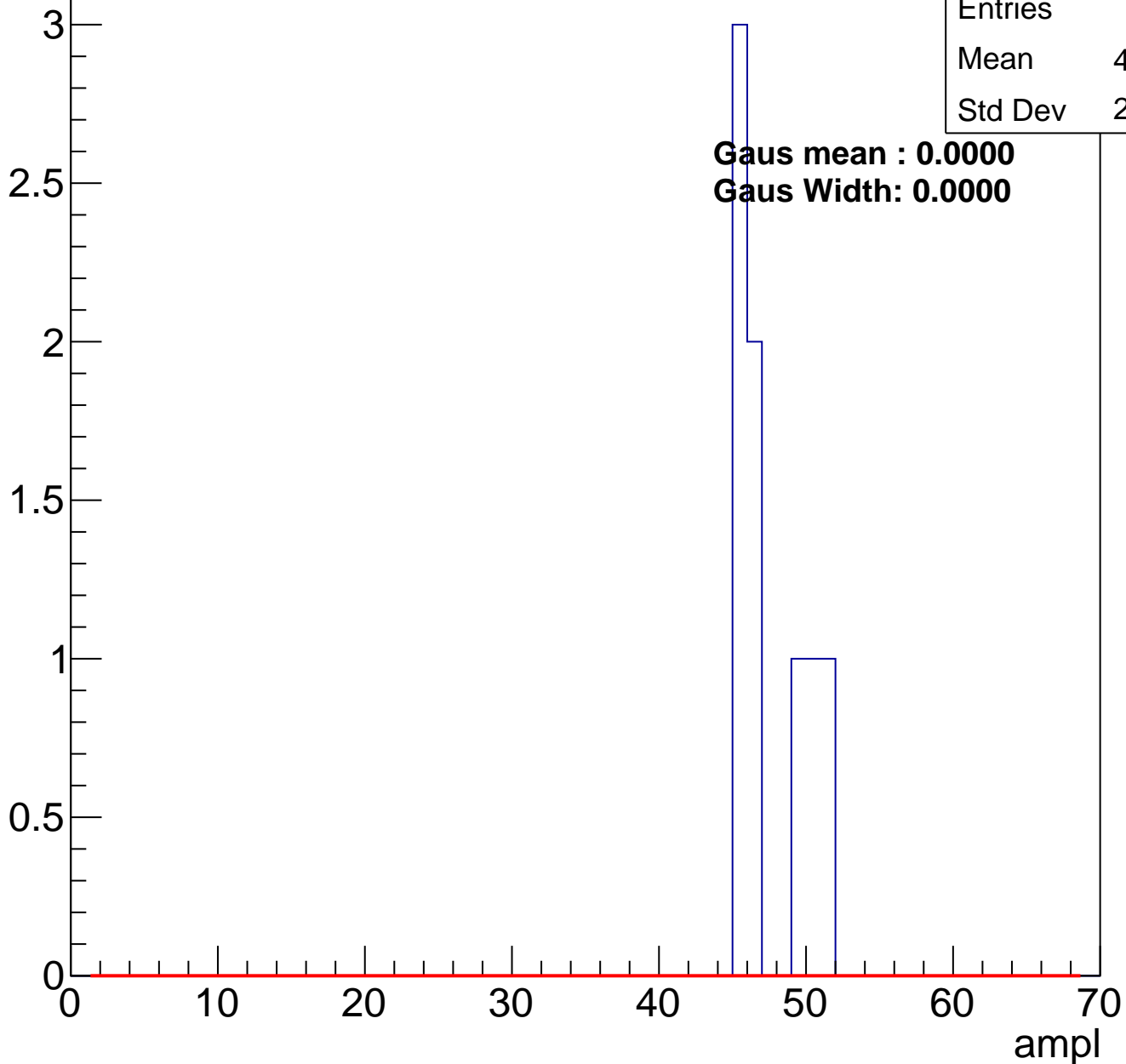
Entries	8
Mean	47.12
Std Dev	2.315

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch40, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	50.93
Std Dev	2.344

Gaus mean : 51.7634
Gaus Width: 4.1215
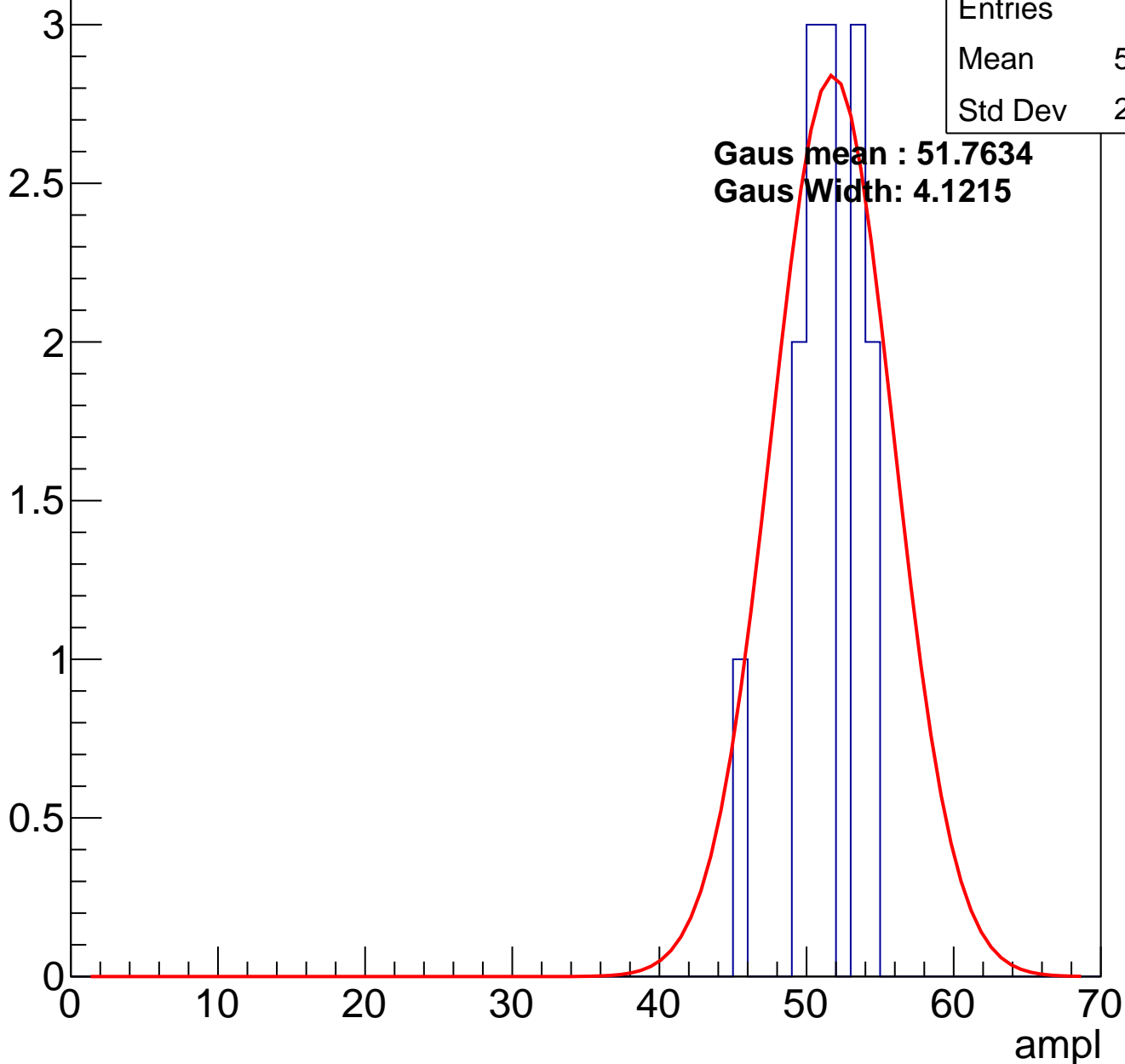
B0L100S, U13-ch40, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	13
Mean	53.15
Std Dev	1.35
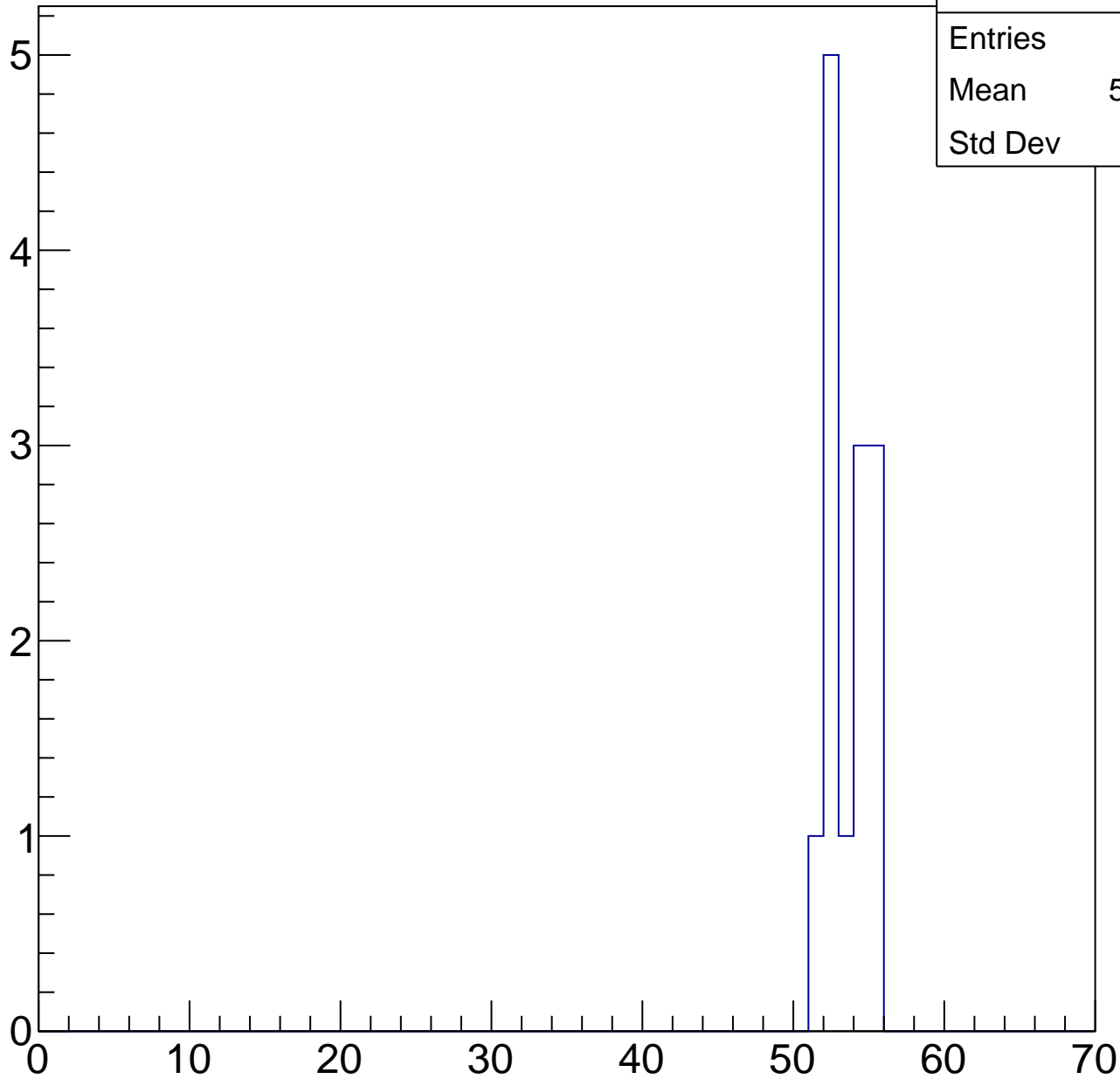
Entry

ampl

B0L100S, U13-ch40, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	2

Entry

ampl

B0L100S, U13-ch40, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch40, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch40, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch41, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch41, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch41, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch41, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch41, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch41, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch41, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch41, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch42, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch42, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch42, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch42, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch42, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch42, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch42, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch42, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch43, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch43, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch43, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch43, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch43, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch43, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch43, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch43, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch44, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch44, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch44, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch44, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch44, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch44, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch44, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch44, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch45, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch45, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch45, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch45, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch45, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch45, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch45, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch45, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch46, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
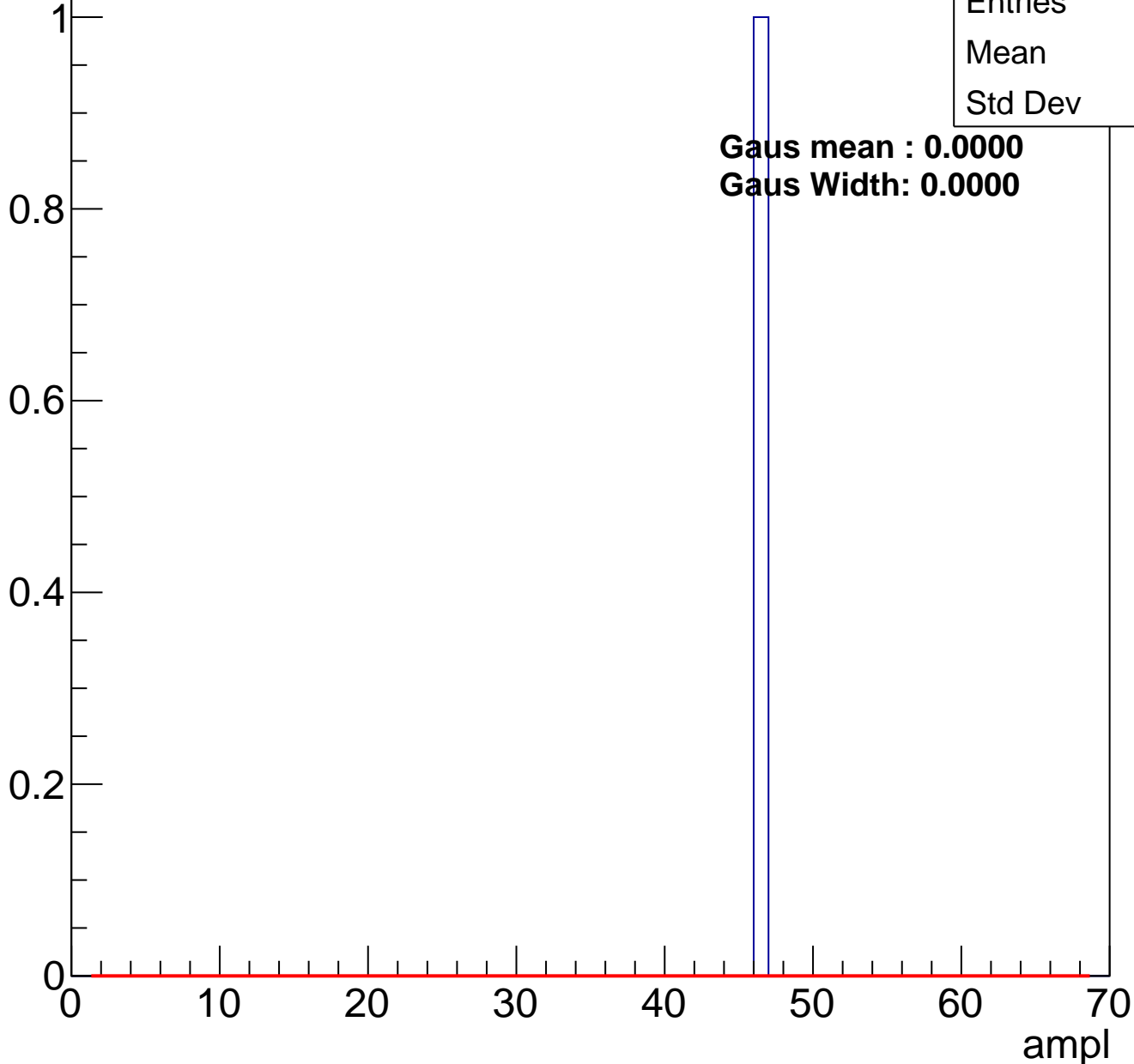
B0L100S, U13-ch46, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	45.86
Std Dev	0.833

Gaus mean : 0.0000
Gaus Width: 0.0000
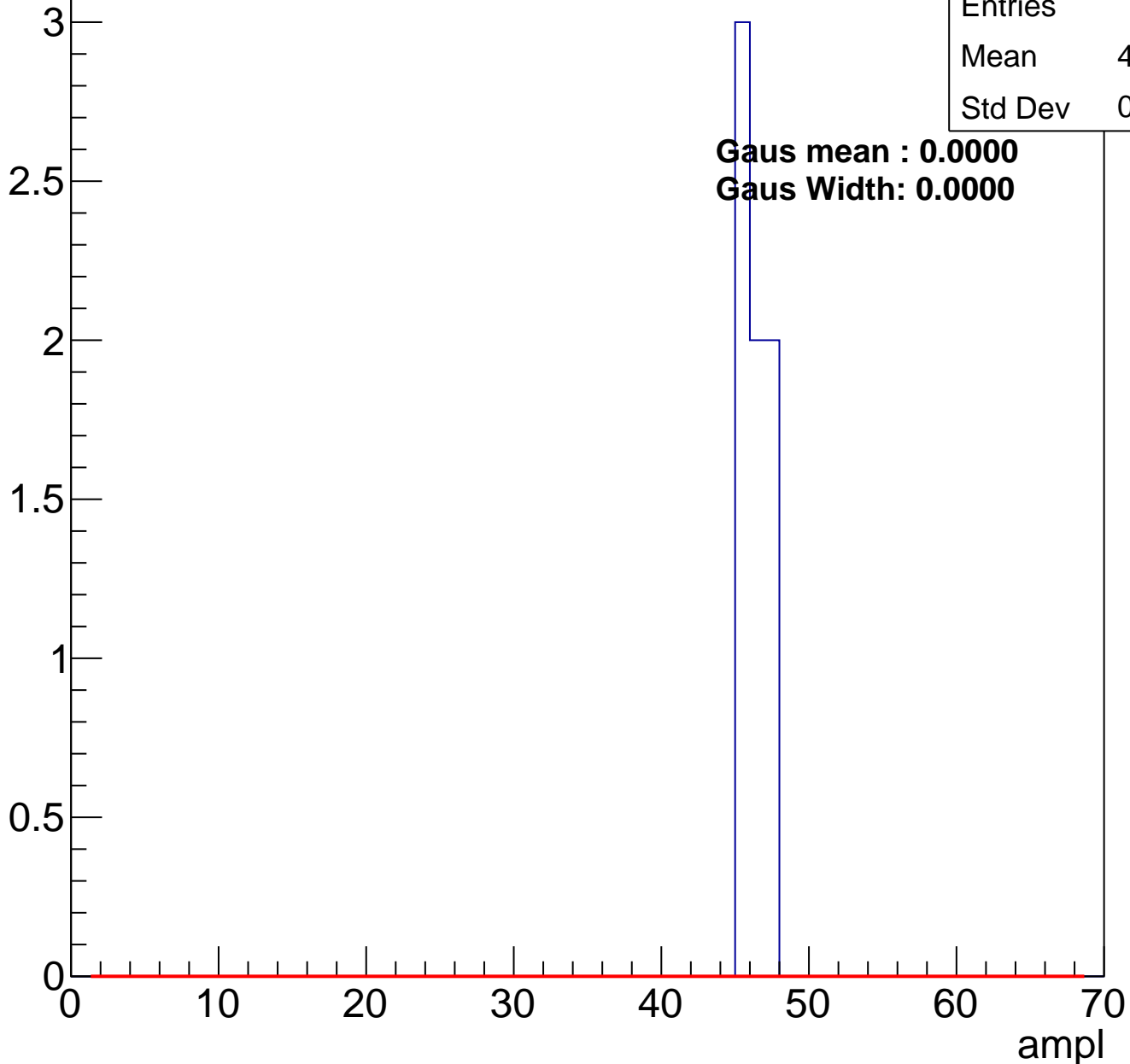
Entry

ampl

B0L100S, U13-ch46, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	46
Std Dev	0.8165

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
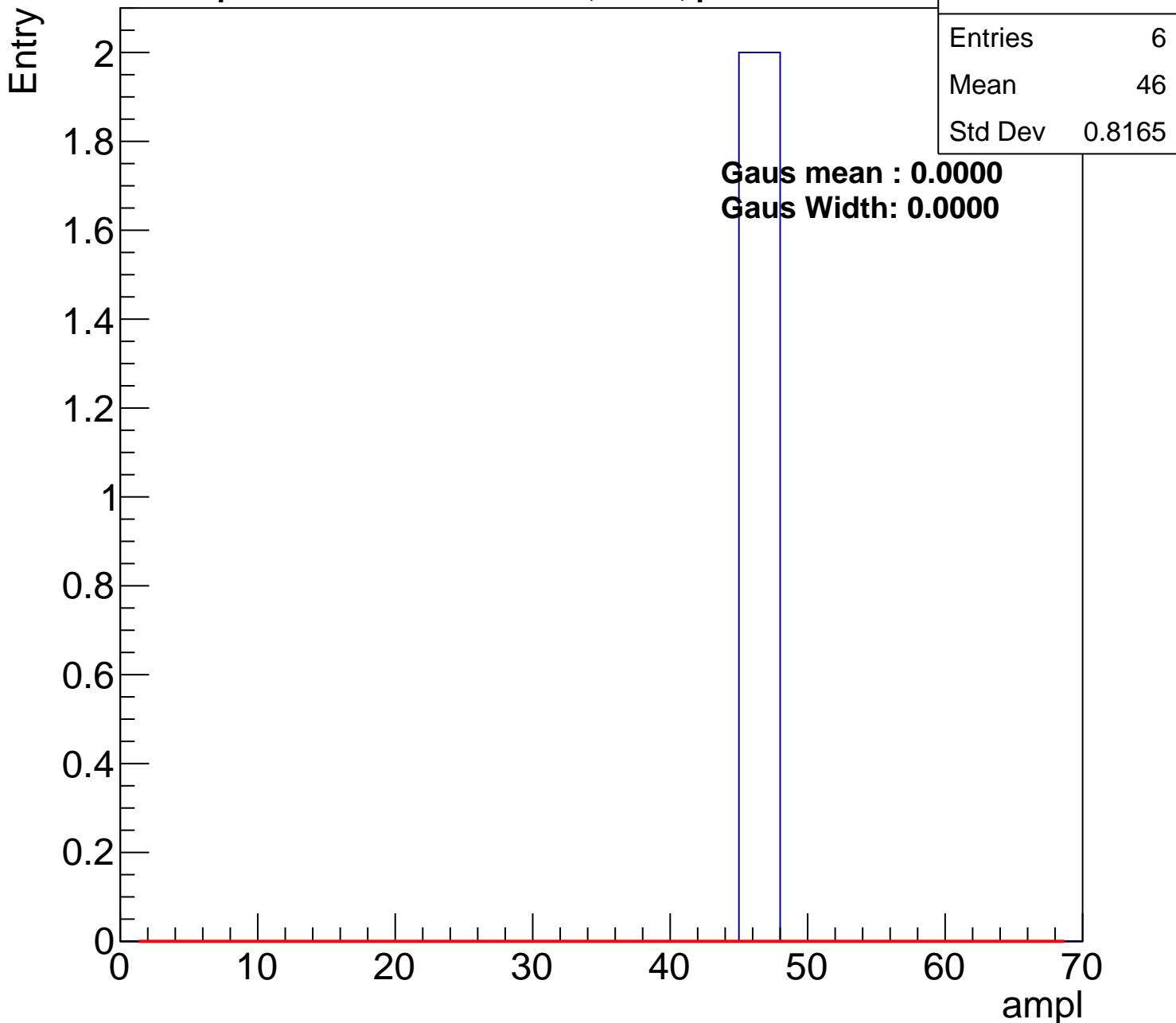
B0L100S, U13-ch46, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch46, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch46, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch46, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch46, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch47, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	5
Mean	44.2
Std Dev	3.37

Entry

ampl
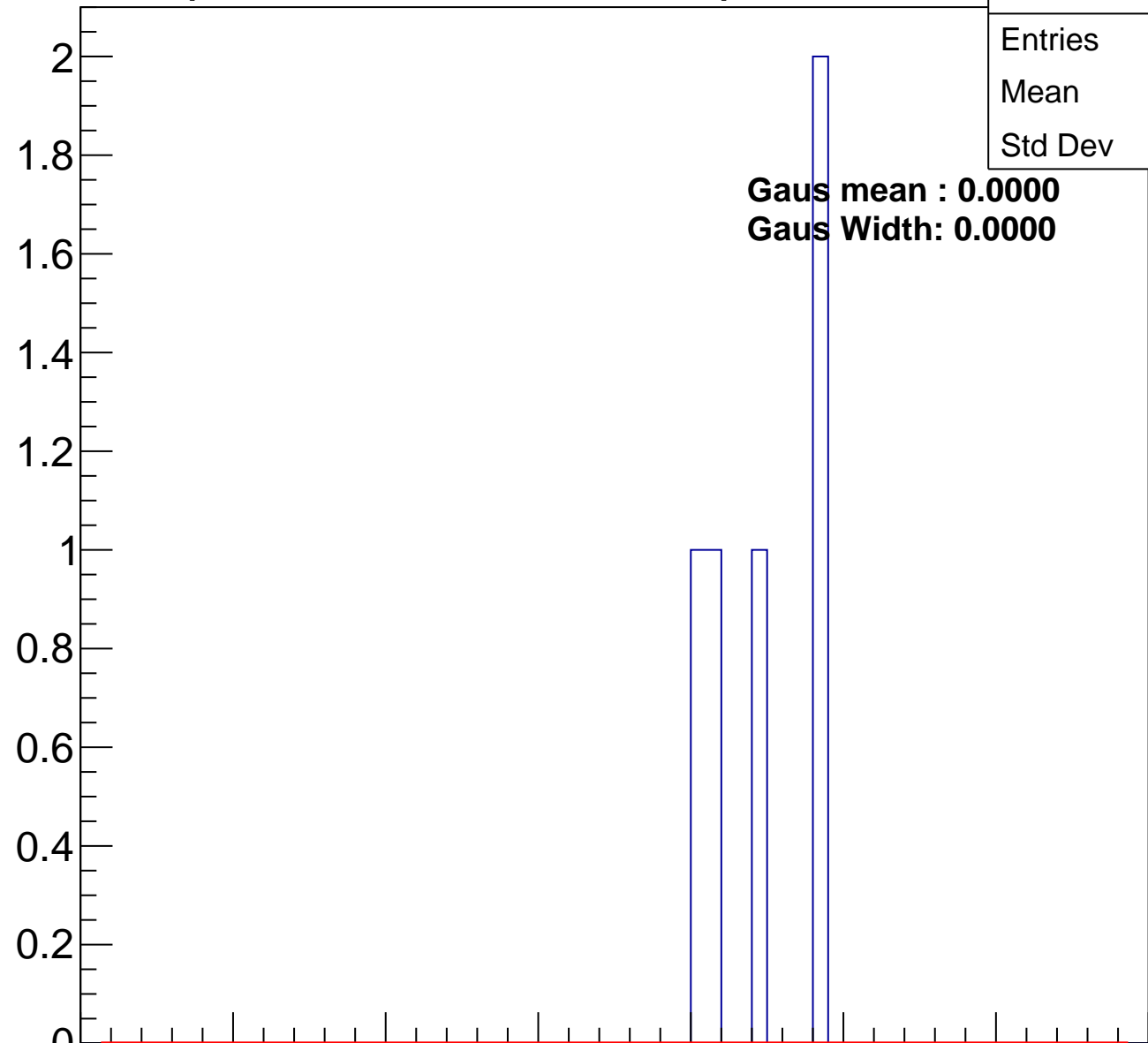
B0L100S, U13-ch47, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	20
Mean	43.95
Std Dev	2.459

Gaus mean : -26.3510
Gaus Width: 20.5573

Entry

ampl
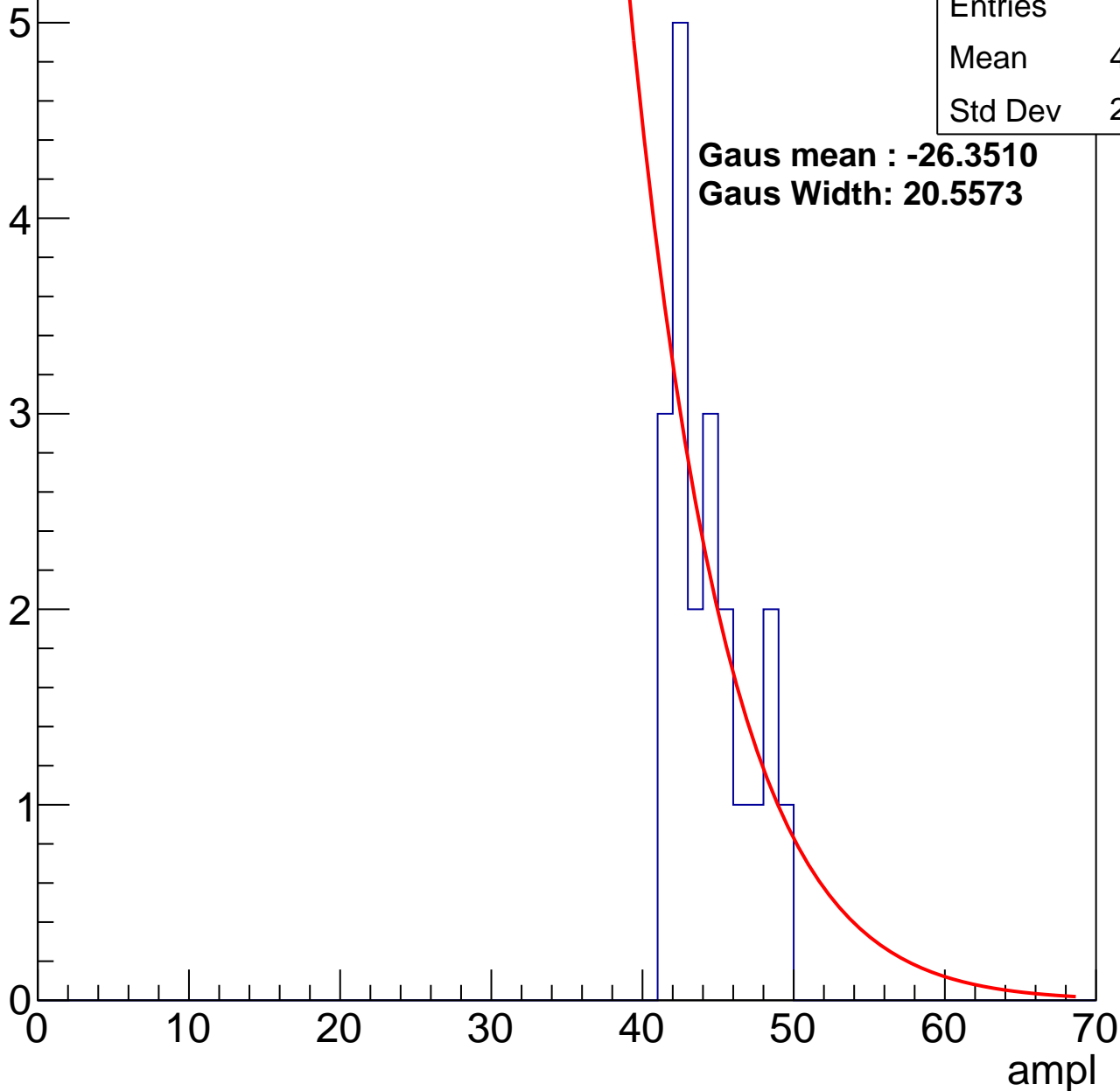
B0L100S, U13-ch47, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	28
Mean	47.54
Std Dev	3.19

Gaus mean : 50.5493
Gaus Width: 7.3999
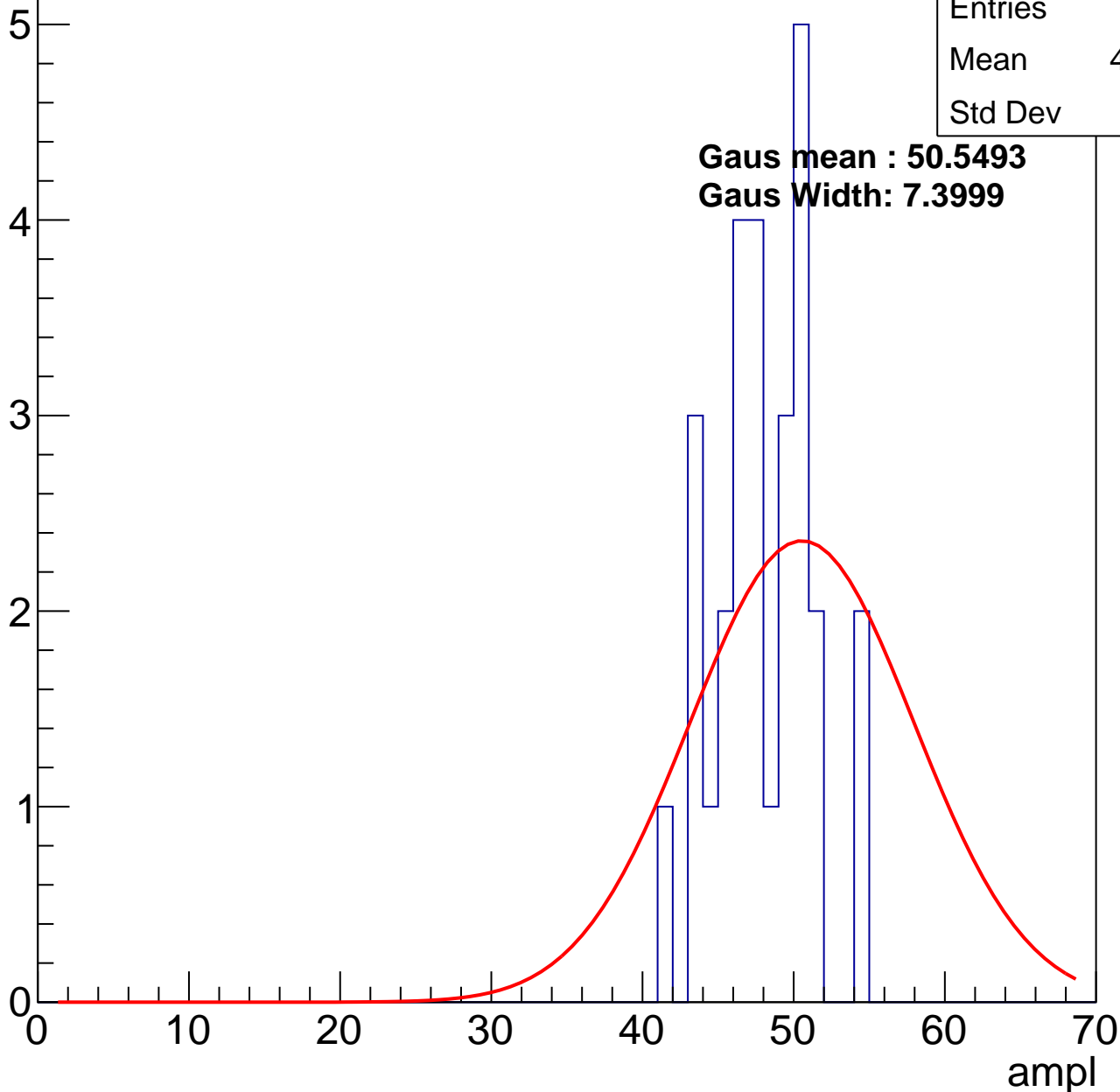
B0L100S, U13-ch47, adc3
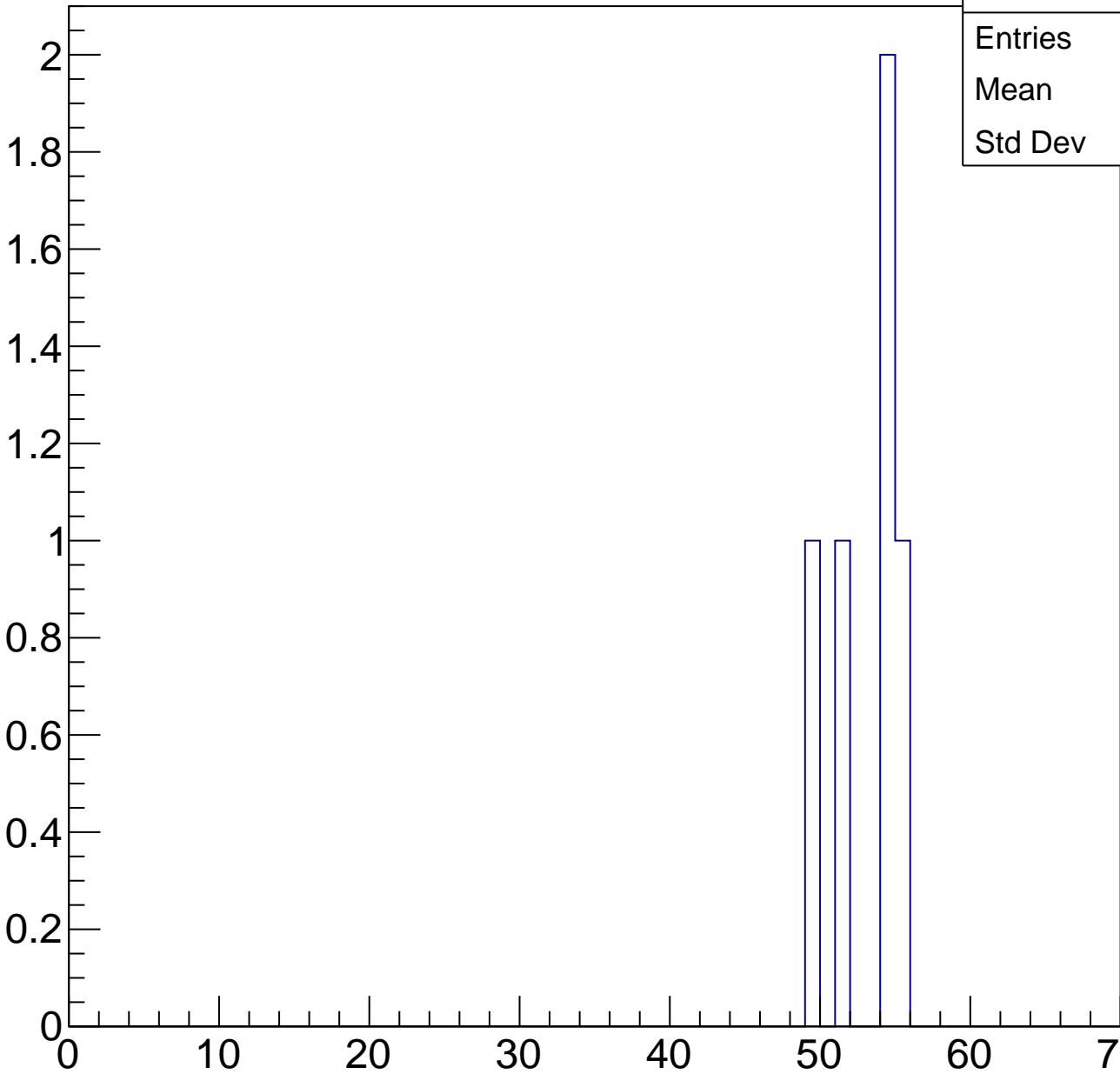
calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.6
Std Dev	2.245

ampl

Entry

B0L100S, U13-ch47, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch47, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch47, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch47, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch48, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch48, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch48, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch48, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch48, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch48, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch48, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch48, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch49, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch49, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch49, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch49, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Entry

ampl
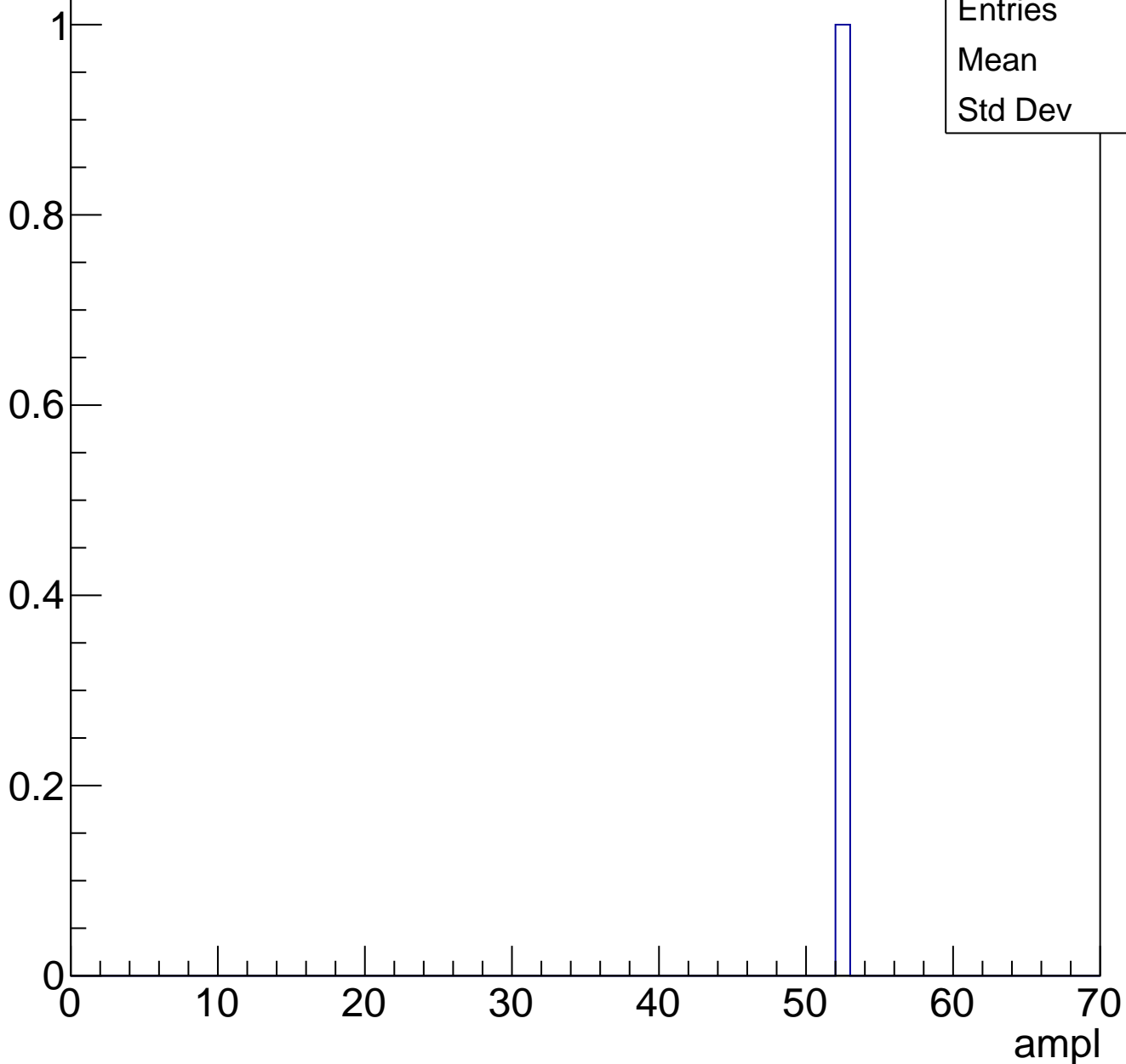
B0L100S, U13-ch49, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch49, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch49, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch49, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch50, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
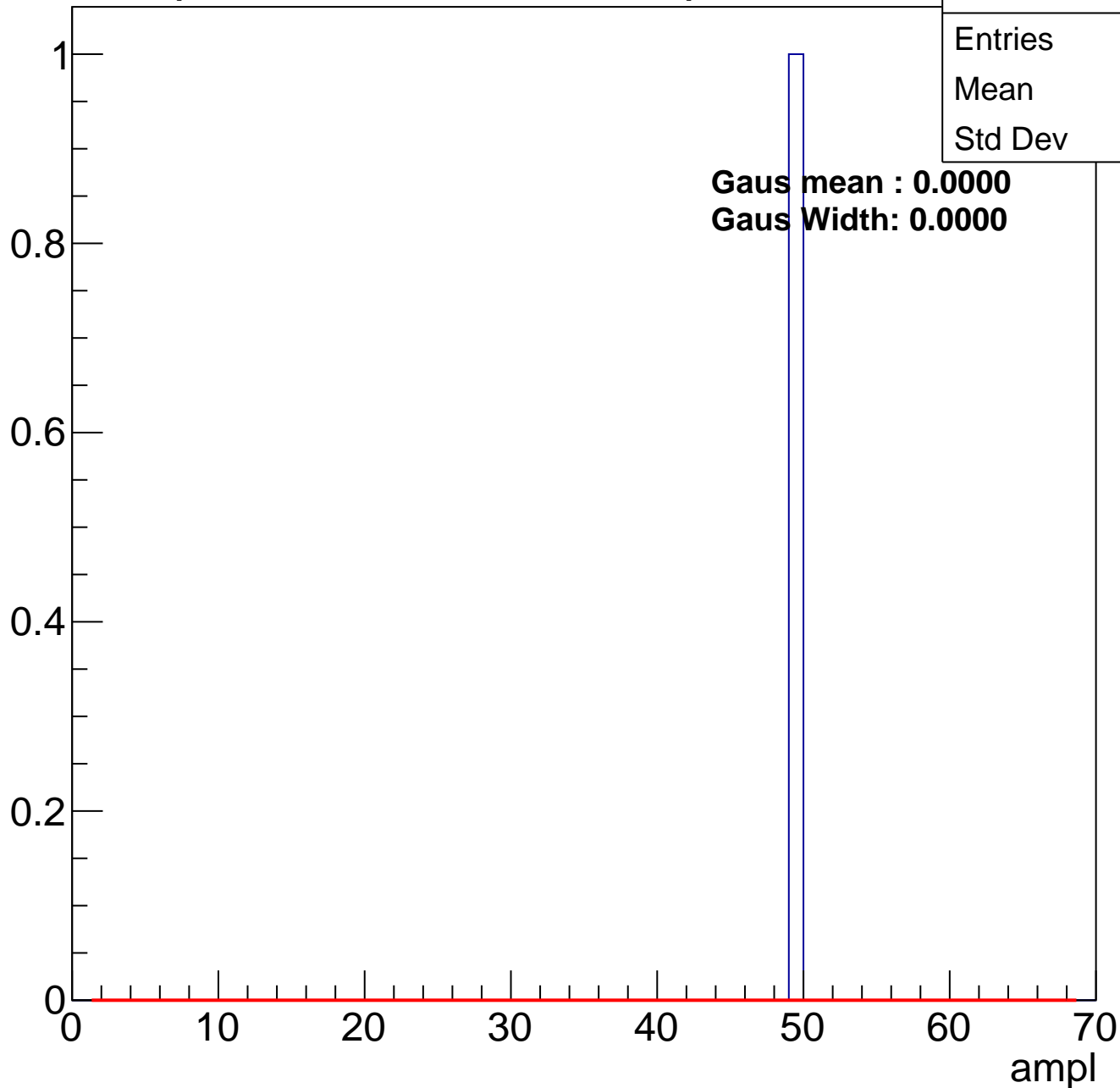
B0L100S, U13-ch50, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch50, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	45
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
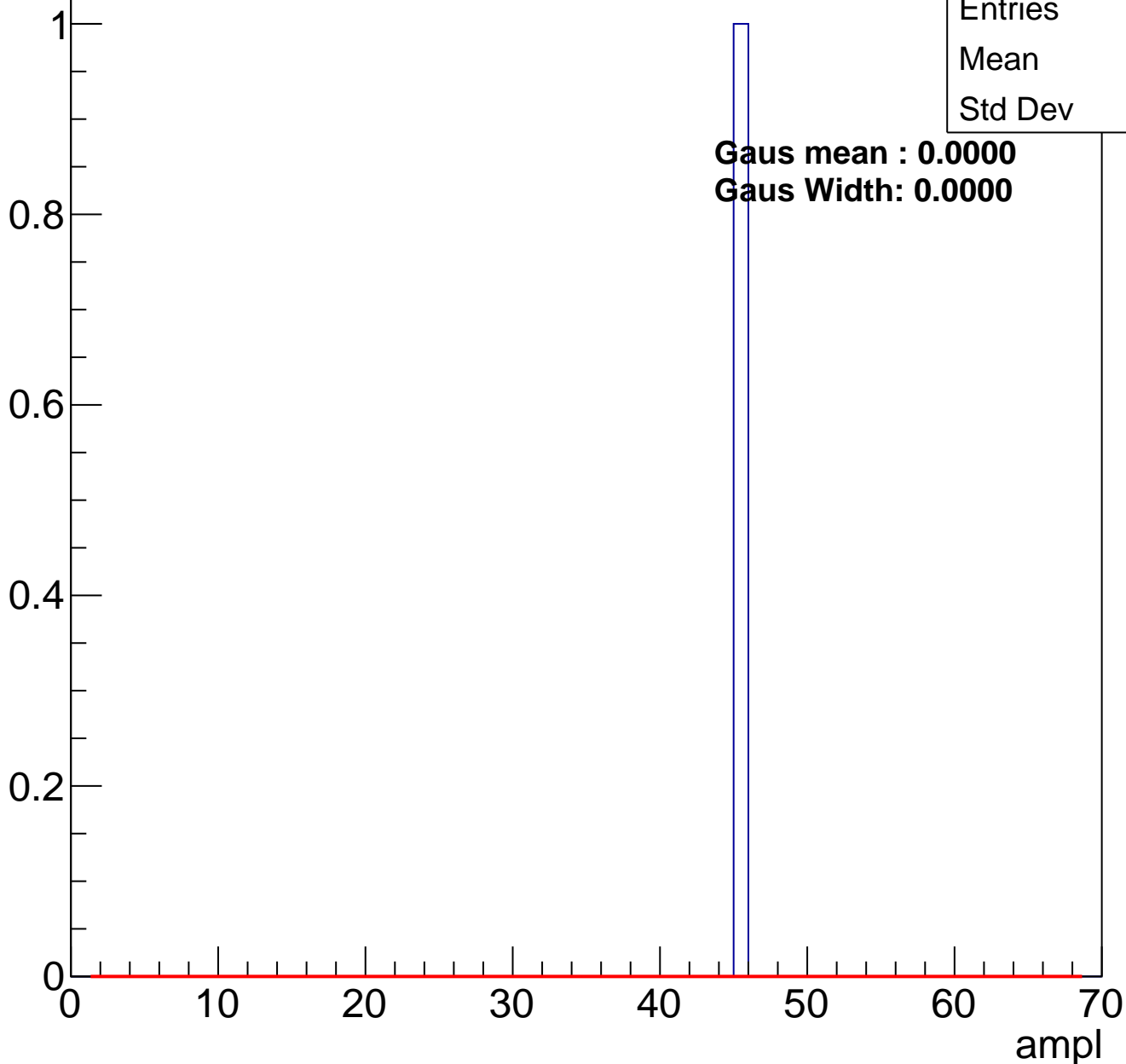
B0L100S, U13-ch50, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch50, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch50, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch50, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch50, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch51, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
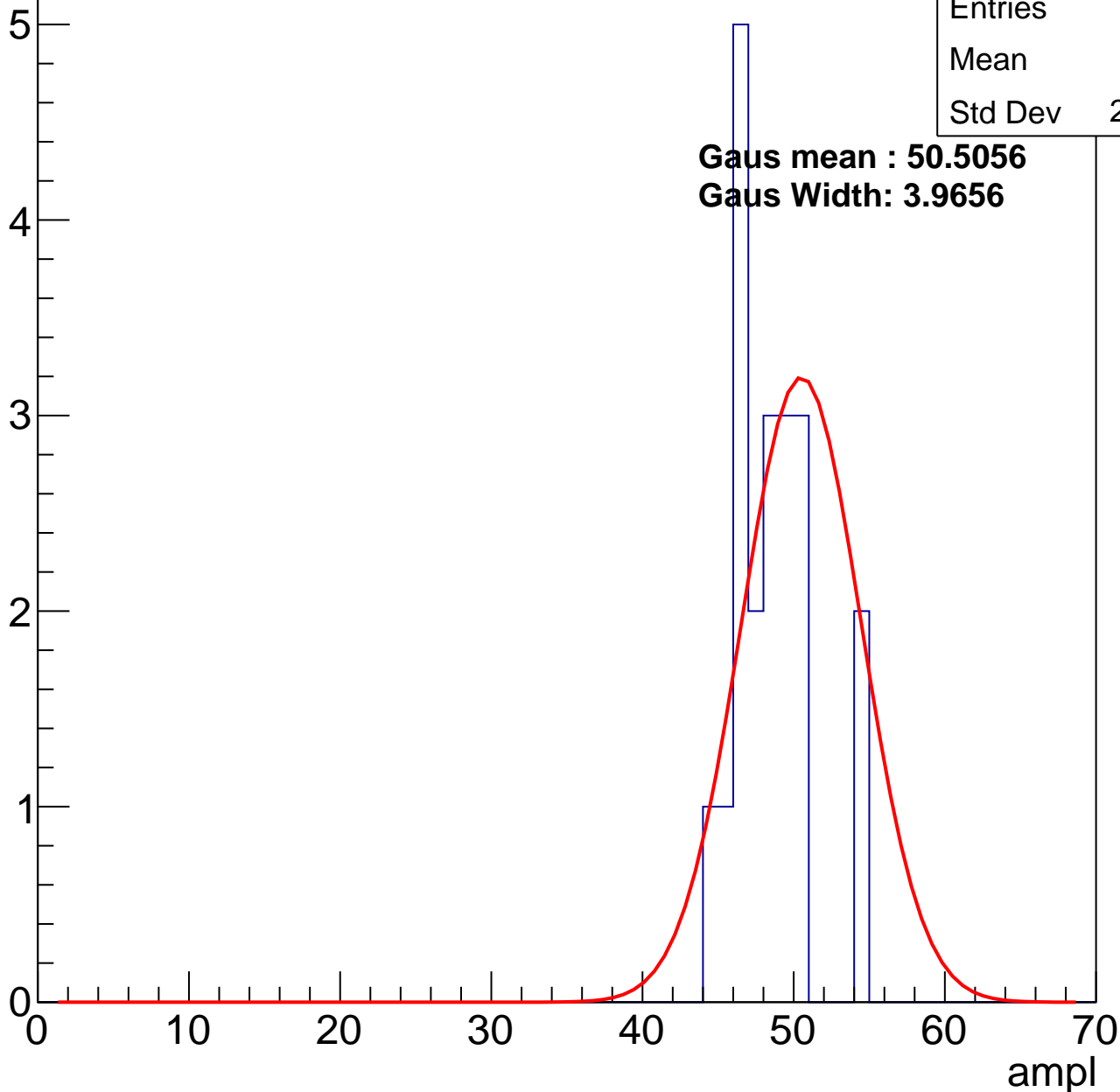**Gaus Width: 0.0000**

Entry

ampl
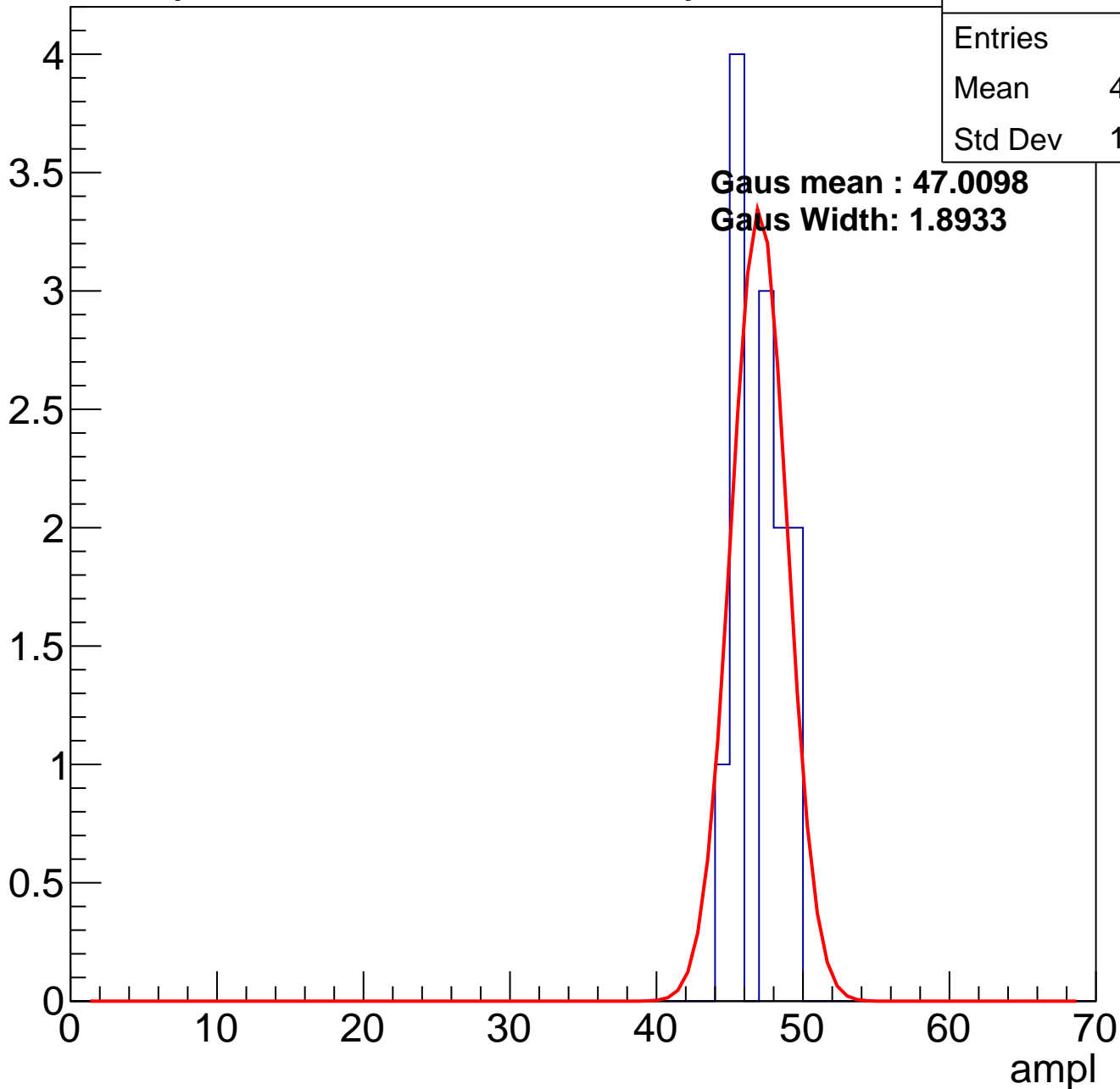
B0L100S, U13-ch51, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	20
Mean	48.1
Std Dev	2.587

Gaus mean : 50.5056
Gaus Width: 3.9656

B0L100S, U13-ch51, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	51
Std Dev	1.225

B0L100S, U13-ch51, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch51, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch51, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch51, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch52, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch52, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch52, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch52, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch52, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch52, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch52, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch52, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch53, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch53, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch53, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch53, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch53, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch53, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch53, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch53, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch54, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch54, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	42.36
Std Dev	1.288

Gaus mean : 36.9636
Gaus Width: 4.3022
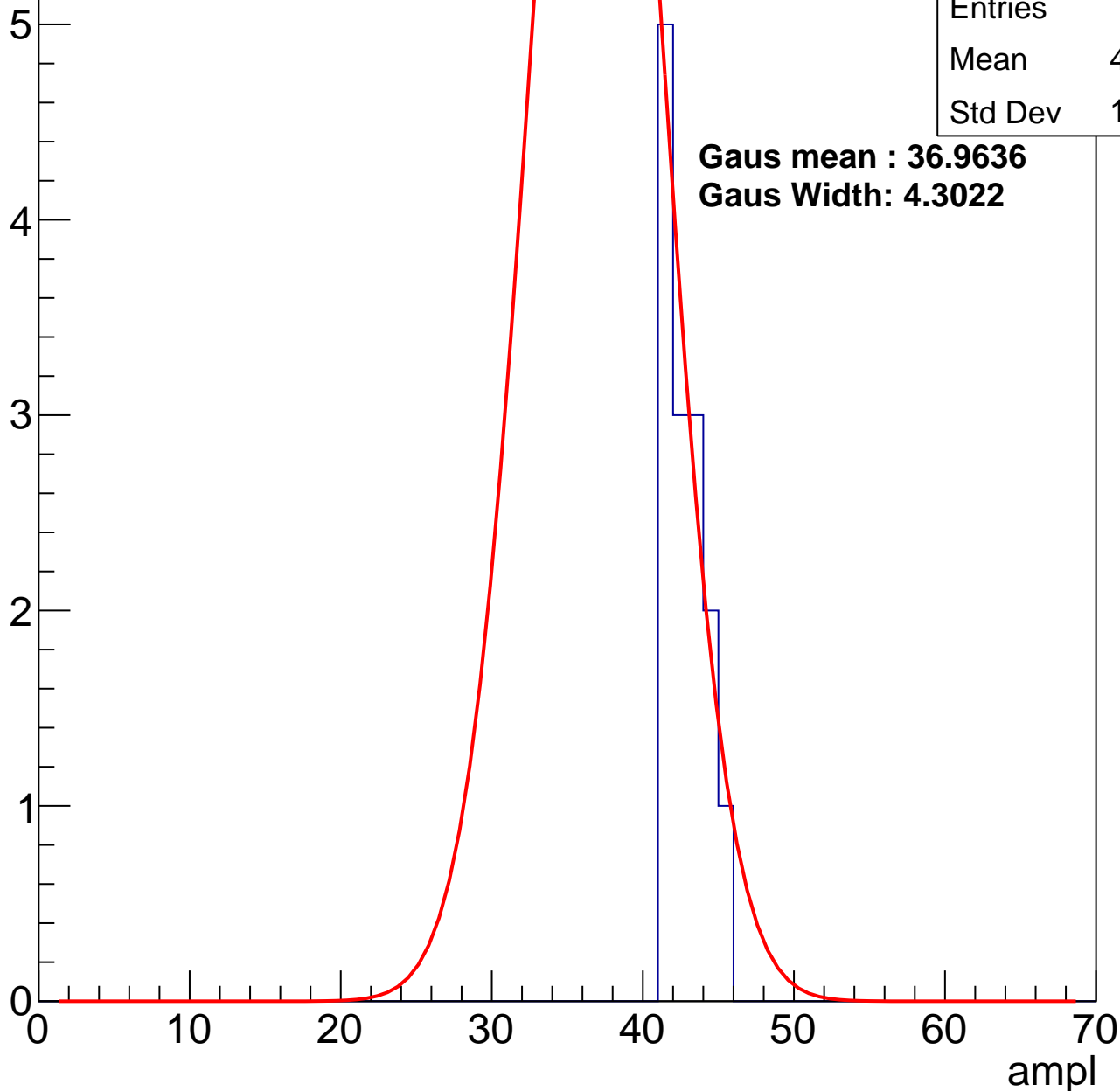
B0L100S, U13-ch54, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 48.2411
Gaus Width: 6.4558

Entries	17
Mean	47.12
Std Dev	3.74
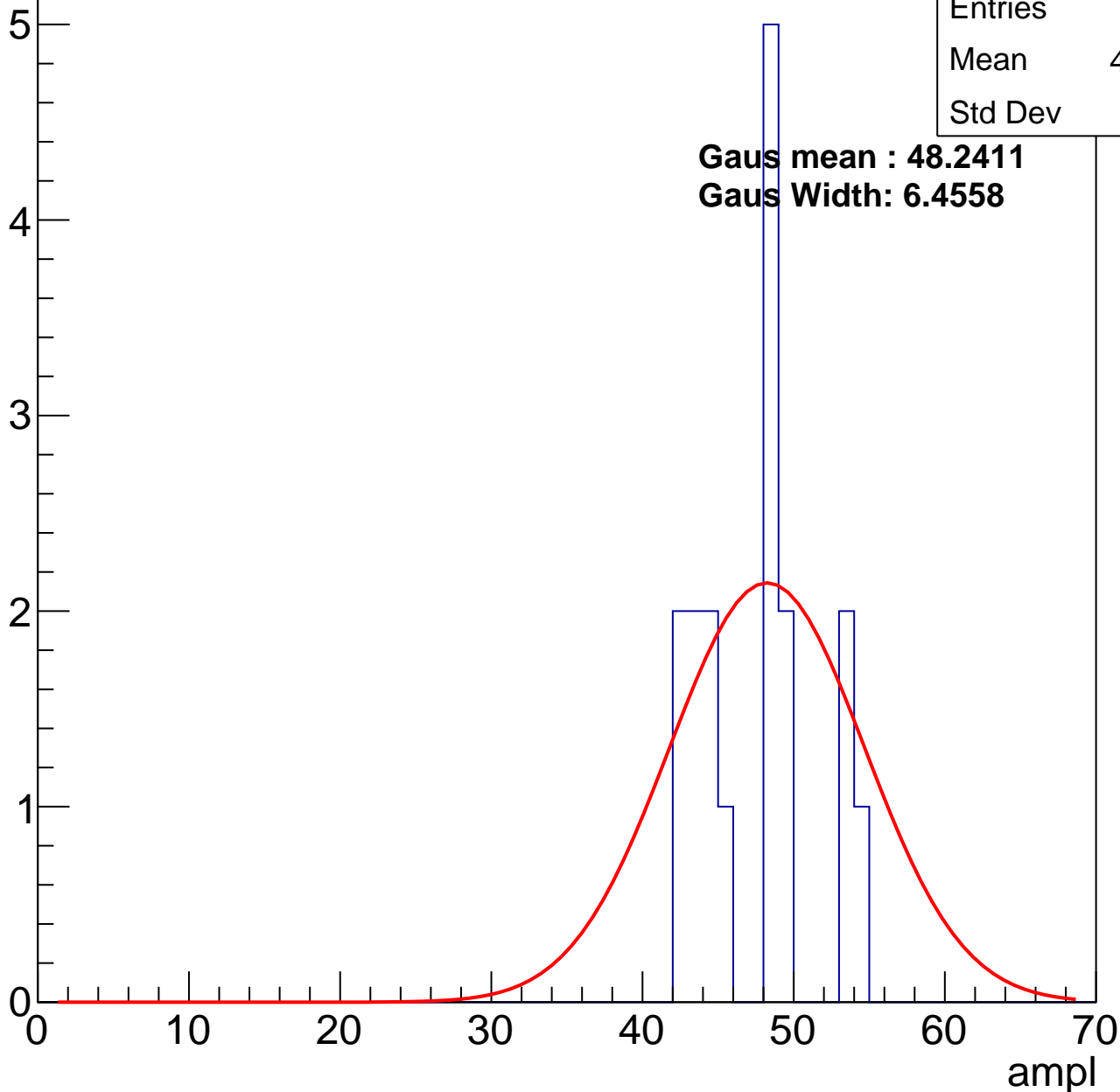
Entry

ampl

B0L100S, U13-ch54, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	53.33
Std Dev	1.491

ampl
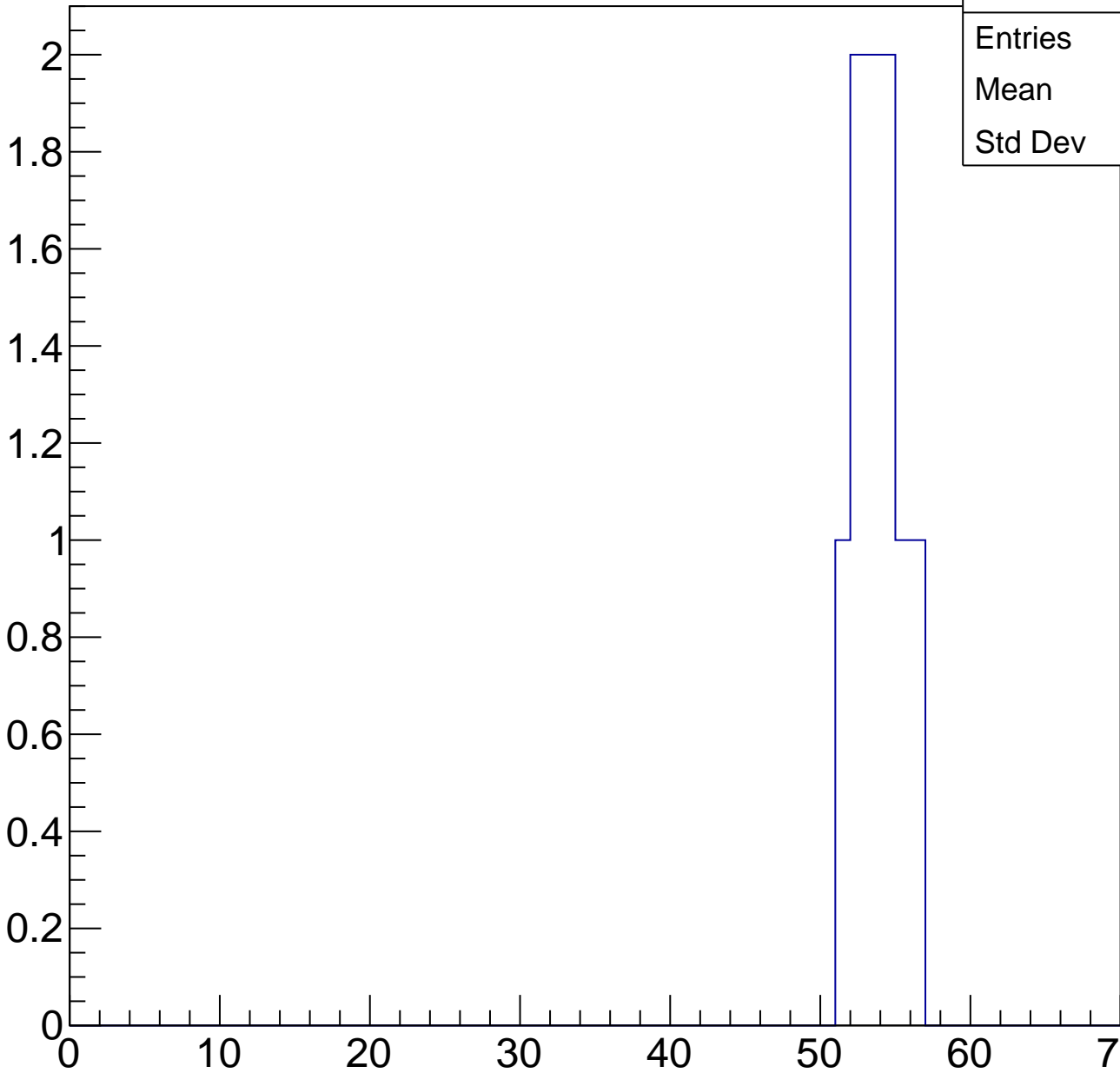
B0L100S, U13-ch54, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch54, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch54, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch54, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch55, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch55, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch55, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
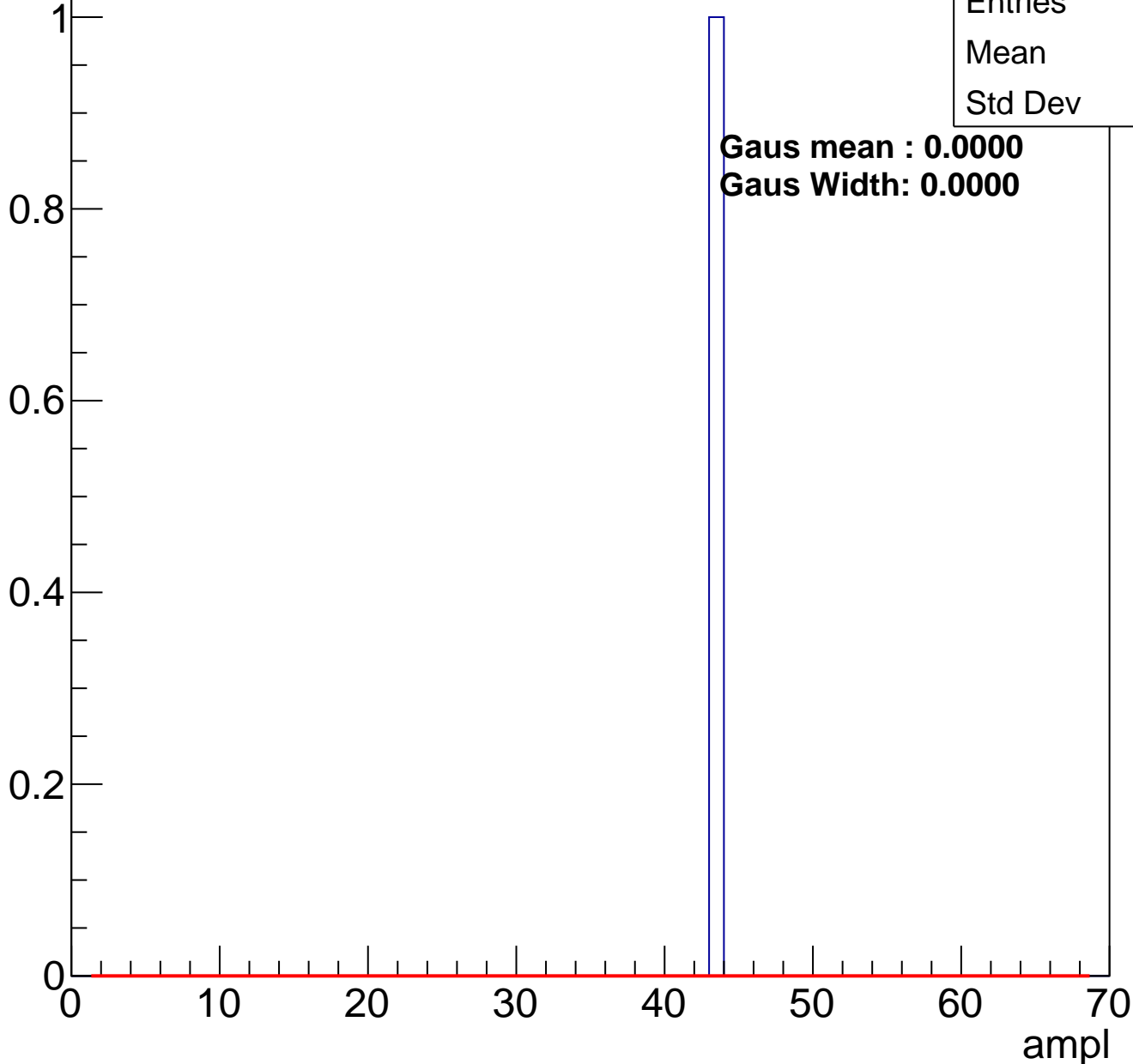
B0L100S, U13-ch55, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch55, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch55, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch55, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch55, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch56, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch56, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0
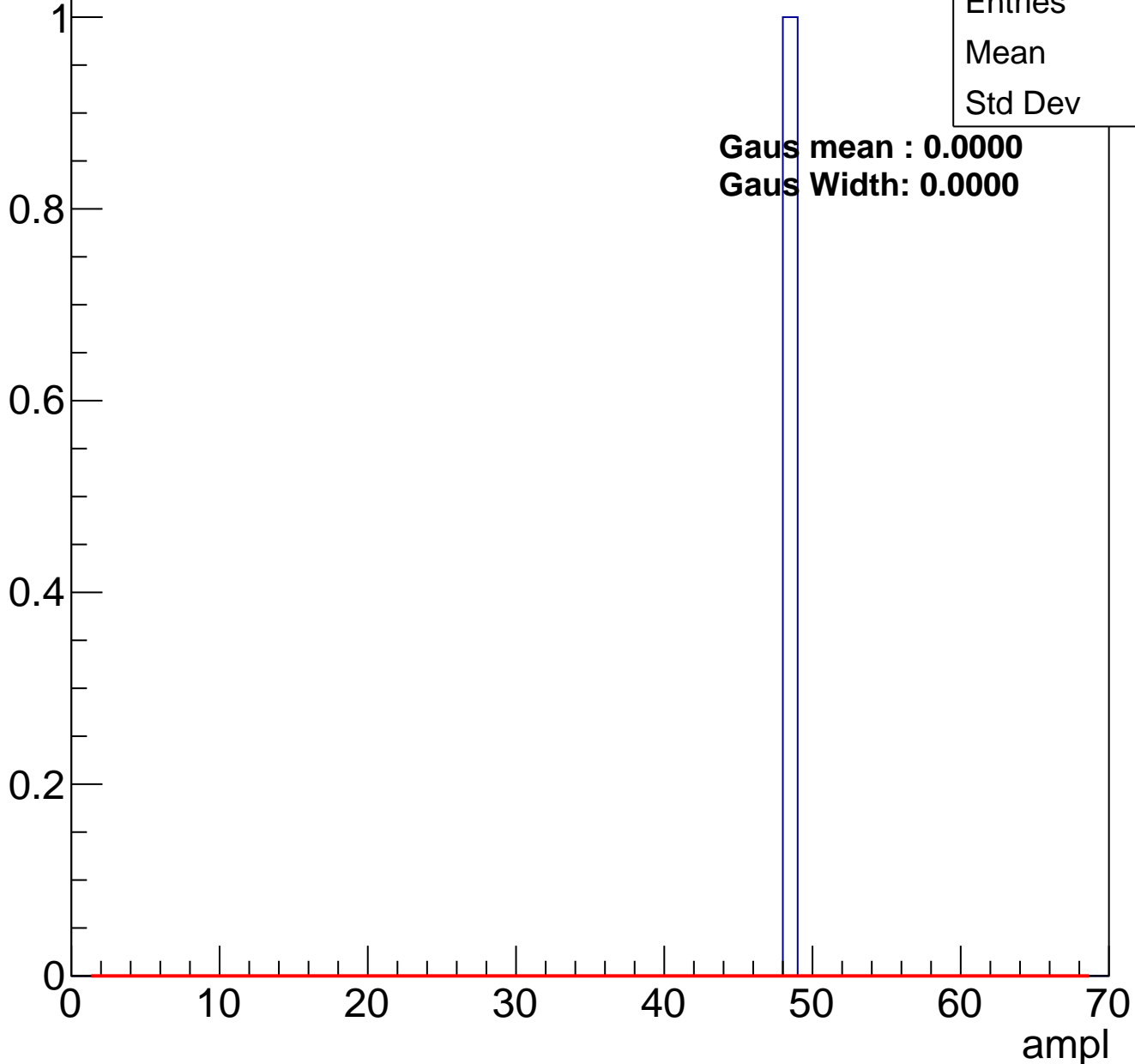
Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch56, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
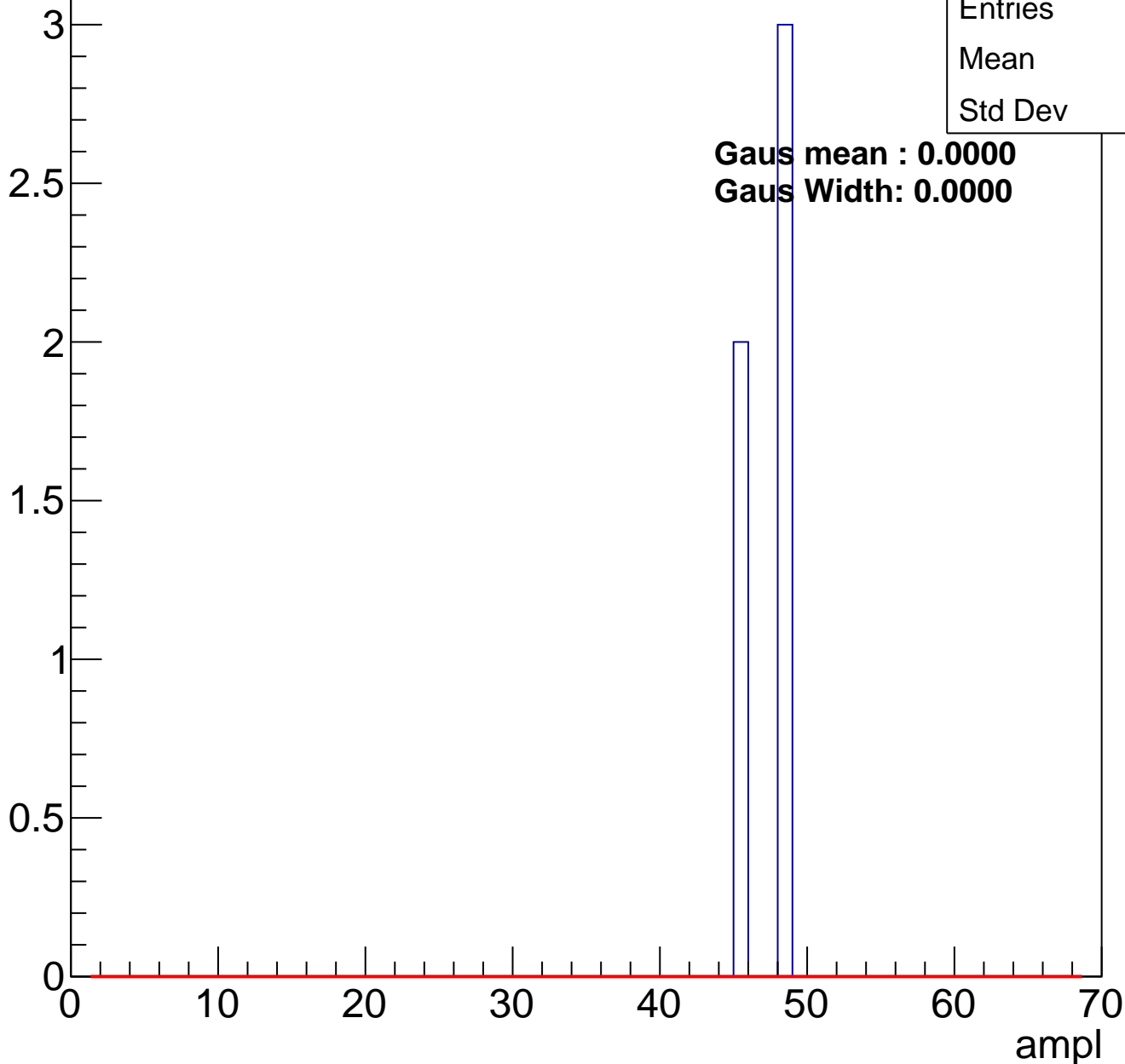
Entries	5
Mean	46.8
Std Dev	1.47

Entry

ampl

B0L100S, U13-ch56, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Entry

ampl
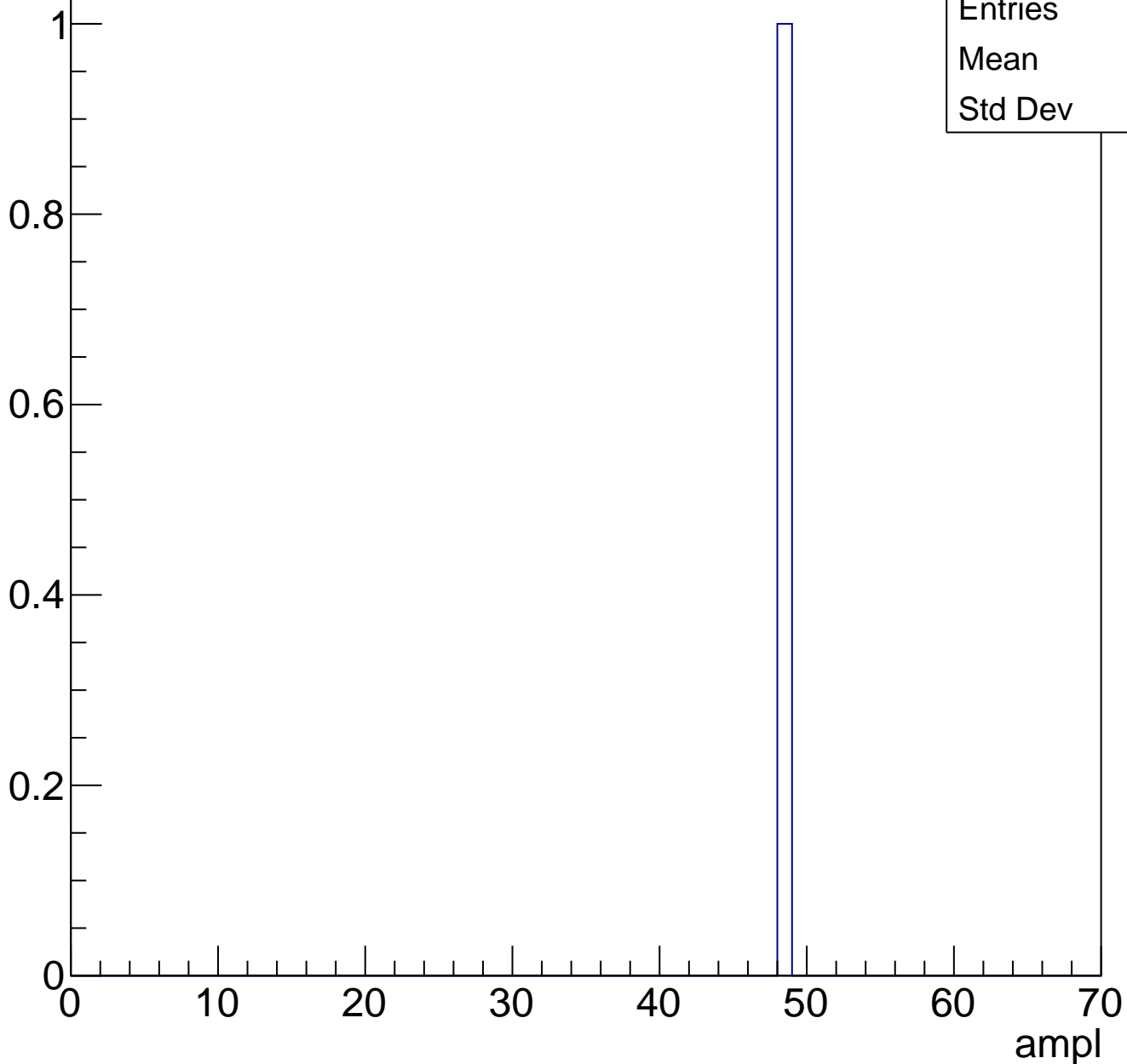
B0L100S, U13-ch56, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch56, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch56, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch56, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch57, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch57, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0
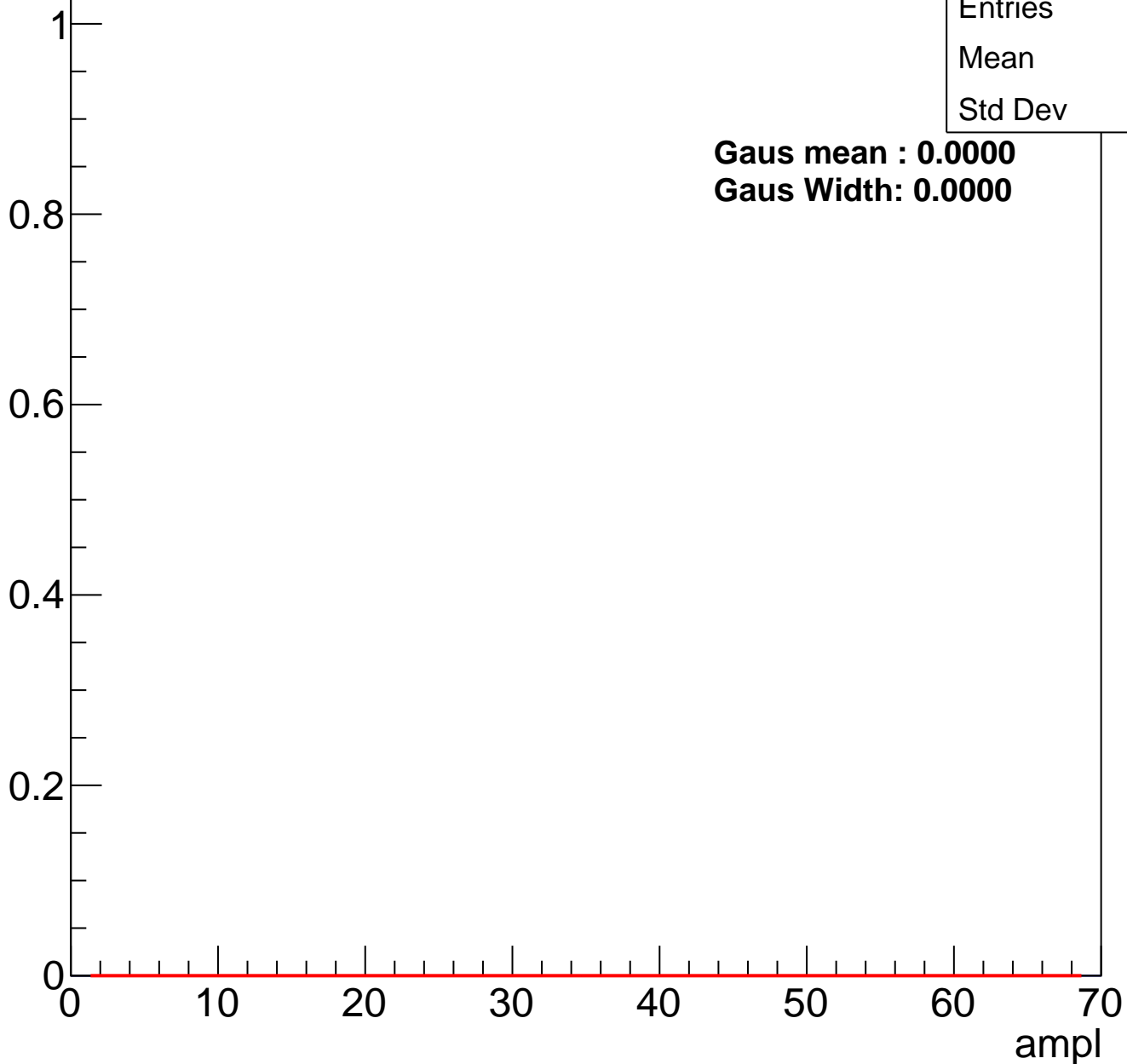
Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch57, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
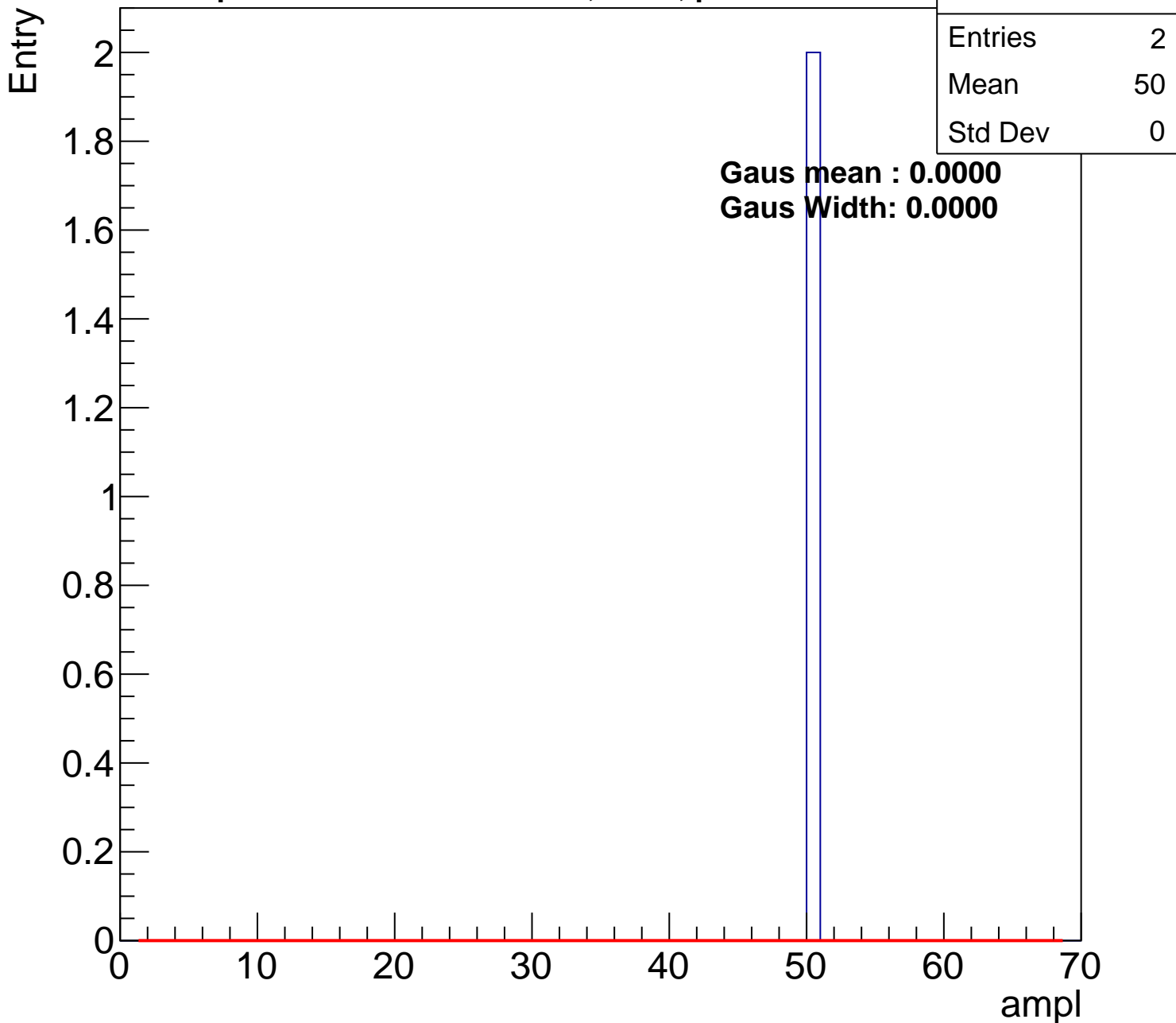
B0L100S, U13-ch57, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch57, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch57, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch57, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch57, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch58, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch58, adc1

calib_packv5_042523_0143.root, FC#6, port A1
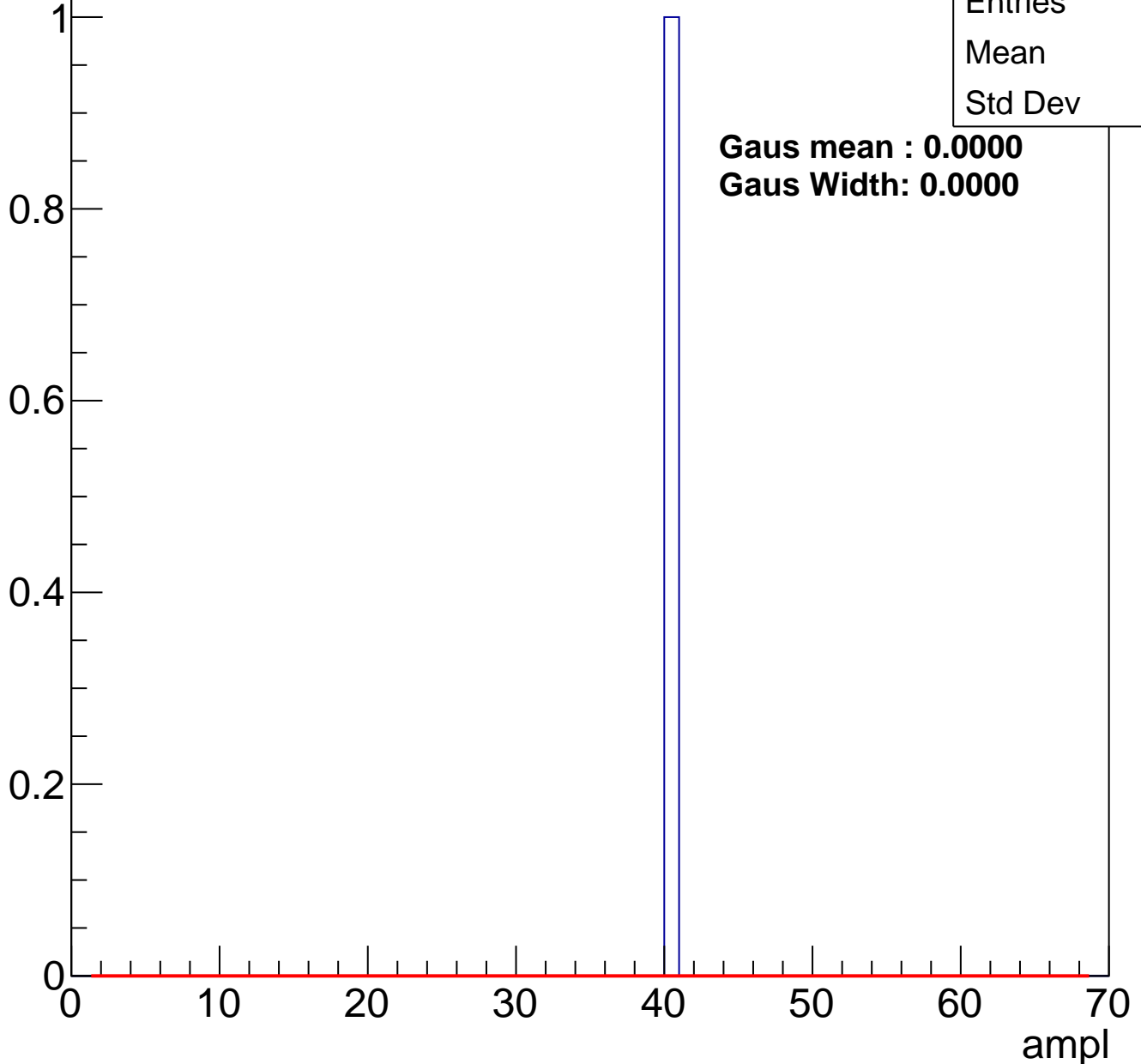
Entries	1
Mean	40
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch58, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
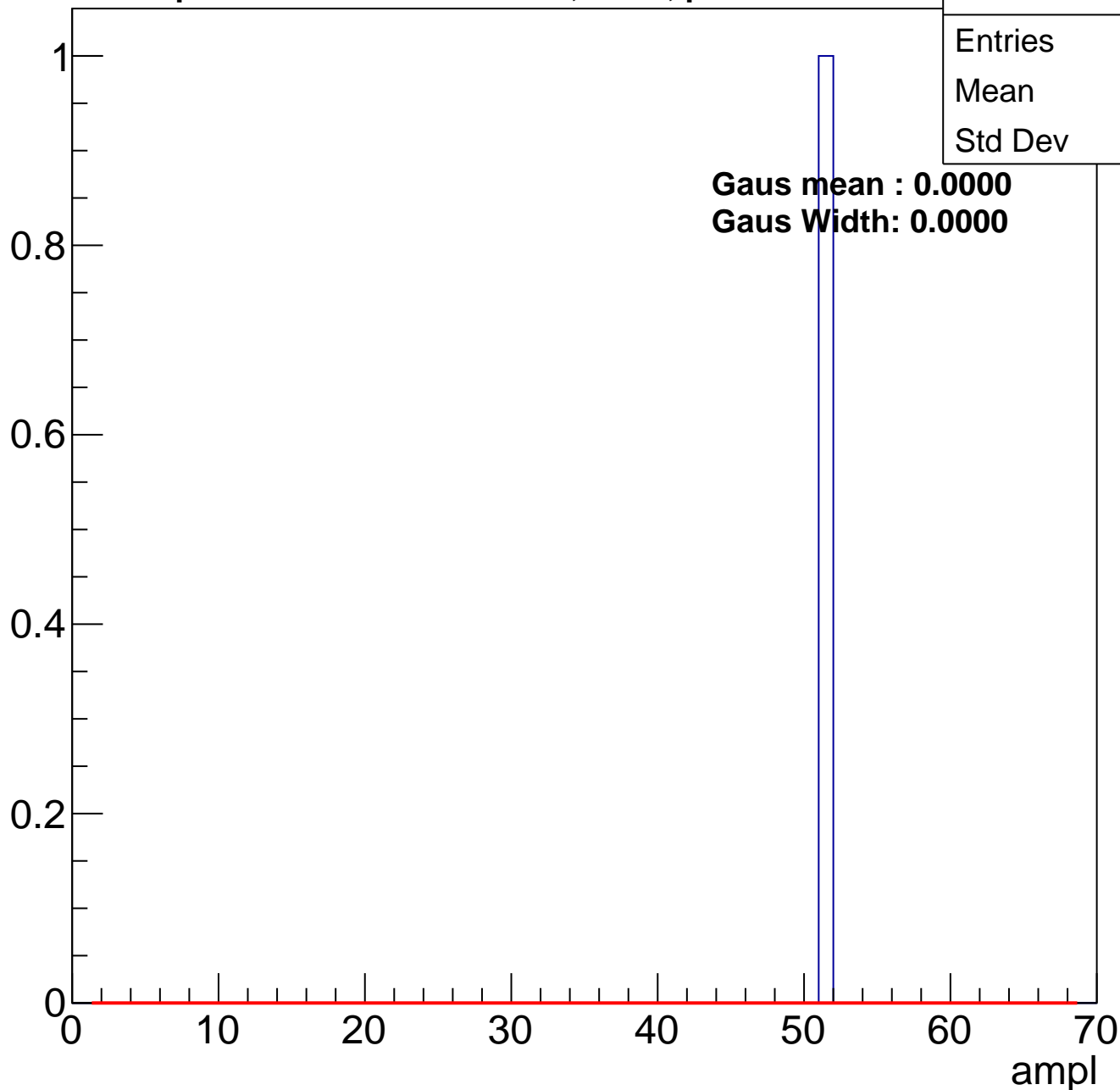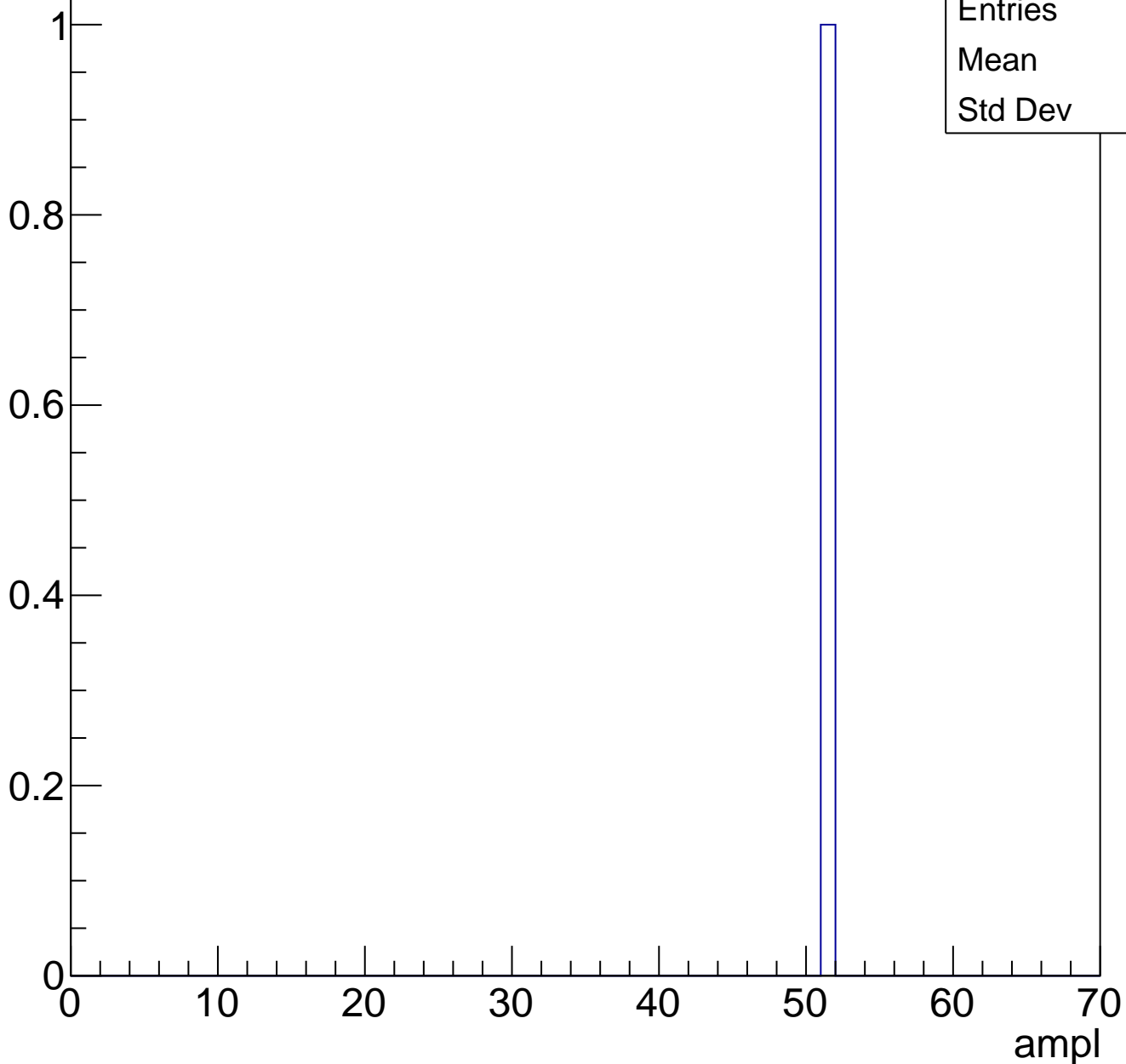
B0L100S, U13-ch58, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

Entry

ampl

B0L100S, U13-ch58, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch58, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch58, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch58, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch59, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	31
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
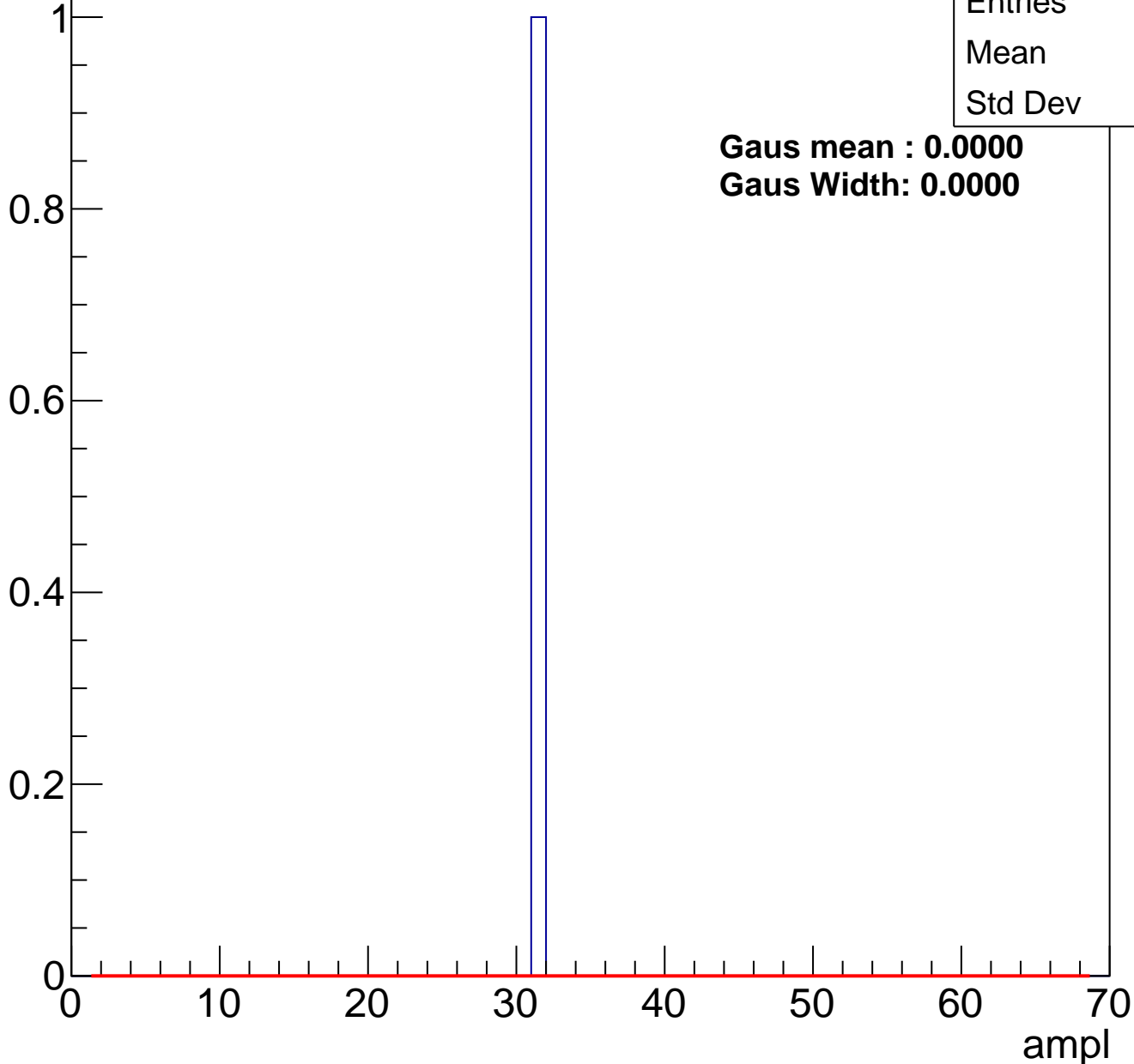
B0L100S, U13-ch59, adc1

calib_packv5_042523_0143.root, FC#6, port A1
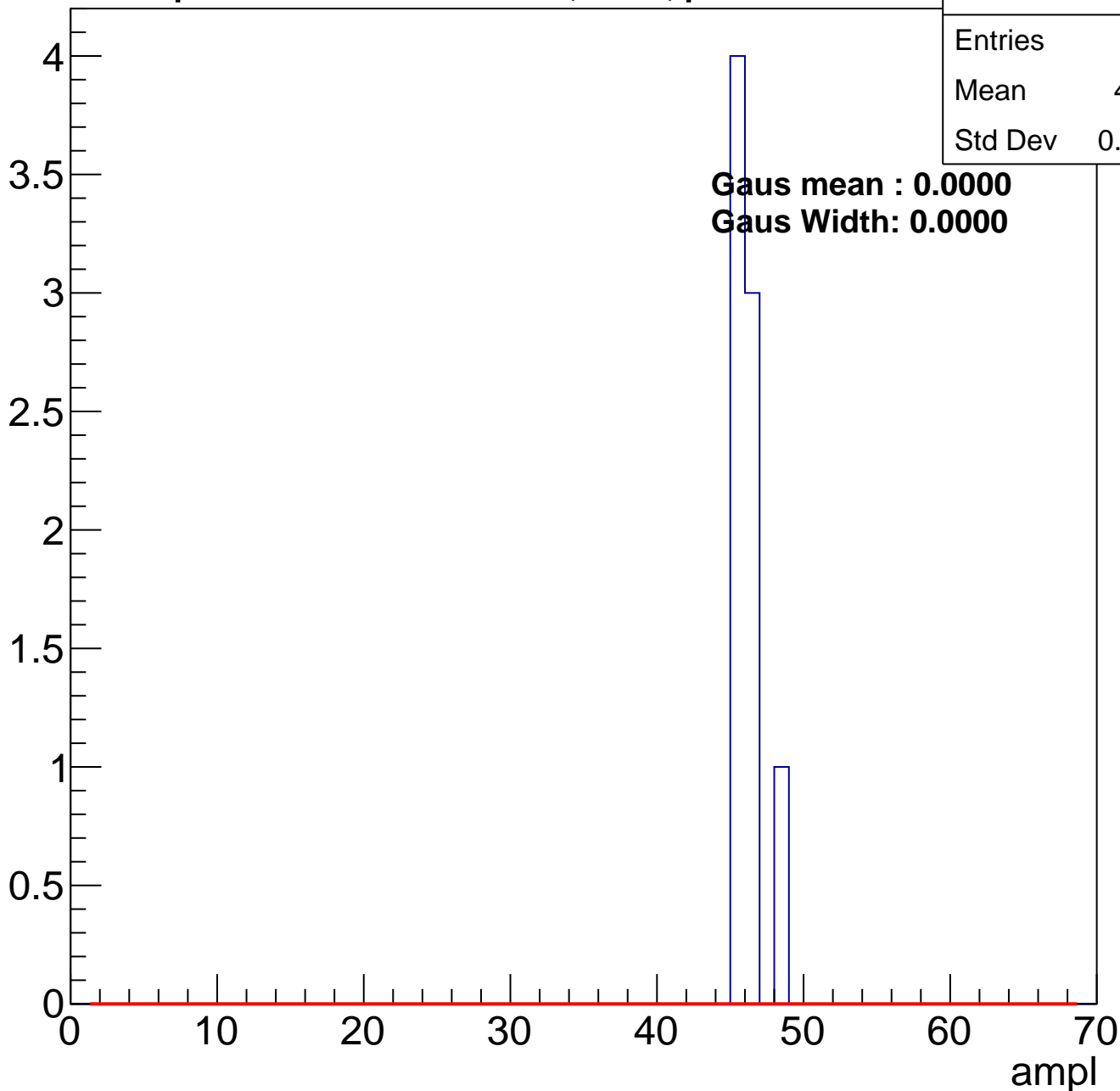
Entries	8
Mean	45.75
Std Dev	0.9682

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
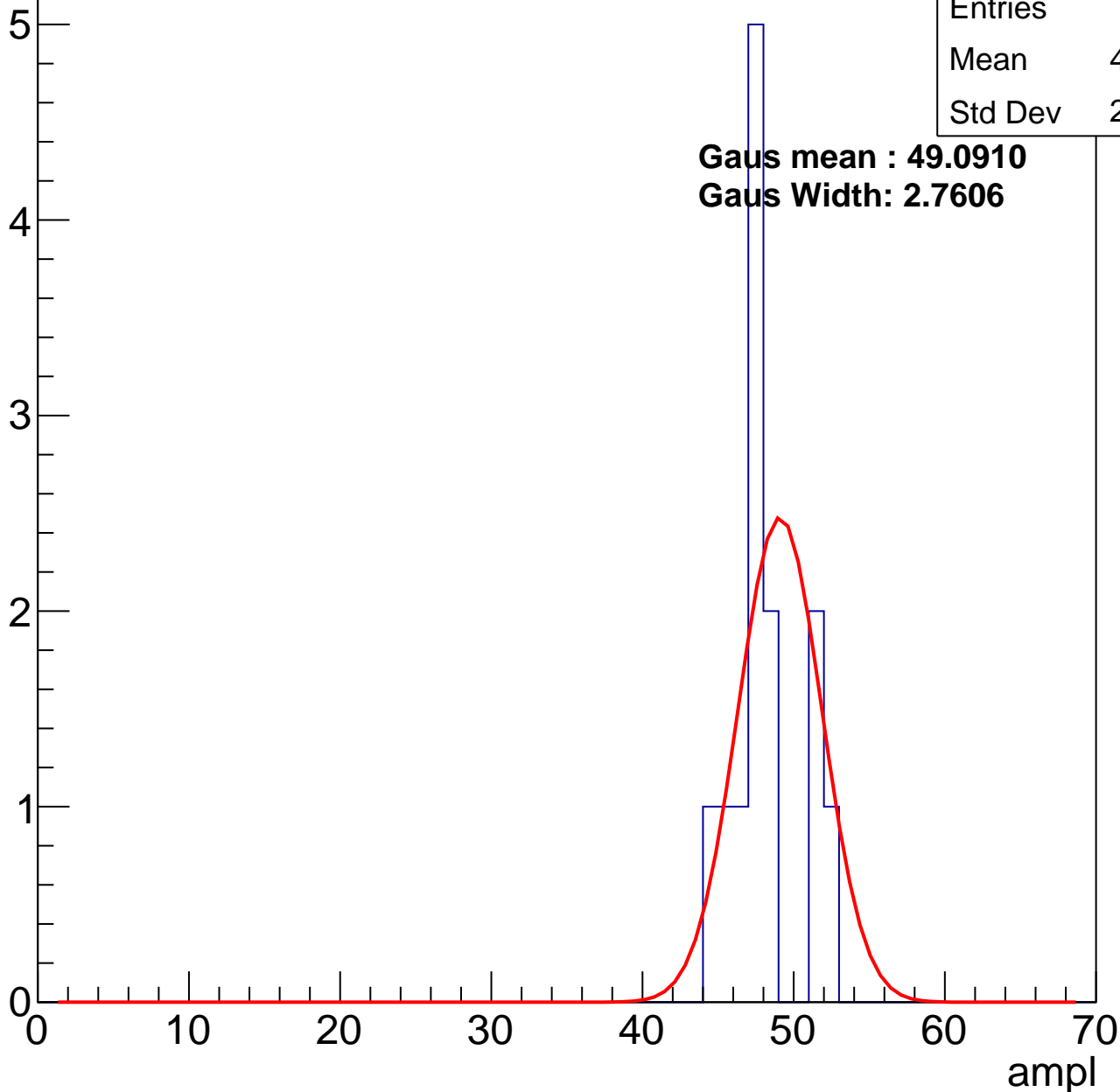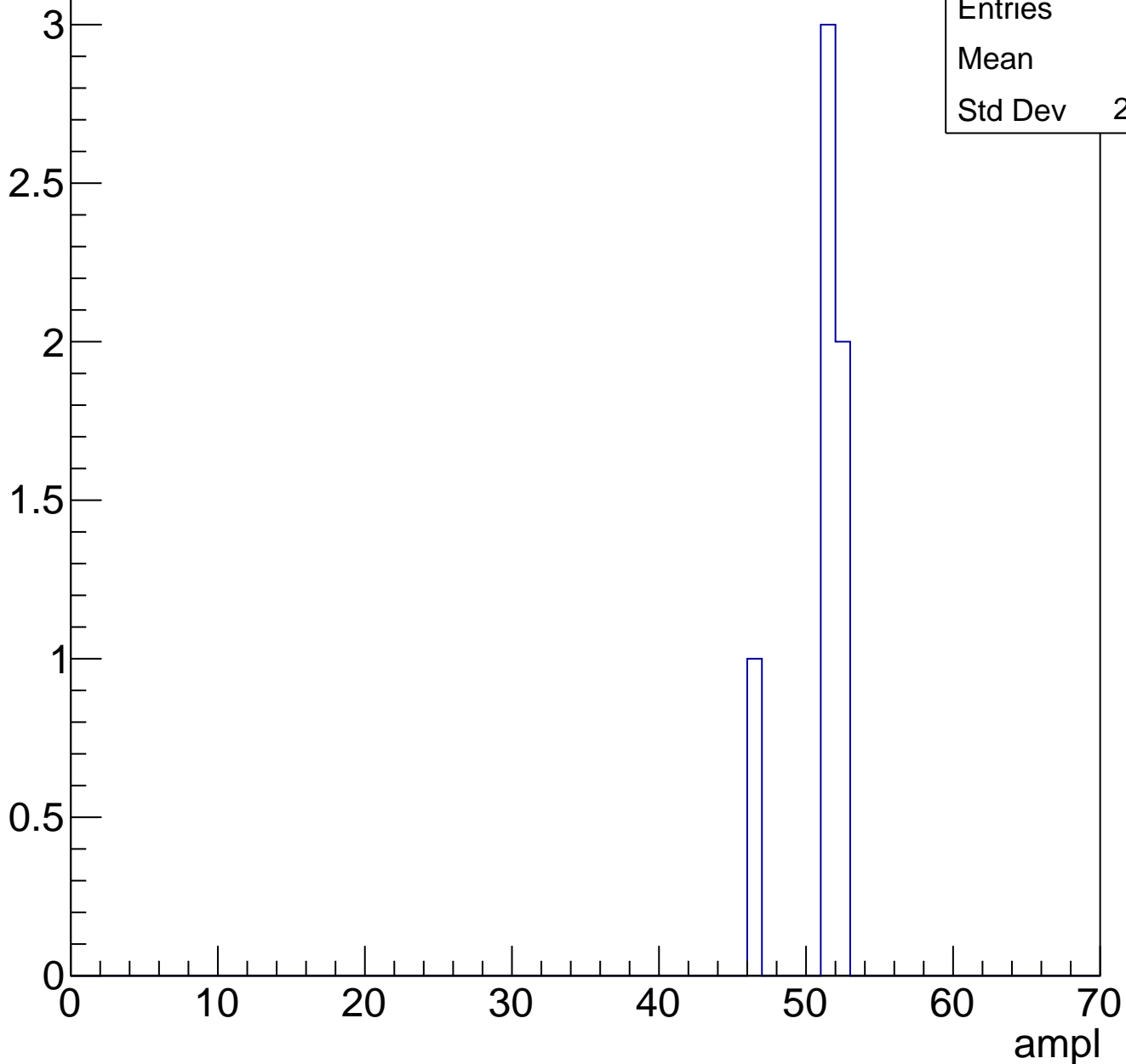
B0L100S, U13-ch59, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch59, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch59, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch59, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch60, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch60, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch60, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
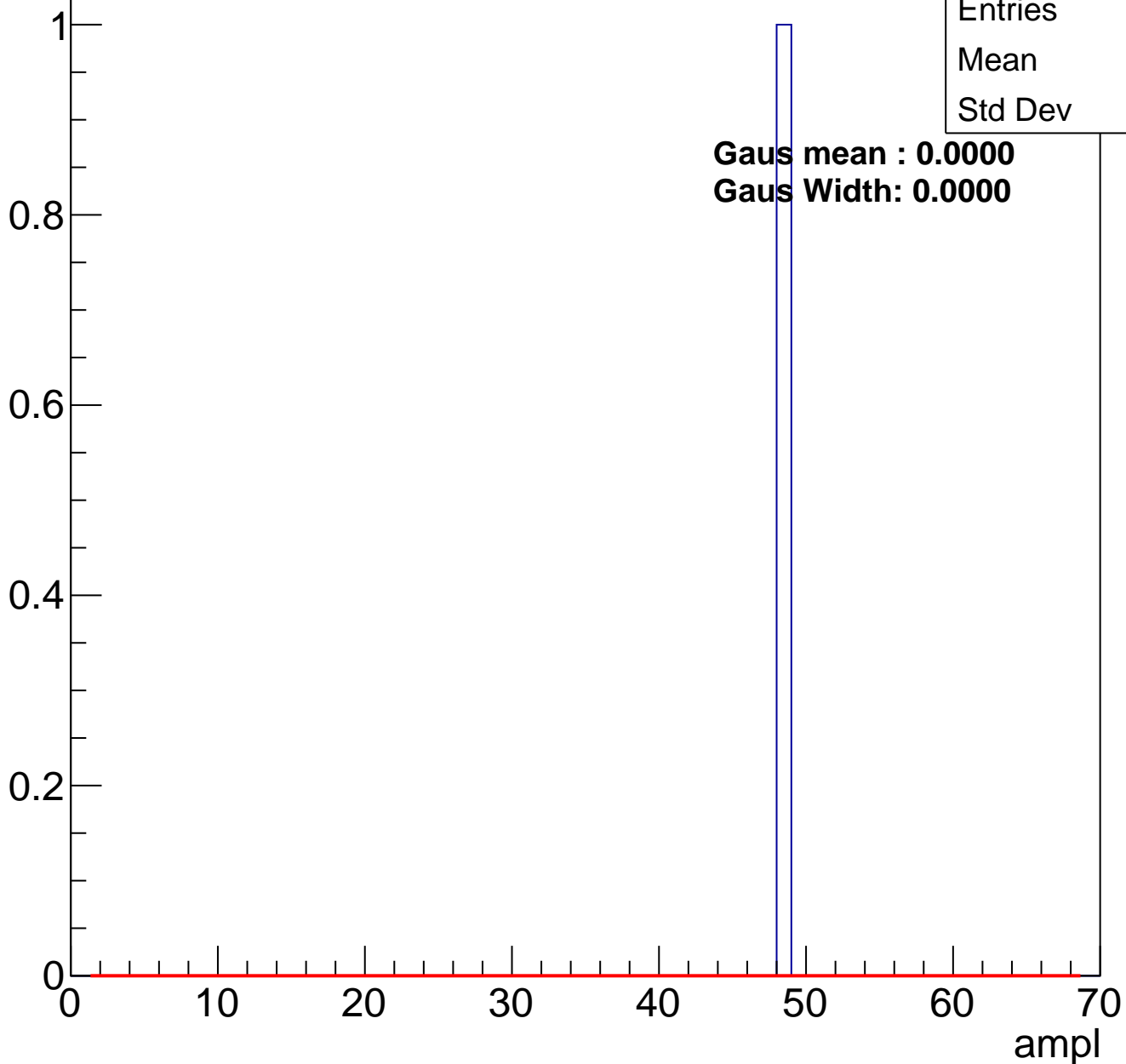
B0L100S, U13-ch60, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch60, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch60, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch60, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch60, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch61, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch61, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch61, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch61, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch61, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch61, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch61, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch61, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch62, adc0
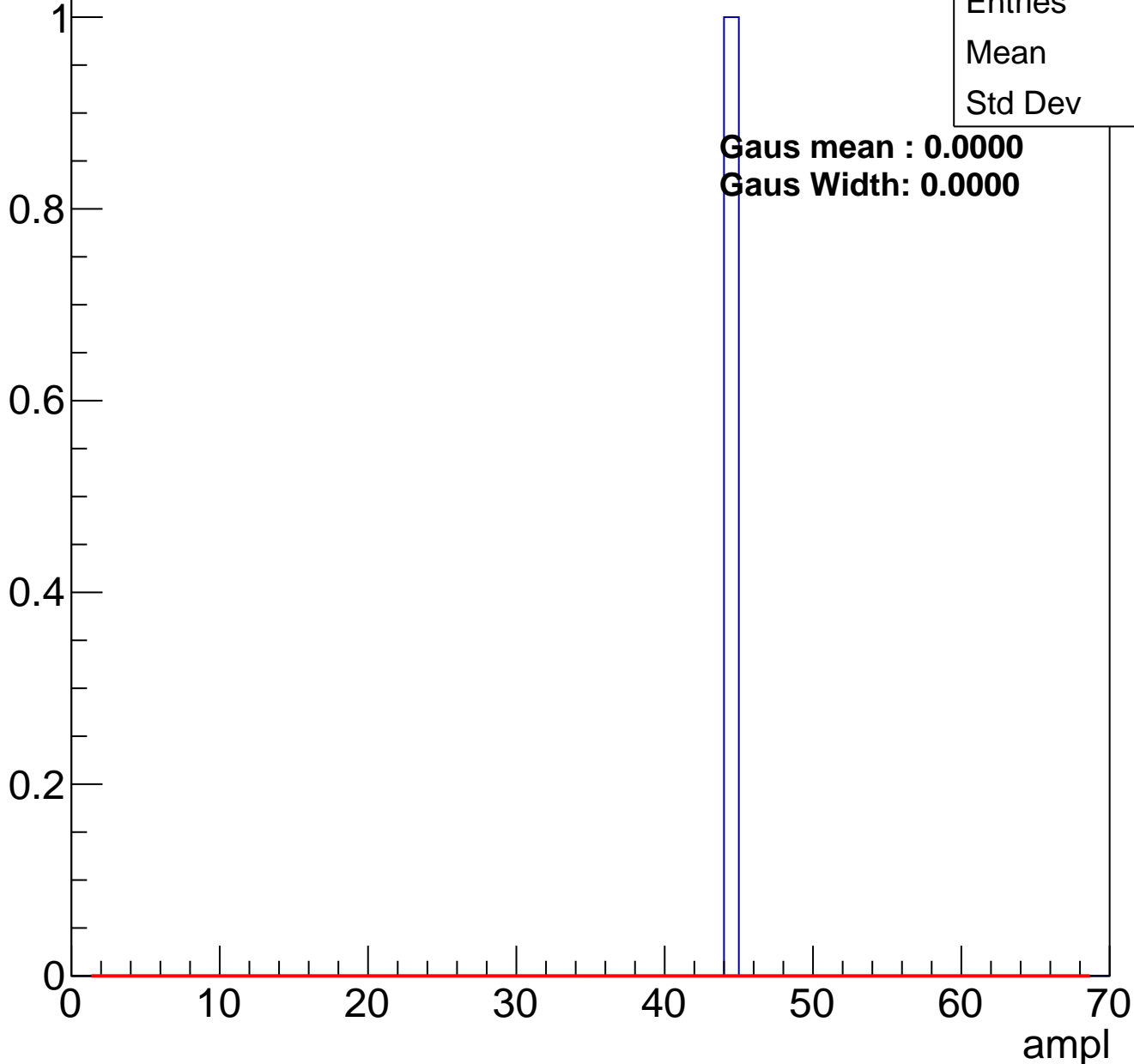
calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch62, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	46.83
Std Dev	2.115

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch62, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	18
Mean	49.67
Std Dev	2.867

Gaus mean : 50.4867
Gaus Width: 3.8635
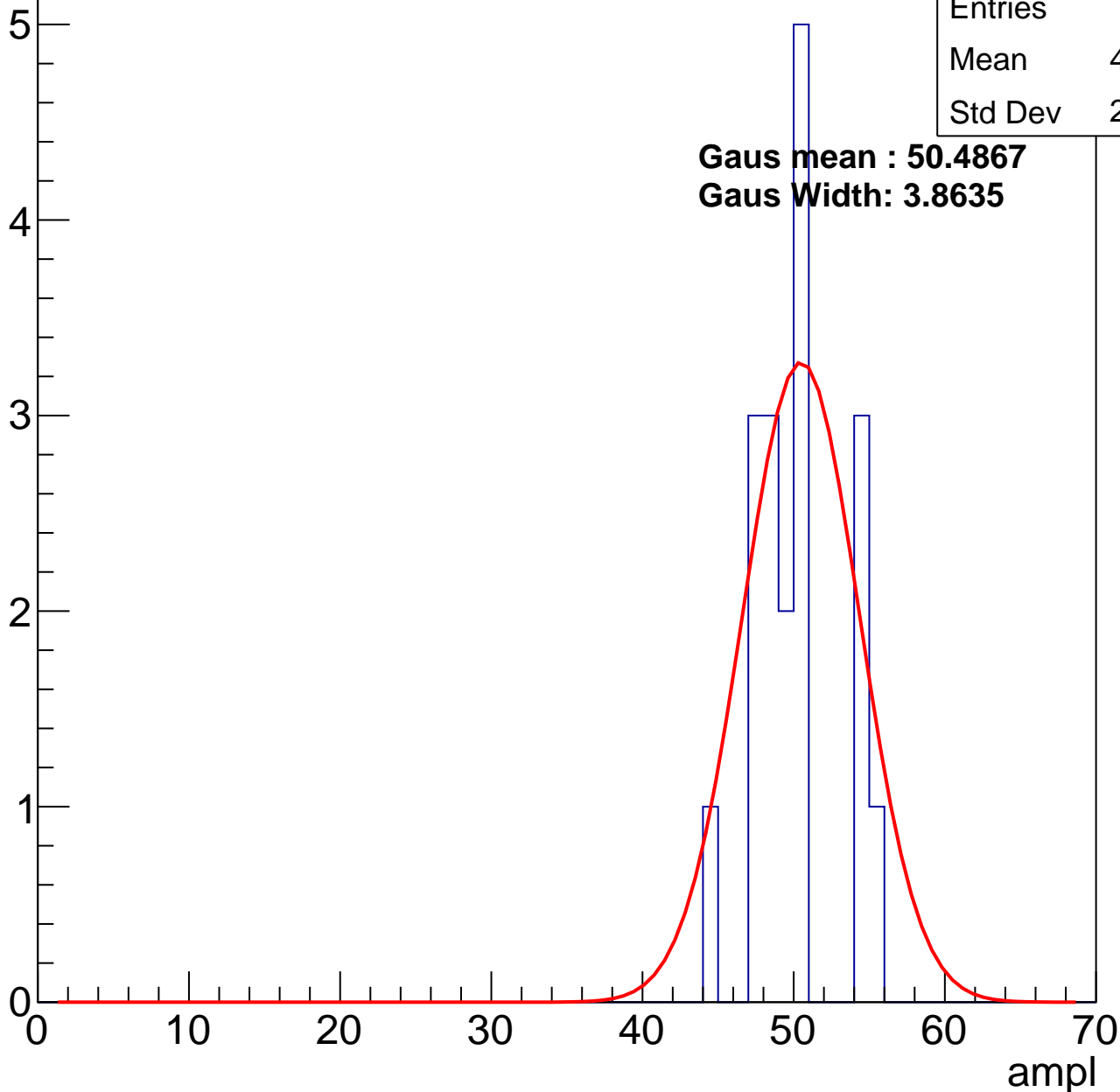
B0L100S, U13-ch62, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	52.44
Std Dev	2.166
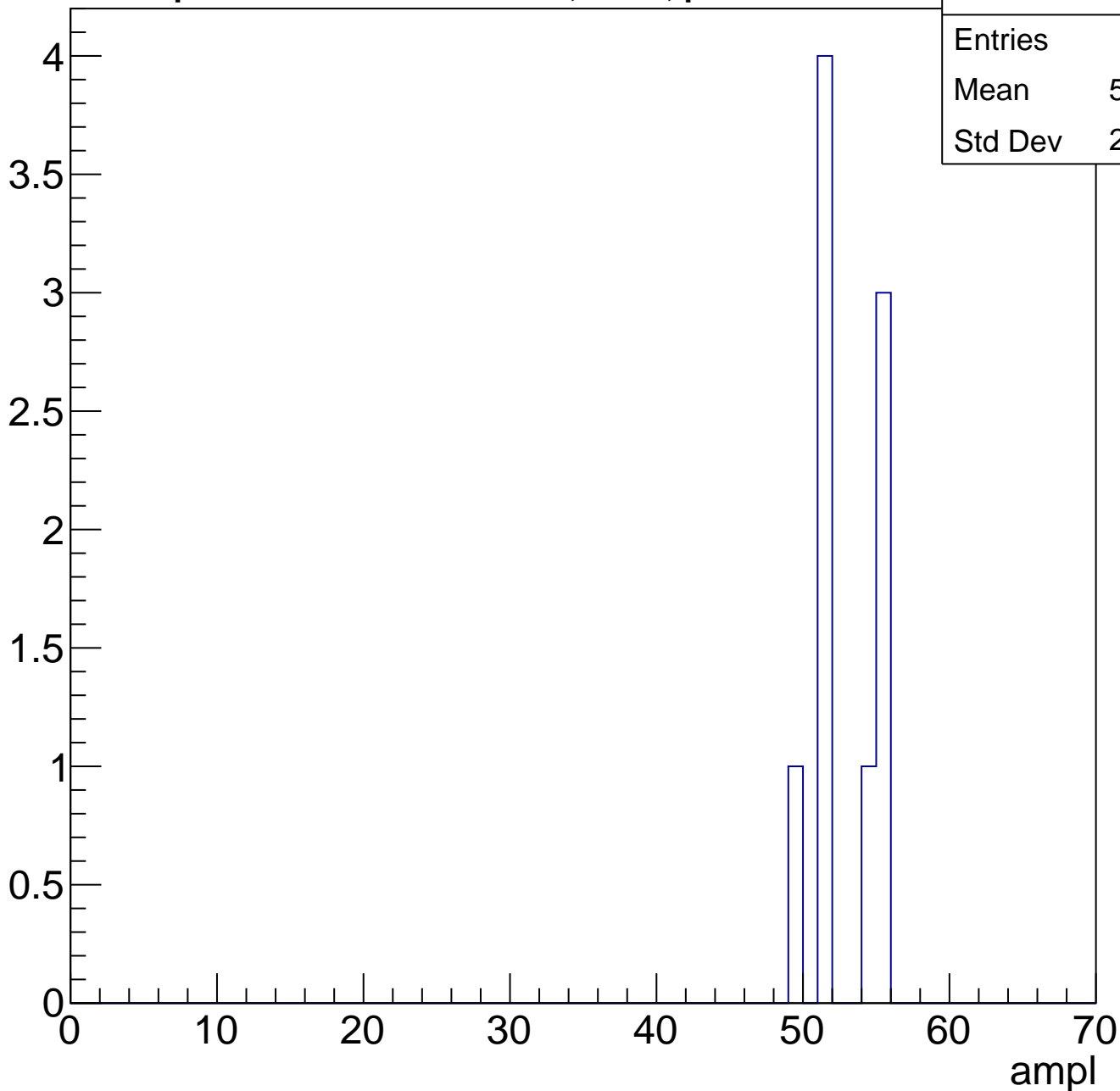
Entry

ampl

B0L100S, U13-ch62, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch62, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	27
Std Dev	0

Entry

ampl
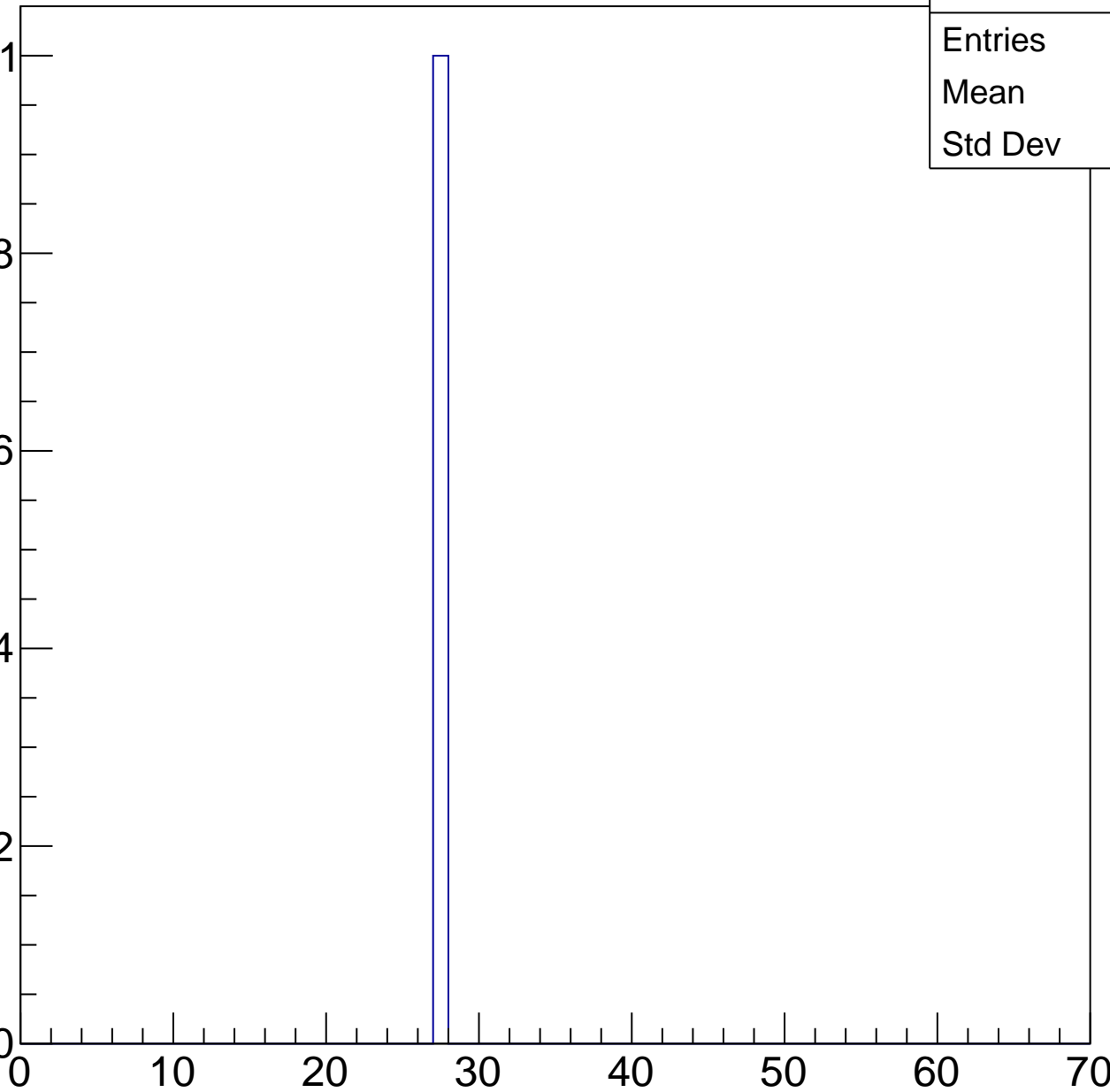
B0L100S, U13-ch62, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch62, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch63, adc0

calib_packv5_042523_0143.root, FC#6, port A1
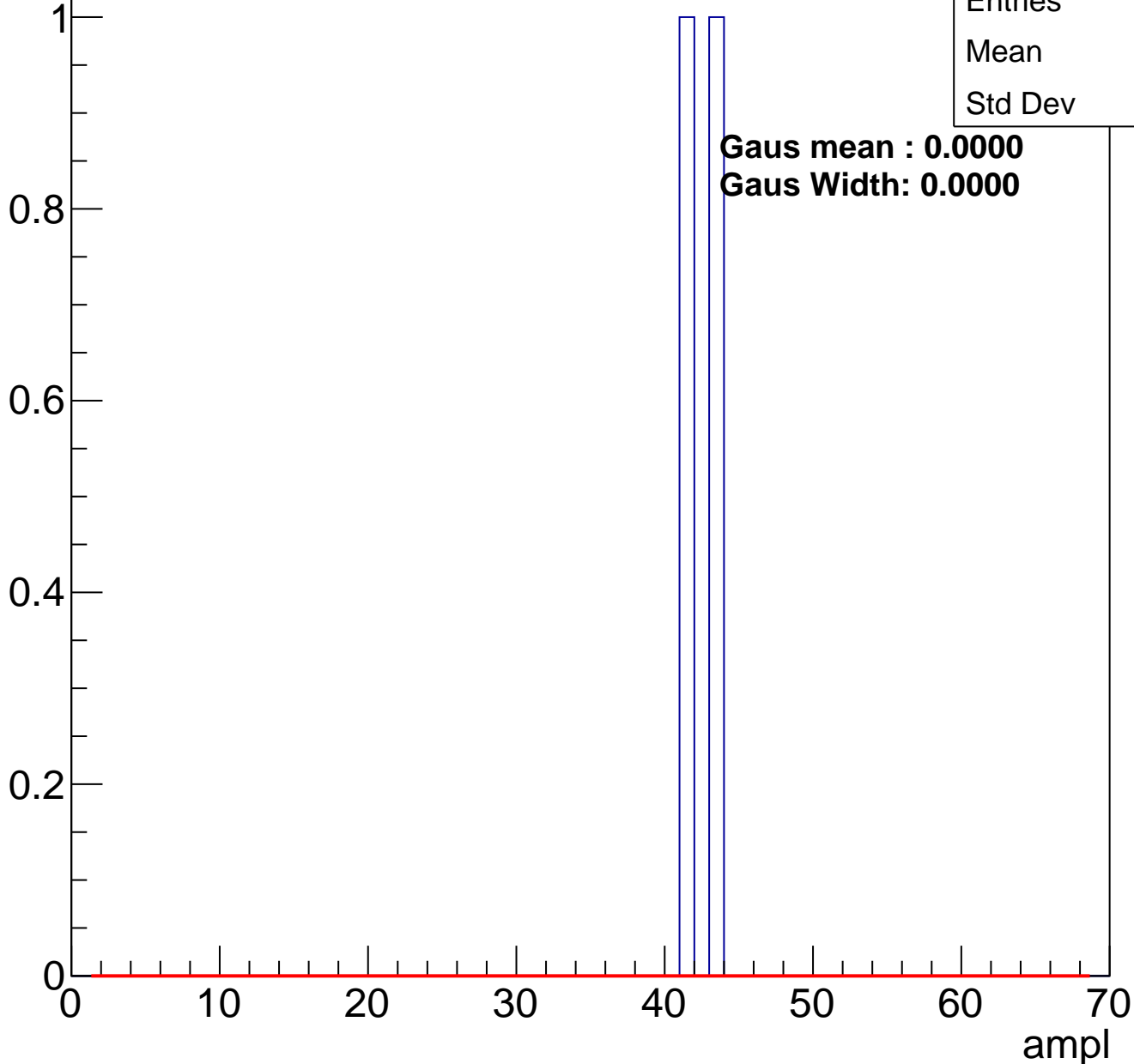
Entry

ampl

Entries	2
Mean	42
Std Dev	1

Gaus mean : 0.0000
Gaus Width: 0.0000
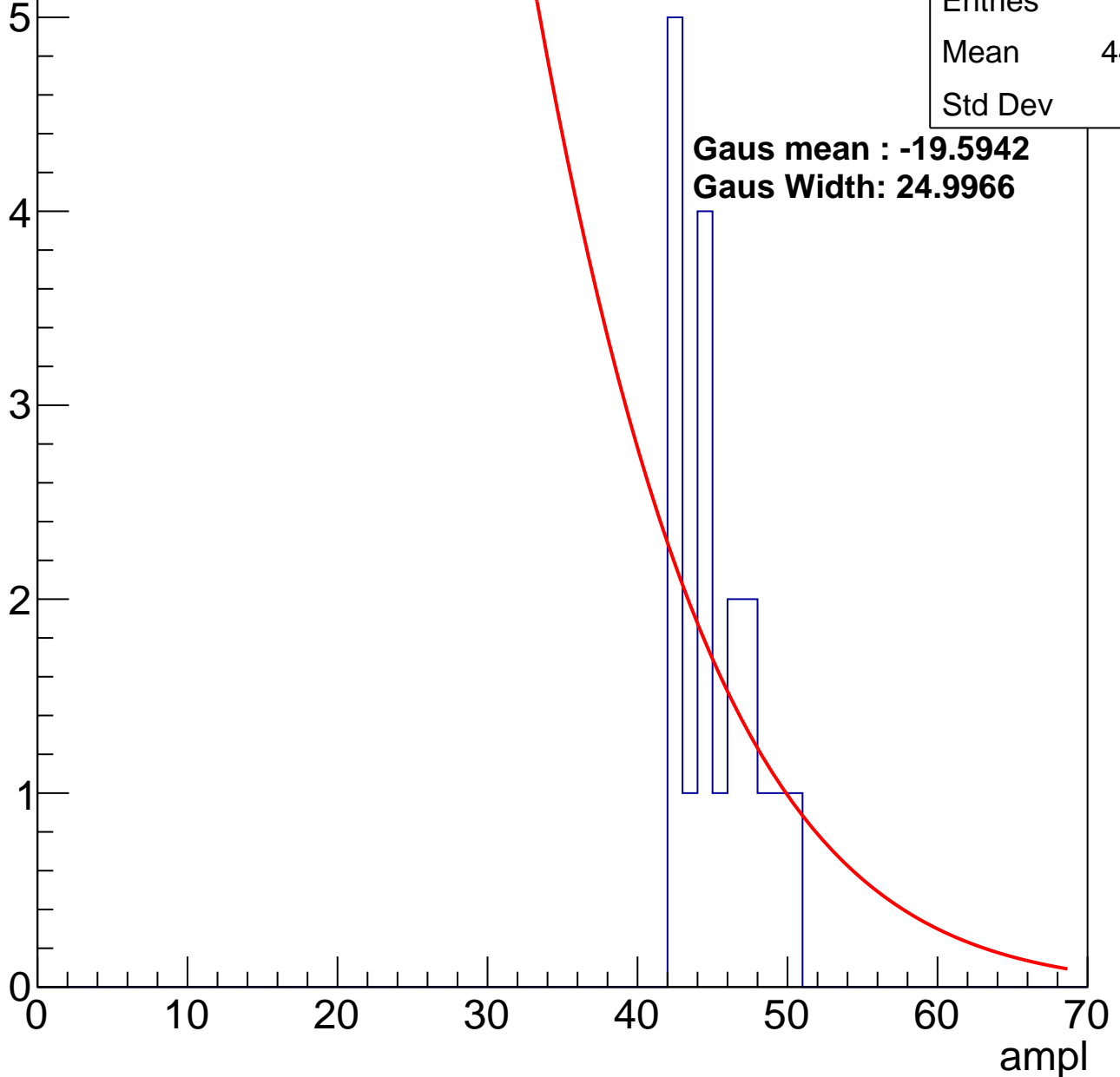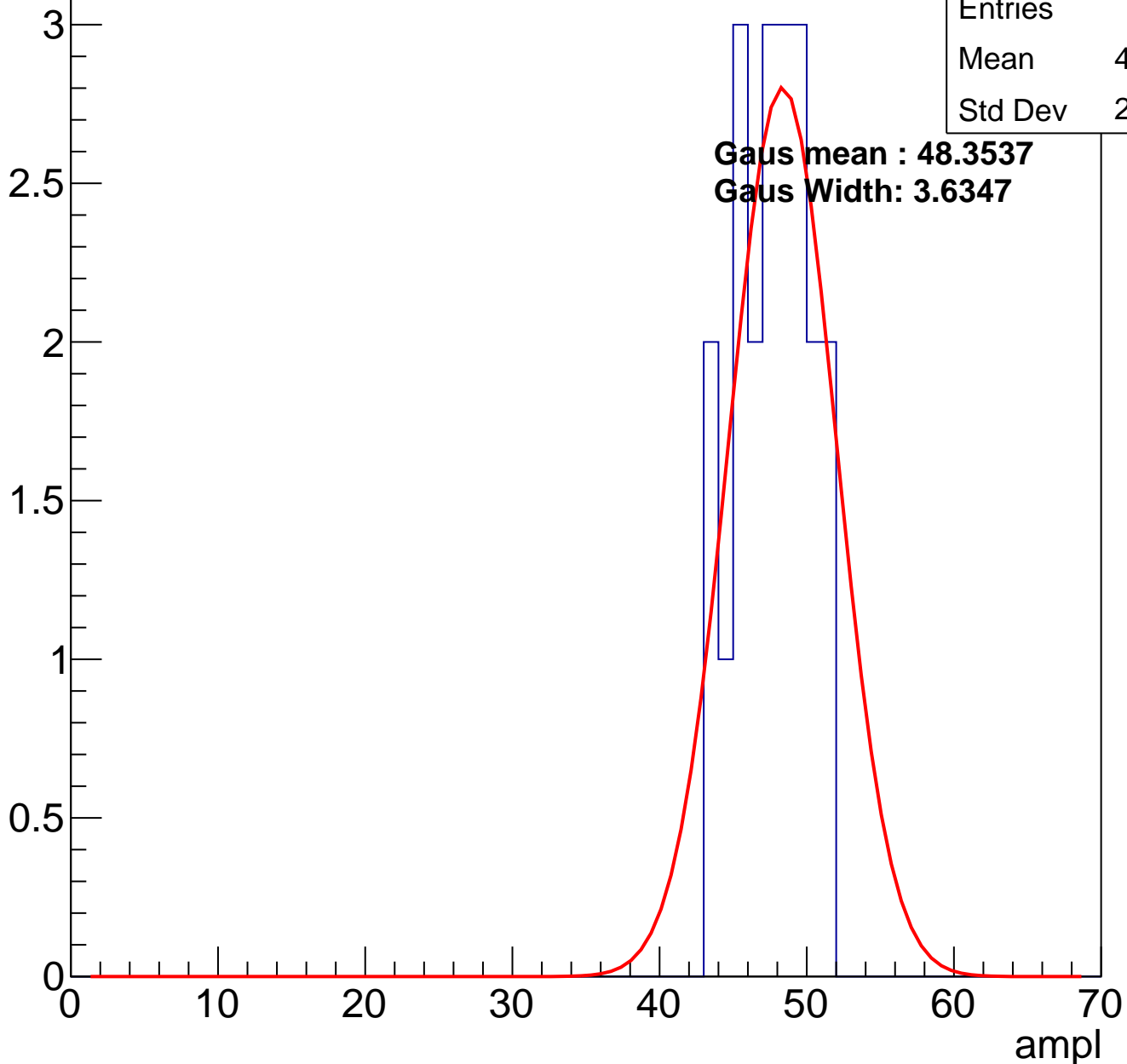
B0L100S, U13-ch63, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	53.67
Std Dev	2.625

Entry

ampl
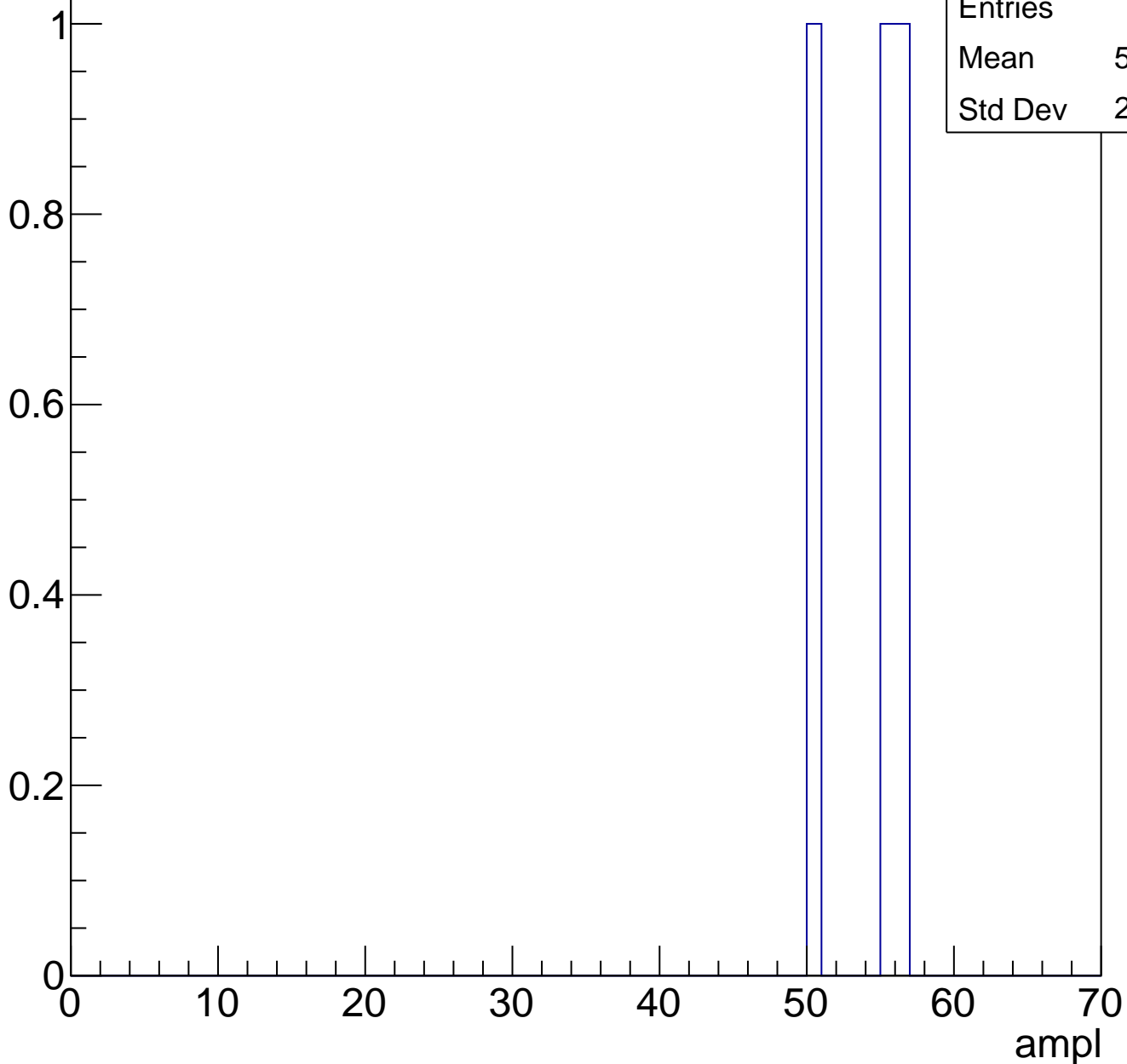
B0L100S, U13-ch63, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch63, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch63, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch63, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch64, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	46
Std Dev	2

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch64, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	15
Mean	44.13
Std Dev	4.485

Gaus mean : 32.9268
Gaus Width: 17.0705

Entry

ampl
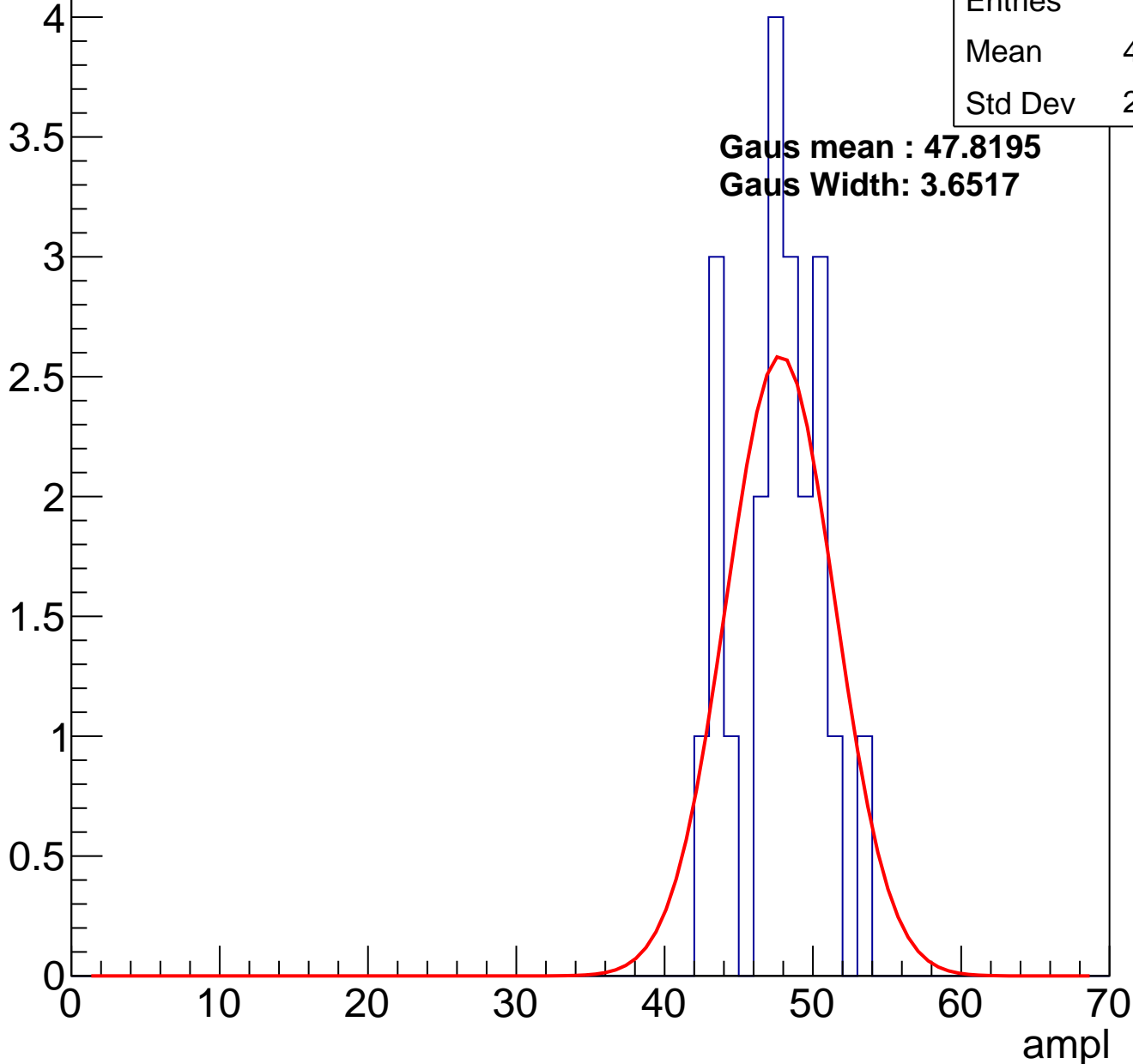
B0L100S, U13-ch64, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48.5
Std Dev	1.5

Entry

ampl
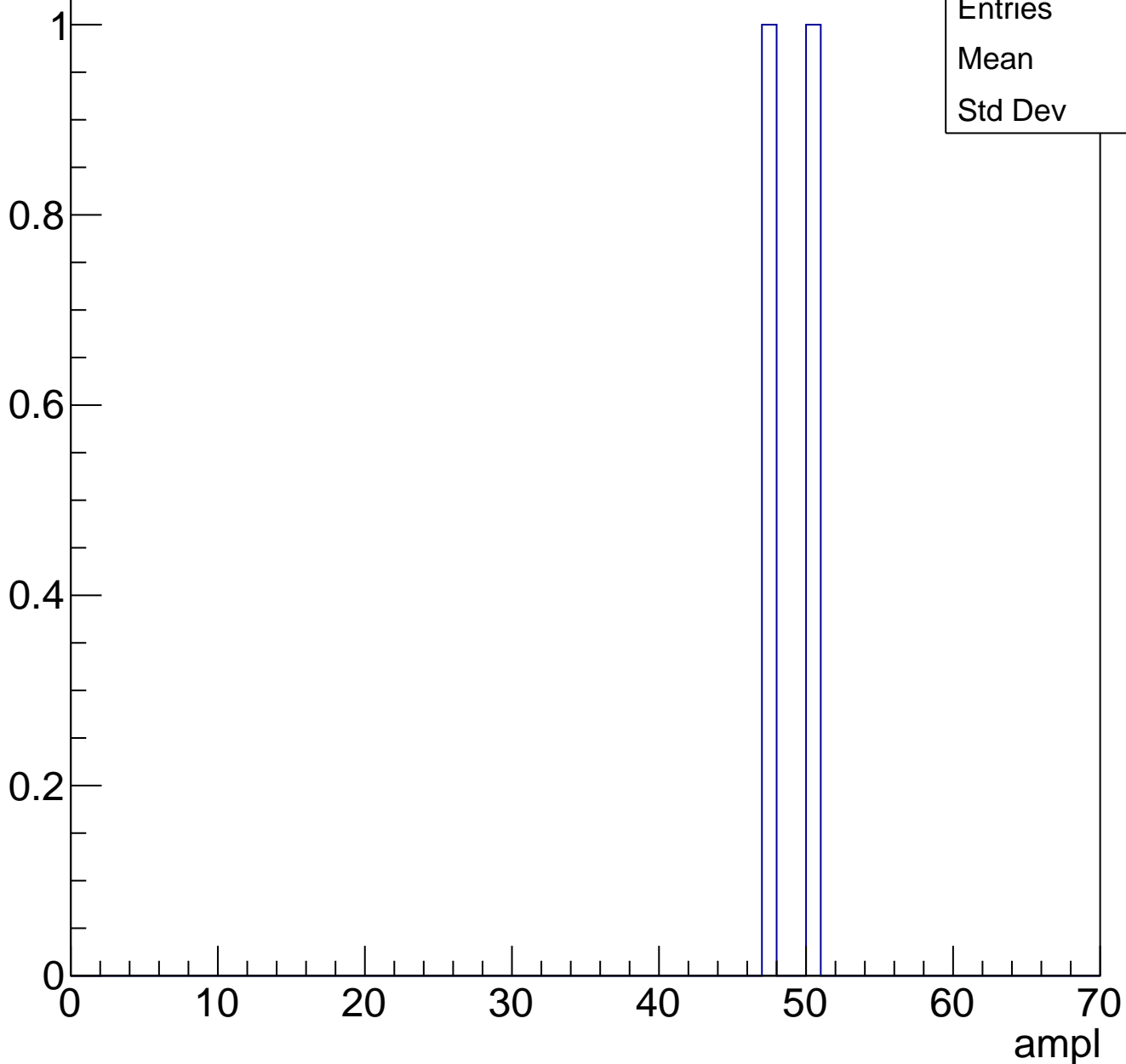
B0L100S, U13-ch64, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch64, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch64, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch64, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch65, adc0
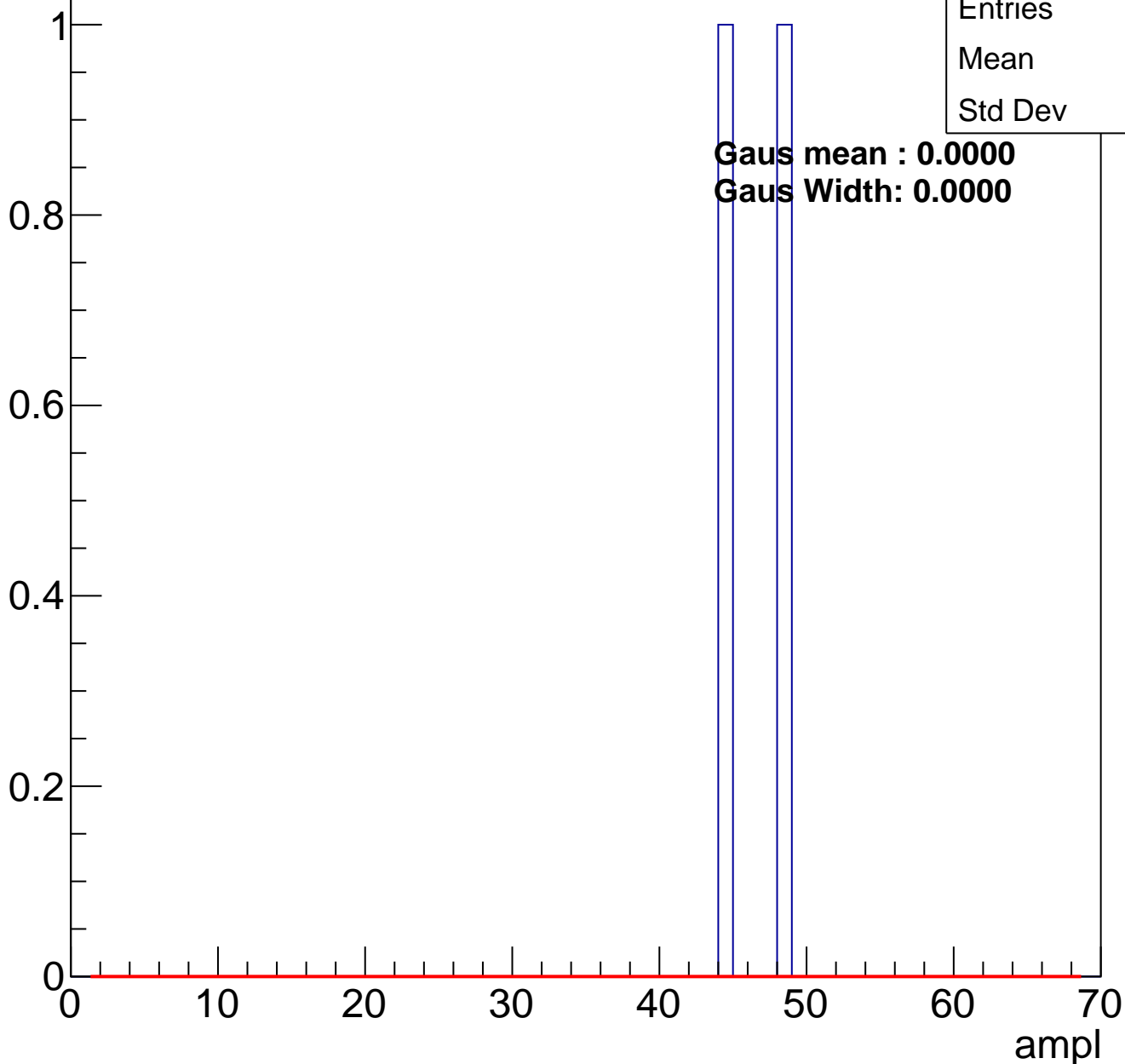
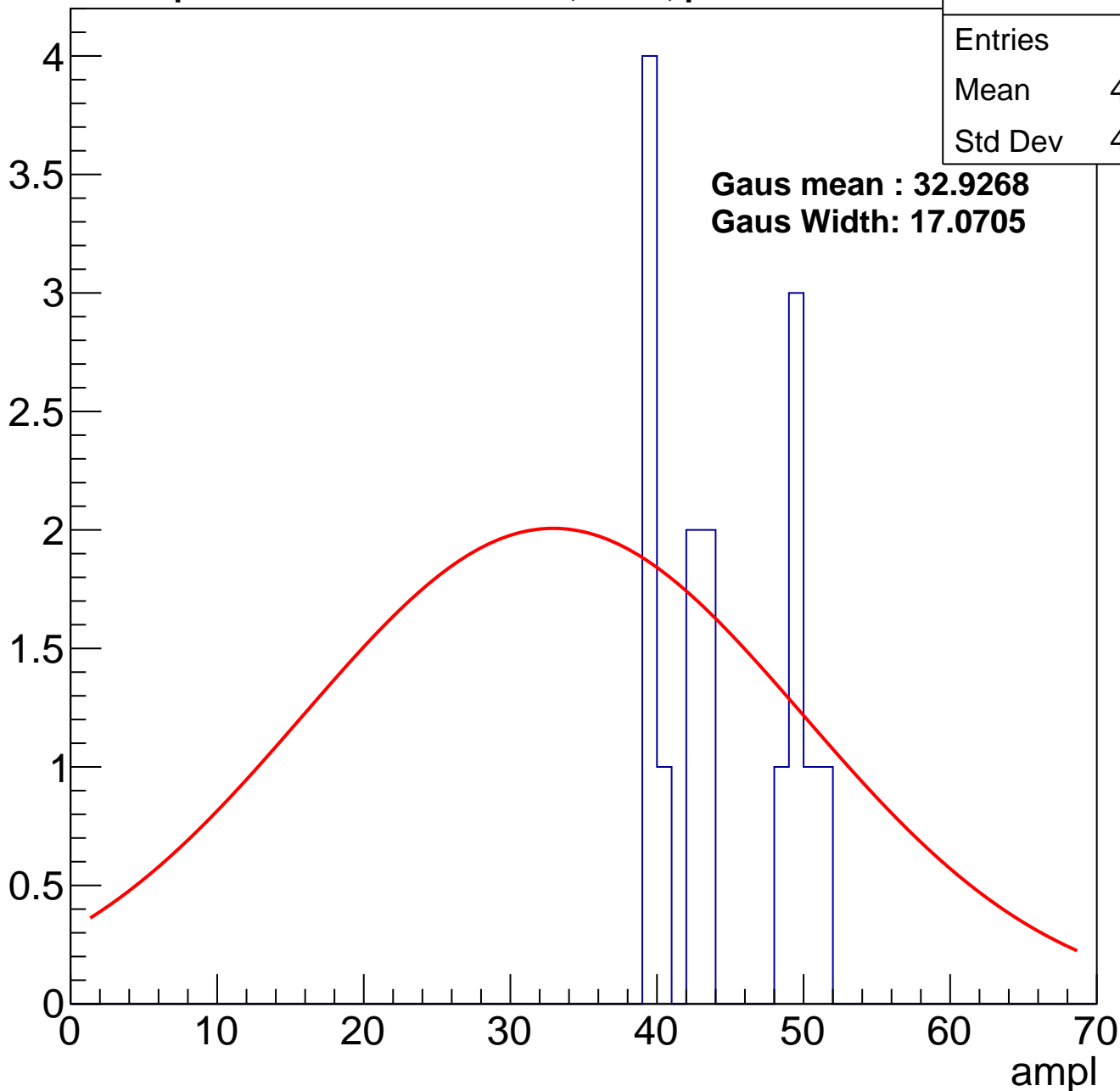**calib_packv5_042523_0143.root, FC#6, port A1**

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch65, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch65, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch65, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch65, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch65, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch65, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch65, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch66, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch66, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	48.43
Std Dev	1.498

Gaus mean : 0.0000
Gaus Width: 0.0000
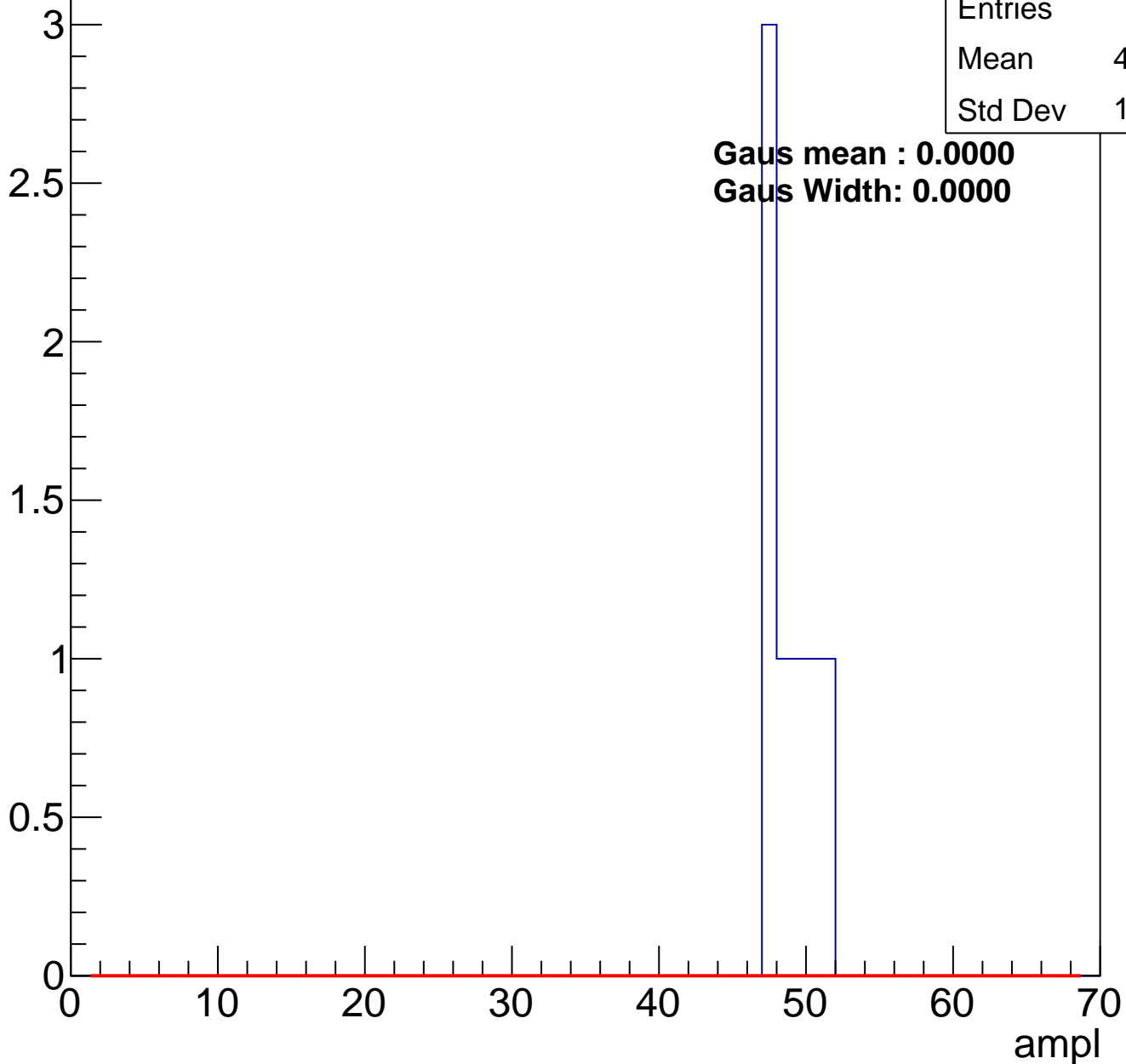
Entry

ampl

B0L100S, U13-ch66, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 49.9449
Gaus Width: 1.9078

Entries	16
Mean	49.62
Std Dev	2.088
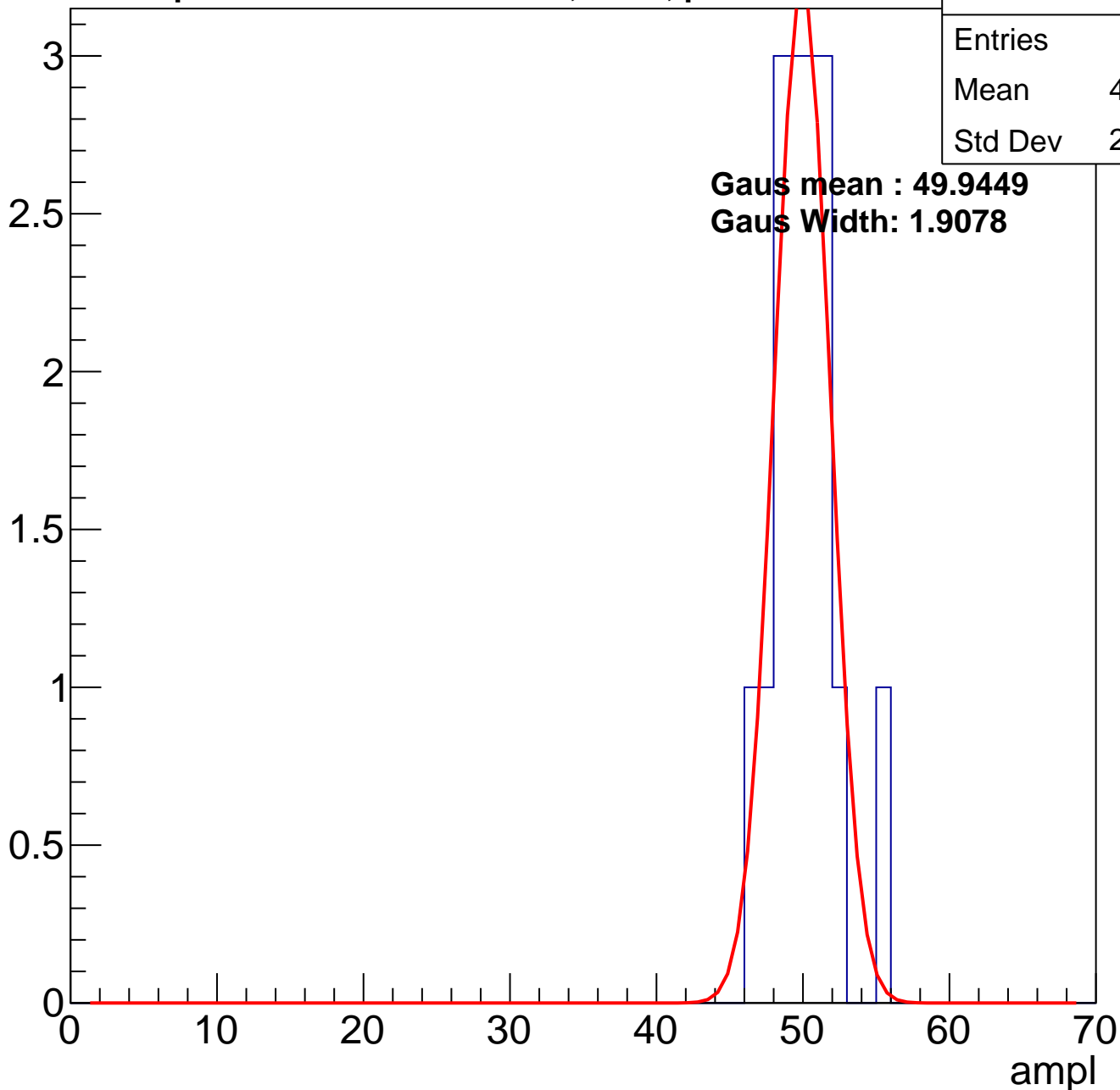
B0L100S, U13-ch66, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	51.82
Std Dev	2.855

Entry

ampl

B0L100S, U13-ch66, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch66, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch66, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch66, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch67, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch67, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch67, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch67, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch67, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch67, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch67, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch67, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch68, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
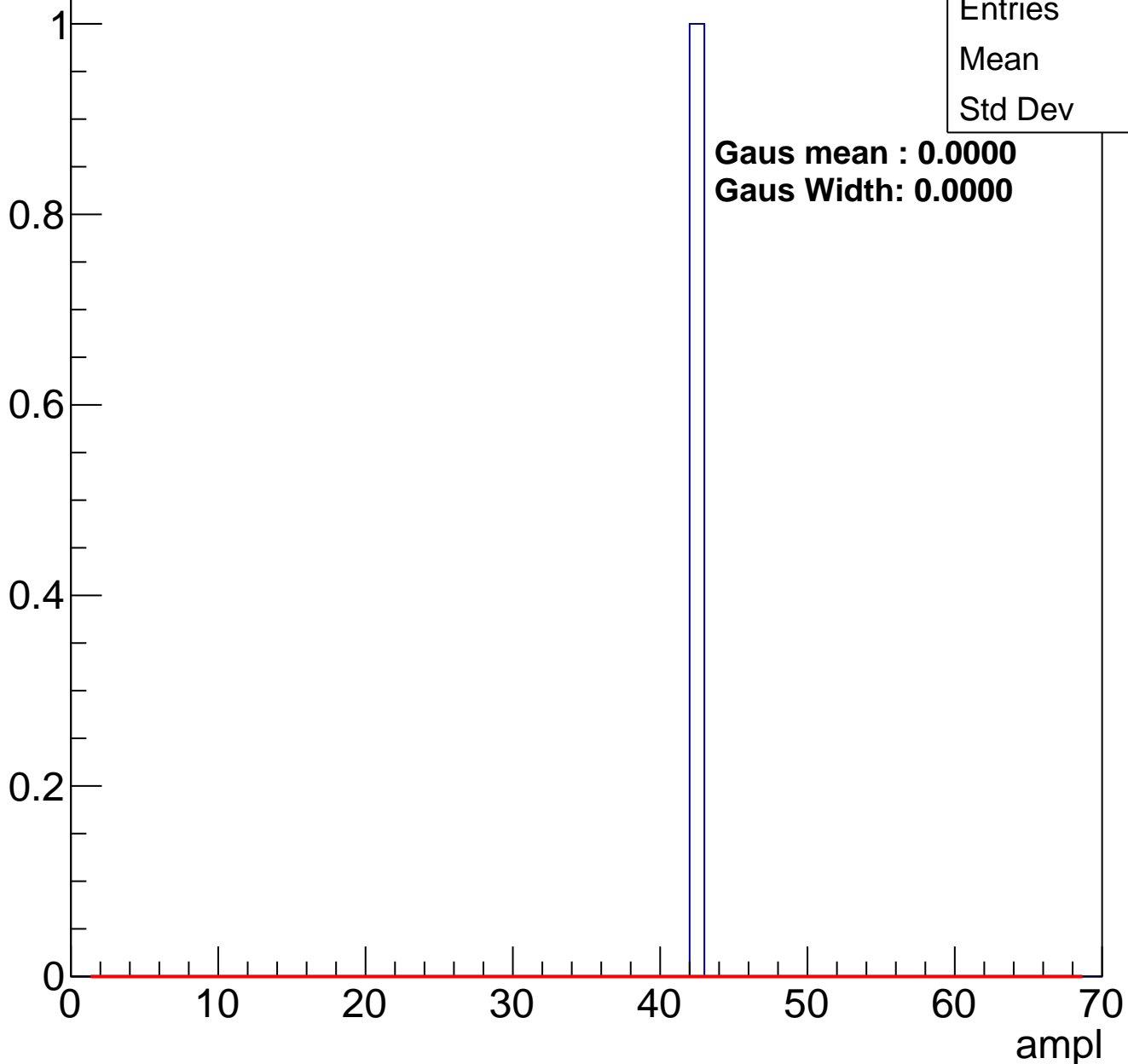
B0L100S, U13-ch68, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
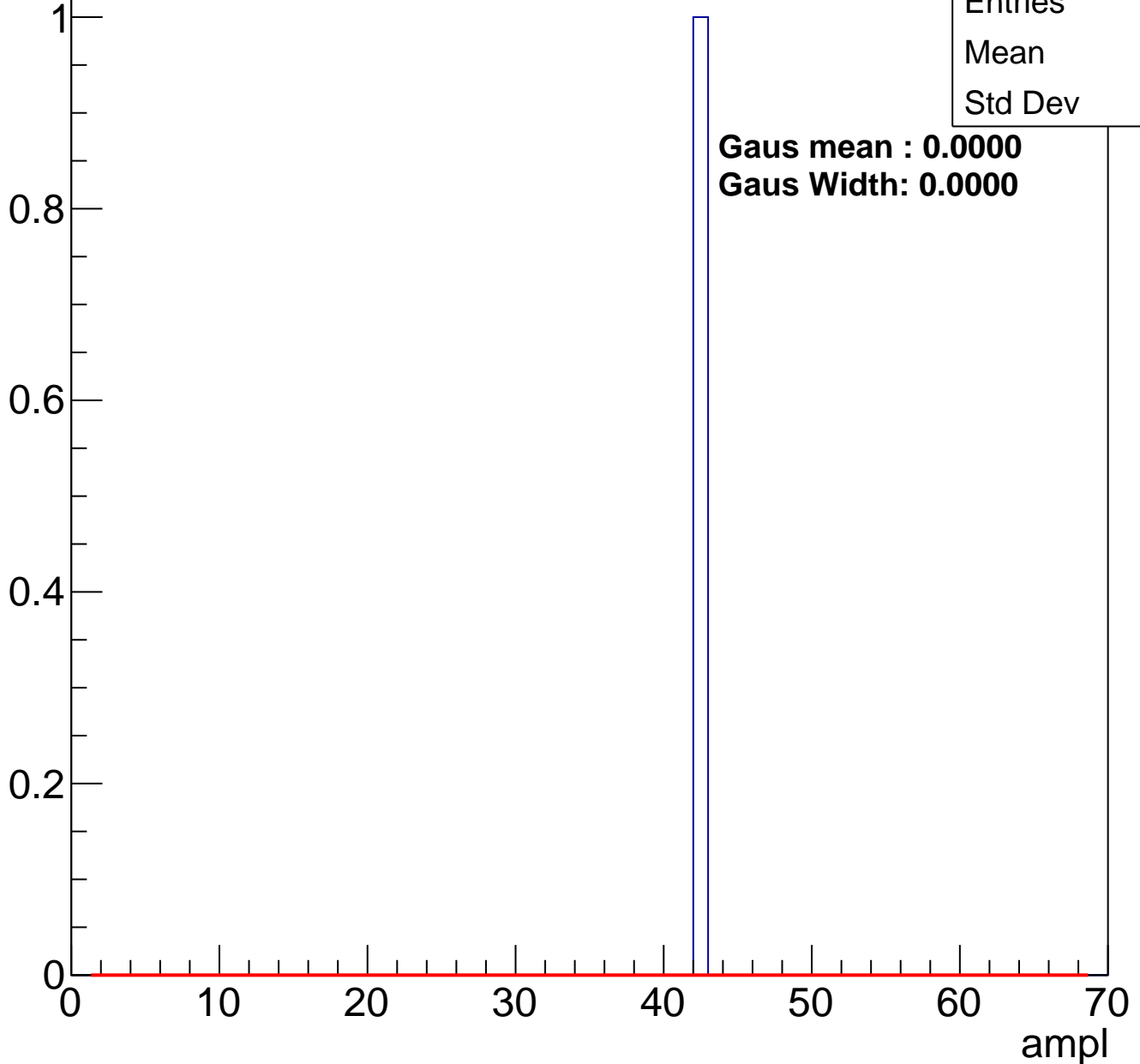
B0L100S, U13-ch68, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch68, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch68, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch68, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch68, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch68, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch69, adc0

calib_packv5_042523_0143.root, FC#6, port A1
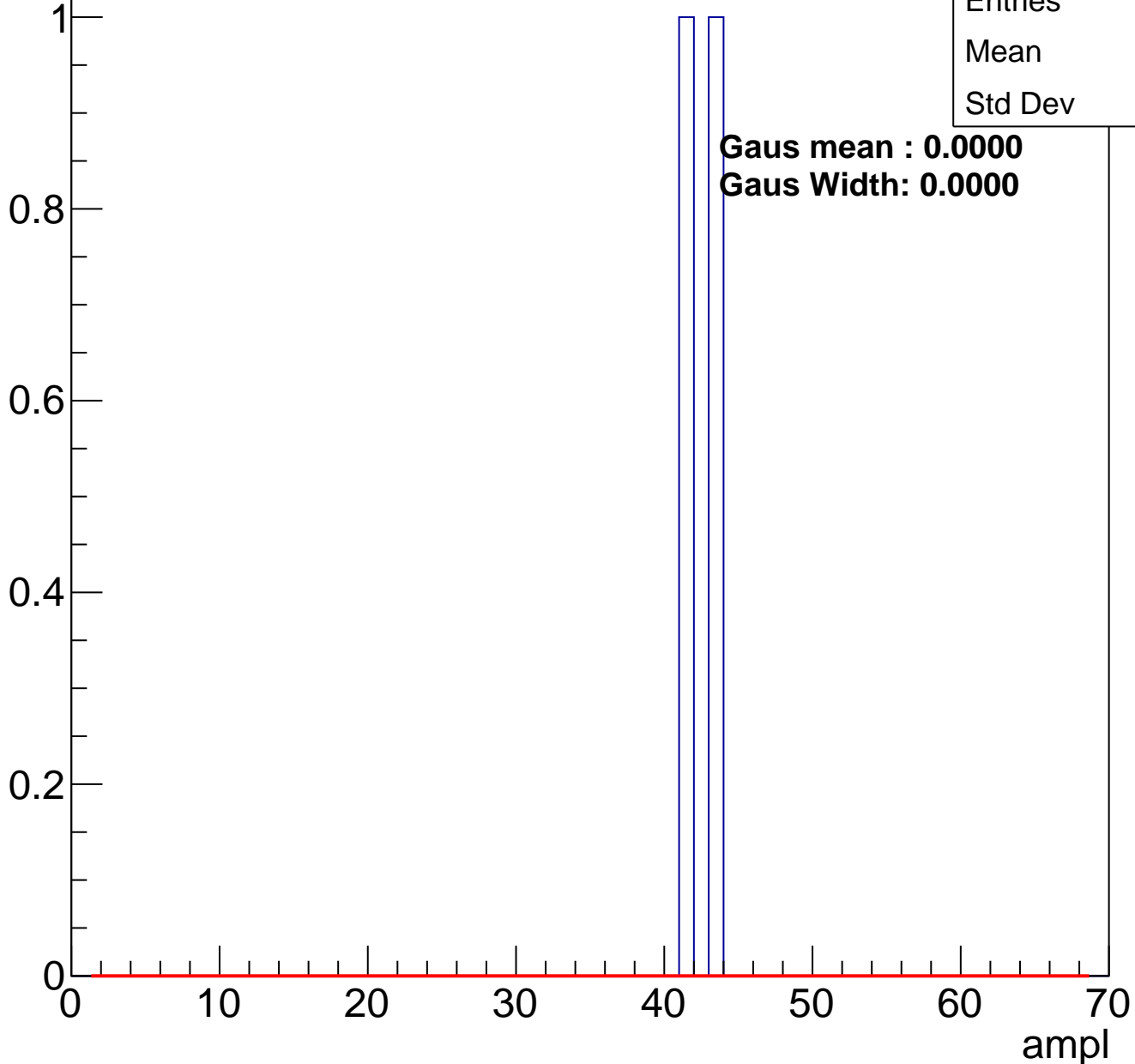
Entry

Entries	2
Mean	42
Std Dev	1

Gaus mean : 0.0000
Gaus Width: 0.0000
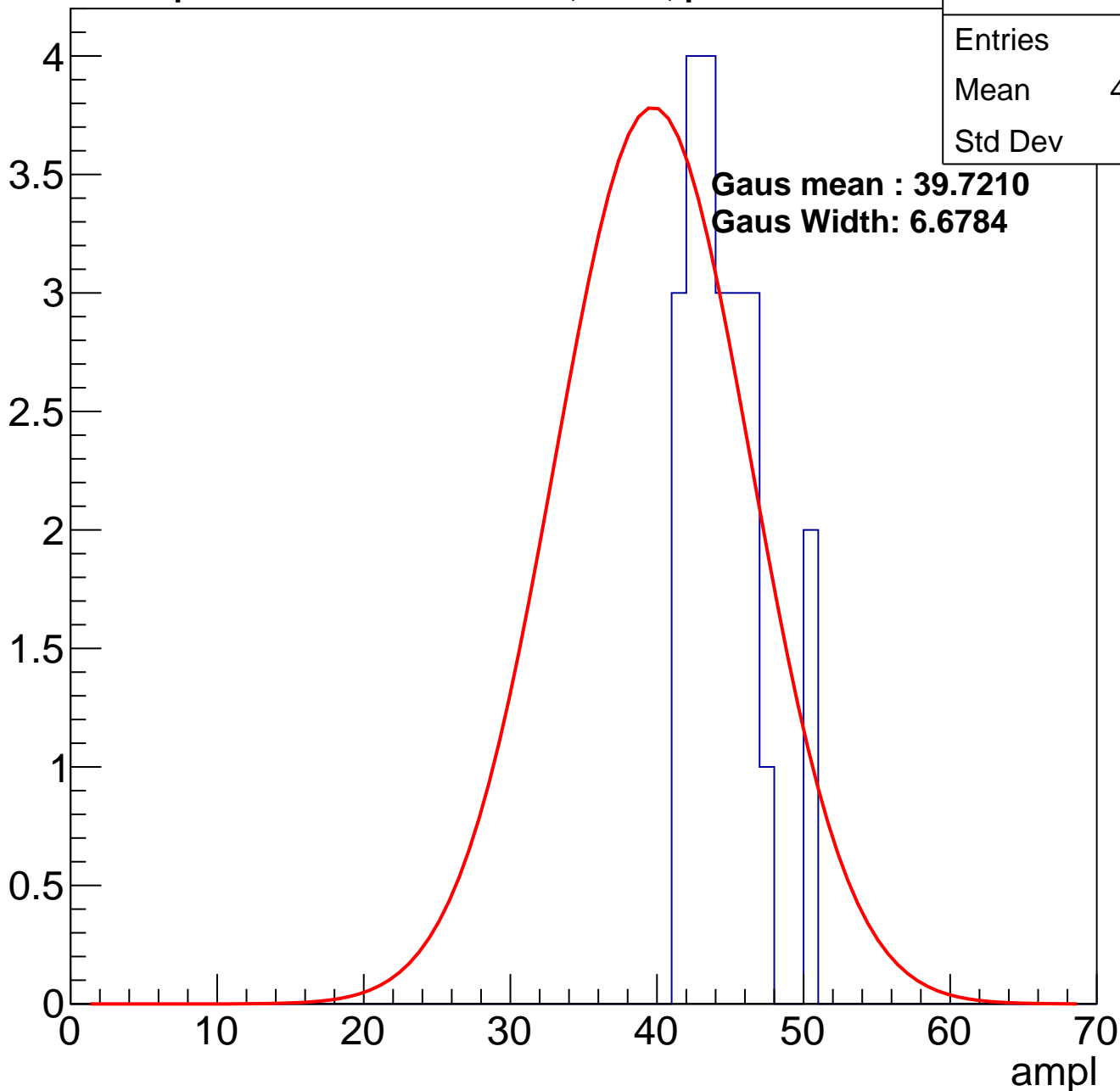
ampl
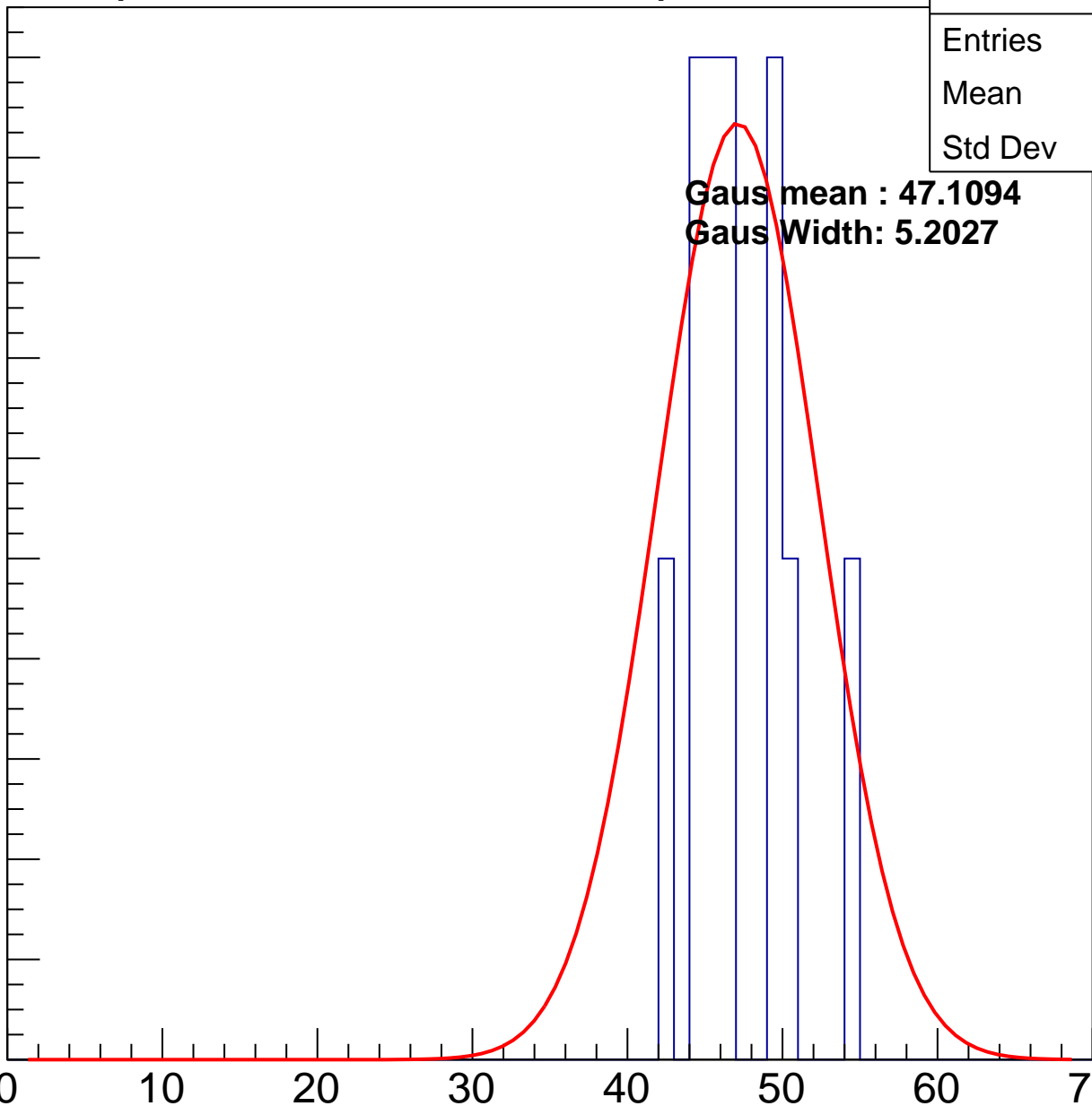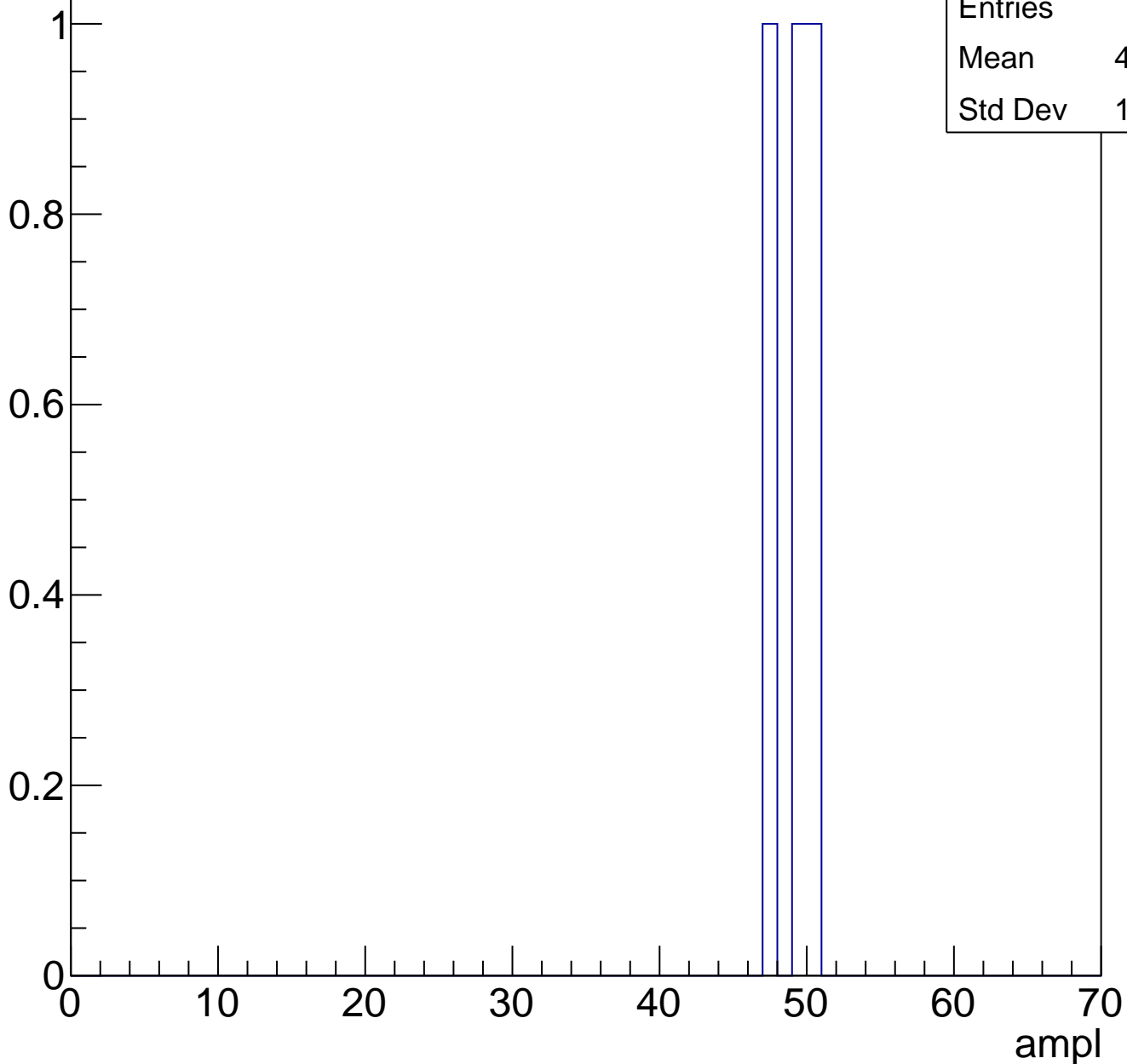
B0L100S, U13-ch69, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch69, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch69, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch69, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch70, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch70, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch70, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch70, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch70, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch70, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch70, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch71, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch71, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch71, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
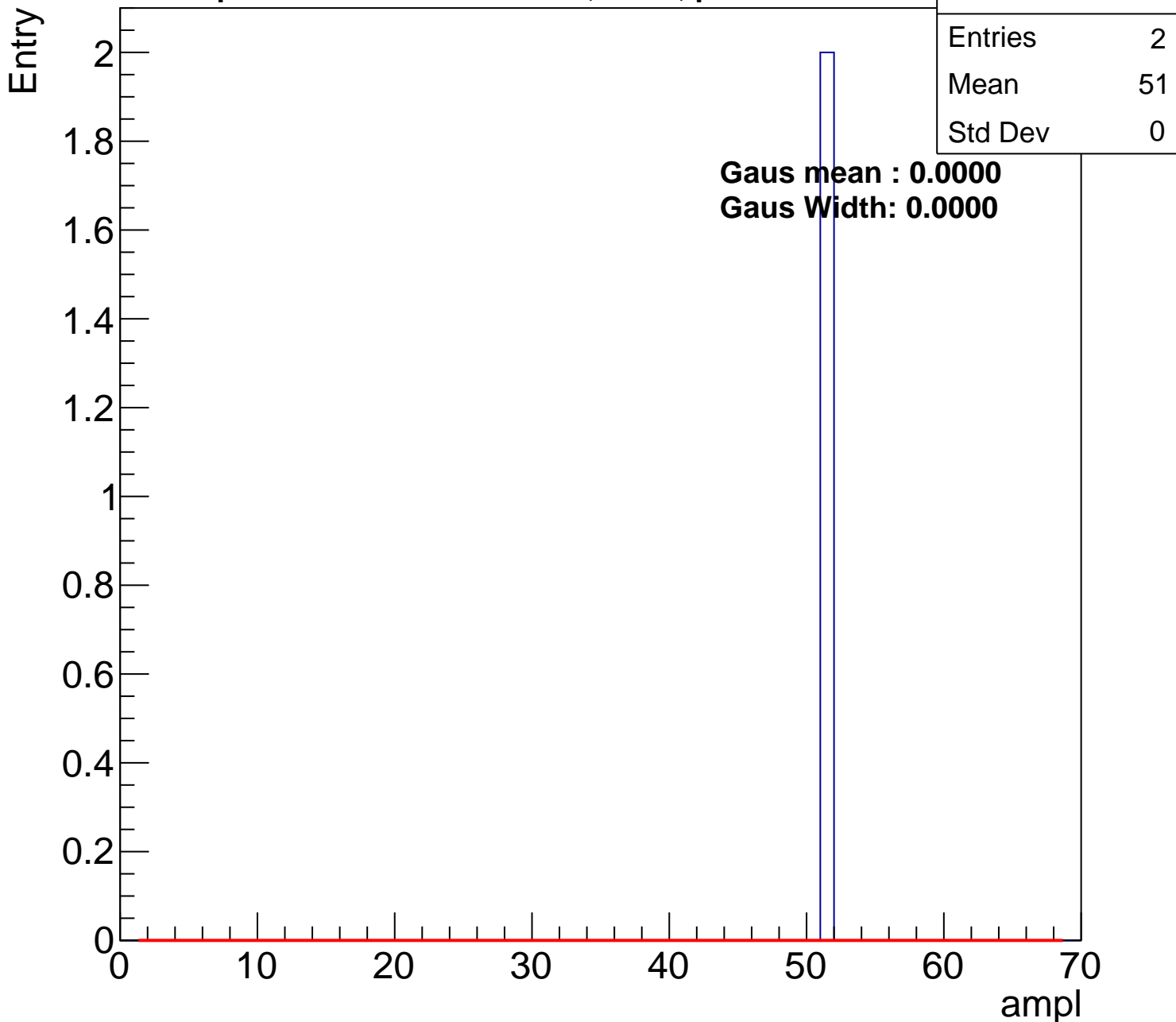
B0L100S, U13-ch71, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch71, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch71, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch71, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch71, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch72, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch72, adc1

calib_packv5_042523_0143.root, FC#6, port A1
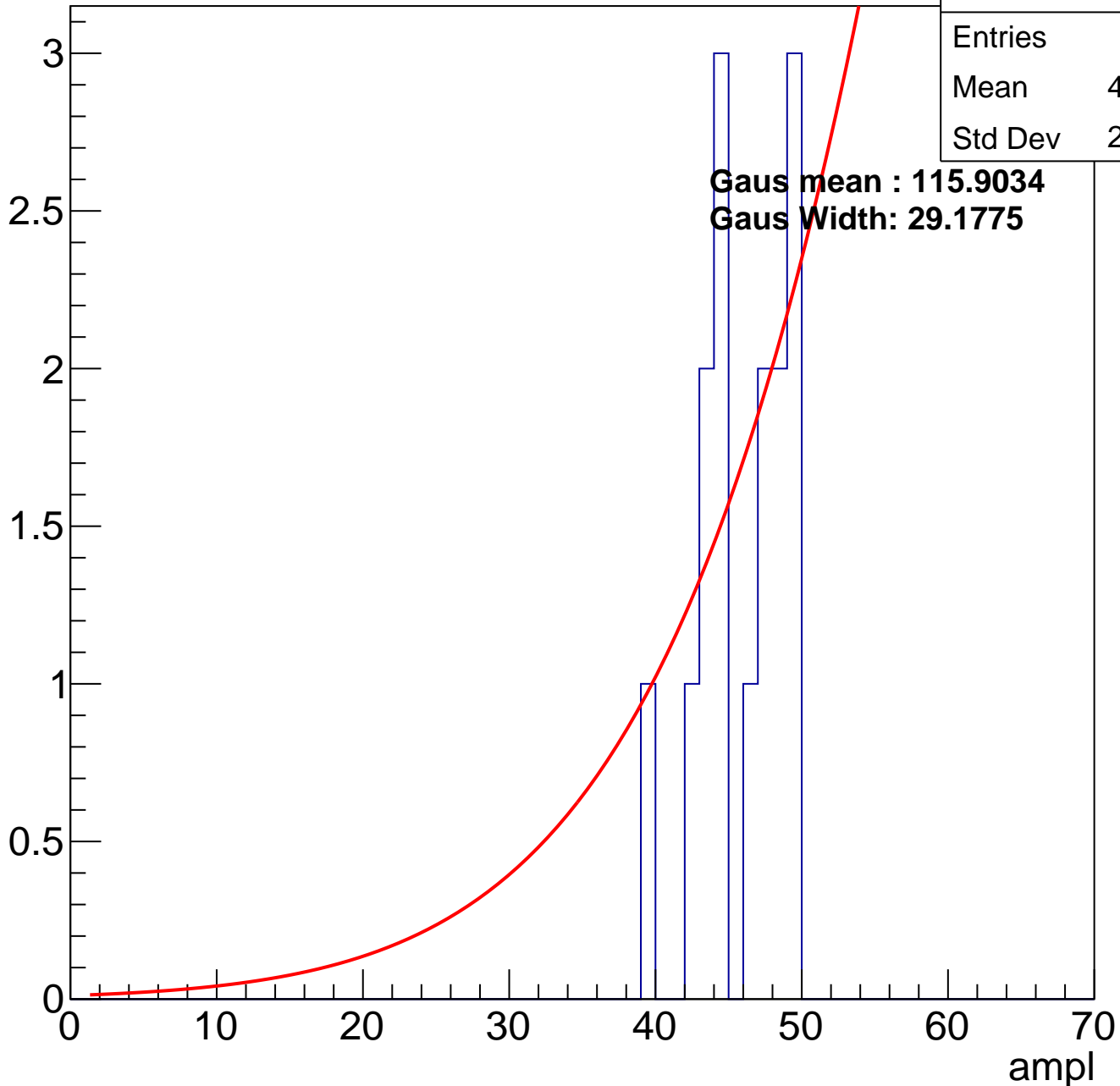
Entries	15
Mean	45.47
Std Dev	2.918

Gaus mean : 115.9034
Gaus Width: 29.1775
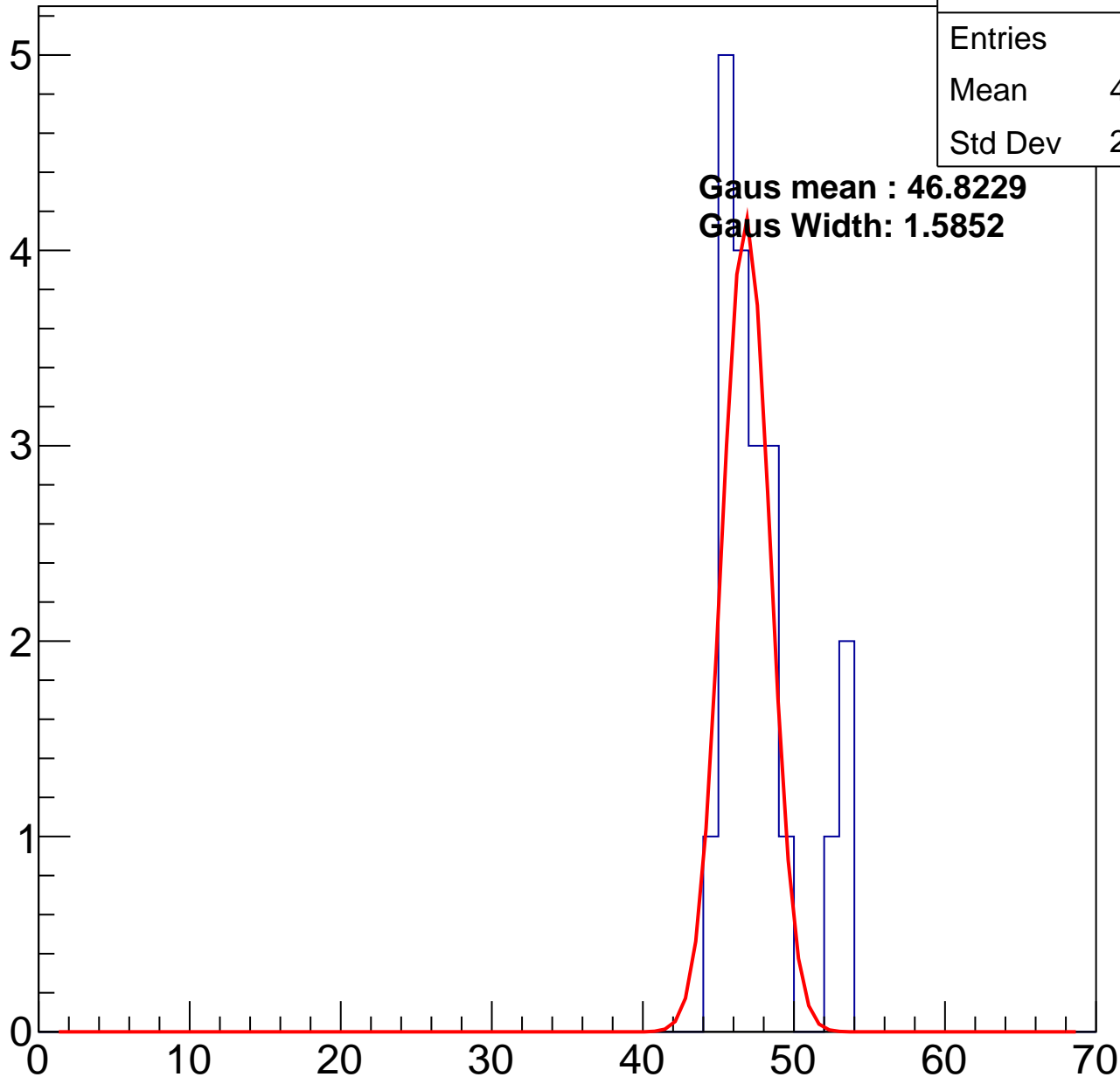
B0L100S, U13-ch72, adc3

calib_packv5_042523_0143.root, FC#6, port A1
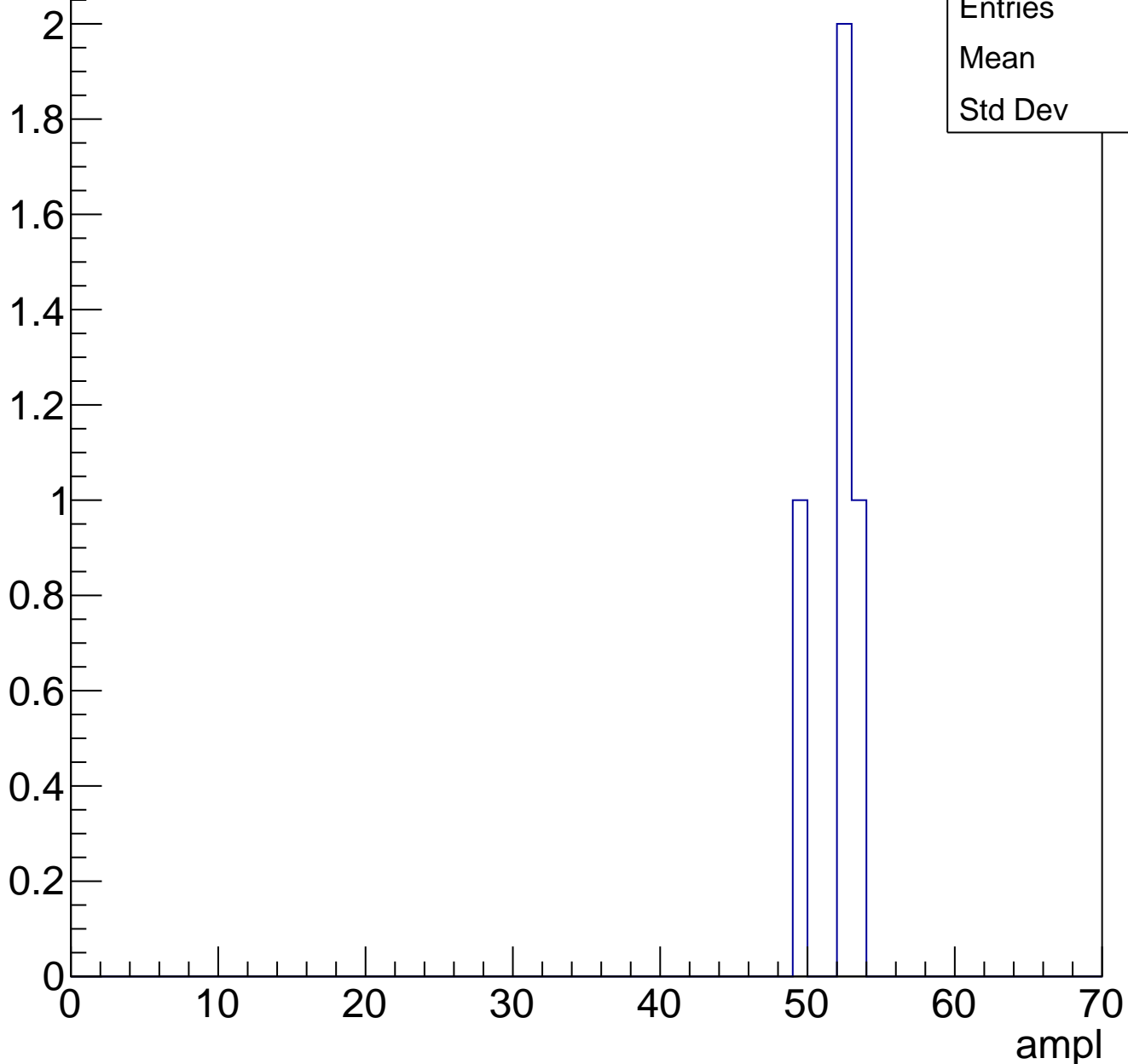
Entries	4
Mean	51.5
Std Dev	1.5

Entry

ampl

B0L100S, U13-ch72, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch72, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch72, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch72, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch73, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch73, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch73, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	51.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
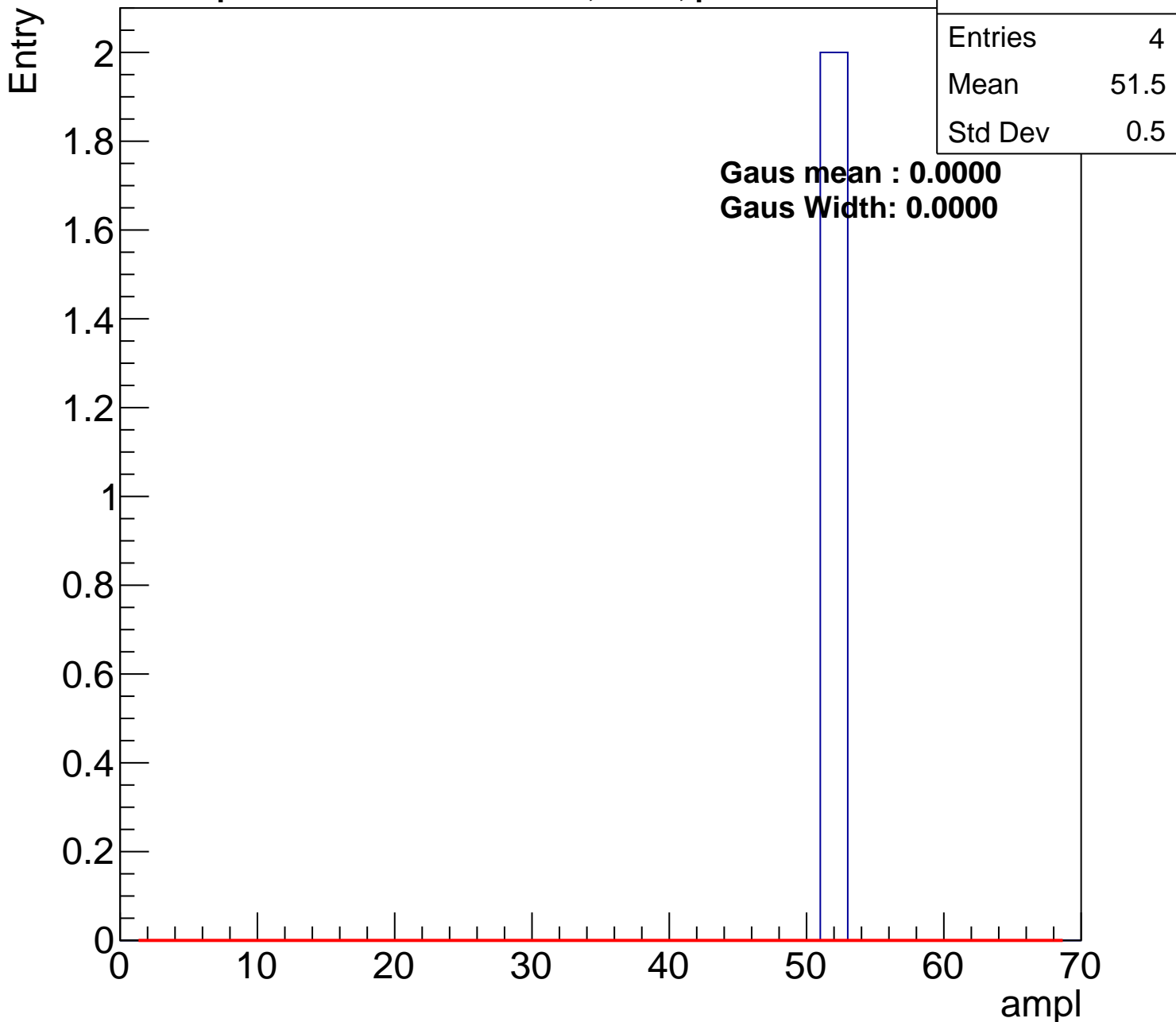
B0L100S, U13-ch73, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

ampl

B0L100S, U13-ch73, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch73, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch73, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch73, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch74, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch74, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	47
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
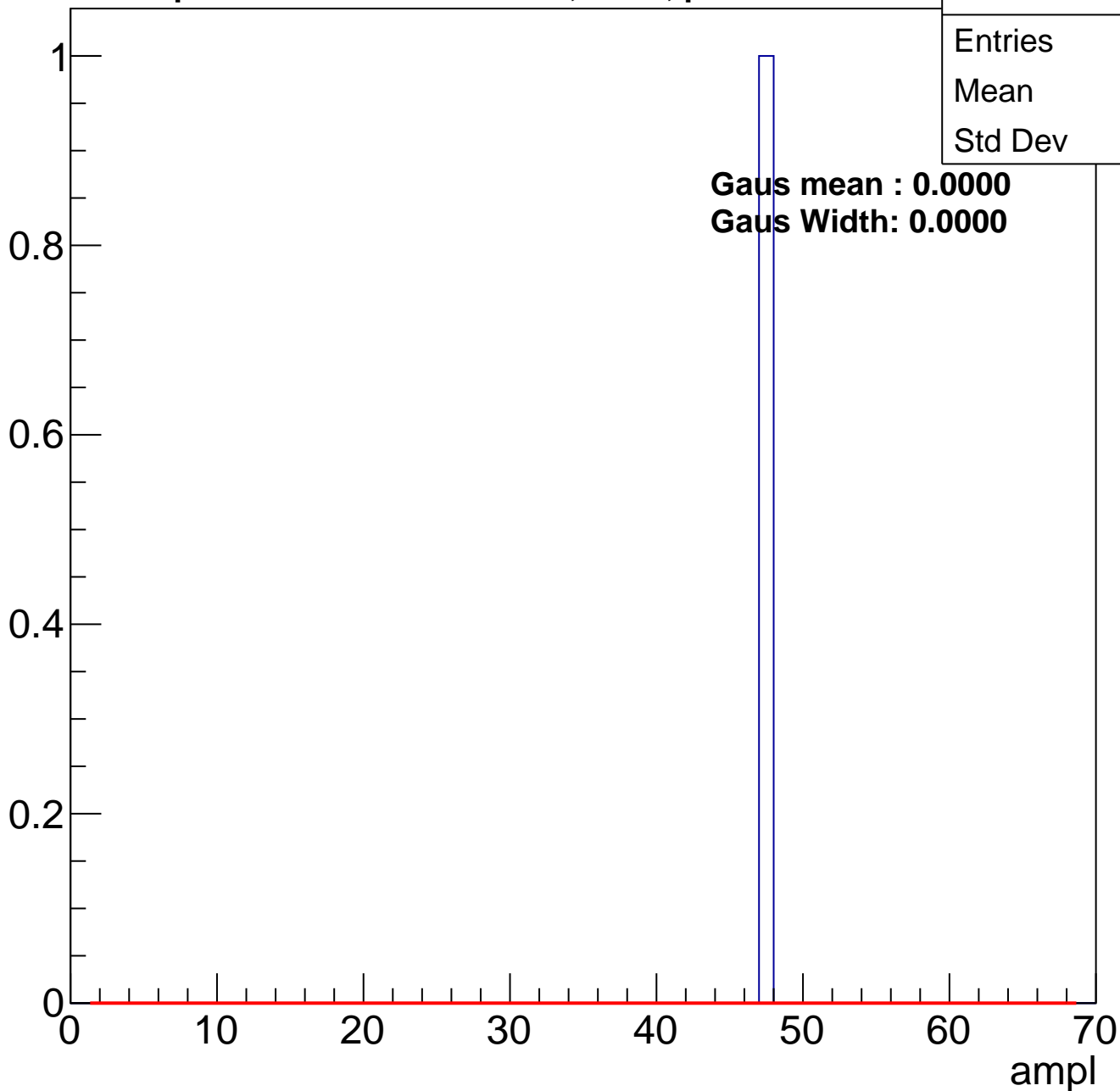
B0L100S, U13-ch74, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	46
Std Dev	1.414

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
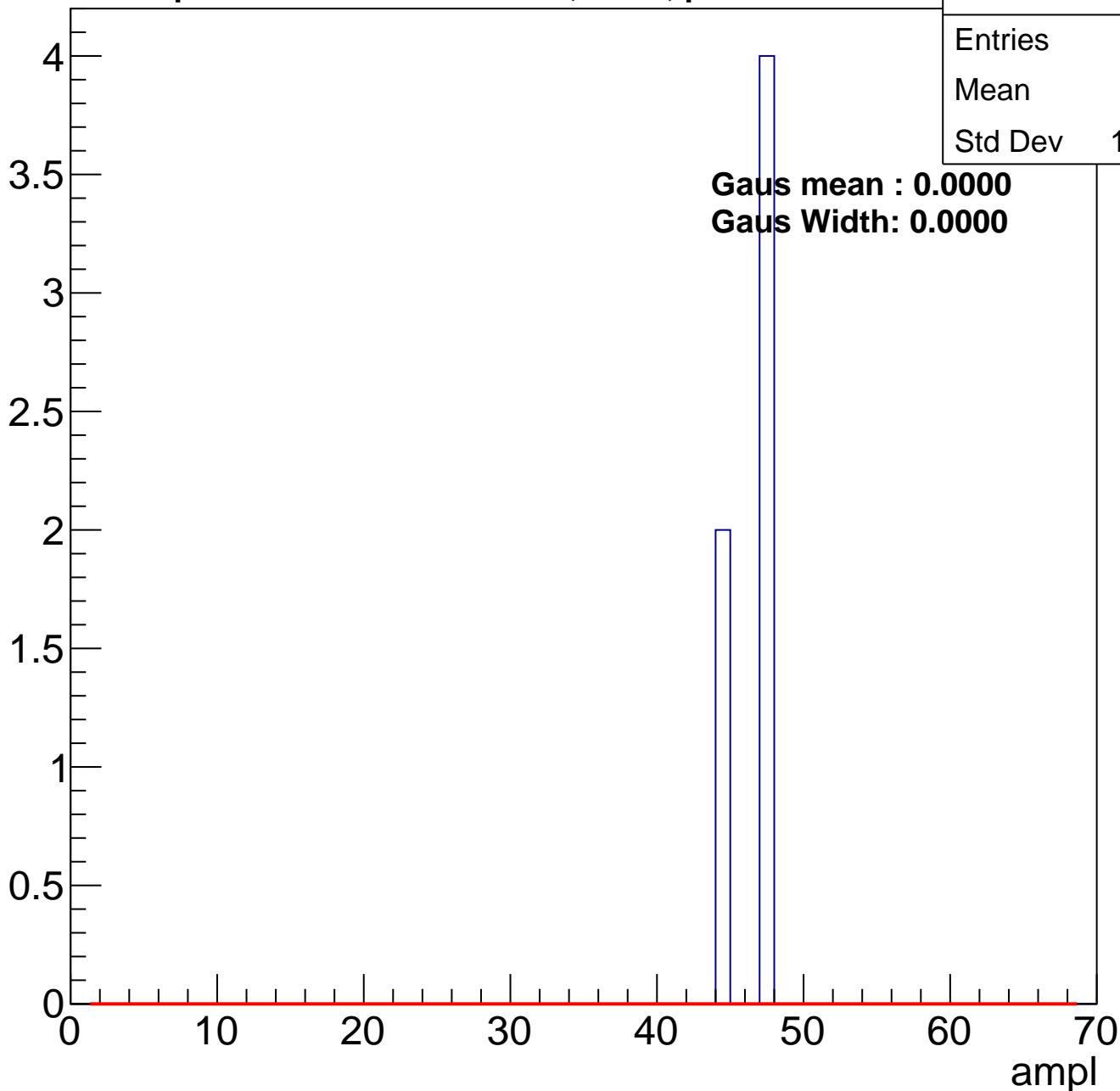
B0L100S, U13-ch74, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch74, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch74, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch74, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch74, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch75, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch75, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch75, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

B0L100S, U13-ch75, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch75, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch75, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch75, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch75, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch76, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	22
Mean	34.27
Std Dev	1.911

Gaus mean : 34.4893
Gaus Width: 2.0201
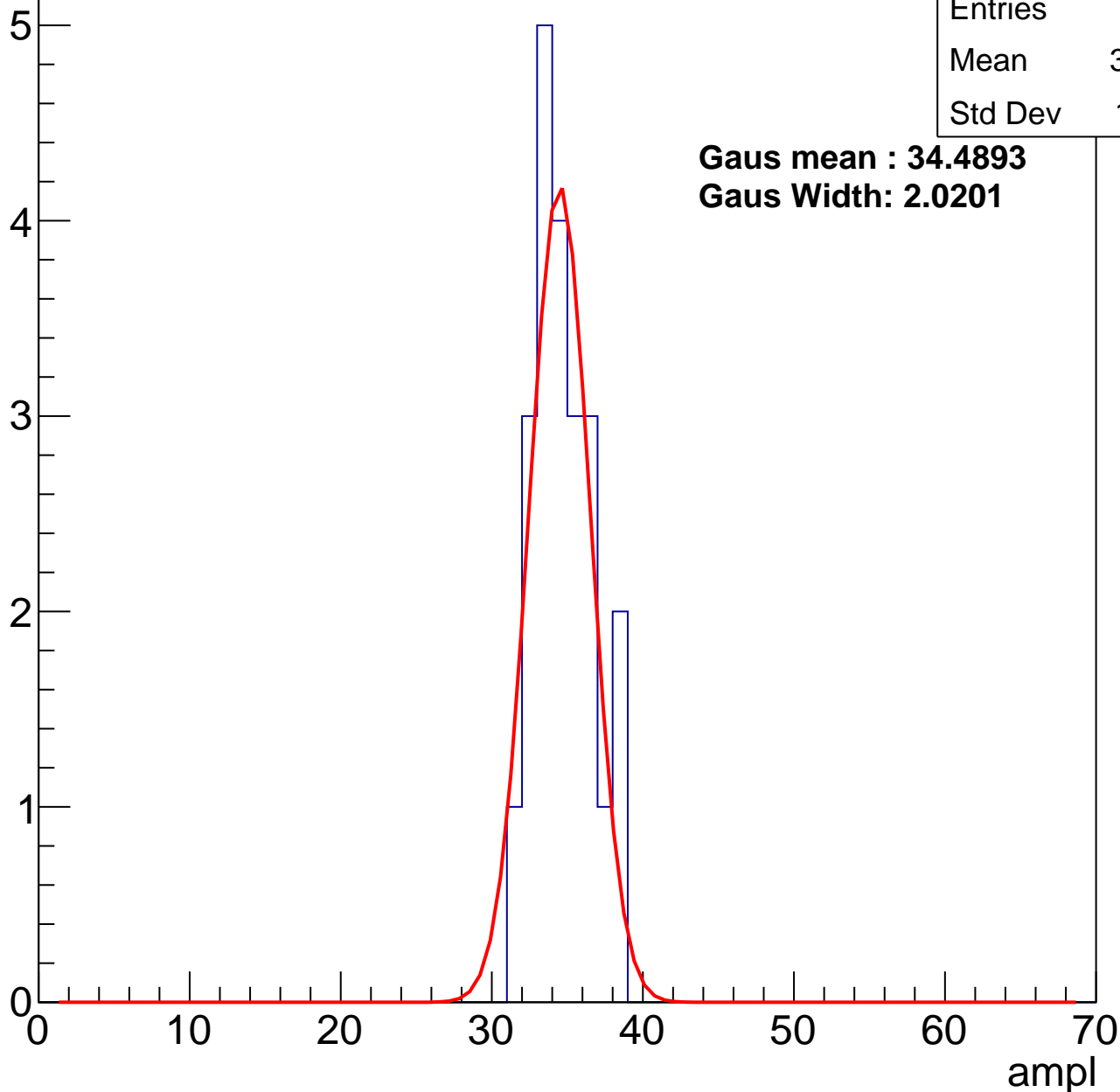
B0L100S, U13-ch76, adc1

calib_packv5_042523_0143.root, FC#6, port A1
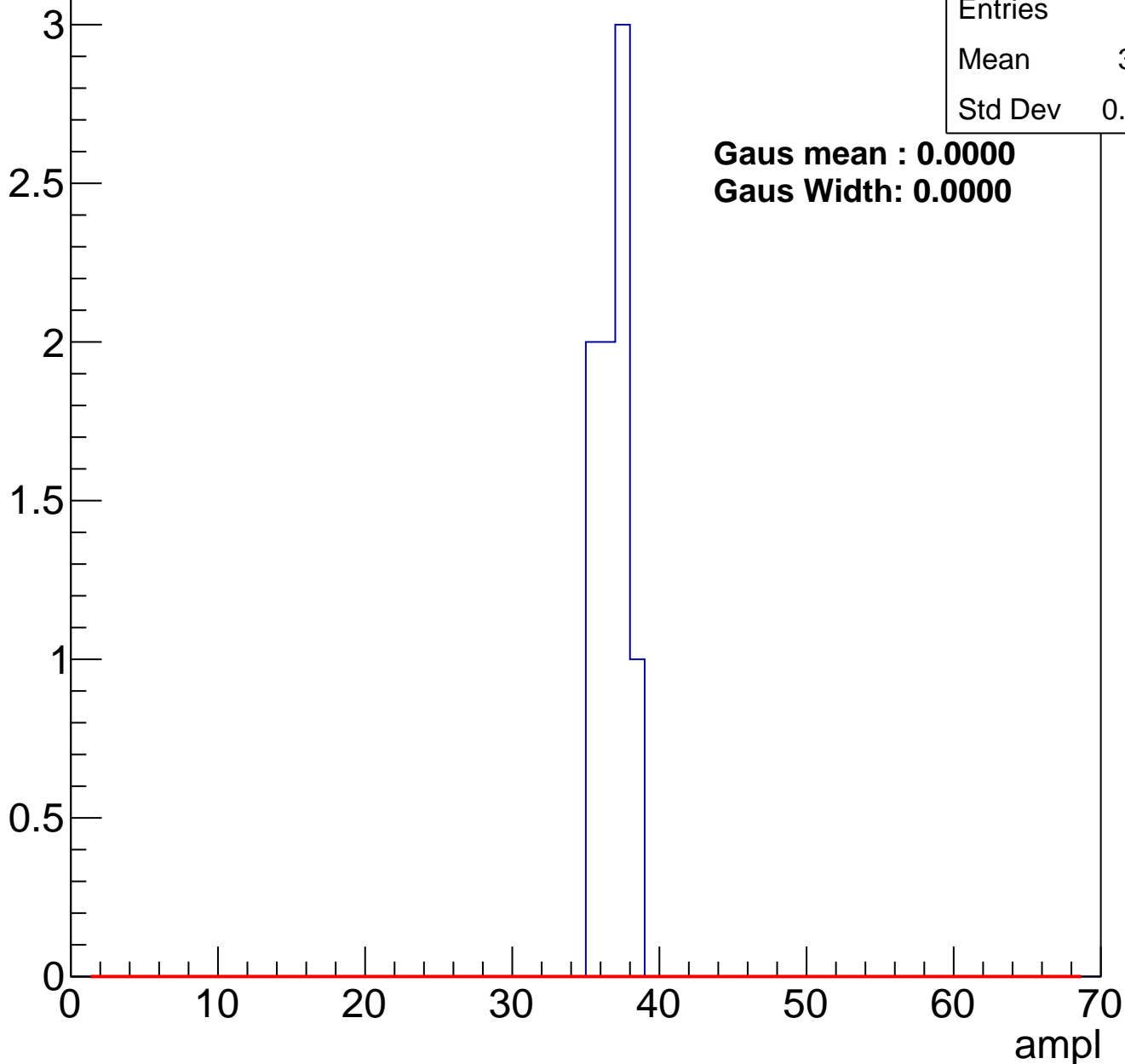
Entries	8
Mean	36.38
Std Dev	0.9922

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch76, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	37
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
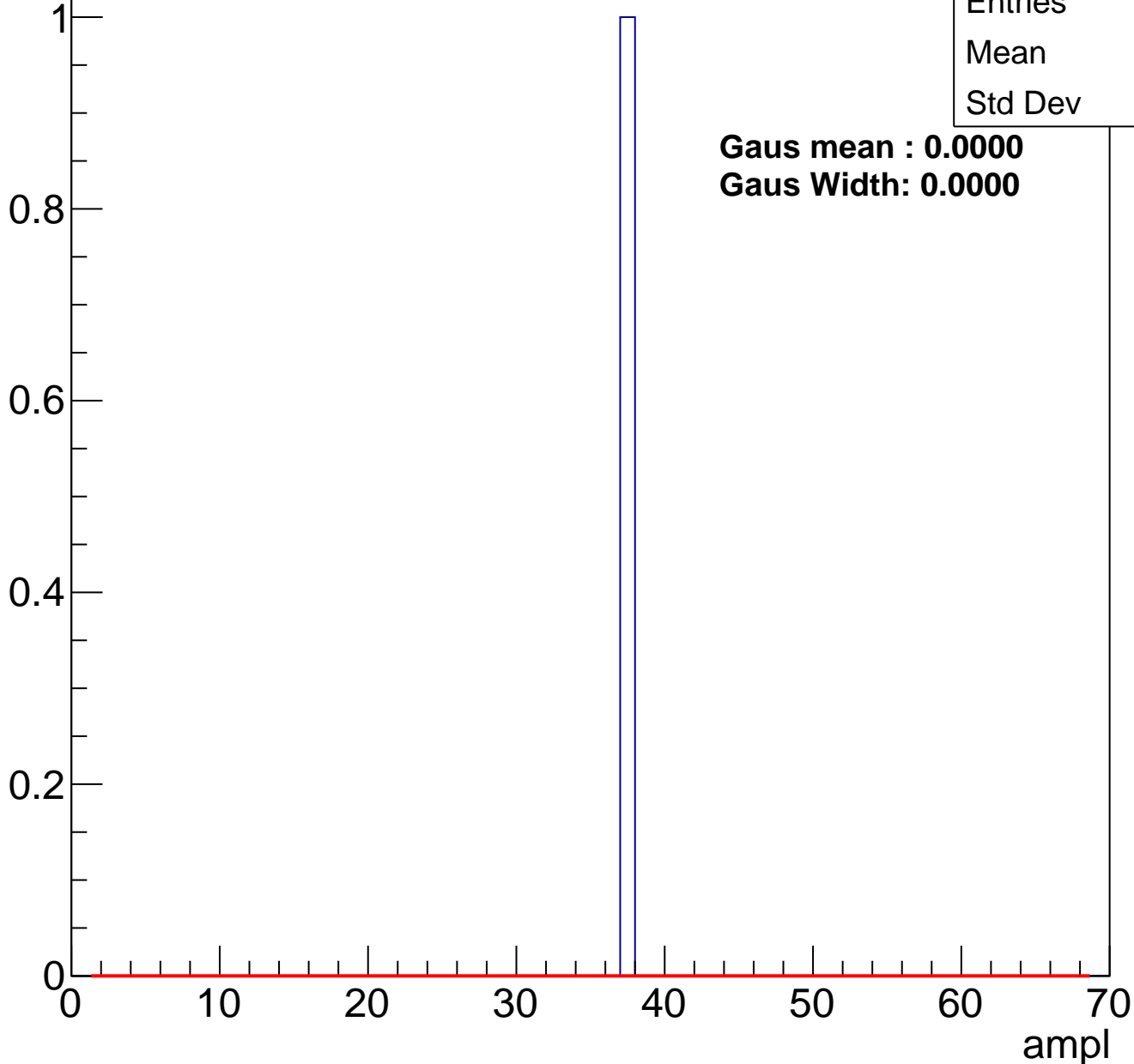
B0L100S, U13-ch76, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch76, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch76, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch76, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch76, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch77, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch77, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch77, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

B0L100S, U13-ch77, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch77, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch77, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch77, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch77, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch78, adc0

calib_packv5_042523_0143.root, FC#6, port A1
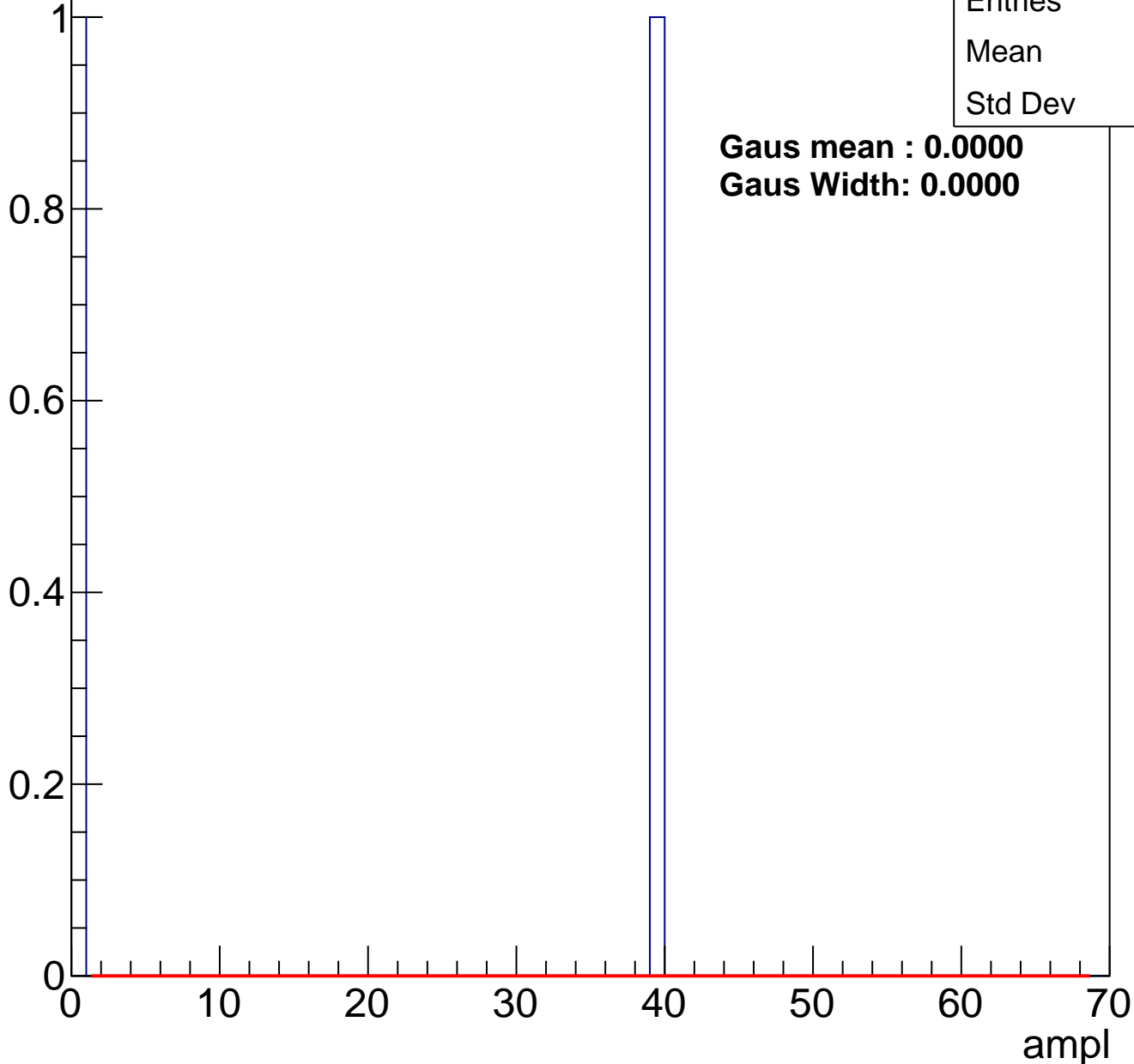
Entries	2
Mean	19.5
Std Dev	19.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch78, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
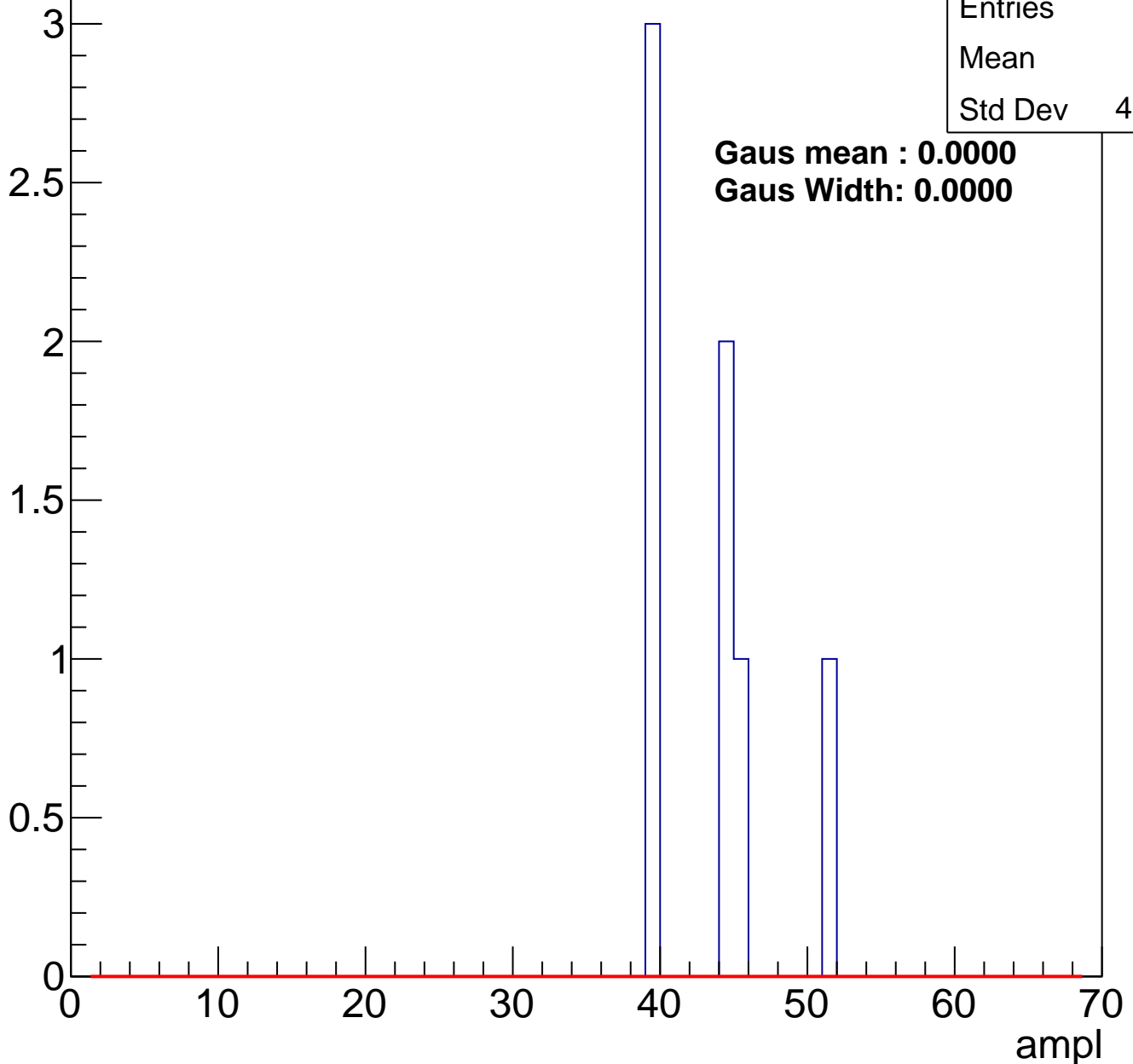
Entries	7
Mean	43
Std Dev	4.106

ampl

Entry

B0L100S, U13-ch78, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	15
Mean	47.53
Std Dev	2.247

Gaus mean : 67.8548
Gaus Width: 10.1107
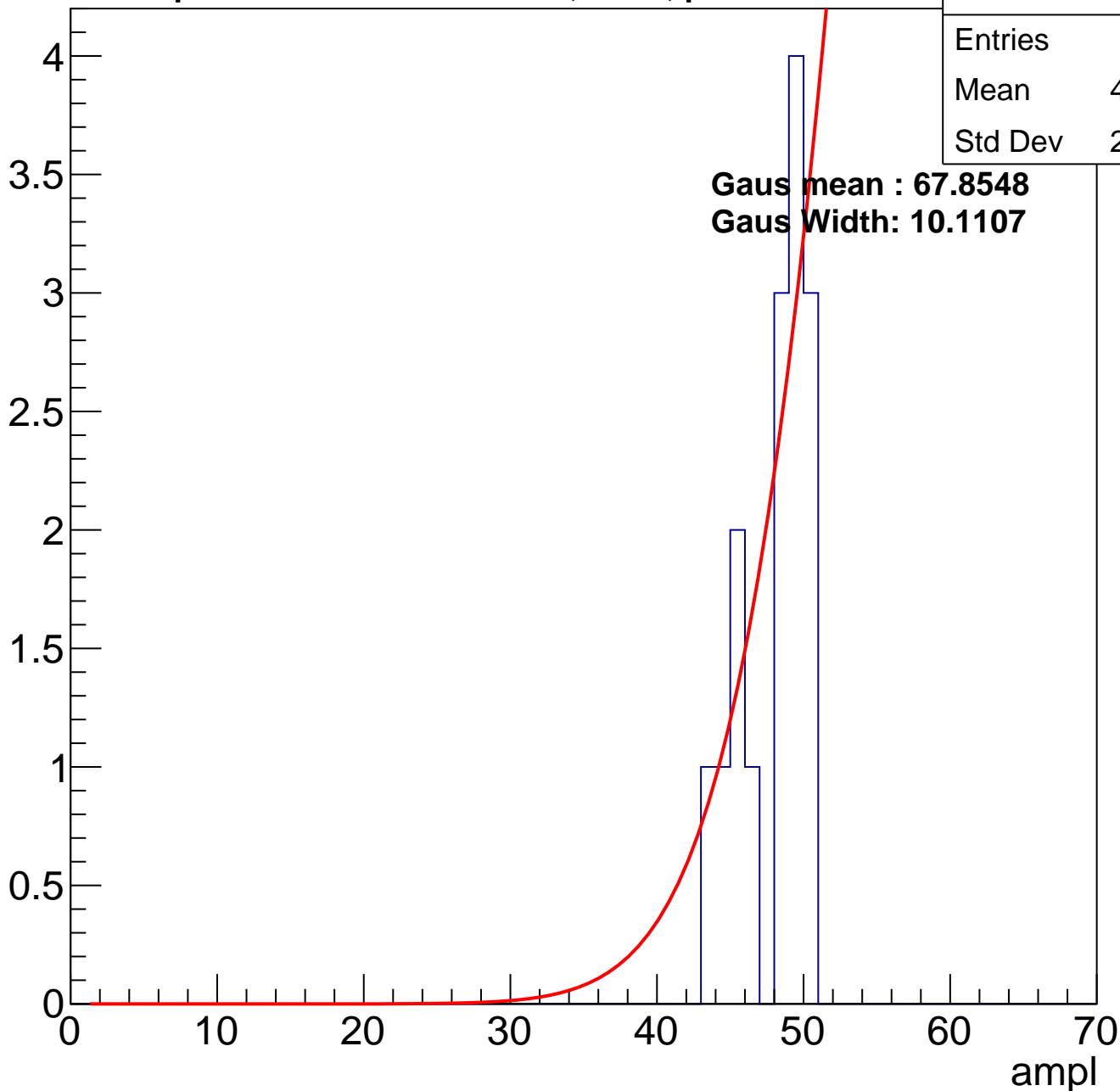
Entry

ampl

B0L100S, U13-ch78, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50
Std Dev	0.7071

Entry

ampl

B0L100S, U13-ch78, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch78, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch78, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch78, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch79, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch79, adc1

calib_packv5_042523_0143.root, FC#6, port A1
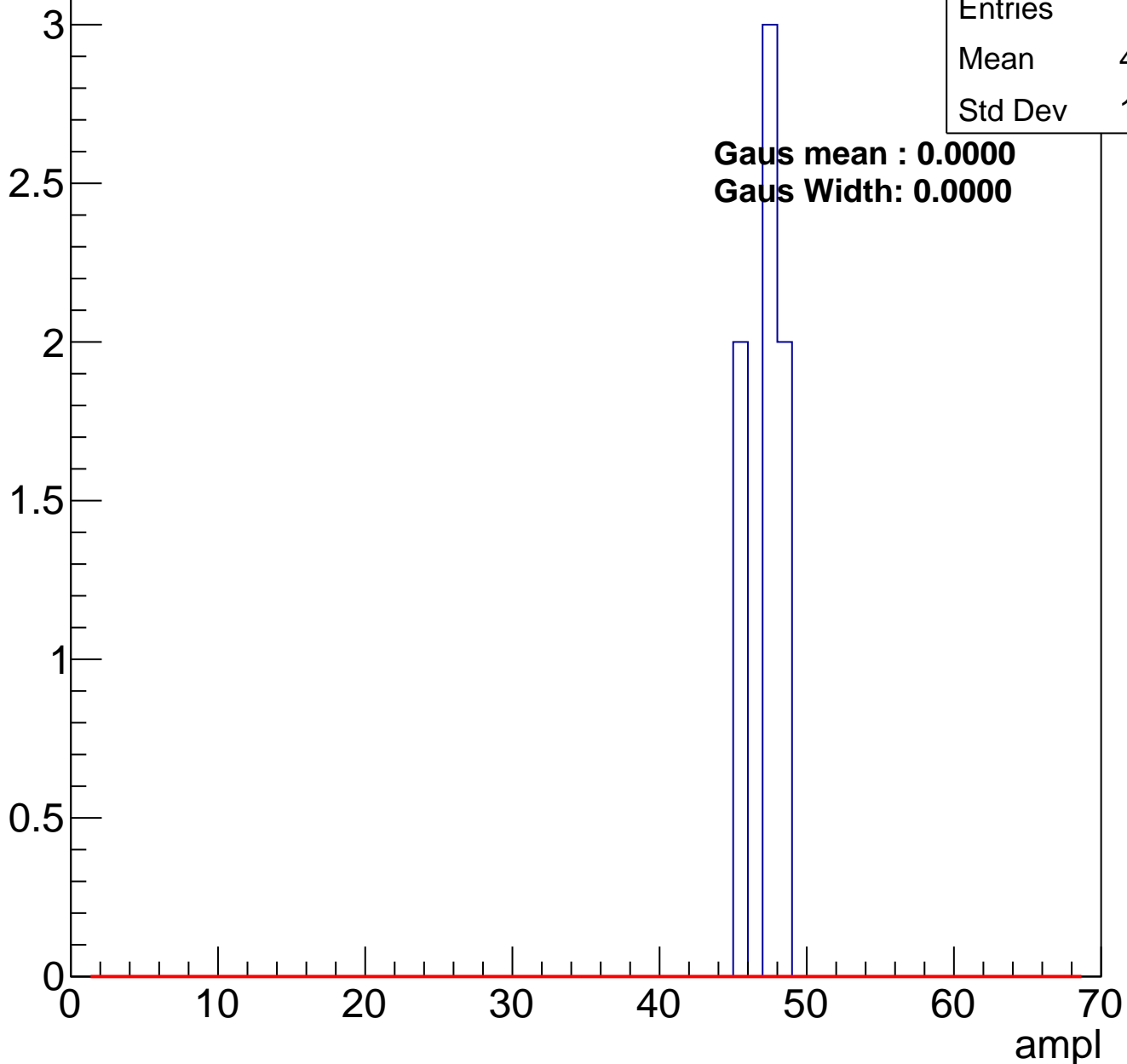
Entries	7
Mean	46.71
Std Dev	1.161

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
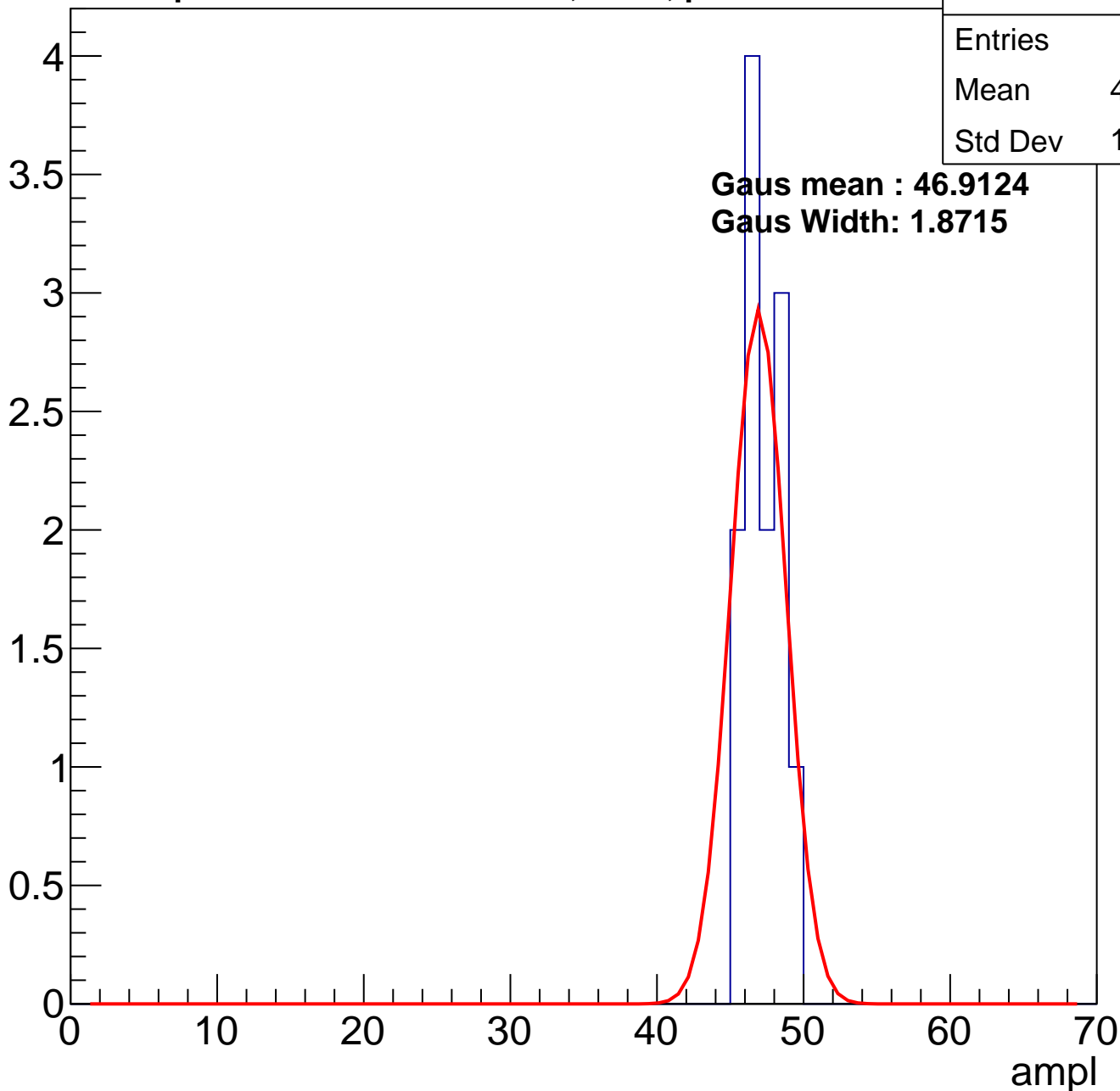
B0L100S, U13-ch79, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	46
Std Dev	0

Entry

ampl
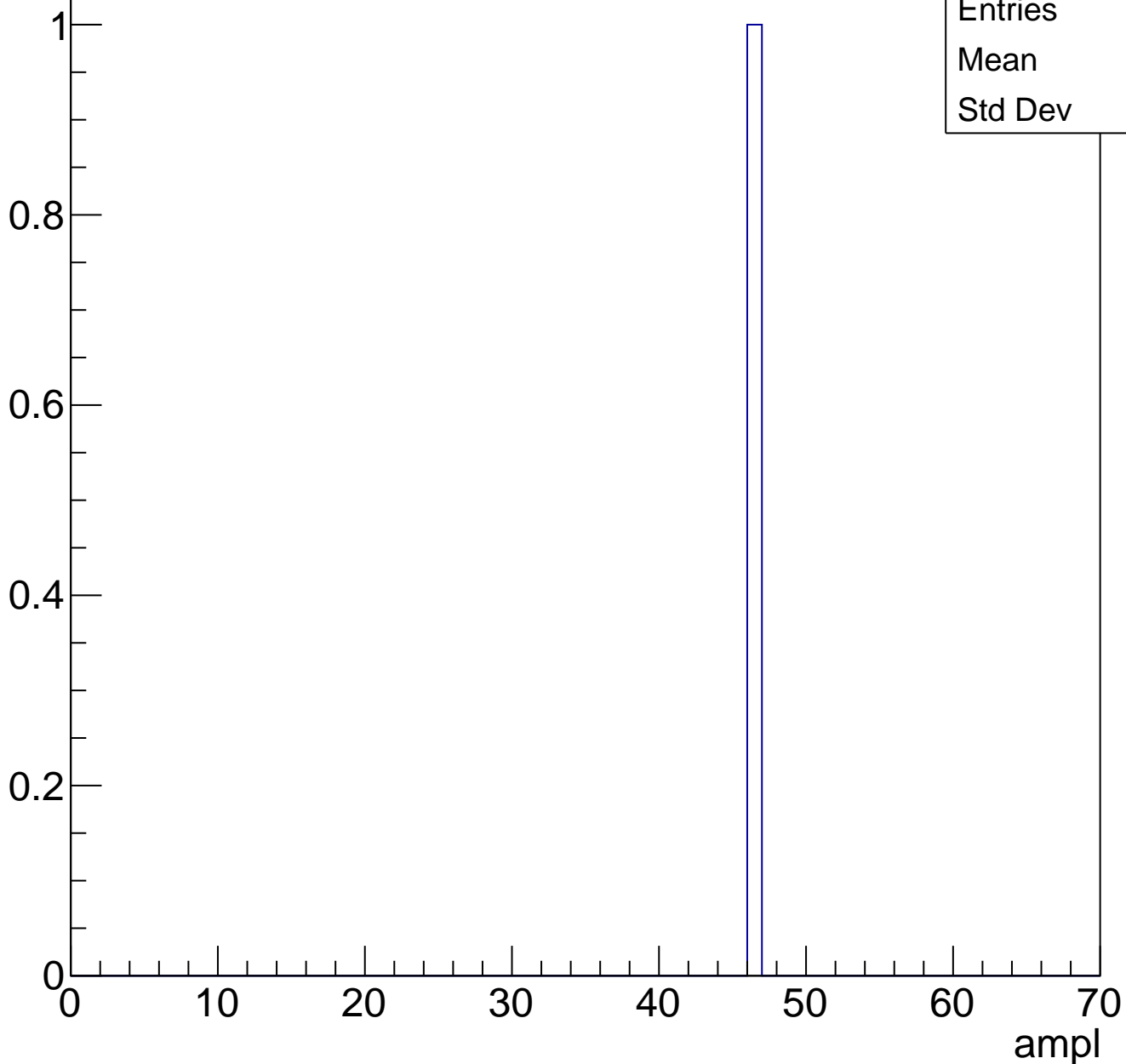
B0L100S, U13-ch79, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch79, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch79, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch79, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch80, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch80, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch80, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	52.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
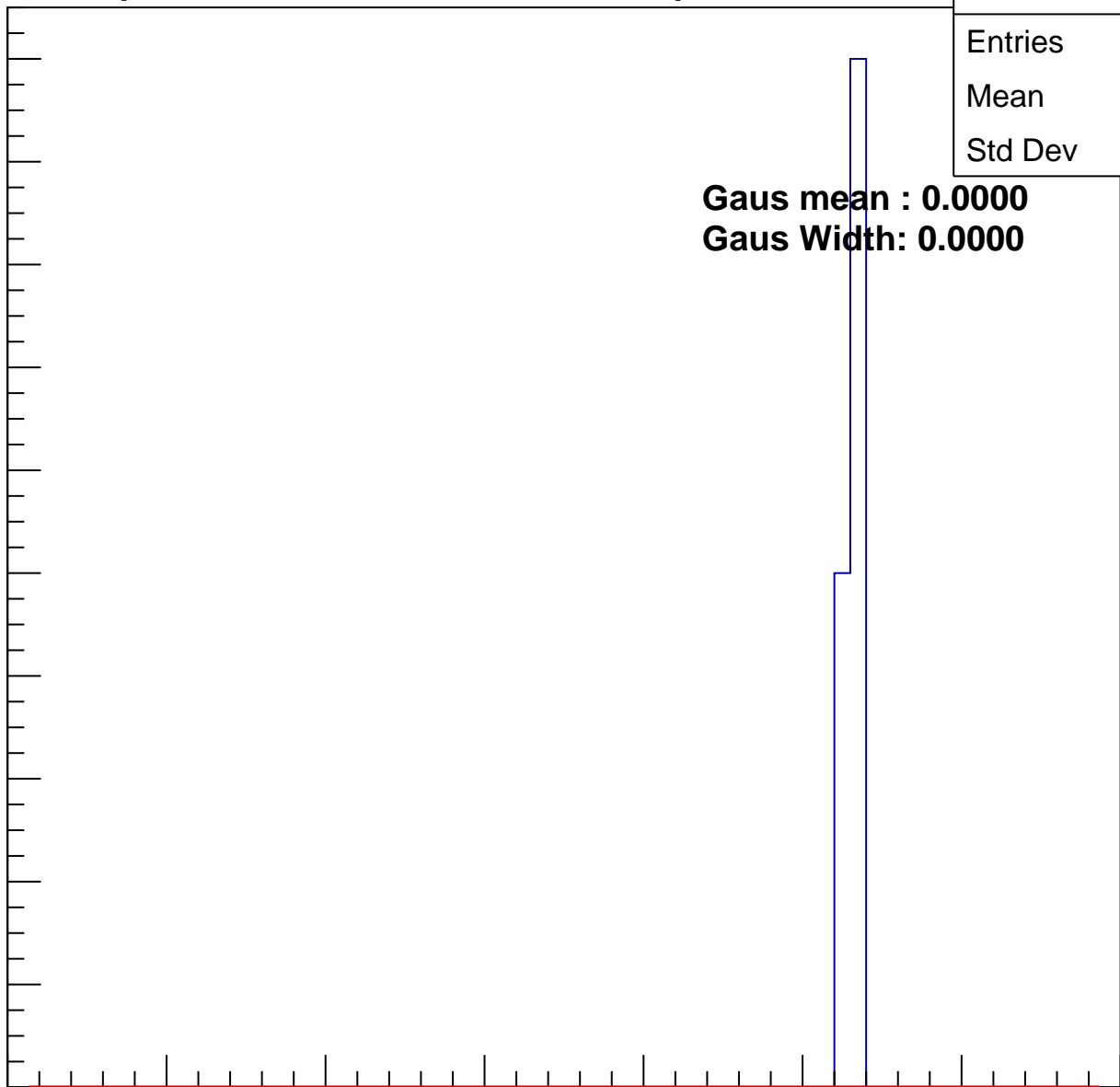
B0L100S, U13-ch80, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.2
Std Dev	0.4

Entry

ampl
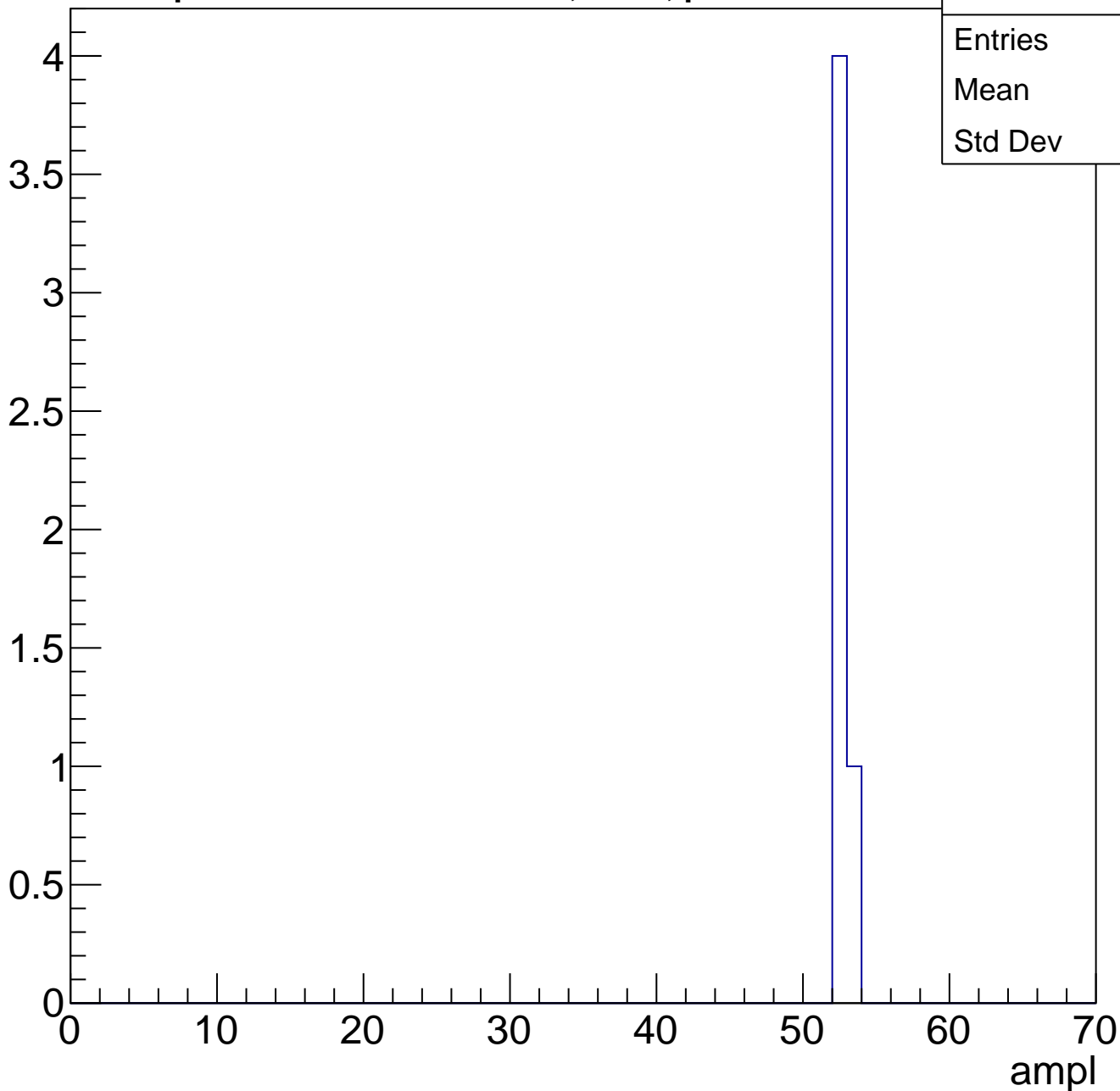
B0L100S, U13-ch80, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch80, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch80, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch80, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch81, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch81, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch81, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	48.75
Std Dev	1.299

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
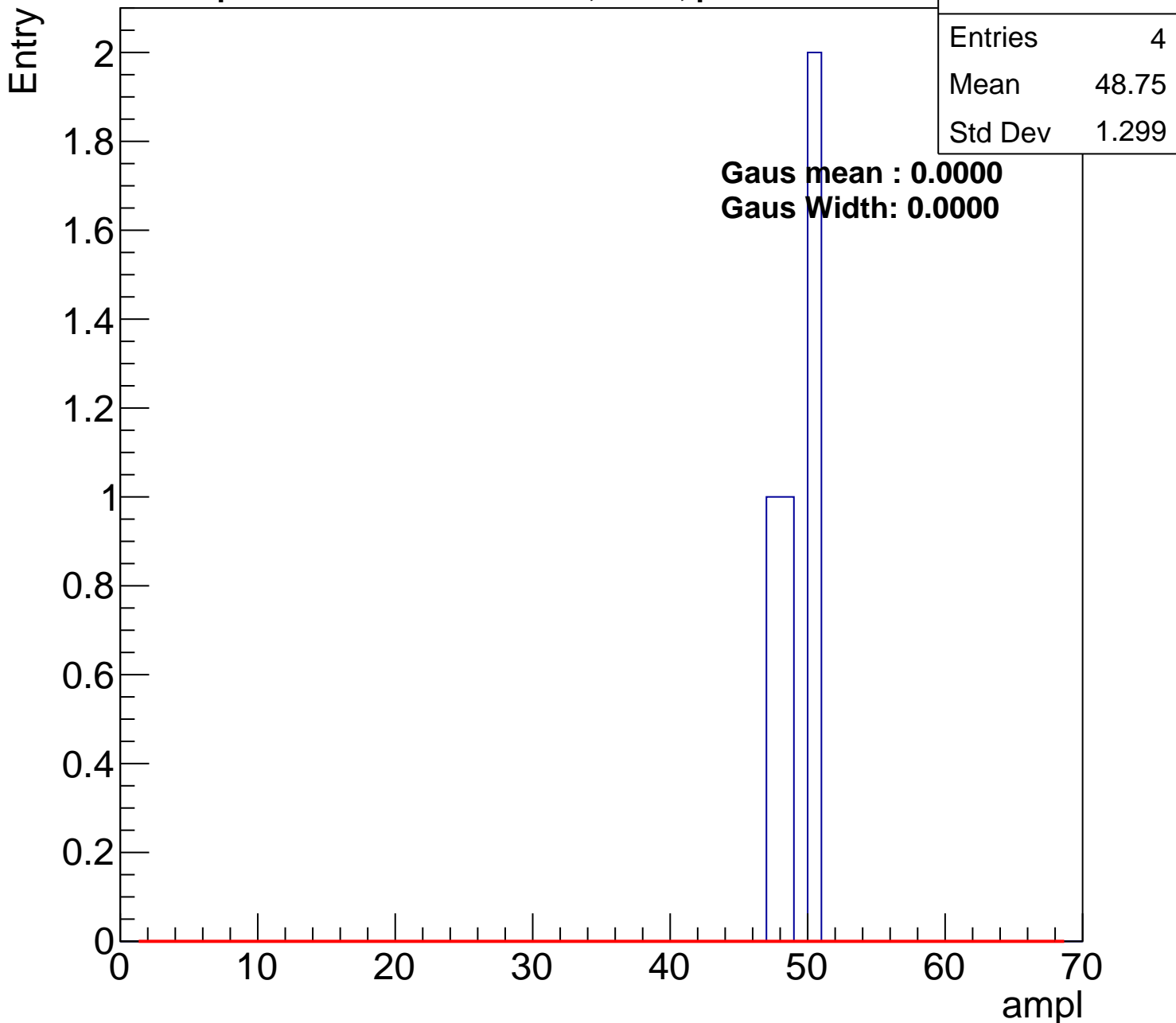
B0L100S, U13-ch81, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch81, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch81, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch81, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch81, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch82, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch82, adc1

calib_packv5_042523_0143.root, FC#6, port A1
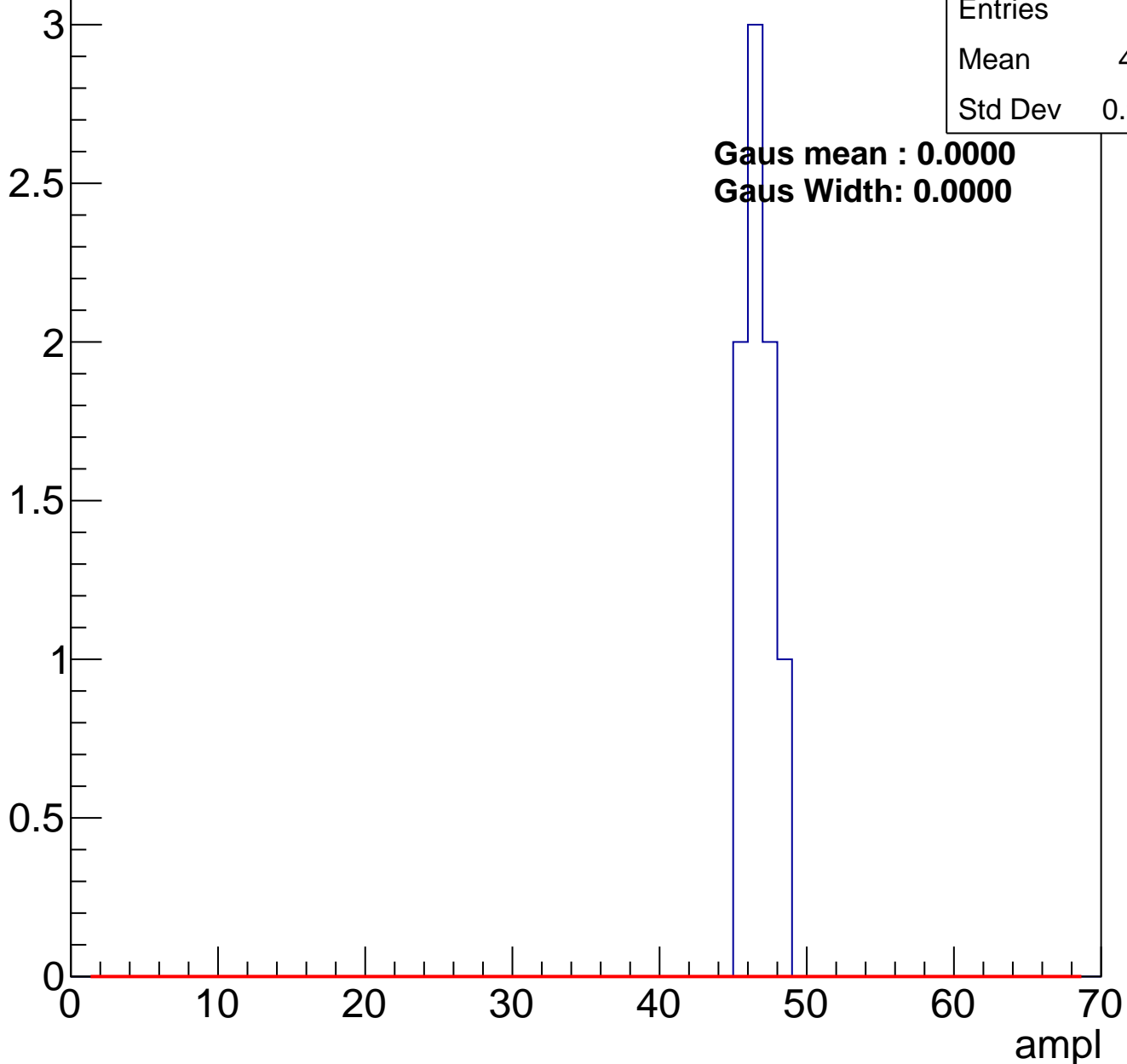
Entries	8
Mean	46.25
Std Dev	0.9682

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch82, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 54.3066
Gaus Width: 11.5708

Entries	11
Mean	46.55
Std Dev	1.157

Entry

ampl
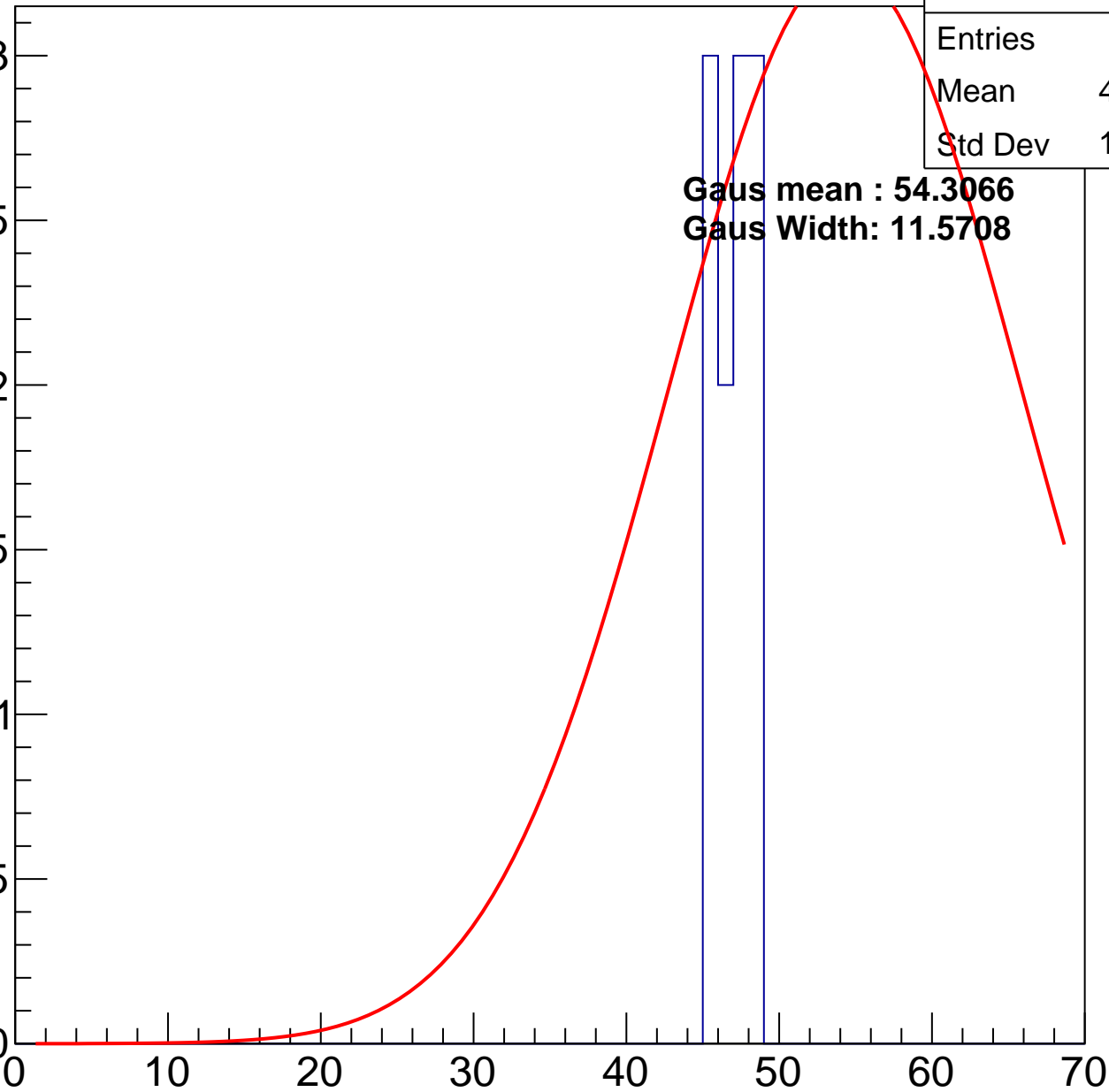
B0L100S, U13-ch82, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48.5
Std Dev	0.5

ampl
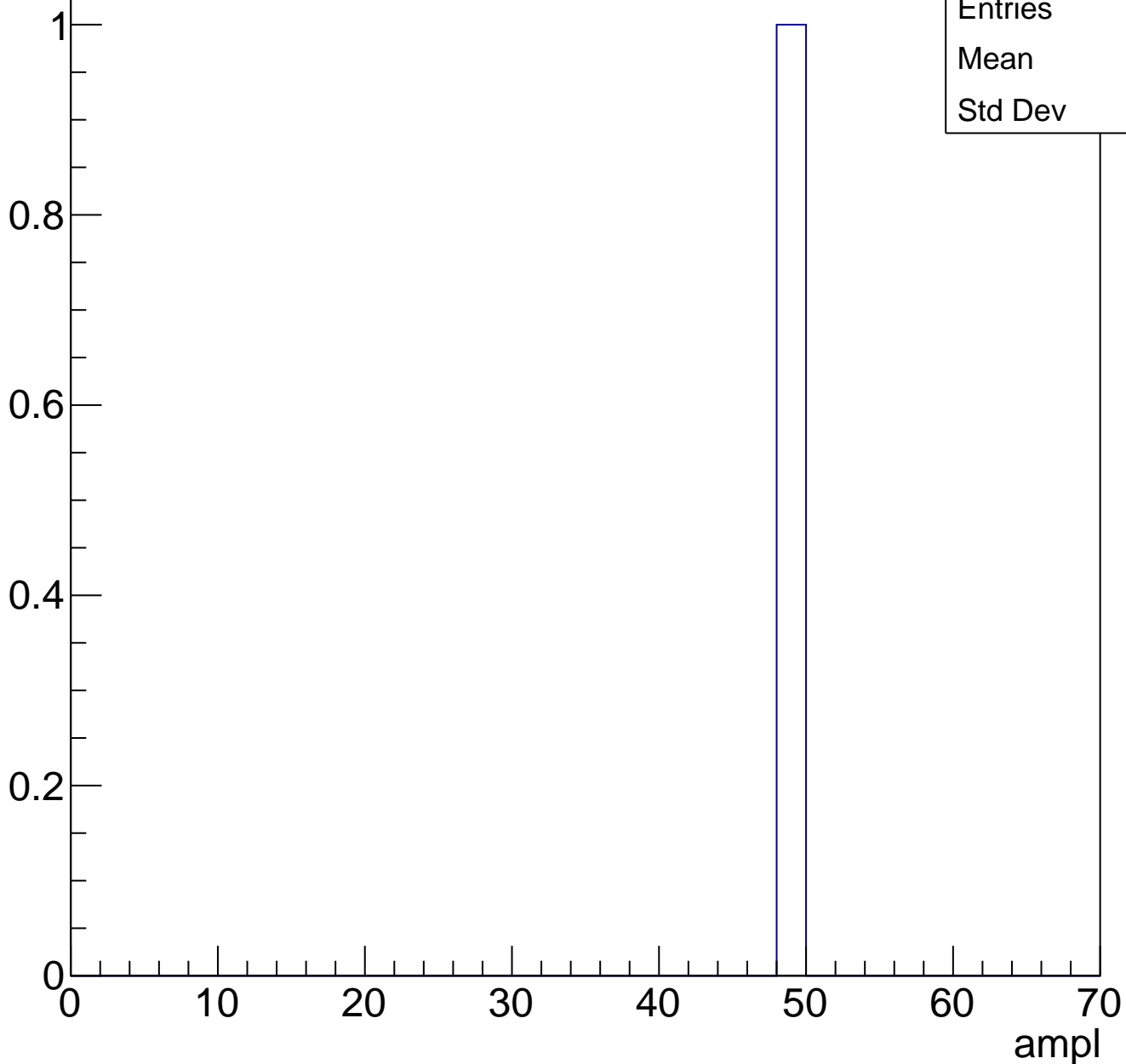
Entry

B0L100S, U13-ch82, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch82, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch82, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch82, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch83, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch83, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch83, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch83, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch83, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch83, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch83, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch83, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch84, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	37.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
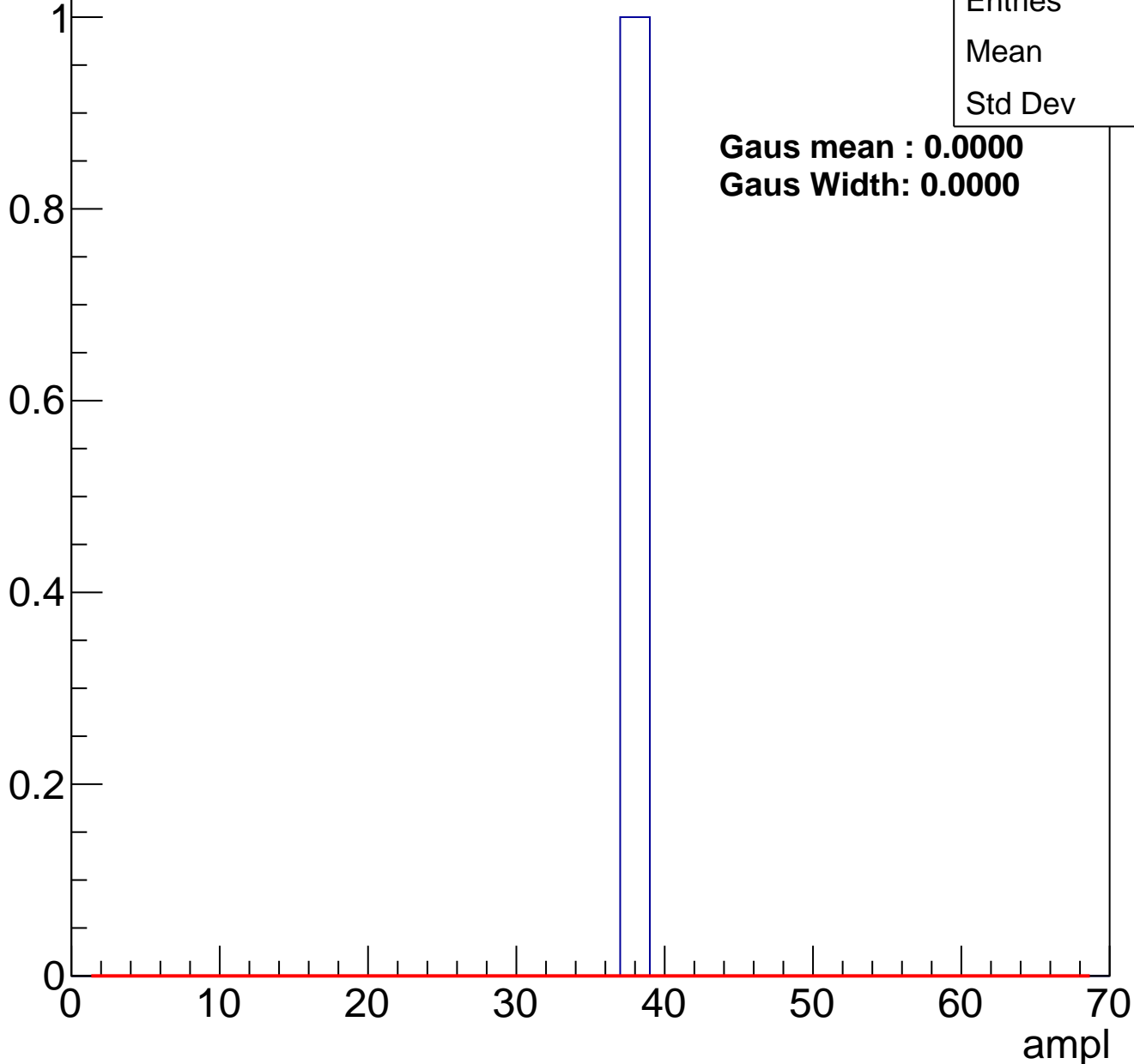
B0L100S, U13-ch84, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	43.09
Std Dev	3.63

Gaus mean : 43.5777
Gaus Width: 4.9282
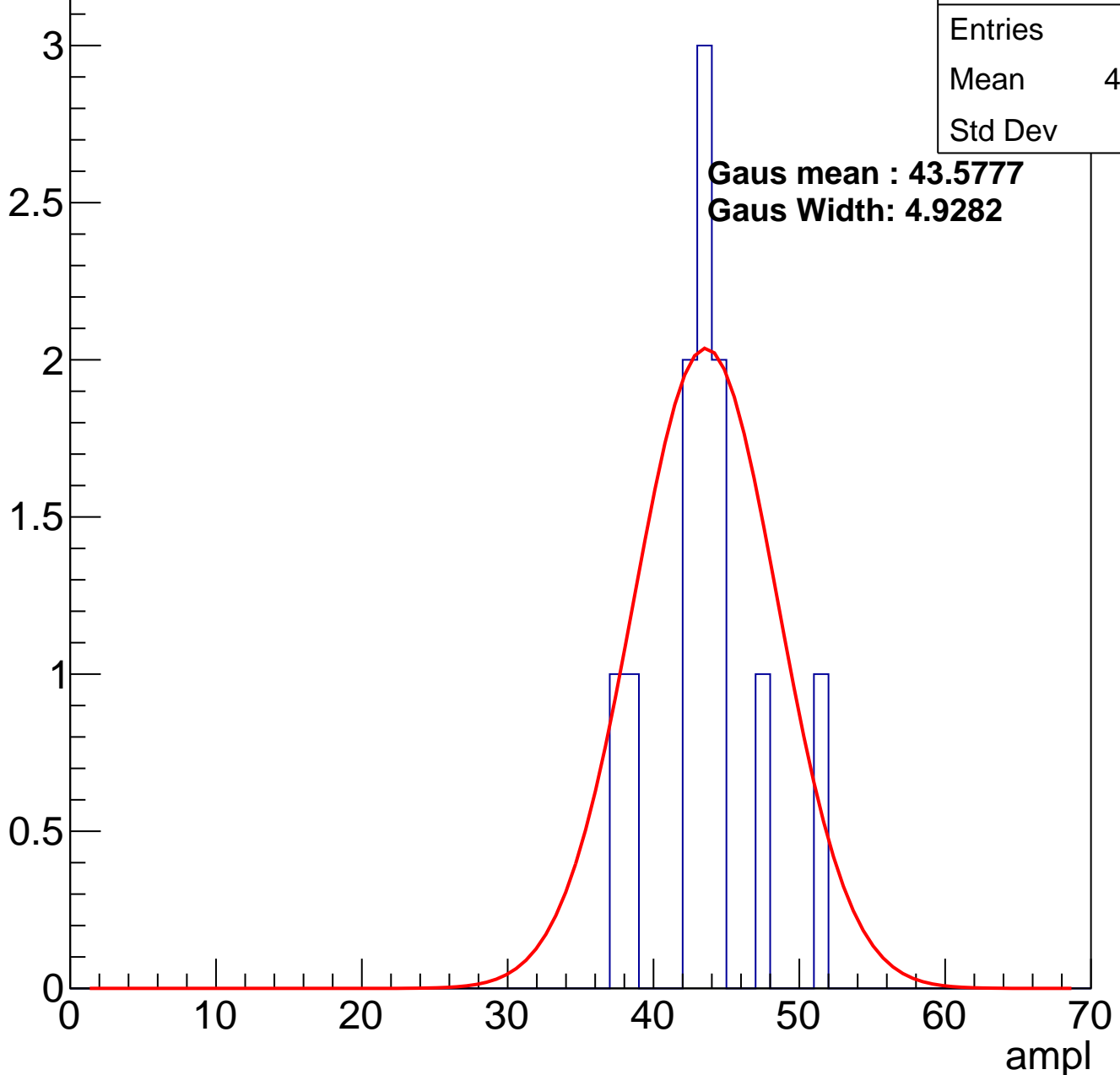
B0L100S, U13-ch84, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	15
Mean	47.73
Std Dev	3.732

Gaus mean : 74.0637
Gaus Width: 37.3186

Entry

ampl
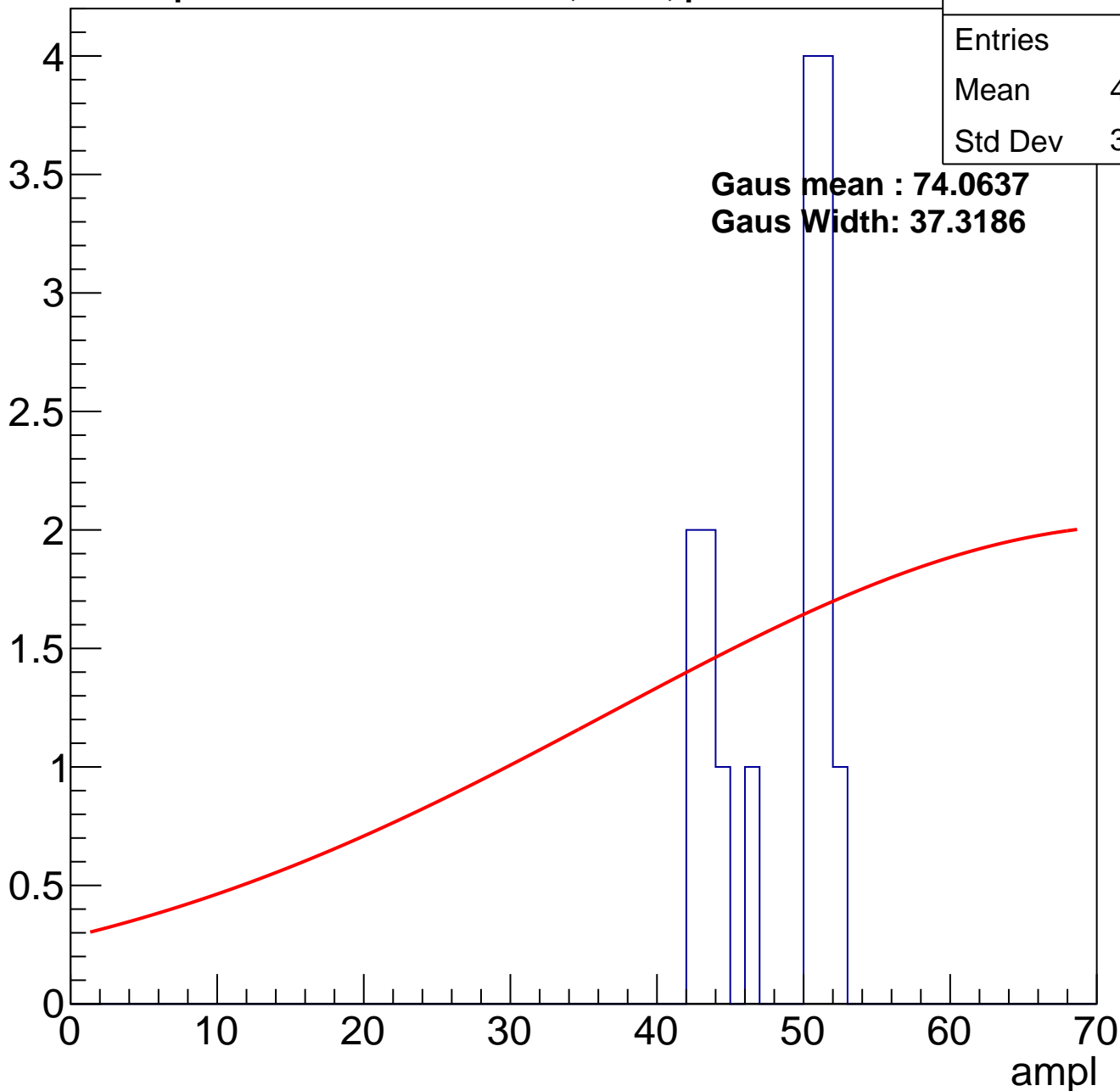
B0L100S, U13-ch84, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

Entry

ampl
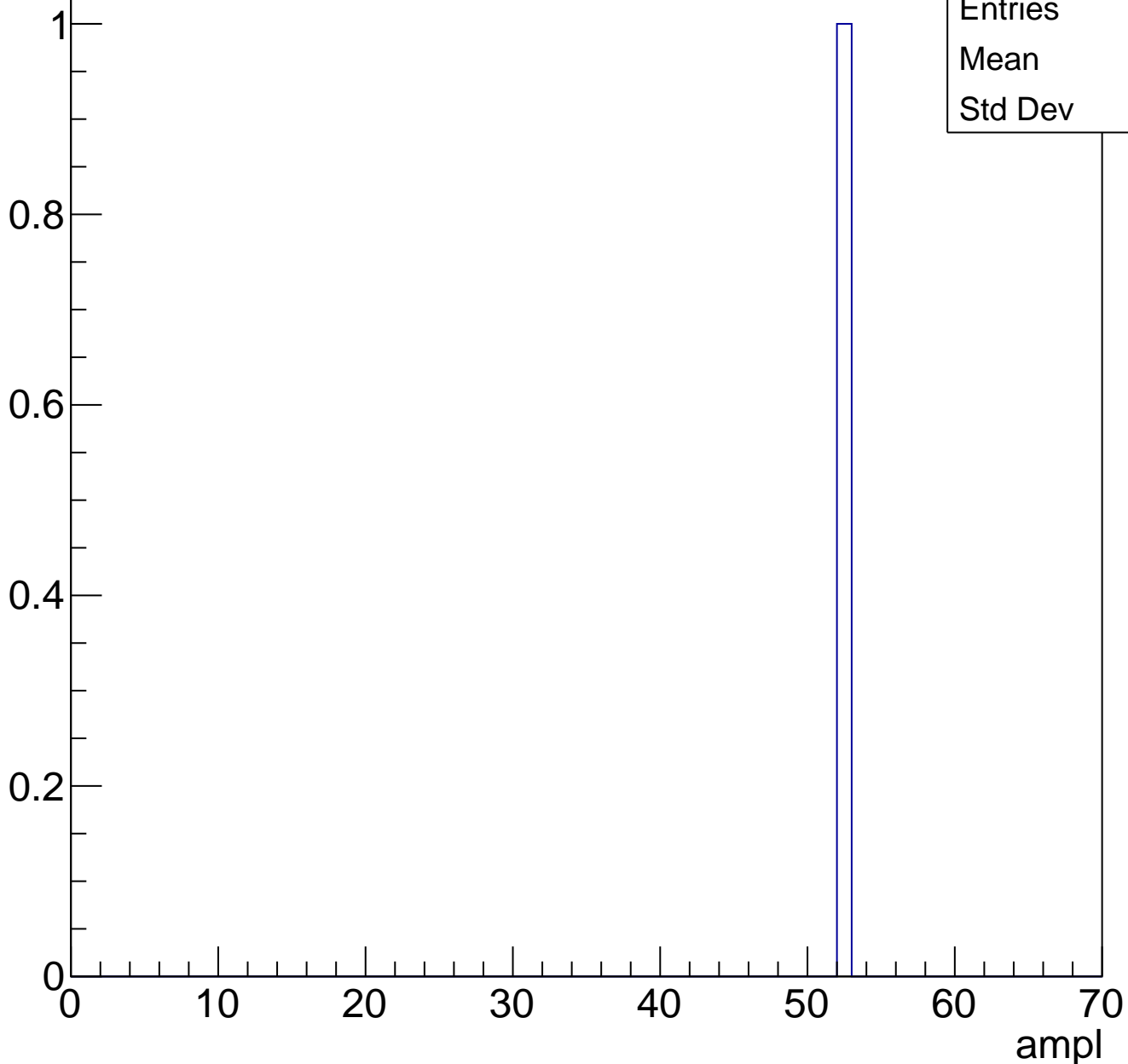
B0L100S, U13-ch84, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch84, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch84, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch84, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch85, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch85, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	47.86
Std Dev	1.959

Gaus mean : 0.0000
Gaus Width: 0.0000
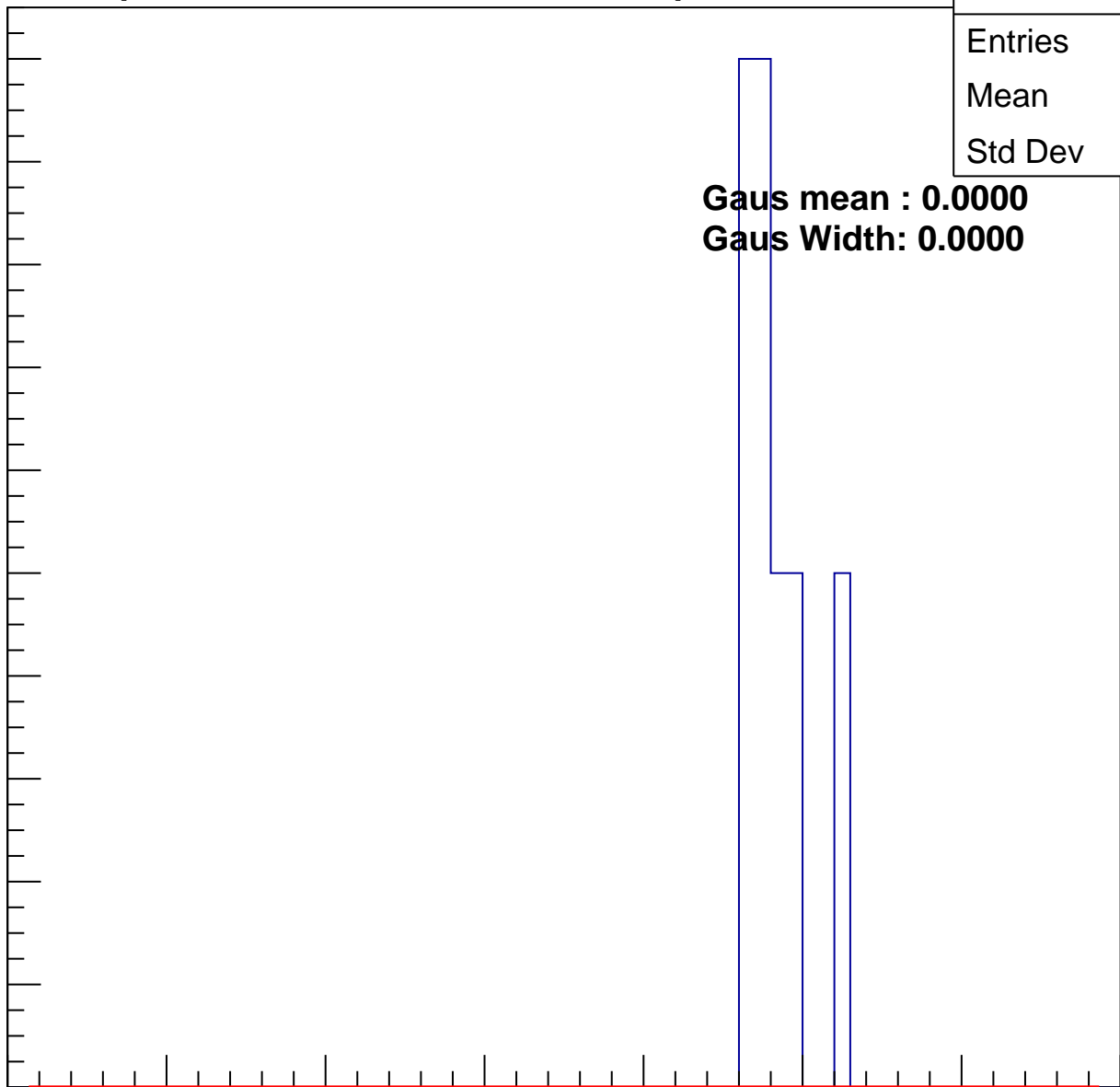
B0L100S, U13-ch85, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 49.3692
Gaus Width: 2.8698

Entries	25
Mean	49.04
Std Dev	2.236

Entry

ampl
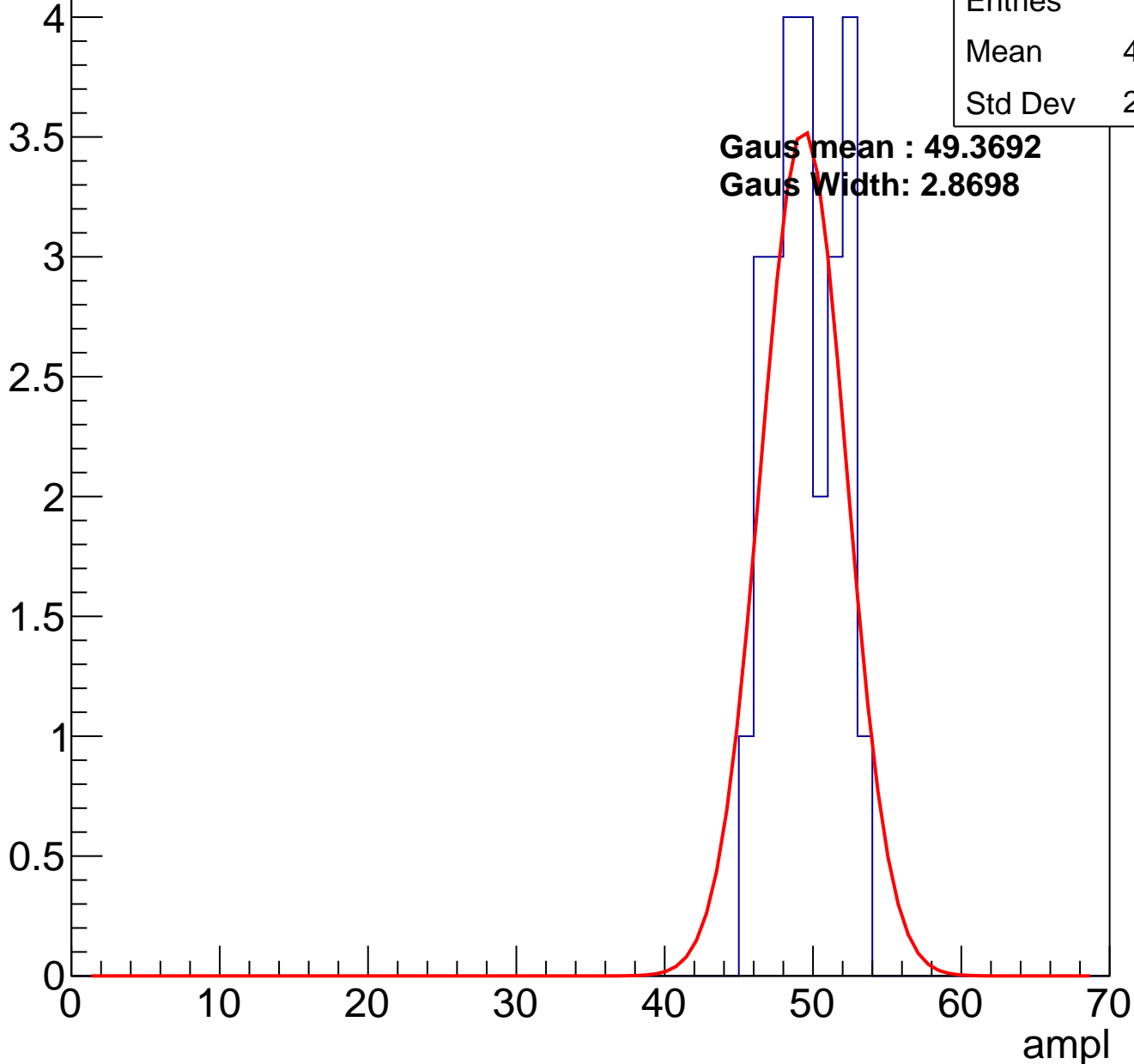
B0L100S, U13-ch85, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	51.56
Std Dev	1.343

B0L100S, U13-ch85, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch85, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch85, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch85, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch86, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch86, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch86, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50.5
Std Dev	2.291

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
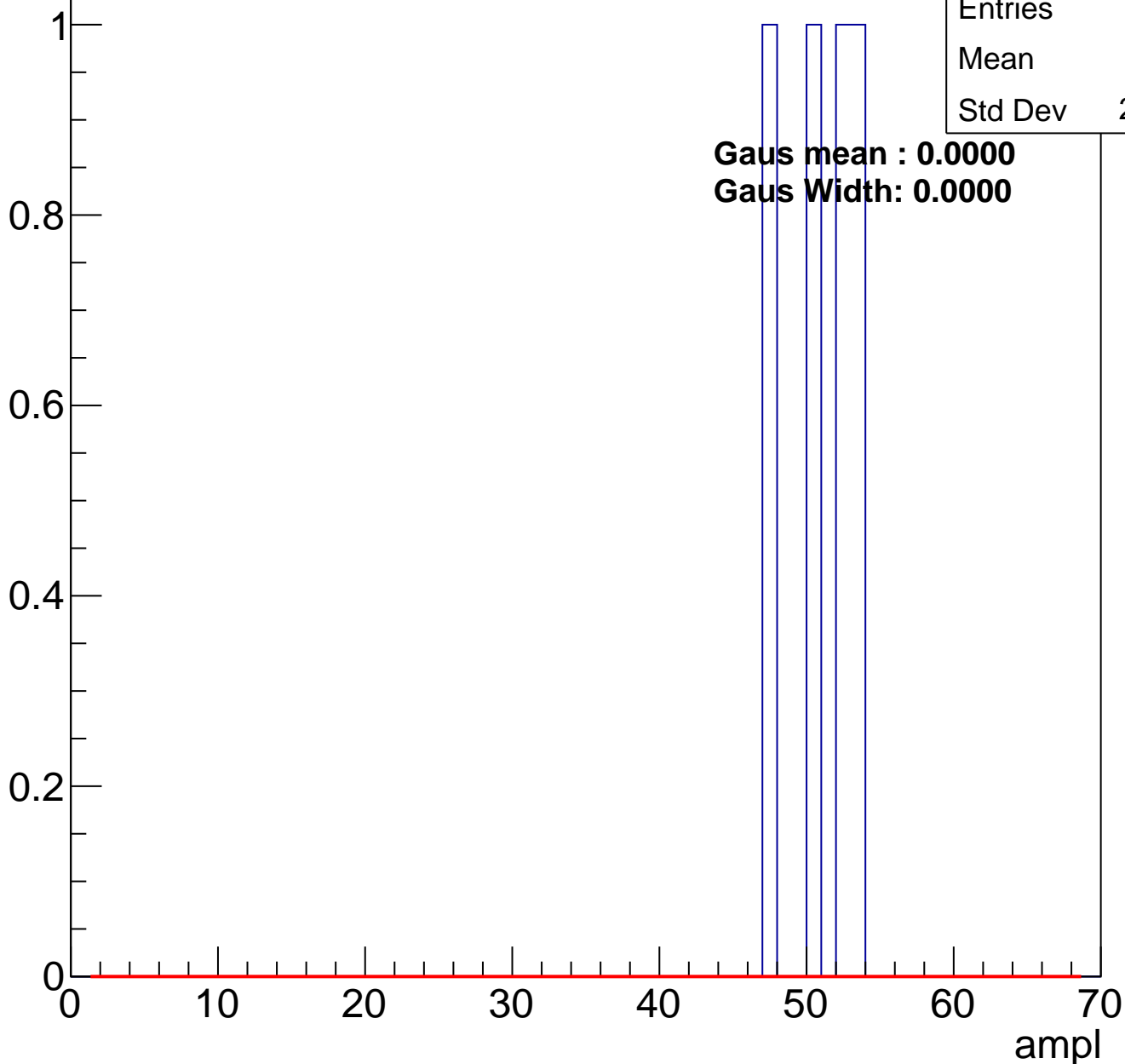
B0L100S, U13-ch86, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	52.25
Std Dev	1.299

Entry

ampl
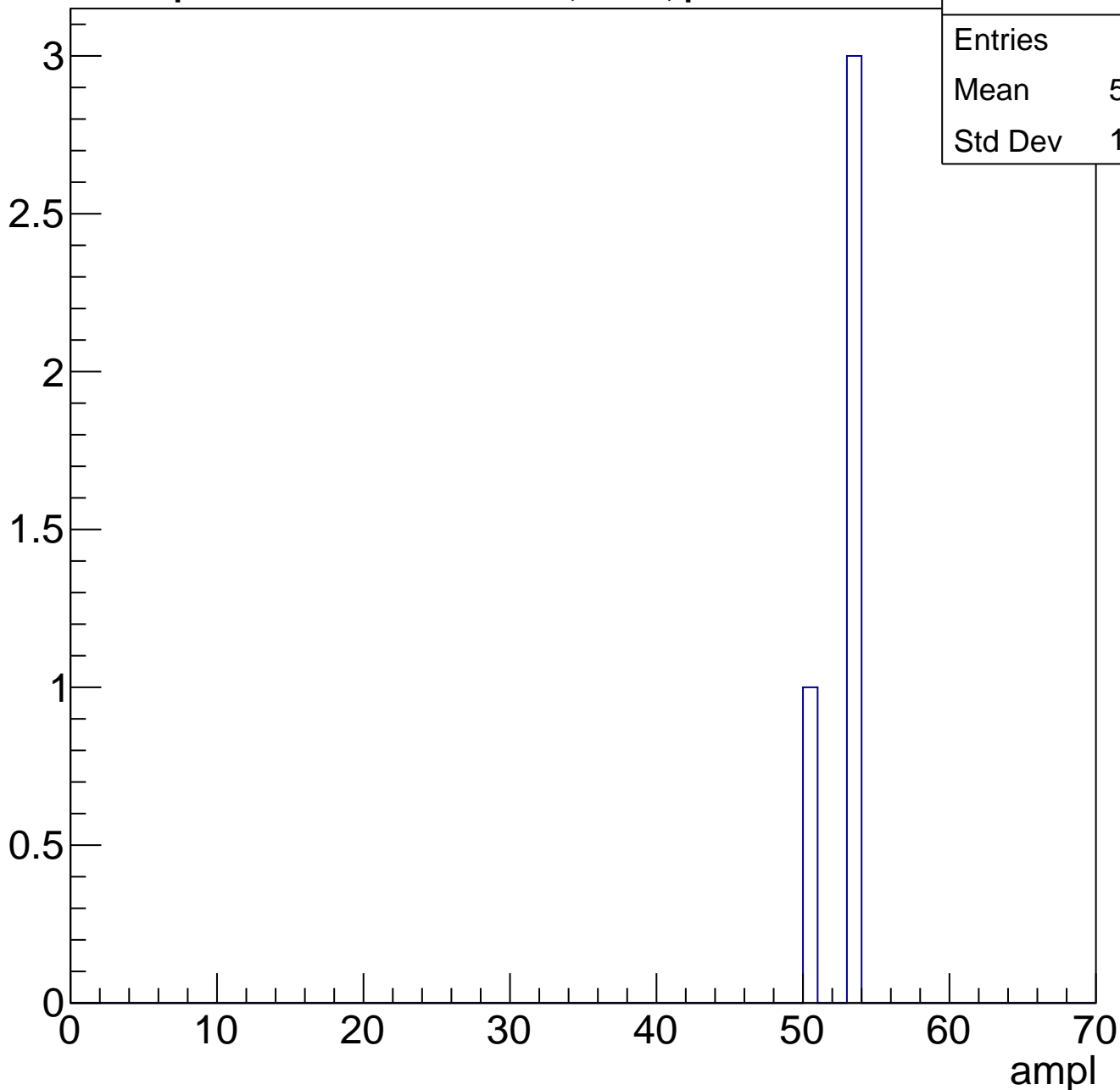
B0L100S, U13-ch86, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch86, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch86, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch86, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch87, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
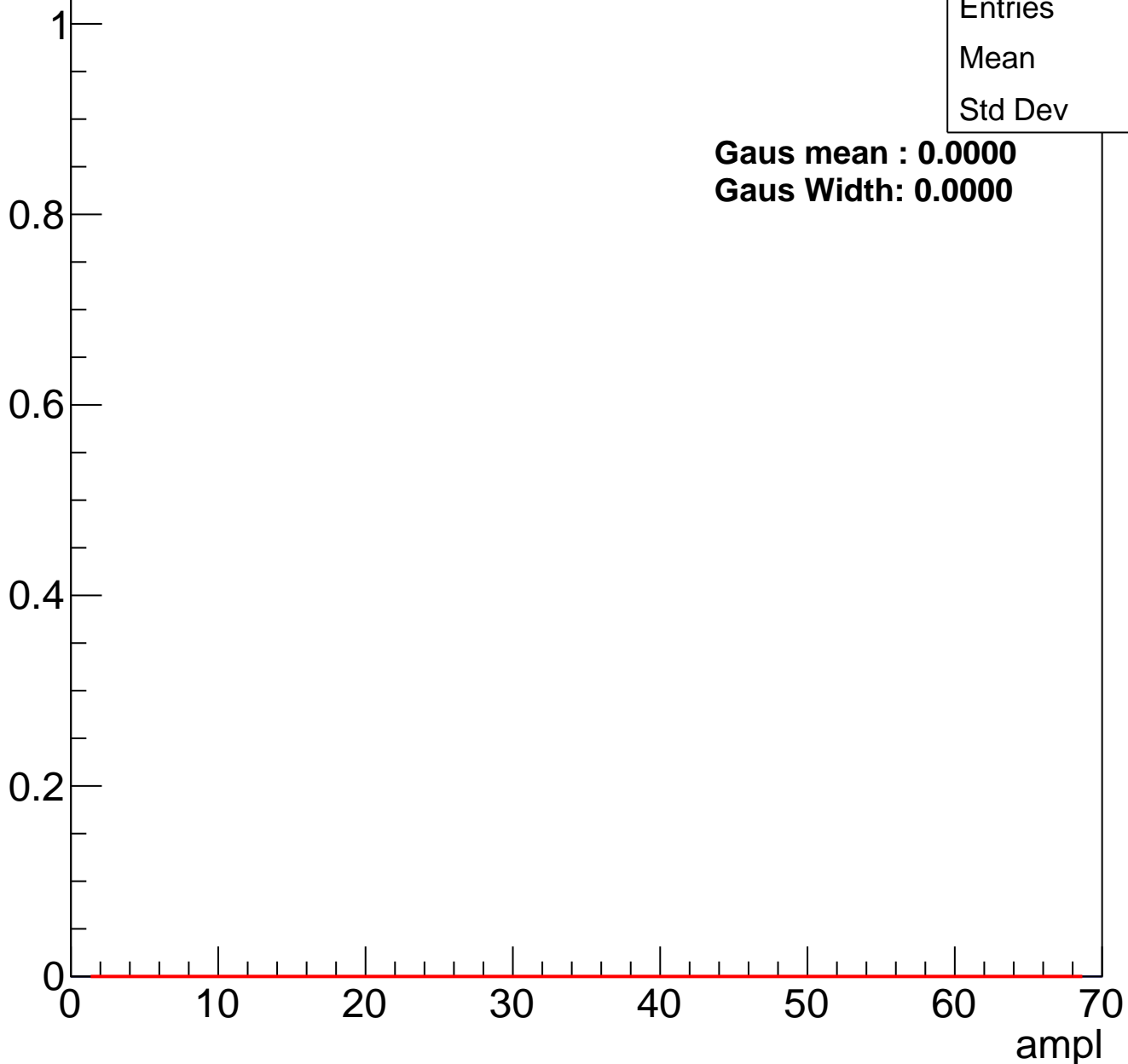
B0L100S, U13-ch87, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch87, adc2

calib_packv5_042523_0143.root, FC#6, port A1

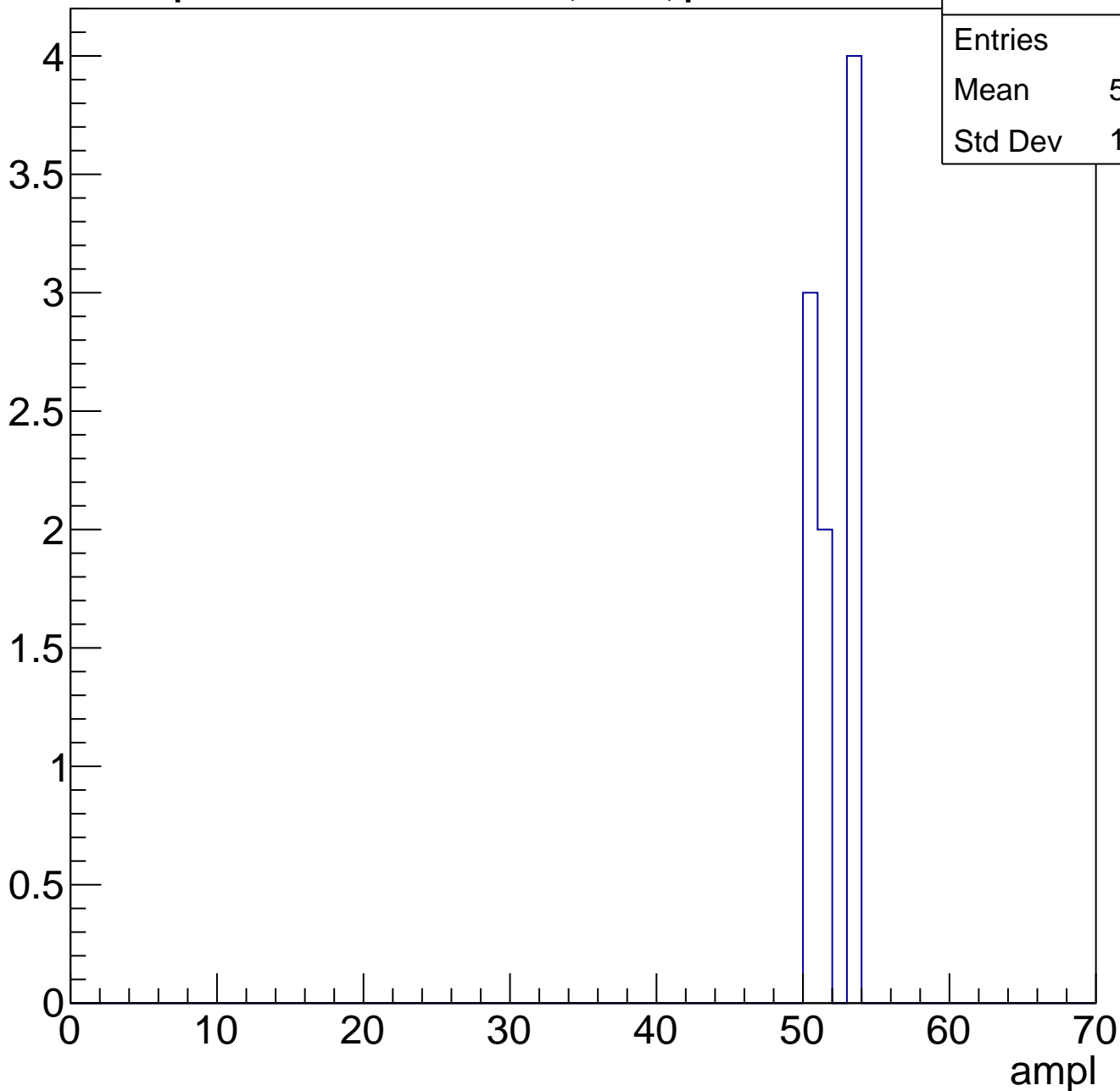|	
Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch87, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	53
Std Dev	0

Entry

ampl
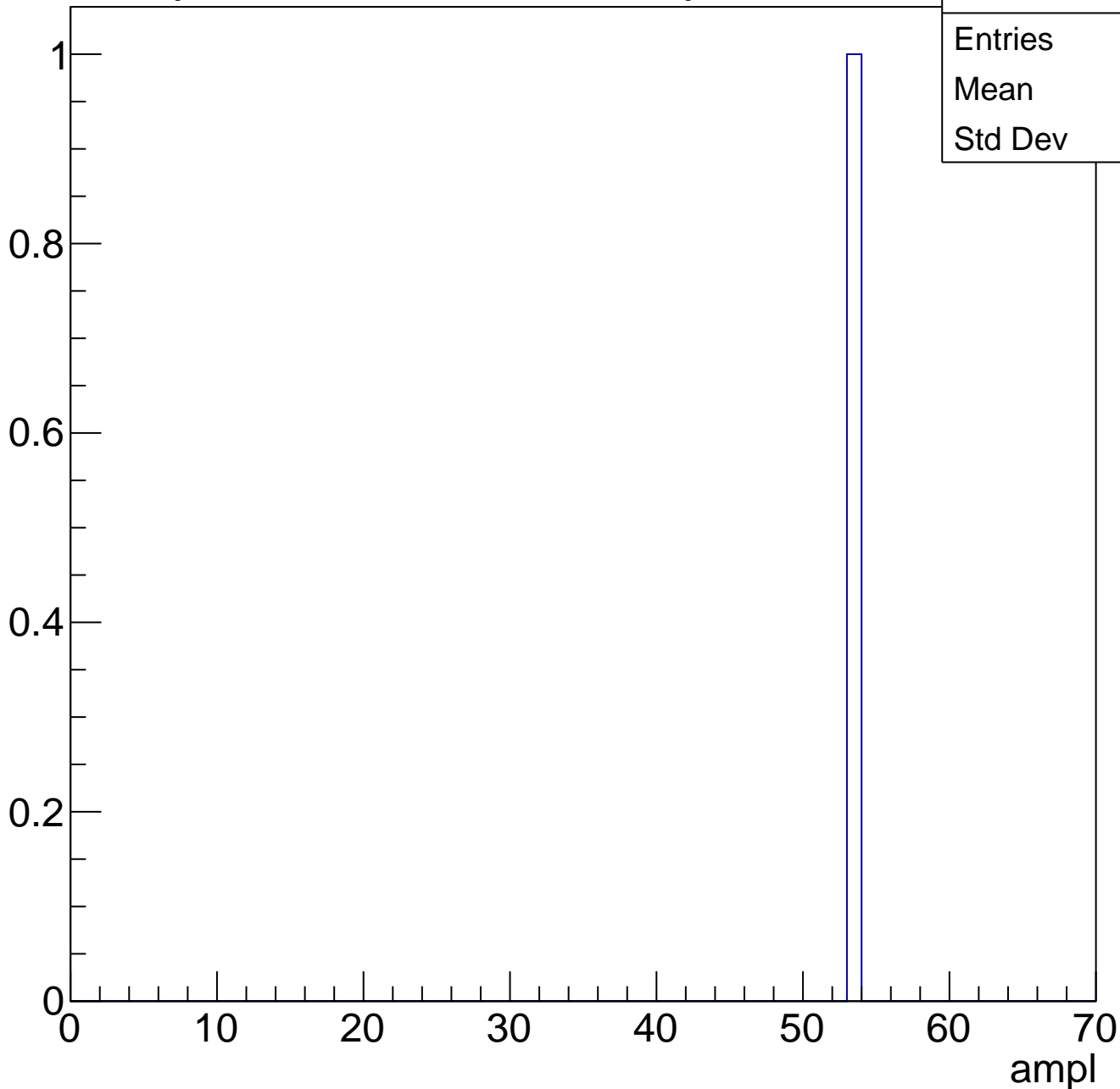
B0L100S, U13-ch87, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch87, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch87, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch87, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch88, adc0

calib_packv5_042523_0143.root, FC#6, port A1
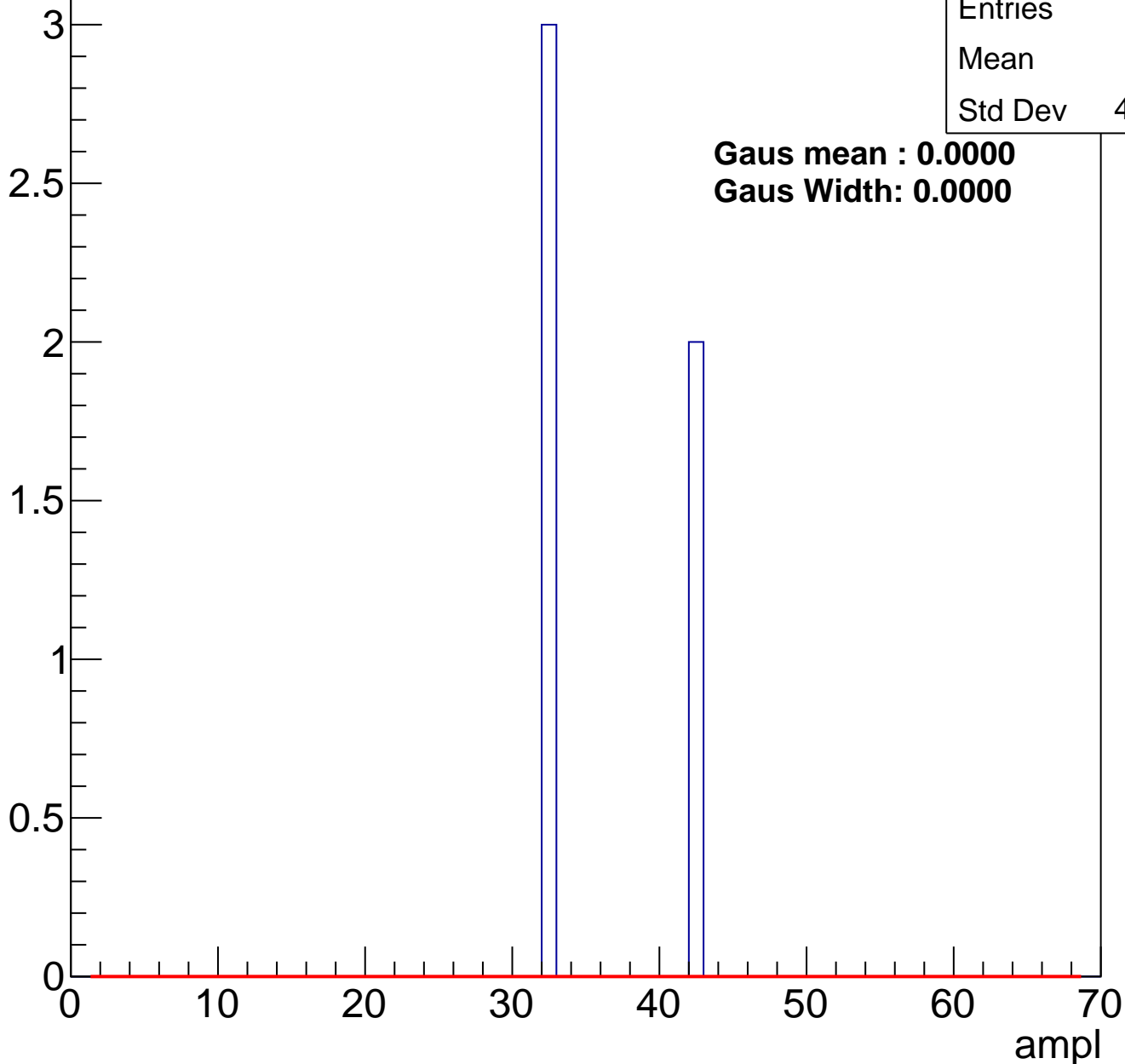
Entries	5
Mean	36
Std Dev	4.899

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch88, adc1

calib_packv5_042523_0143.root, FC#6, port A1
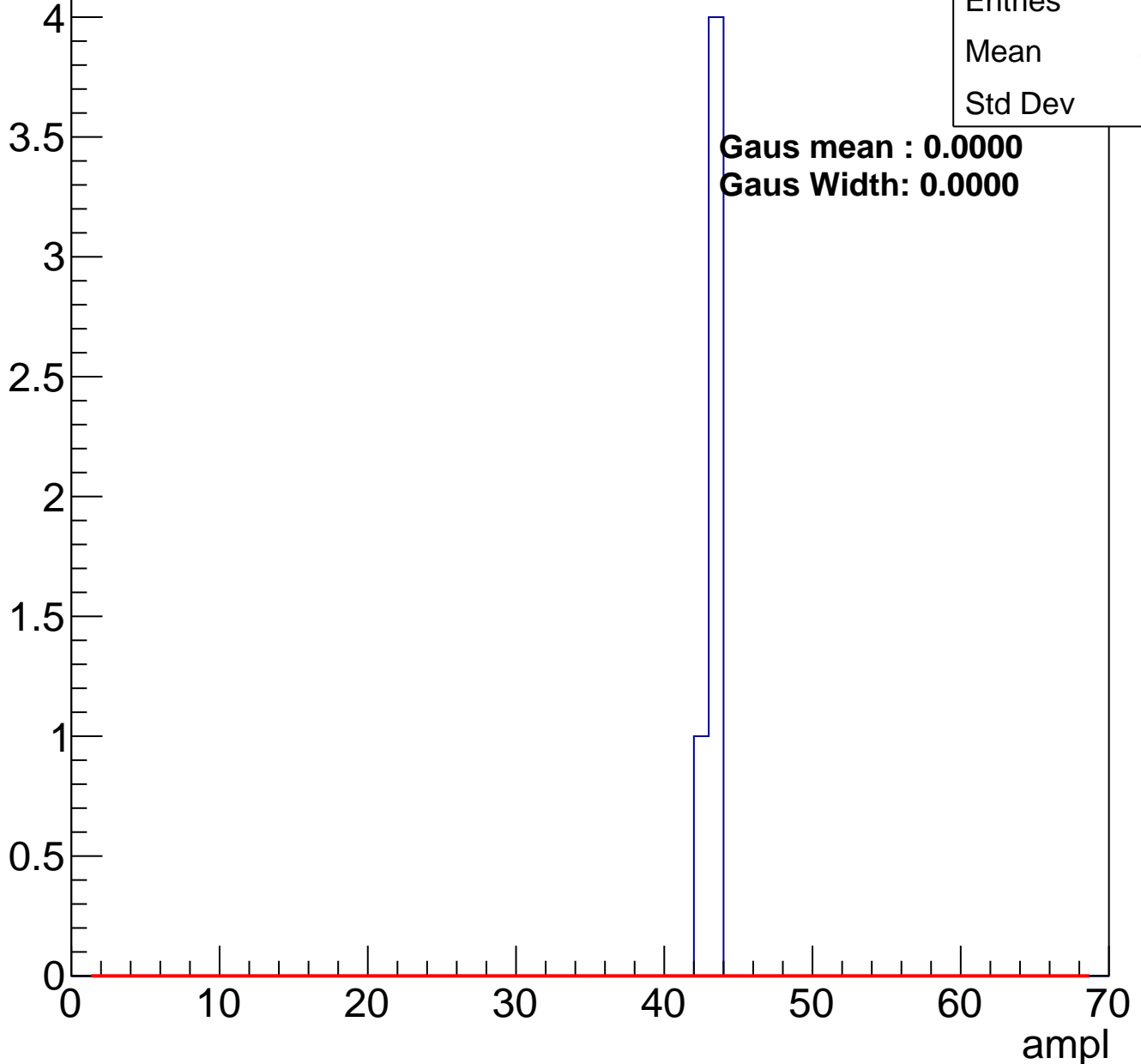
Entries	5
Mean	42.8
Std Dev	0.4

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch88, adc2

calib_packv5_042523_0143.root, FC#6, port A1
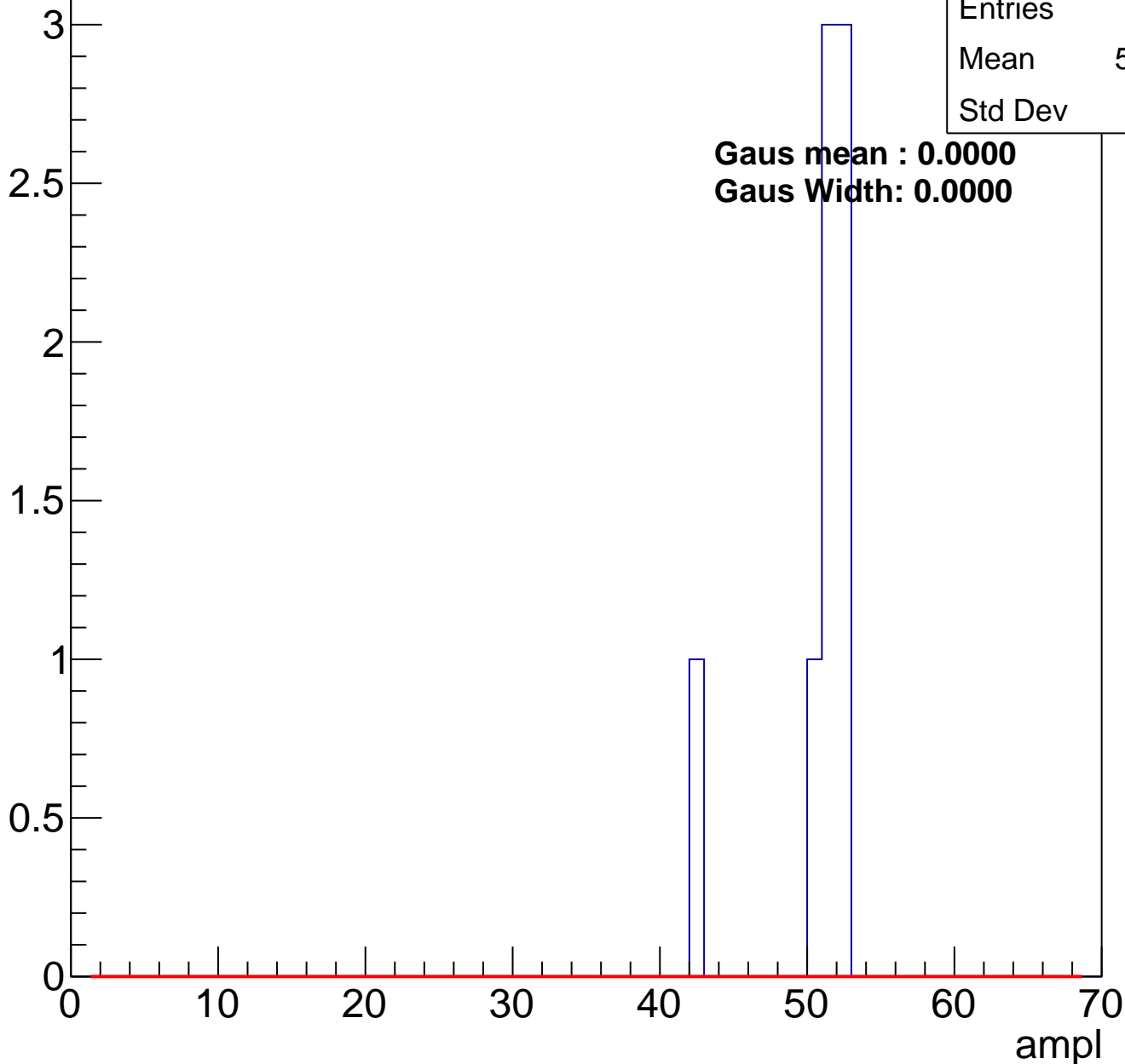
Entries	8
Mean	50.12
Std Dev	3.14

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch88, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	50.67
Std Dev	0.7454

Entry

ampl
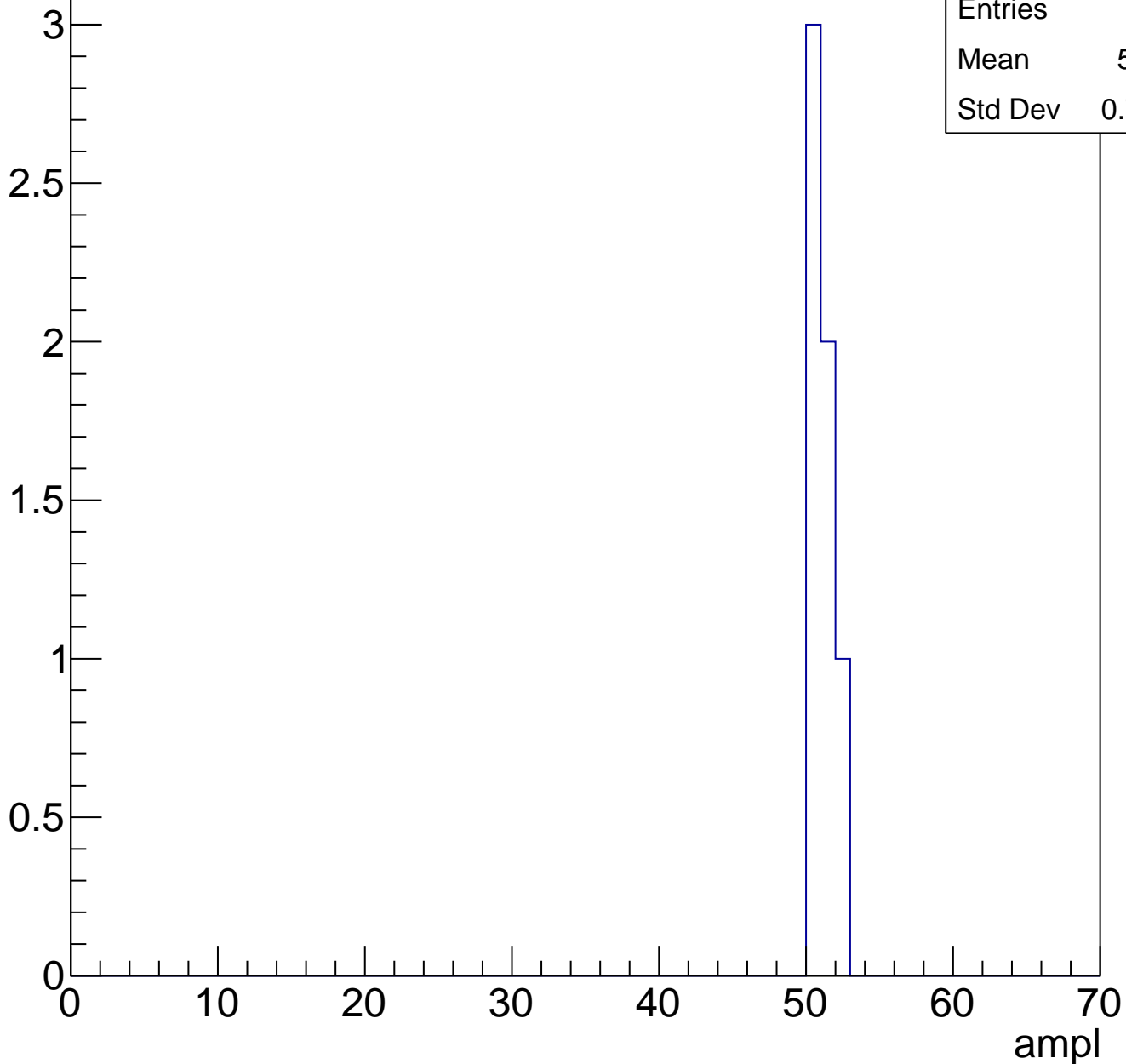
B0L100S, U13-ch88, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	30
Std Dev	0

Entry

ampl
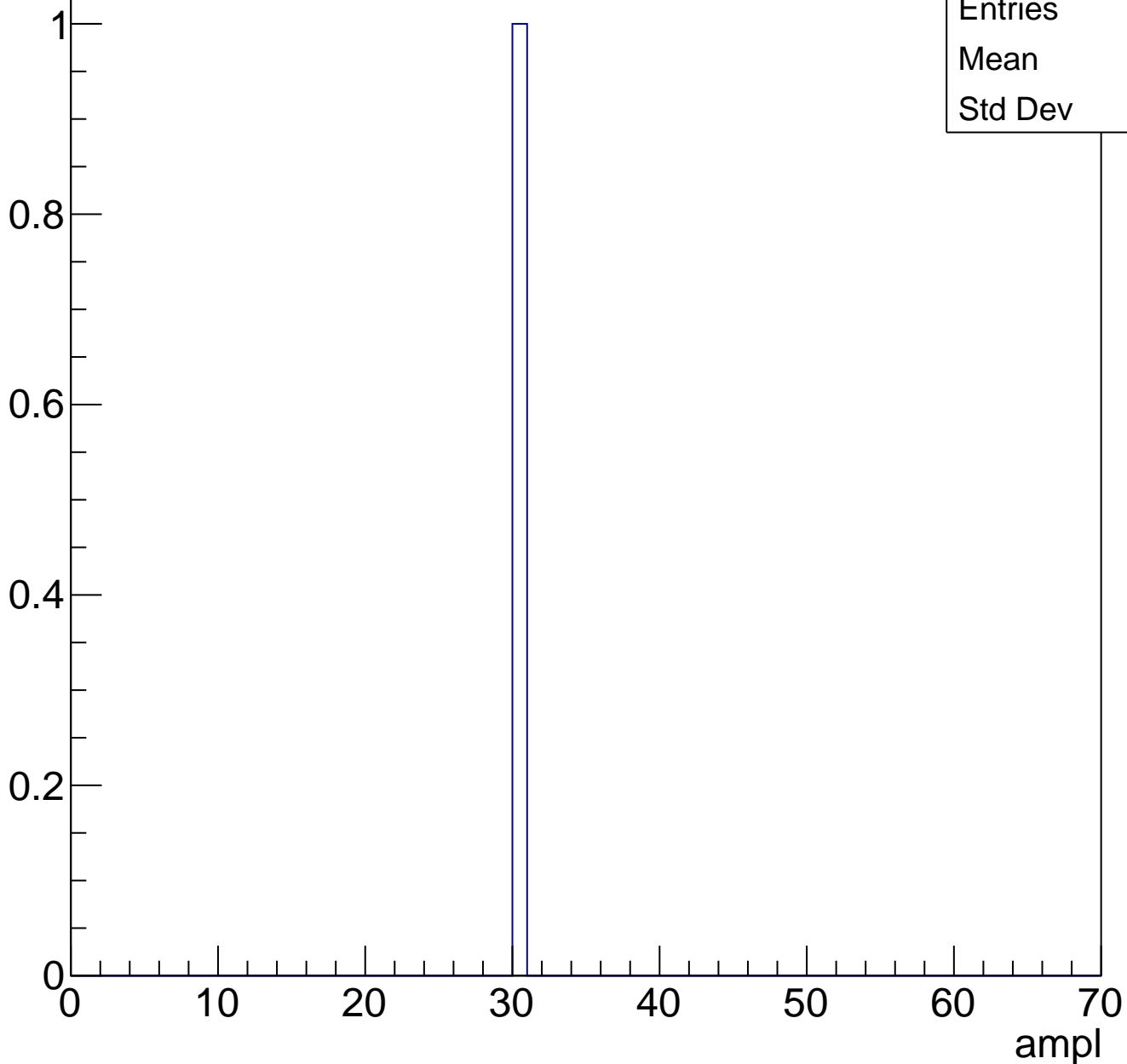
B0L100S, U13-ch88, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch88, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch88, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch89, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch89, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
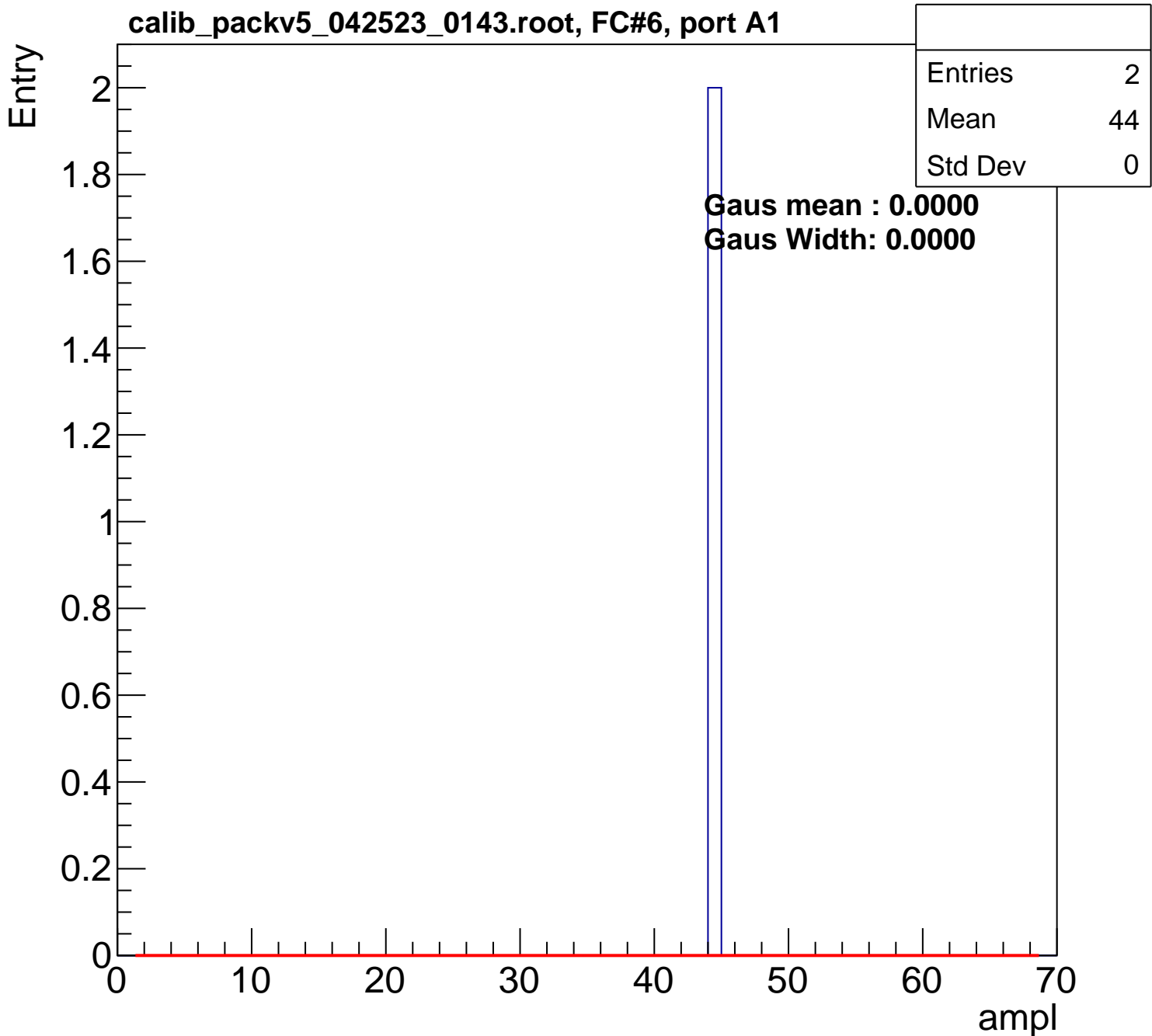
B0L100S, U13-ch89, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch89, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch89, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch89, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch89, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch89, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch90, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	47
Std Dev	2

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
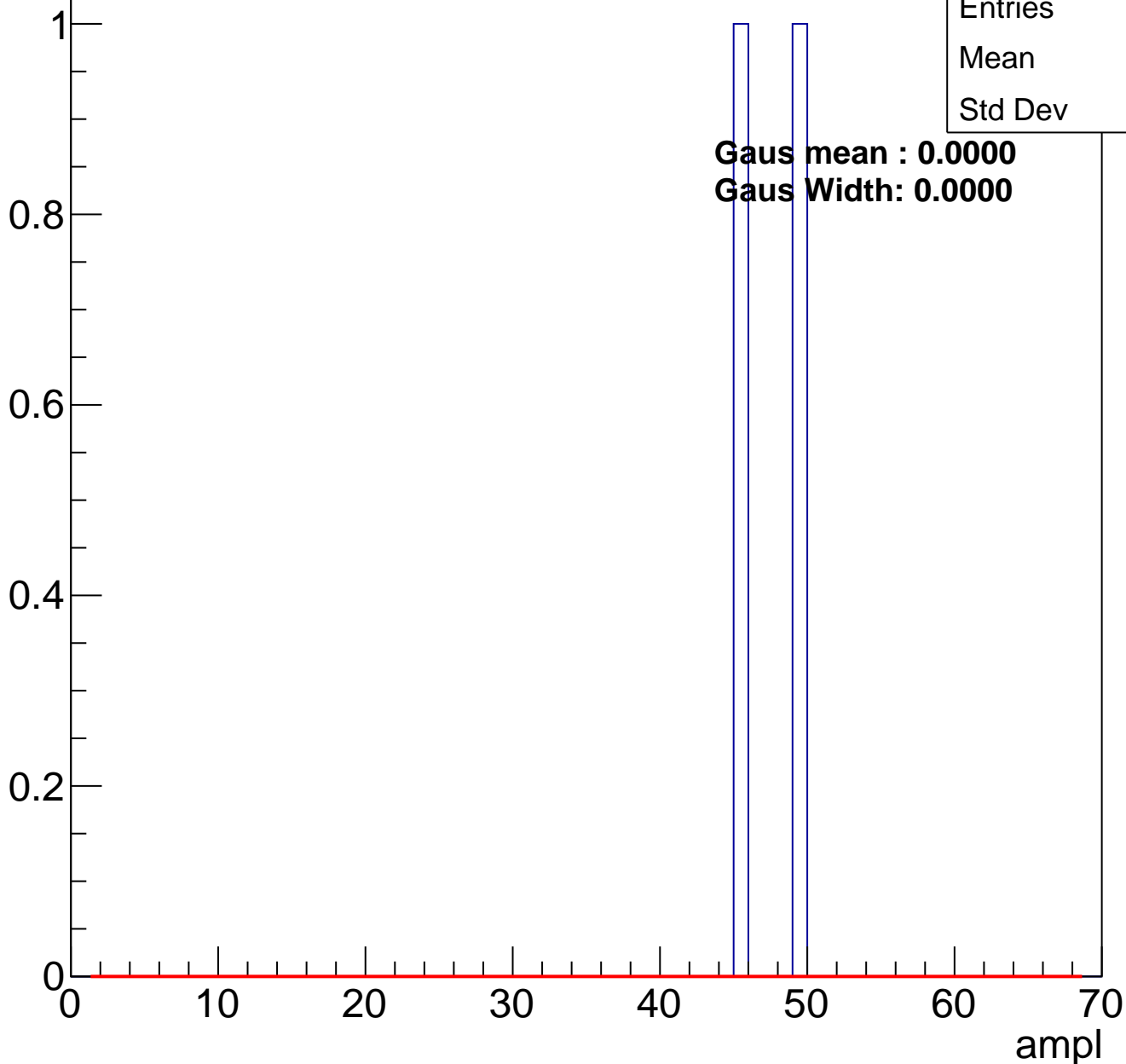
B0L100S, U13-ch90, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	45.81
Std Dev	2.651

Gaus mean : 2.6248
Gaus Width: 26.5066
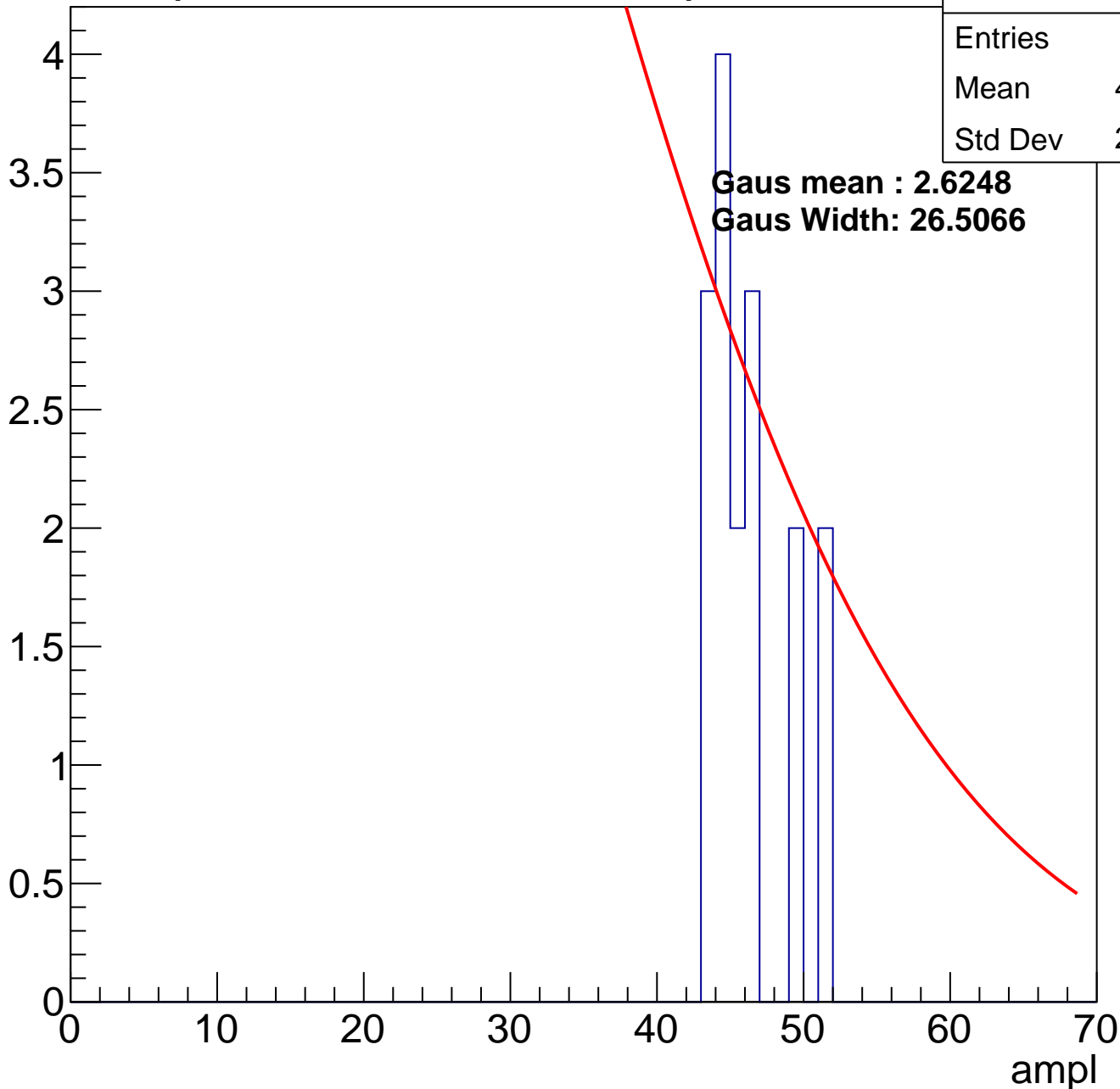
B0L100S, U13-ch90, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	48.36
Std Dev	3.199

Gaus mean : 48.3556
Gaus Width: 3.8048
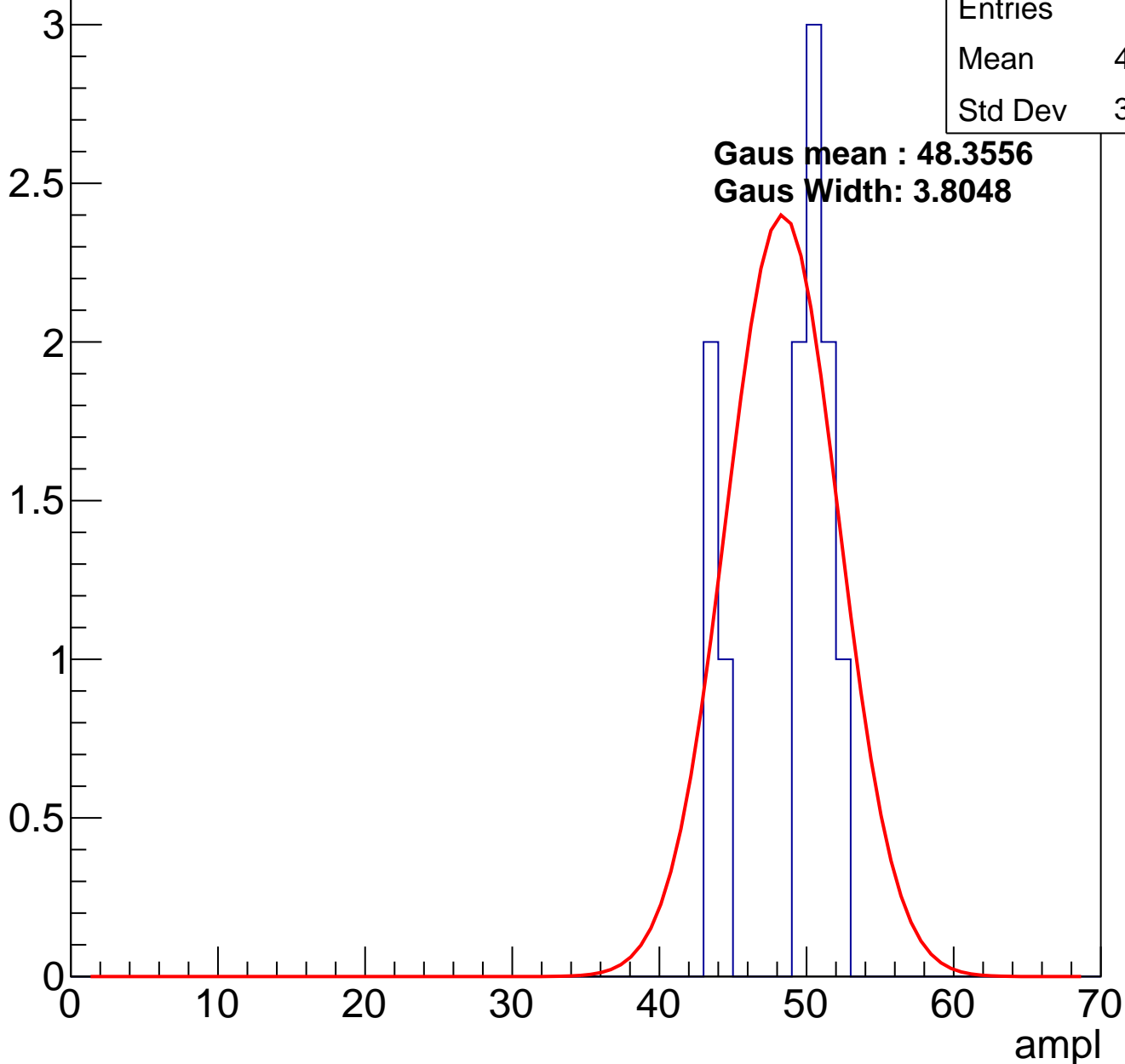
B0L100S, U13-ch90, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	3
Mean	49.33
Std Dev	0.9428
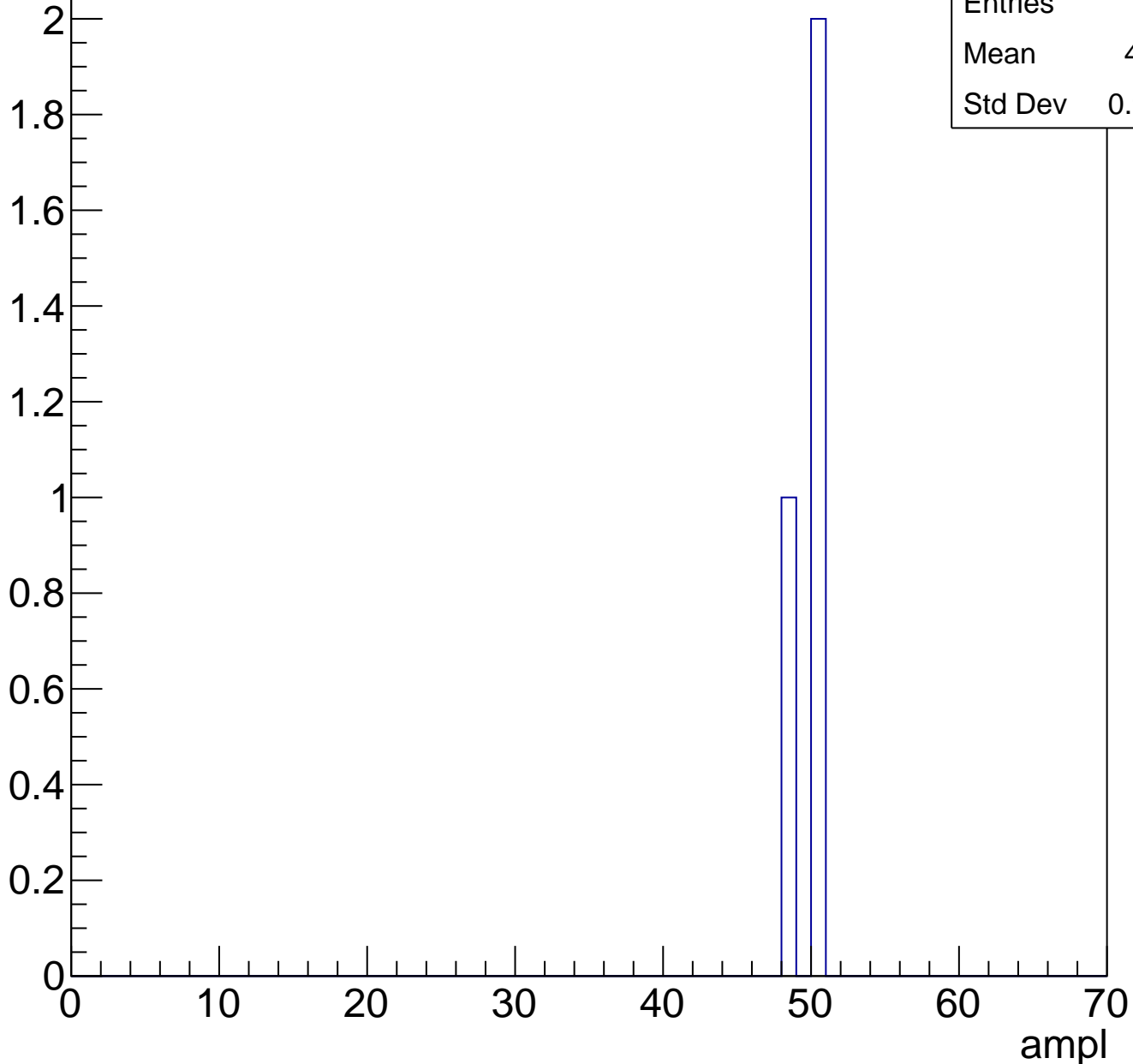
B0L100S, U13-ch90, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch90, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch90, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch91, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch91, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch91, adc2
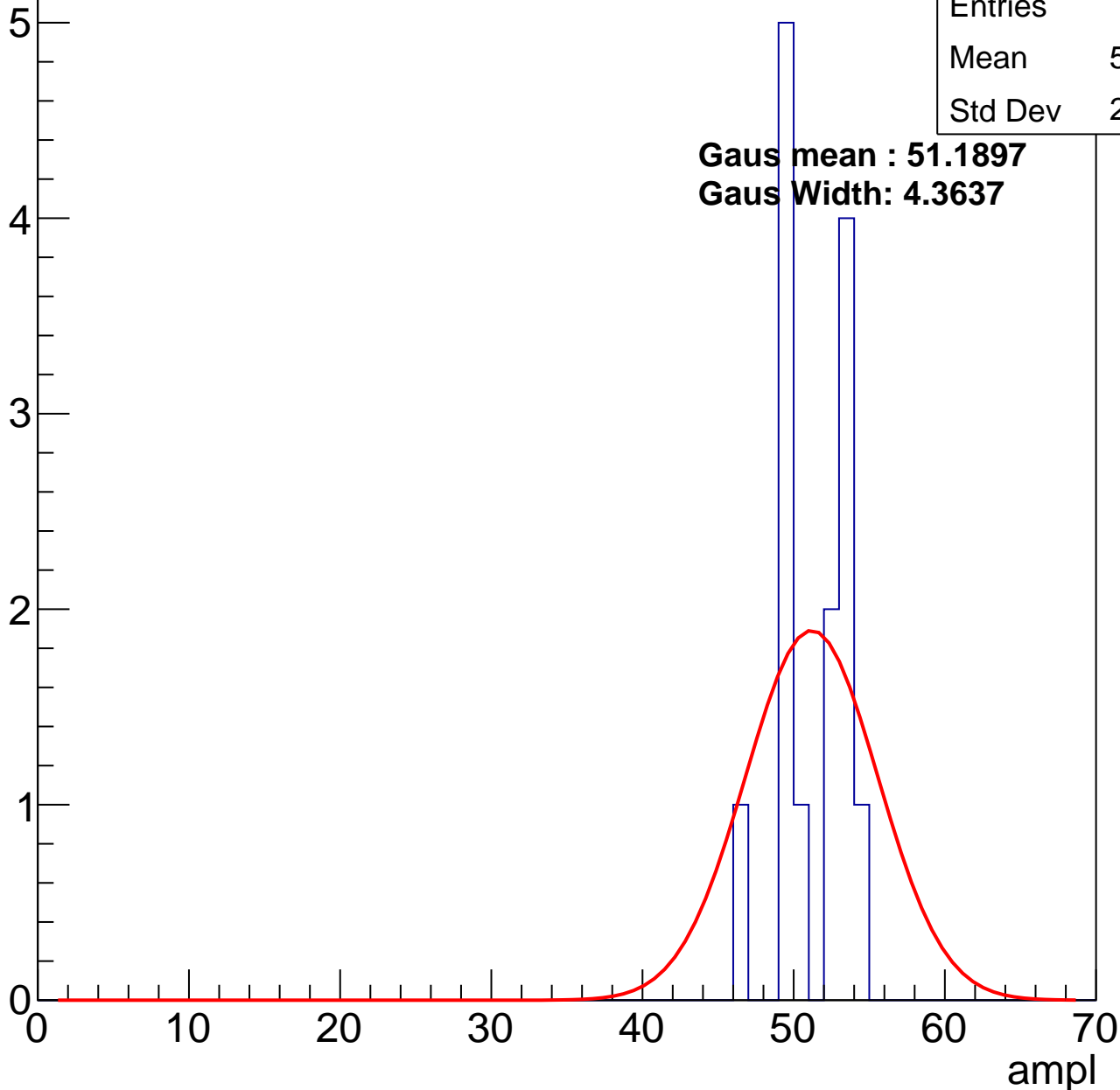
B0L100S, U13-ch91, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	53.78
Std Dev	1.133

Entry

ampl
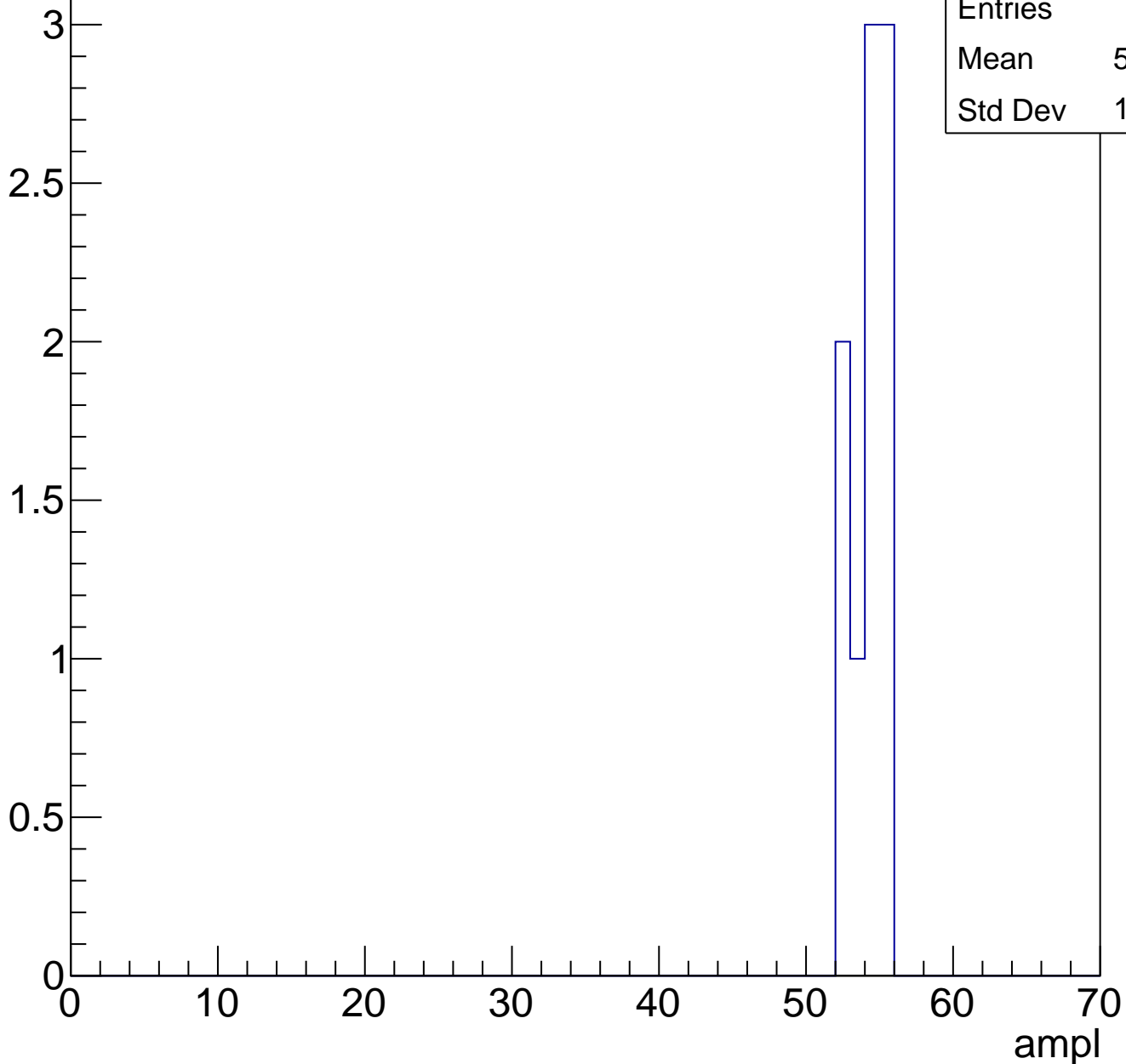
B0L100S, U13-ch91, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	54.5
Std Dev	0.5

Entry

ampl
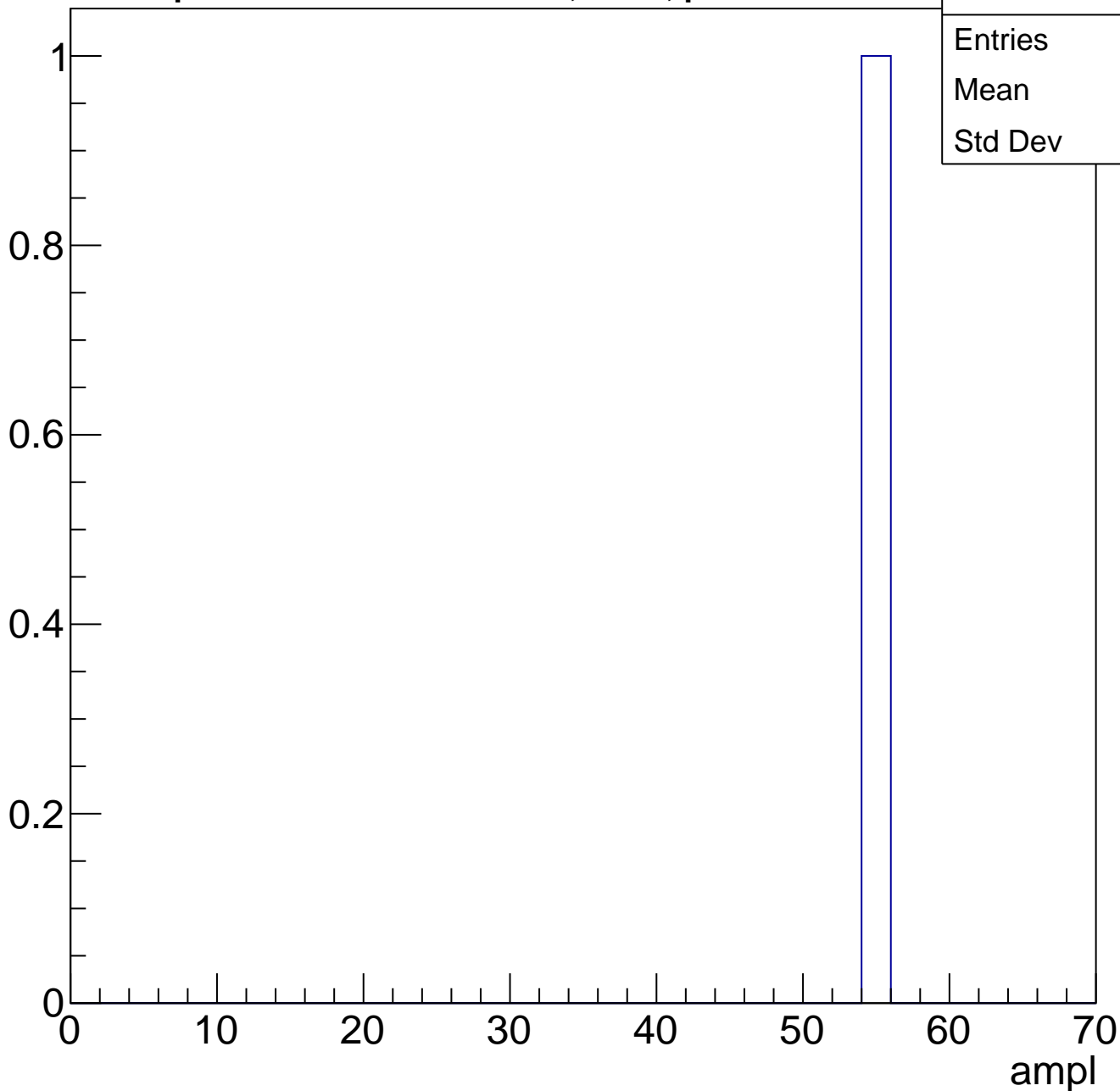
B0L100S, U13-ch91, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch91, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch91, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch92, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch92, adc1

calib_packv5_042523_0143.root, FC#6, port A1
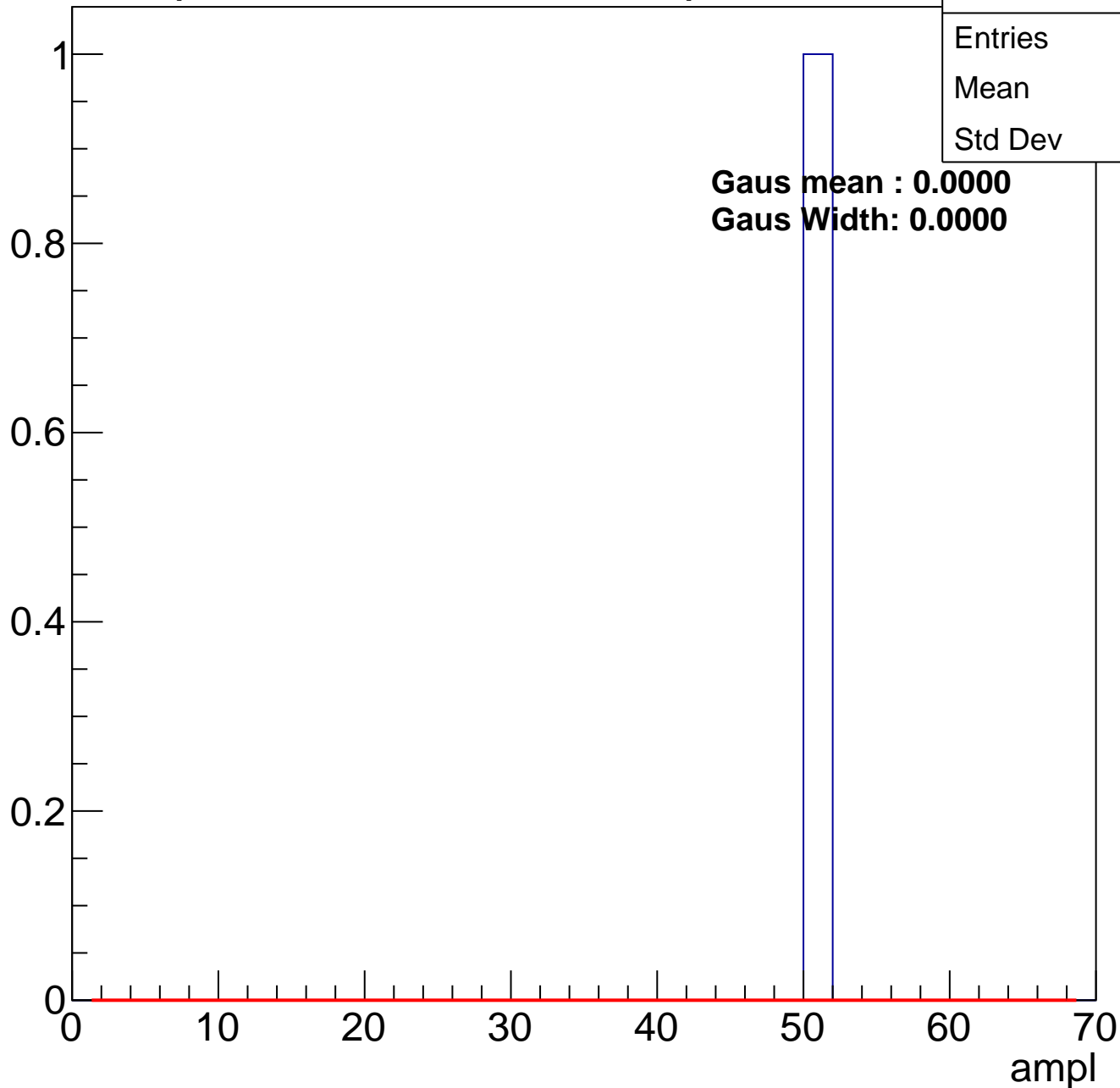
Entries	2
Mean	50.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch92, adc2

calib_packv5_042523_0143.root, FC#6, port A1
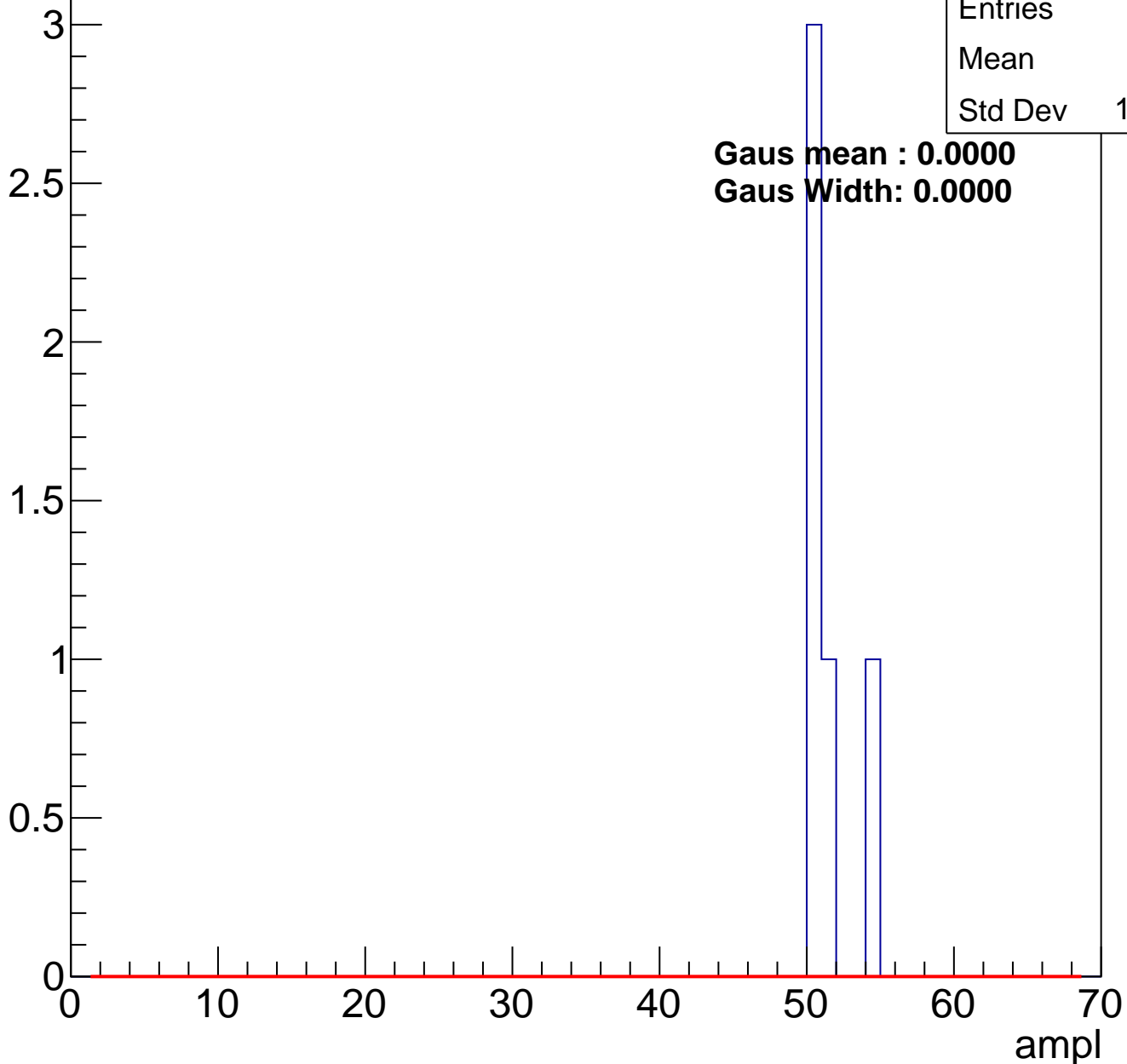
Entries	5
Mean	51
Std Dev	1.549

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch92, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	50.71
Std Dev	1.578

Entry

ampl
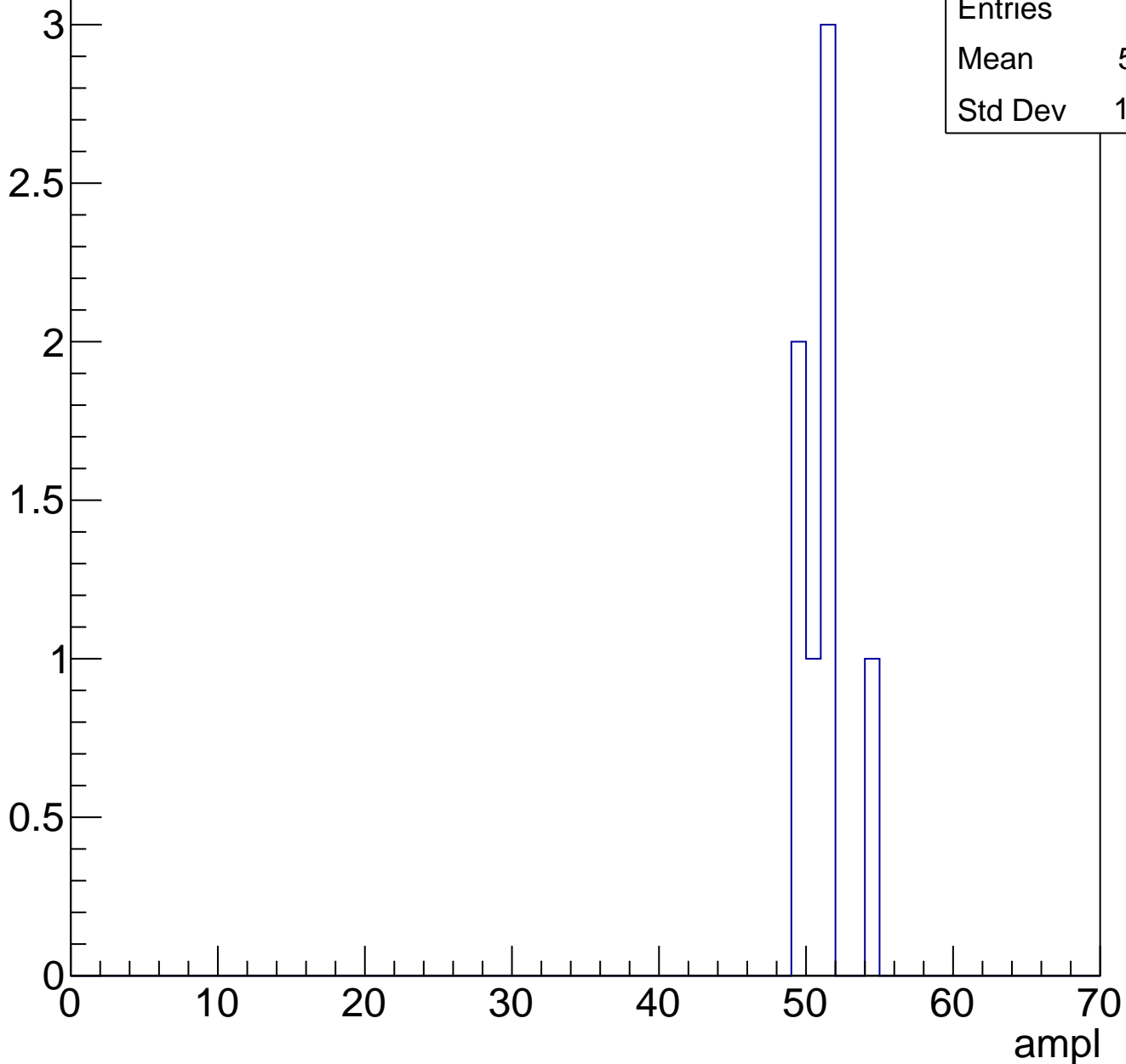
B0L100S, U13-ch92, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch92, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch92, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch92, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch93, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch93, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch93, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	6
Mean	49.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
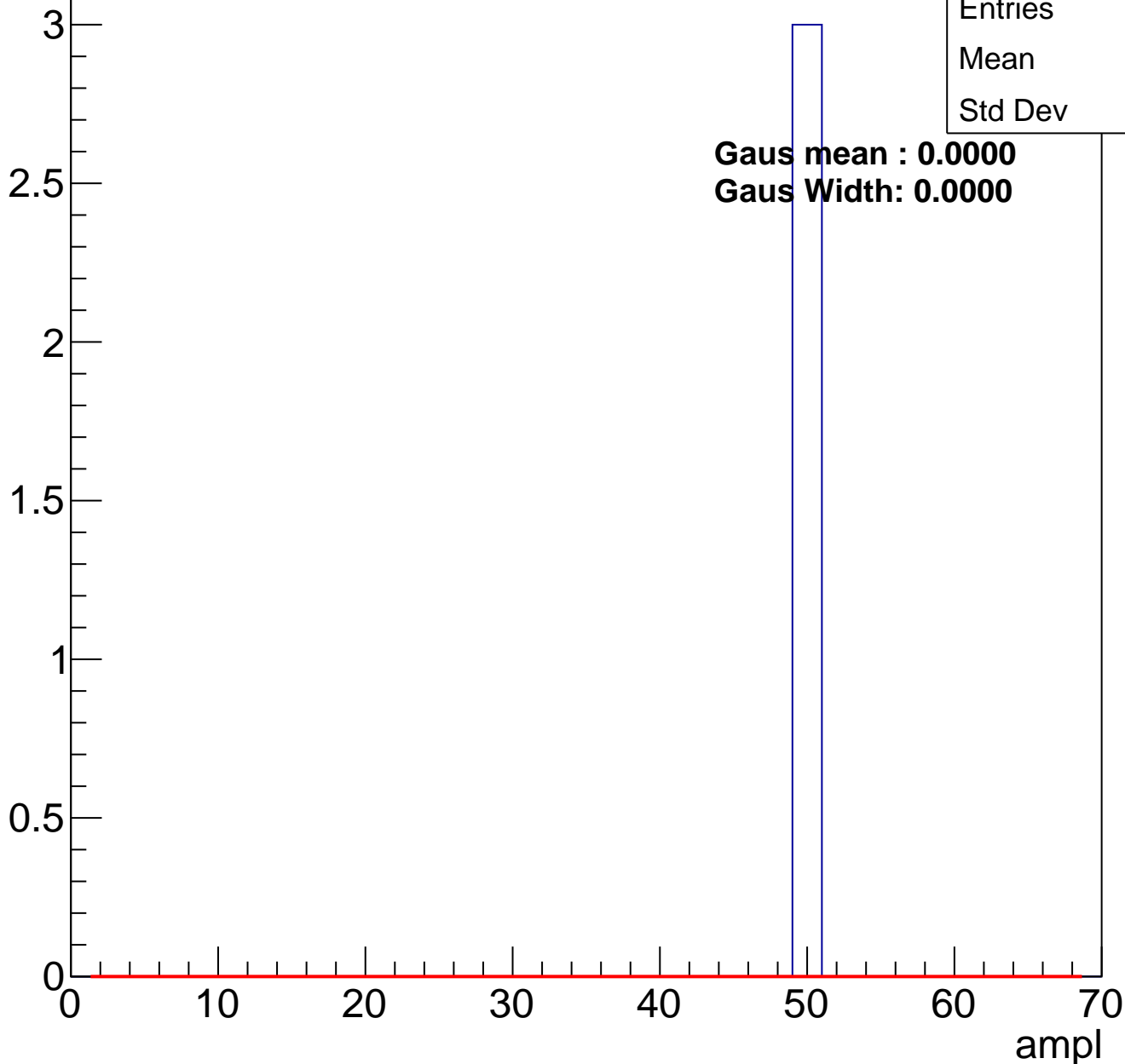
B0L100S, U13-ch93, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Entry

ampl

B0L100S, U13-ch93, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch93, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch93, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch93, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch94, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	42
Std Dev	0

ampl
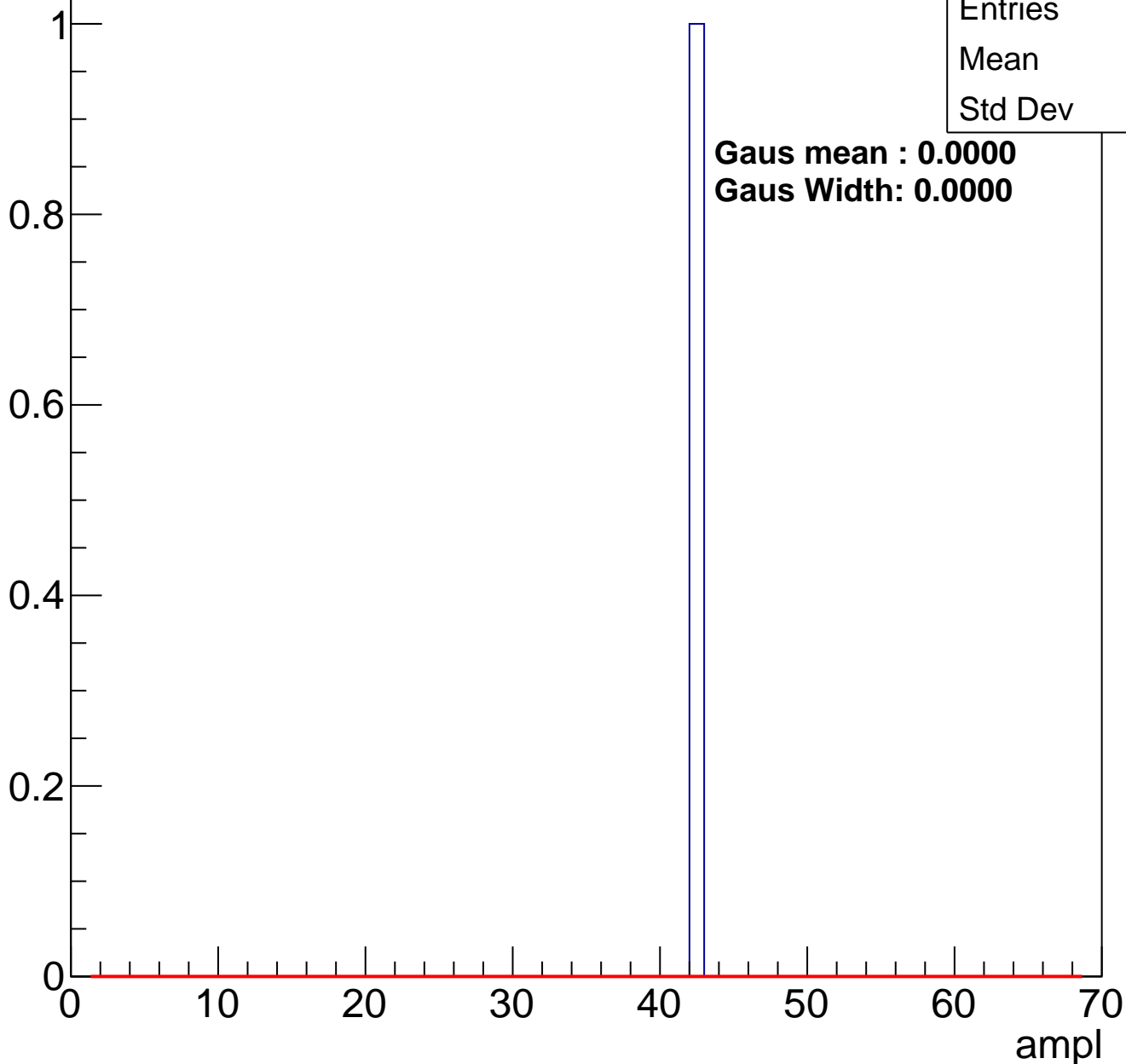
B0L100S, U13-ch94, adc1

calib_packv5_042523_0143.root, FC#6, port A1
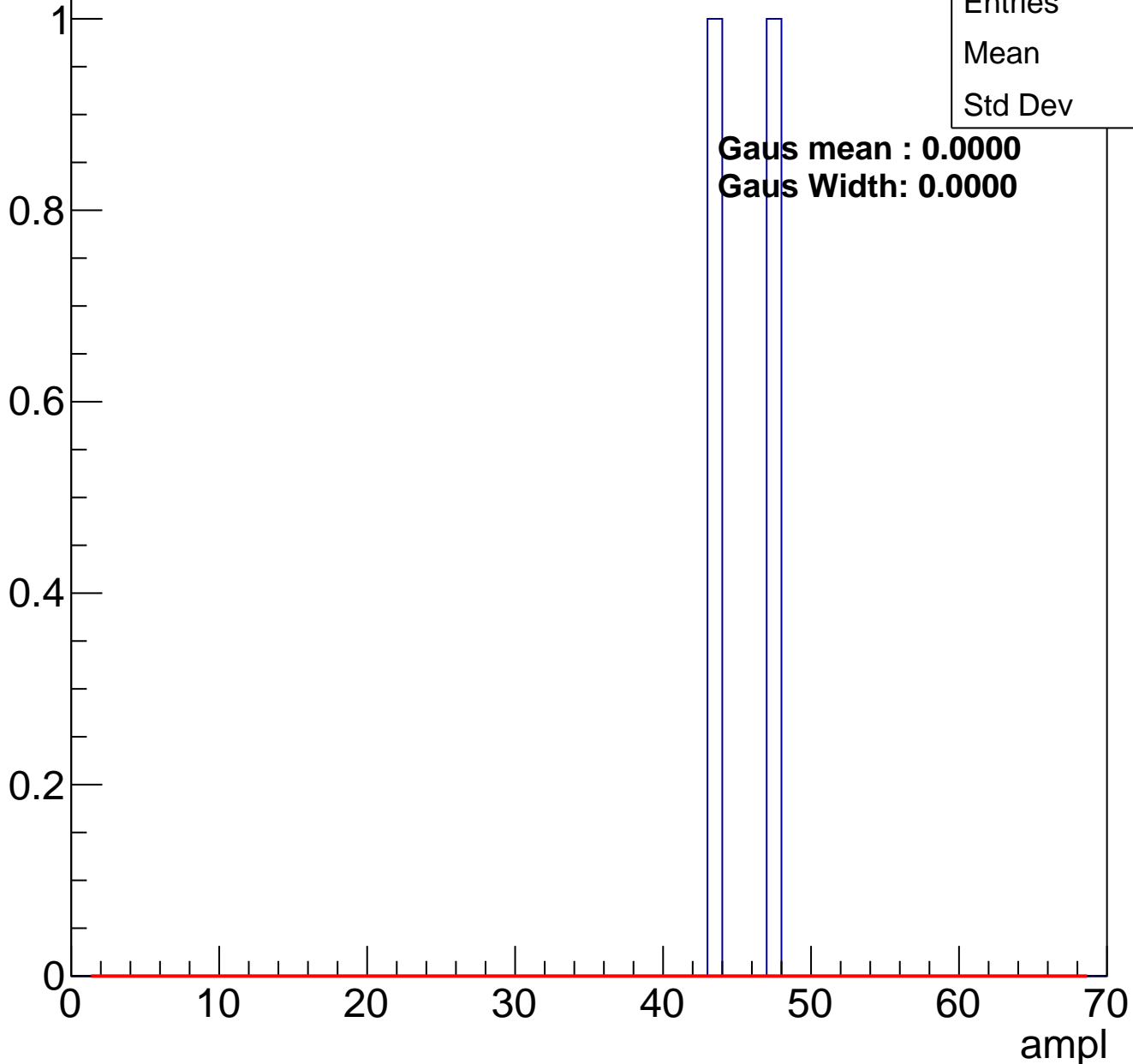
Entries	2
Mean	45
Std Dev	2

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch94, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	45.71
Std Dev	2.814

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
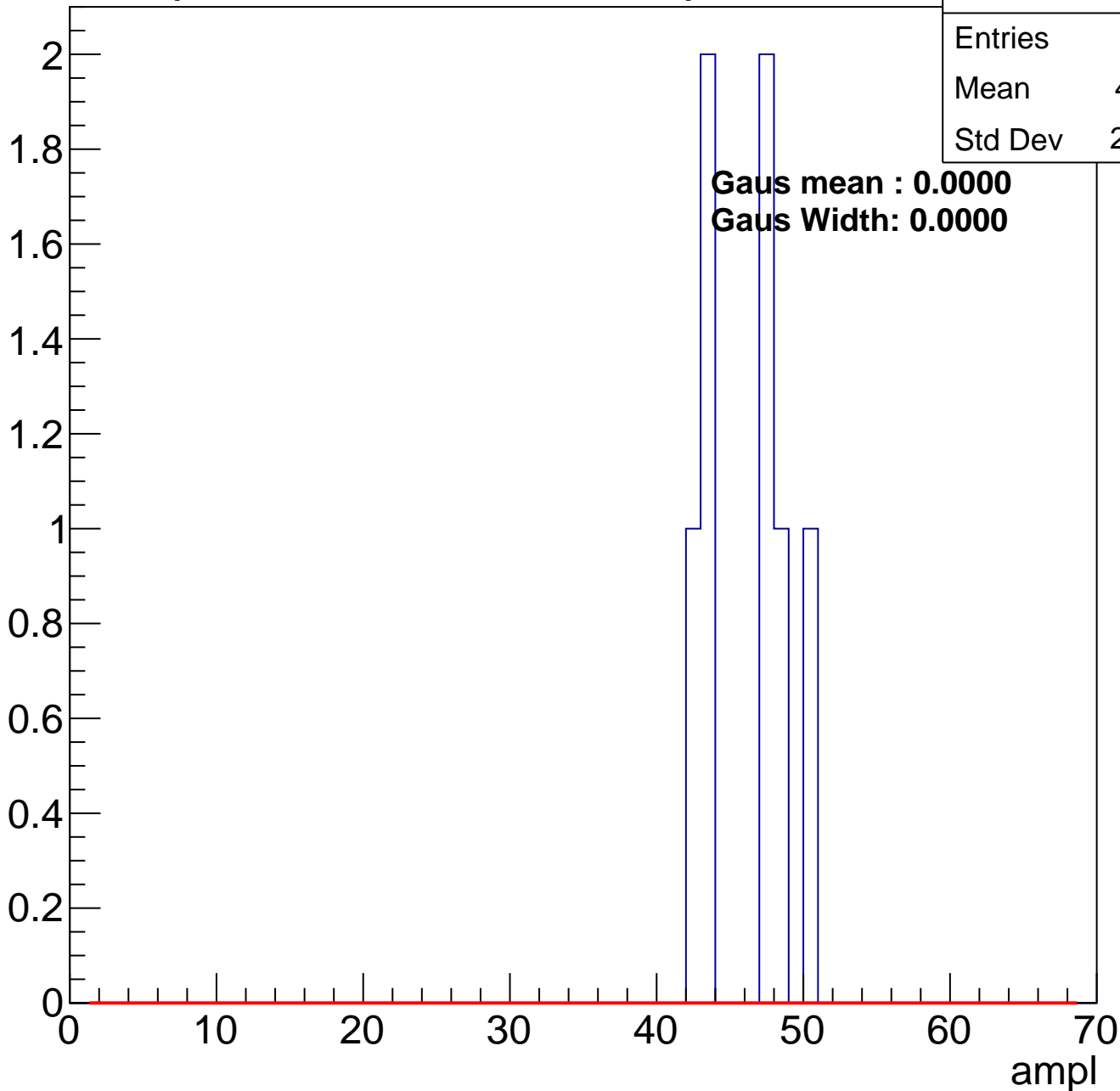
B0L100S, U13-ch94, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	49.4
Std Dev	3.007

Entry

ampl
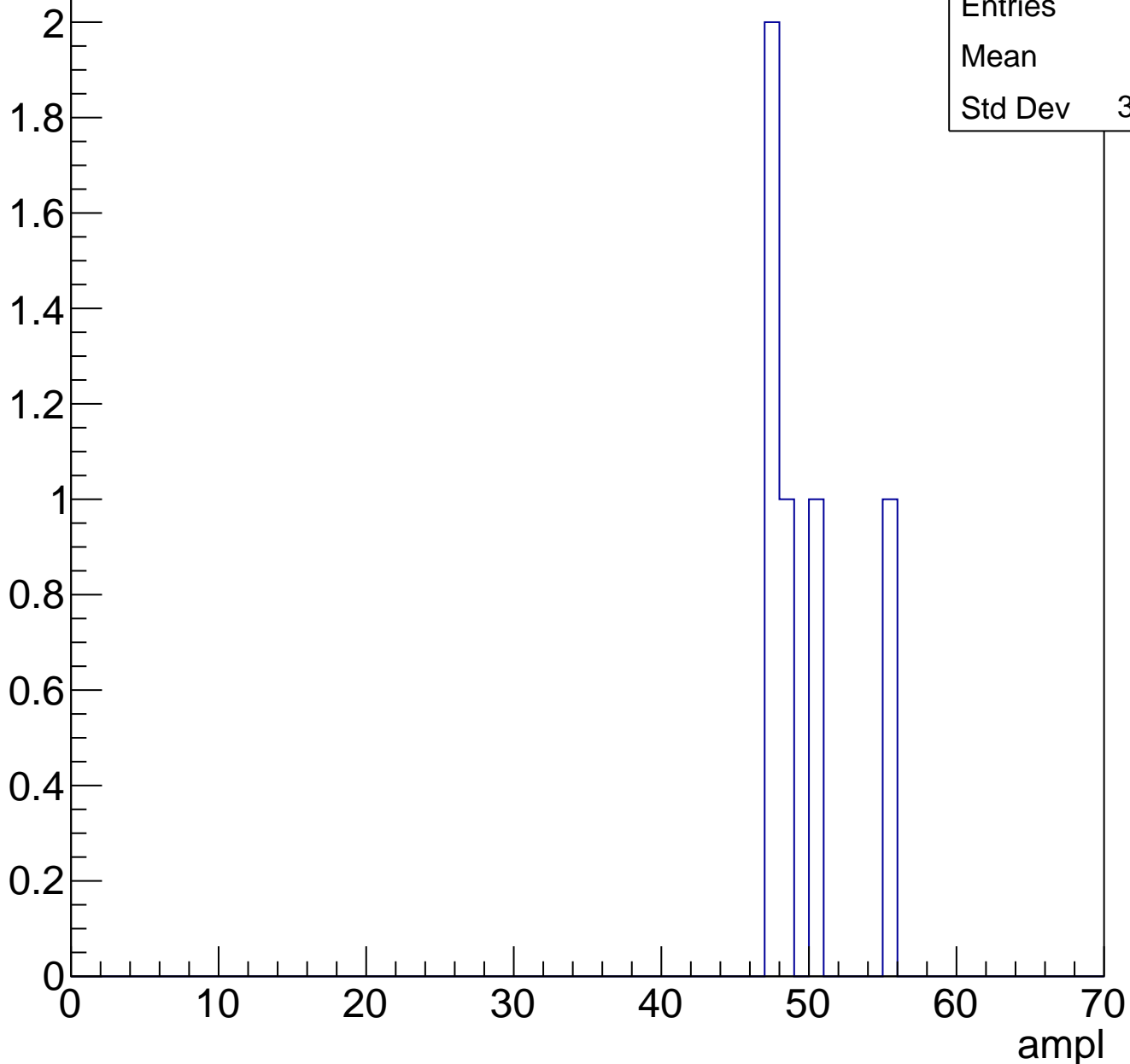
B0L100S, U13-ch94, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch94, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch94, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch94, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch95, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	47
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
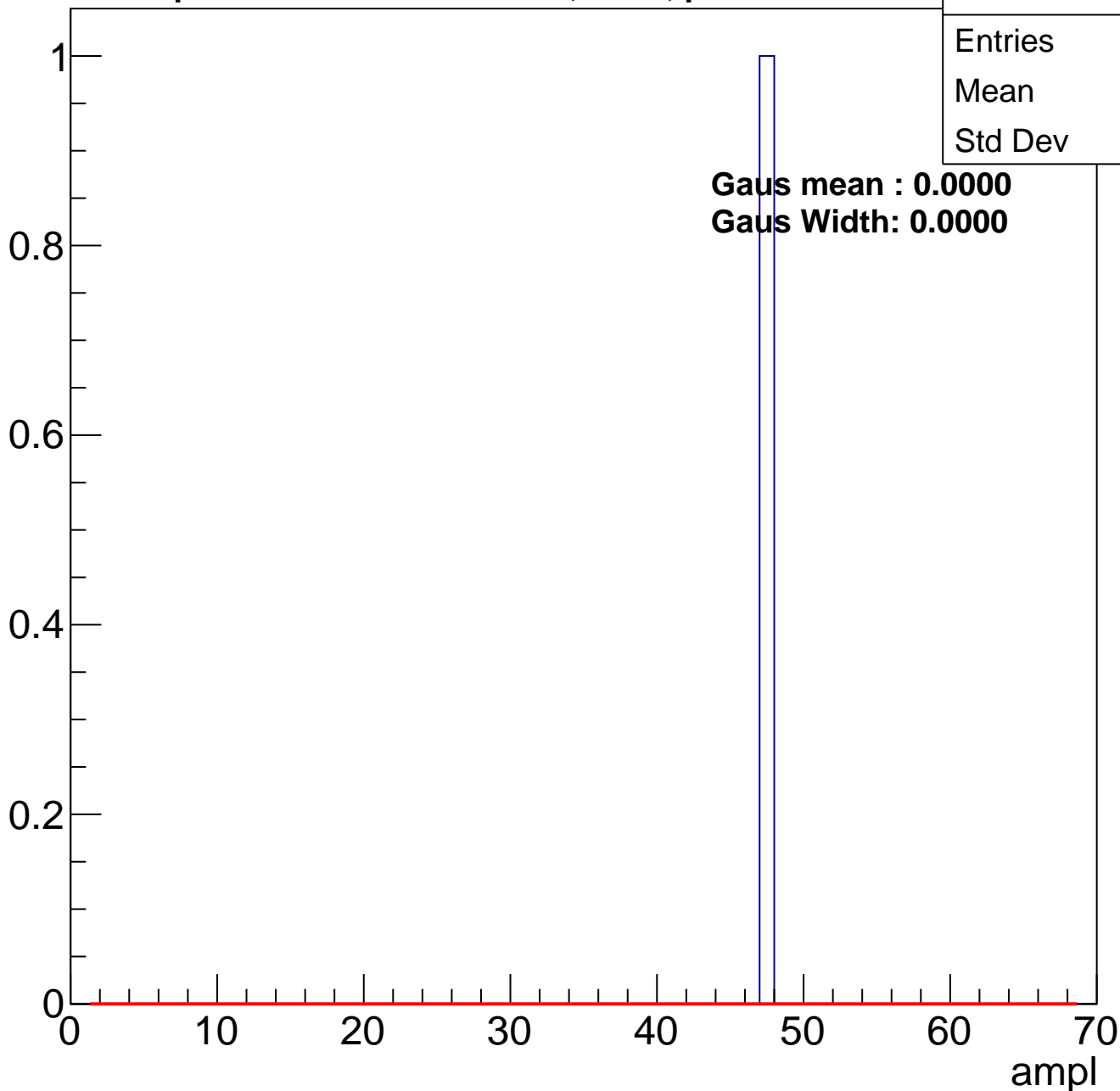
B0L100S, U13-ch95, adc1

calib_packv5_042523_0143.root, FC#6, port A1
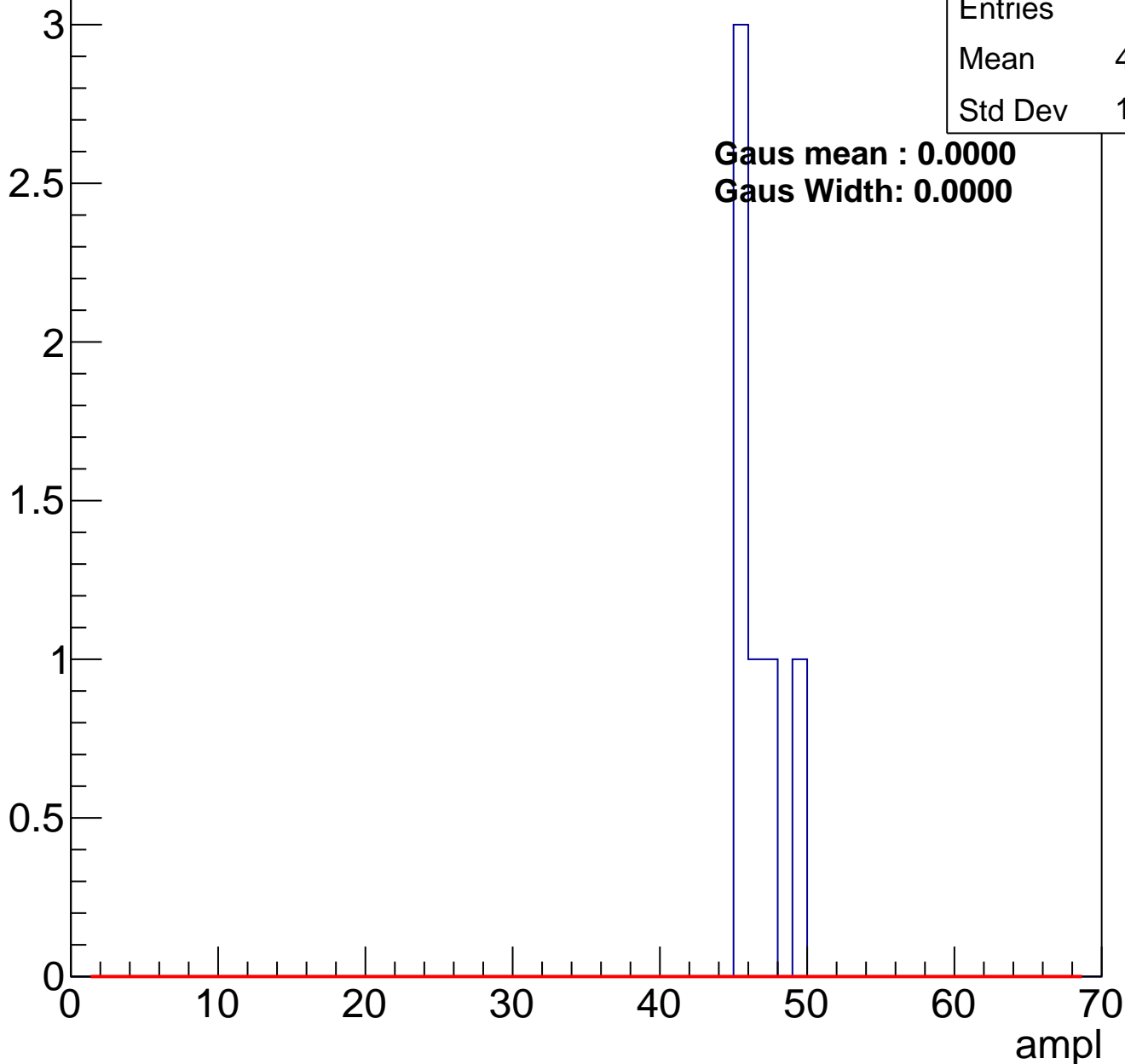
Entries	6
Mean	46.17
Std Dev	1.462

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch95, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	23
Mean	48
Std Dev	1.794

Gaus mean : 48.5000
Gaus Width: 2.7524
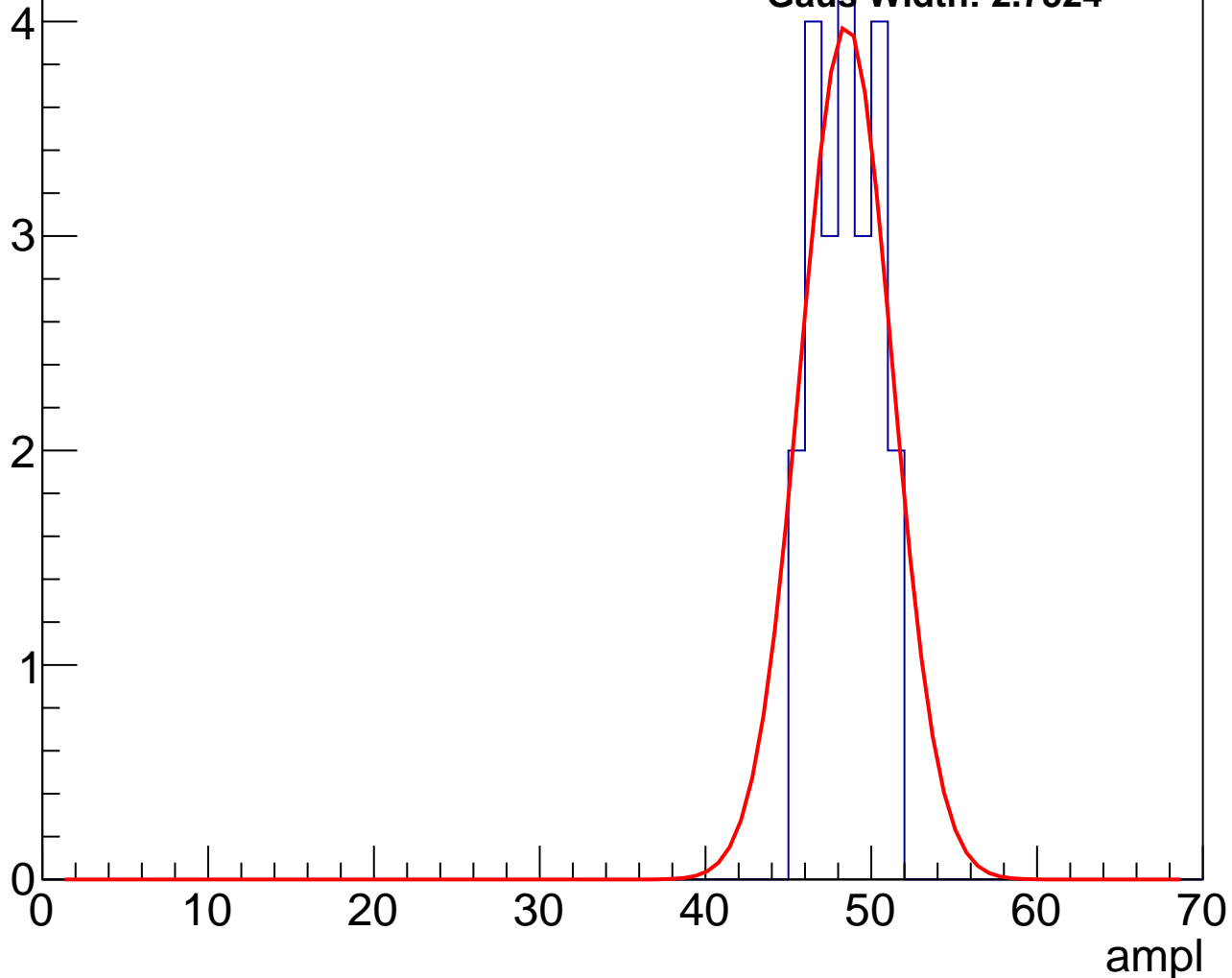
B0L100S, U13-ch95, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50.25
Std Dev	0.8292

Entry

ampl
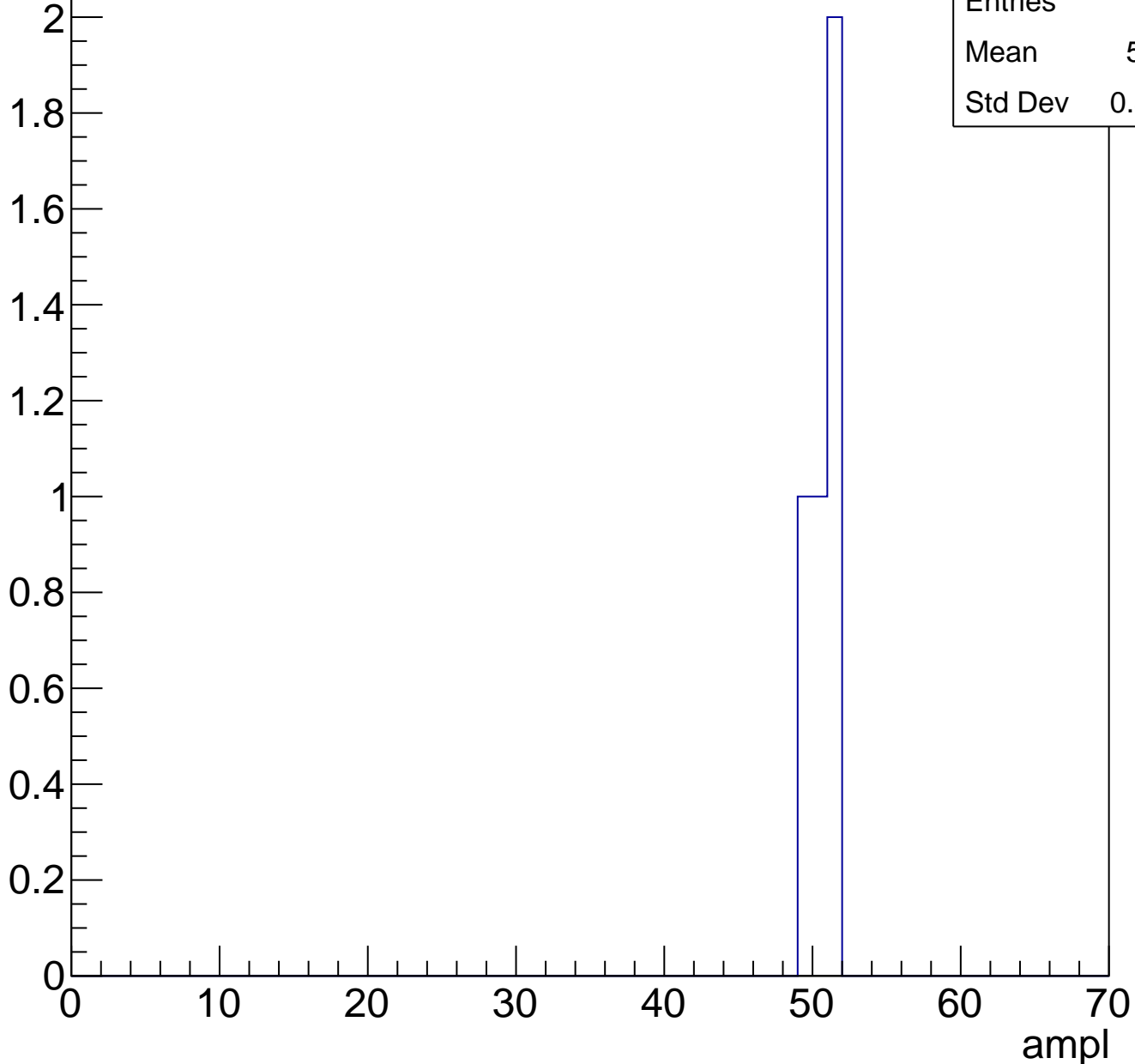
B0L100S, U13-ch95, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch95, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch95, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch95, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch96, adc0

calib_packv5_042523_0143.root, FC#6, port A1
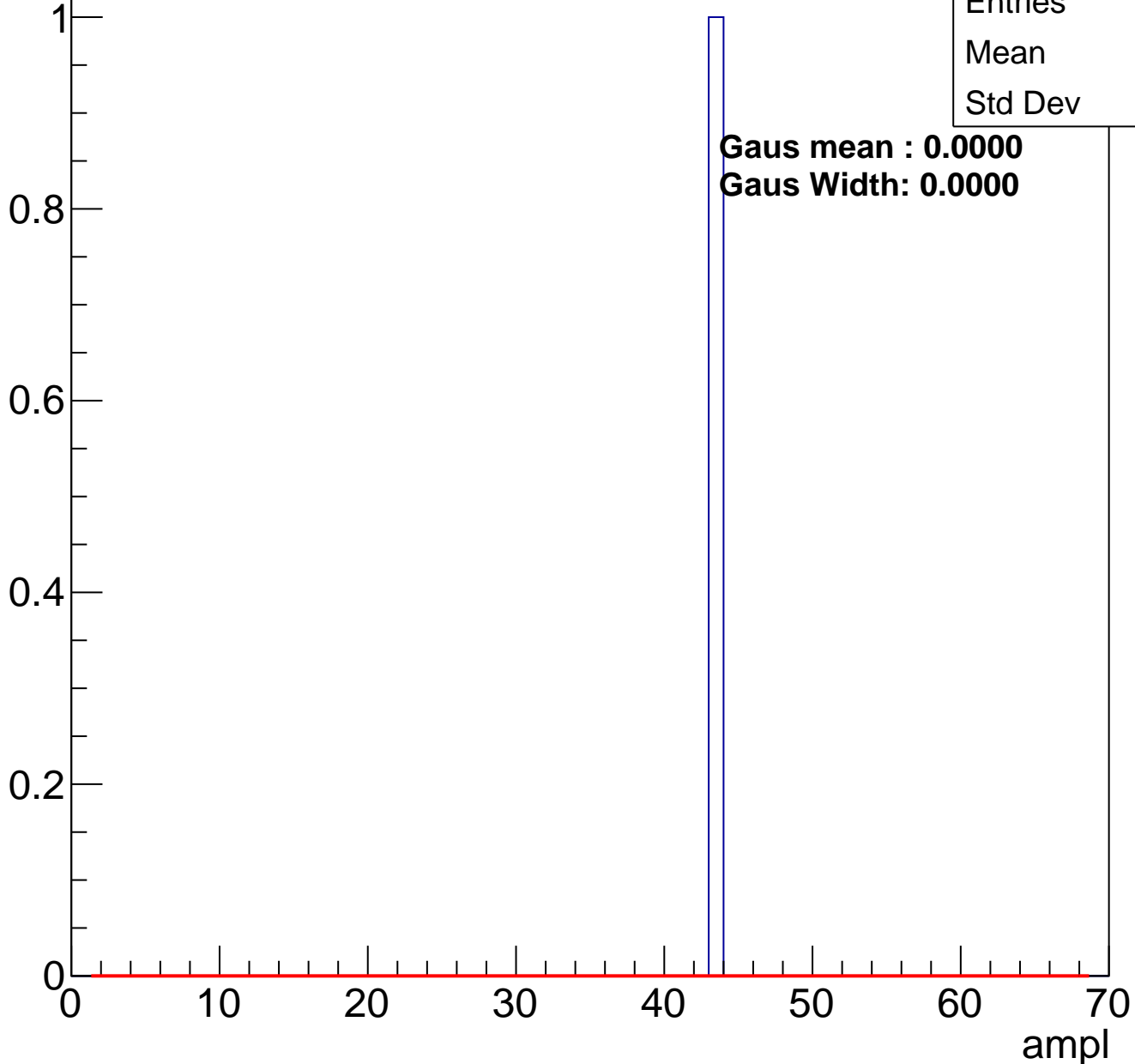
Entry

Entries	1
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch96, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch96, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
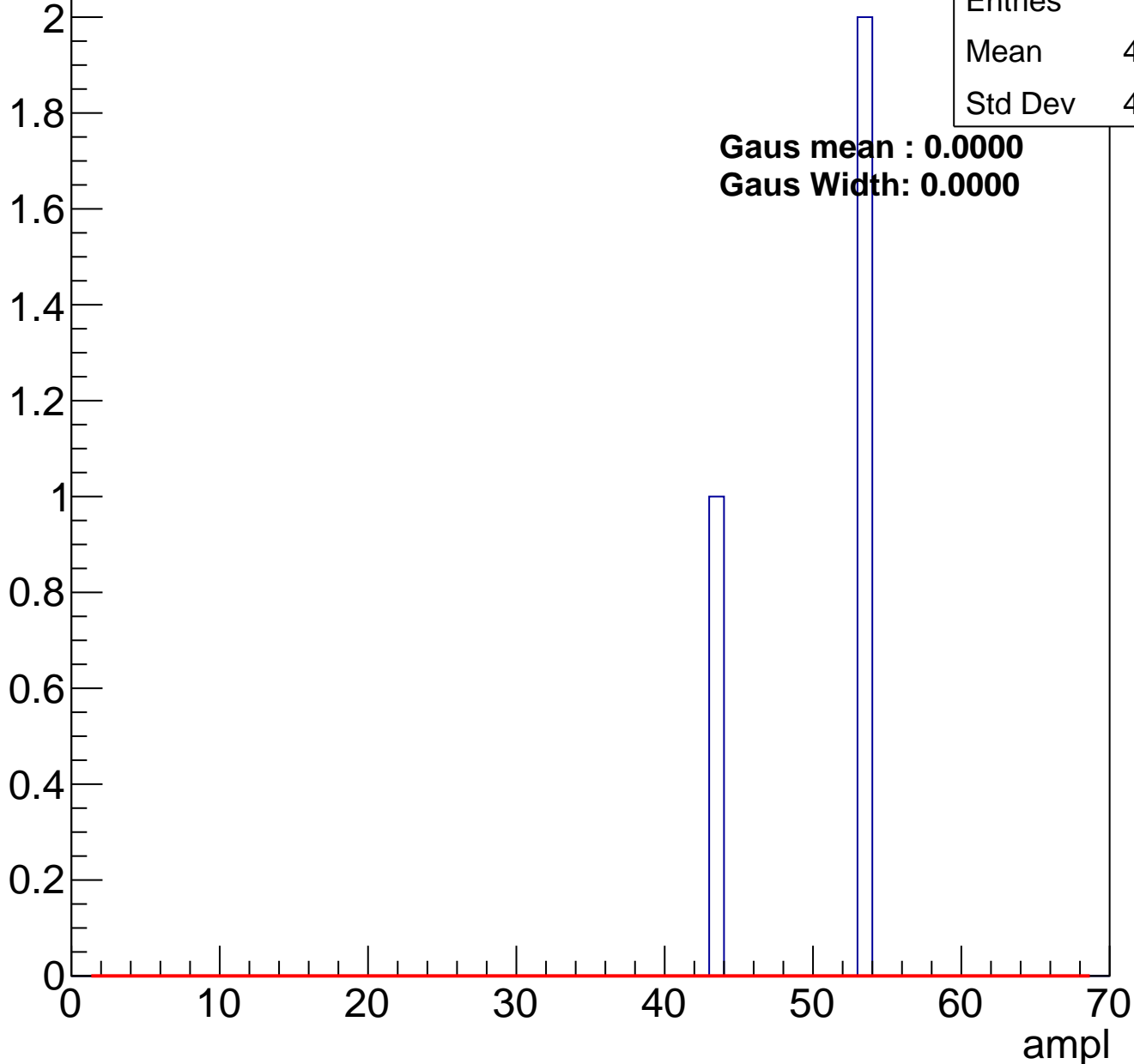
Entries	3
Mean	49.67
Std Dev	4.714

Entry

ampl

B0L100S, U13-ch96, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch96, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch96, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch96, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch96, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch97, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch97, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch97, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	47.5
Std Dev	3.202

Gaus mean : 0.0000
Gaus Width: 0.0000
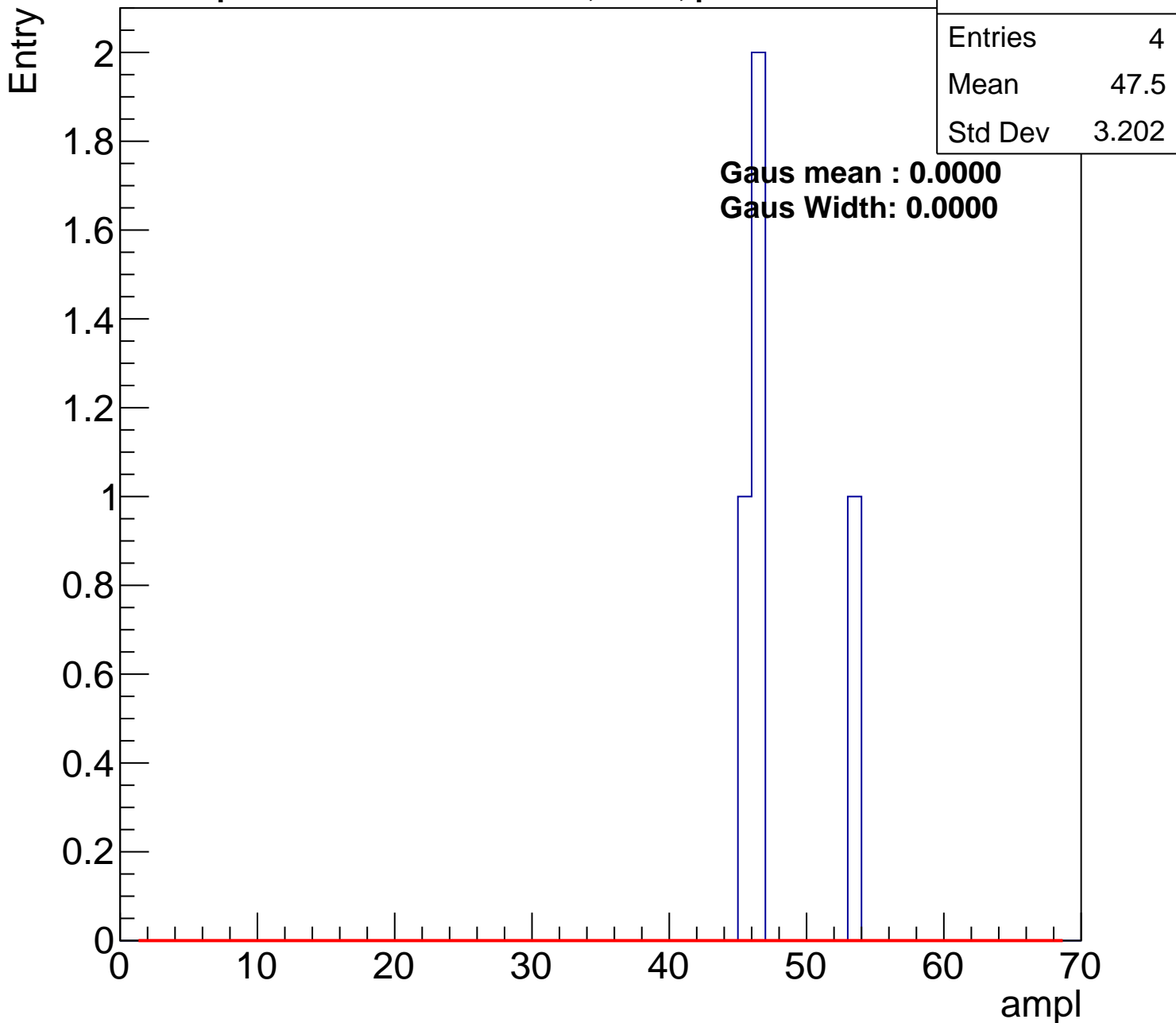
B0L100S, U13-ch97, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	53
Std Dev	0

ampl
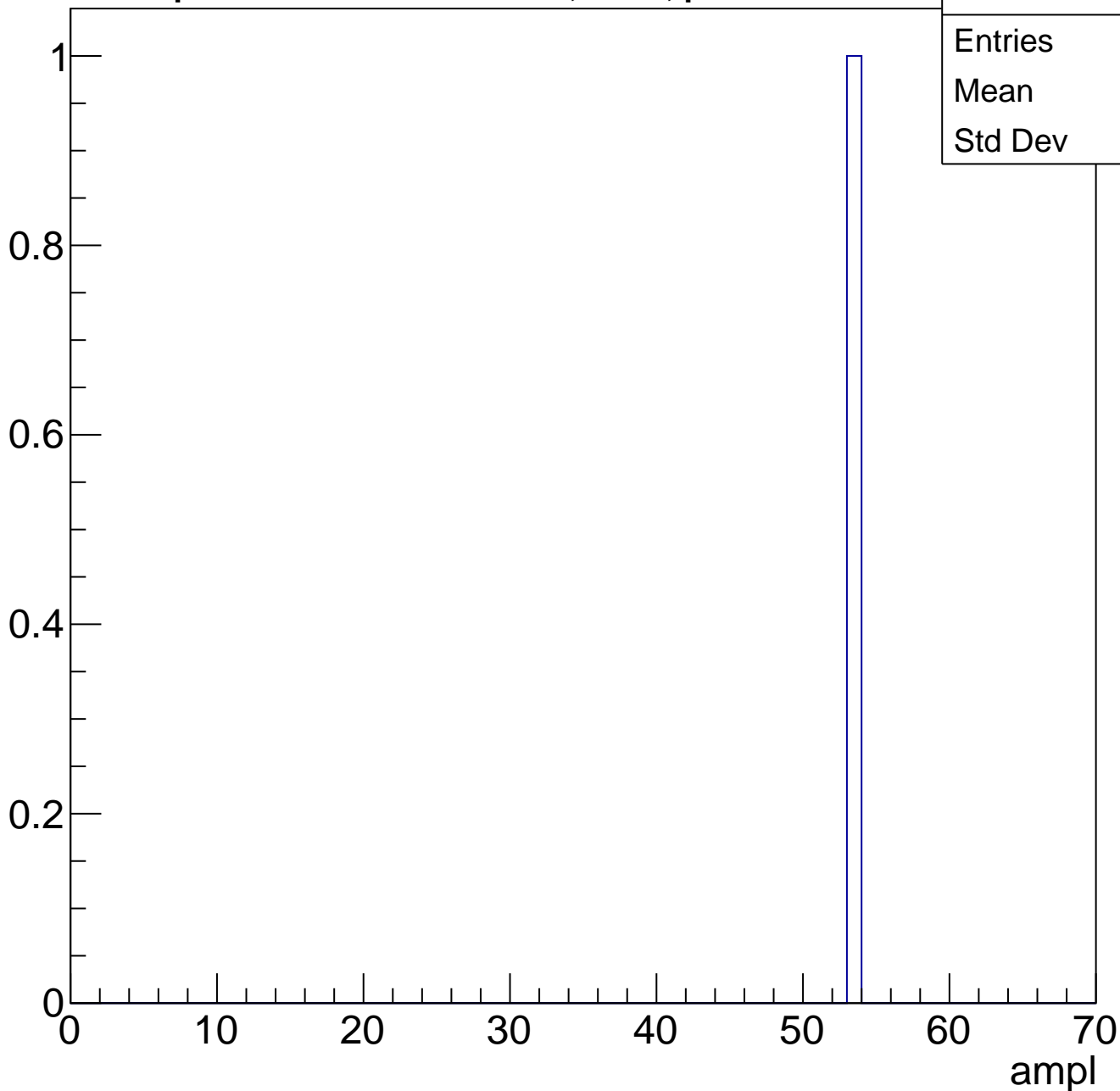
B0L100S, U13-ch97, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch97, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch97, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch97, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch98, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch98, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch98, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch98, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch98, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch98, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch98, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch98, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch99, adc1

calib_packv5_042523_0143.root, FC#6, port A1
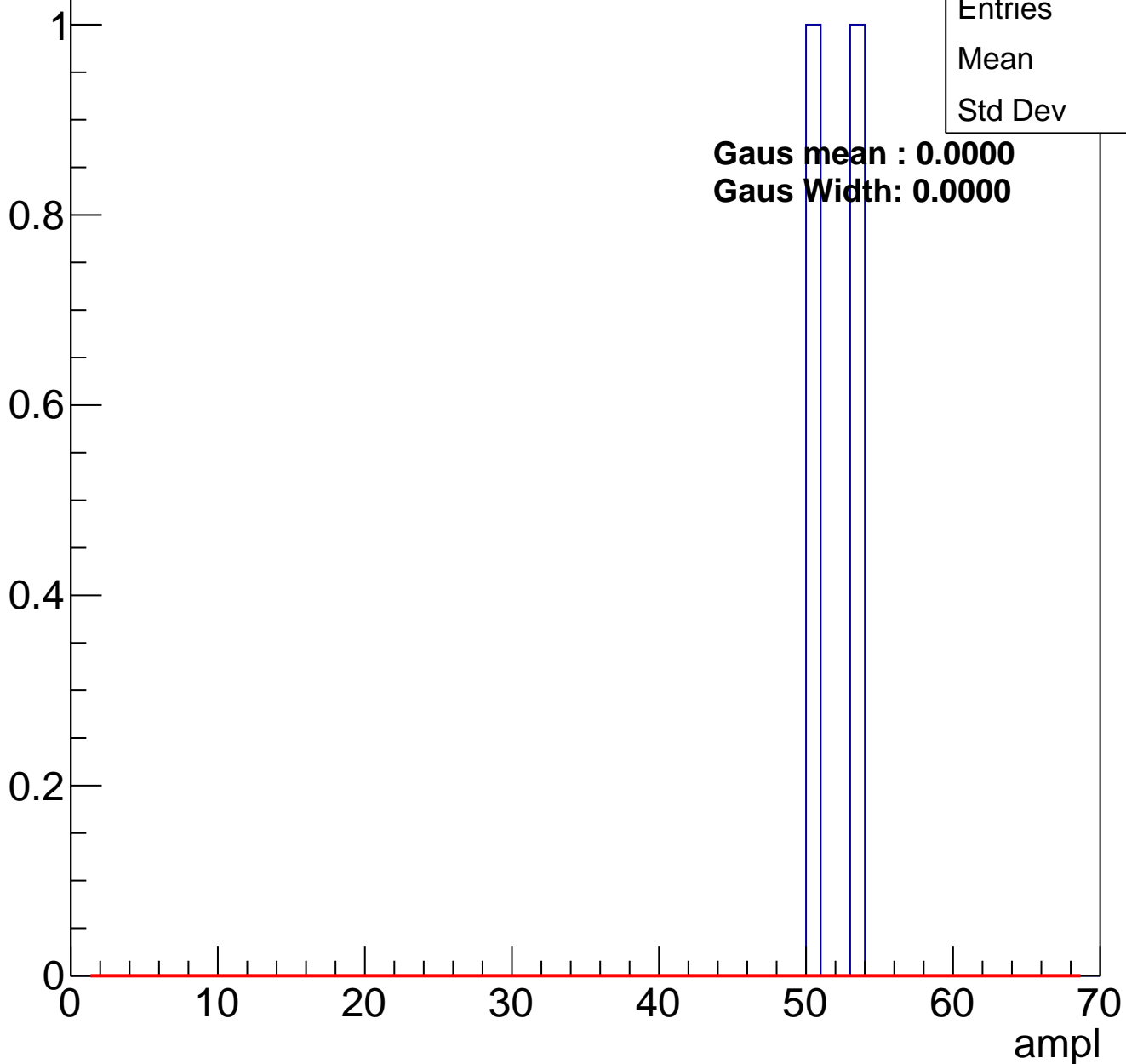
Entries	2
Mean	51.5
Std Dev	1.5

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch99, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	53
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch99, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	52.5
Std Dev	1.5
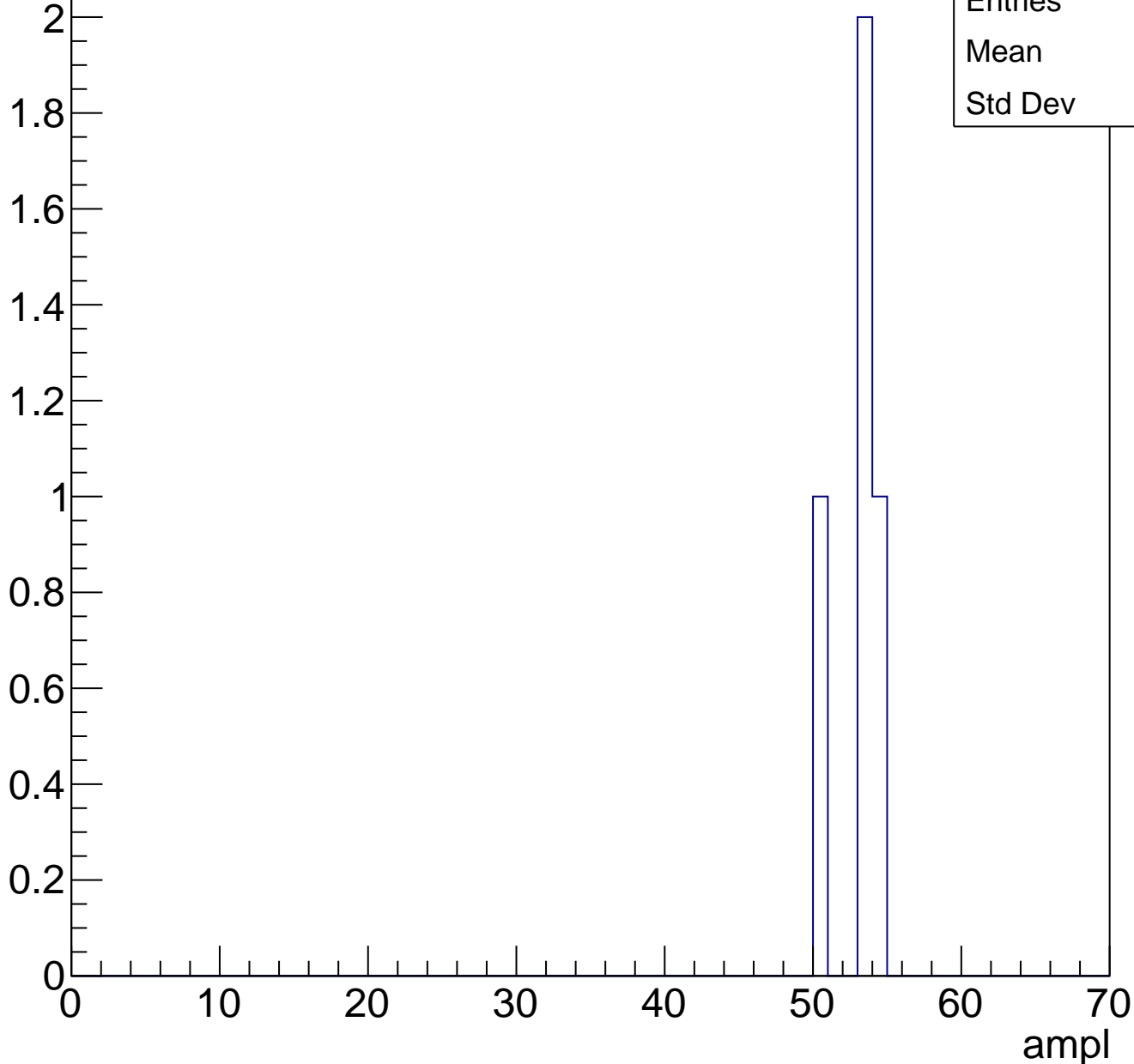
B0L100S, U13-ch99, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch99, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch99, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch99, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch100, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
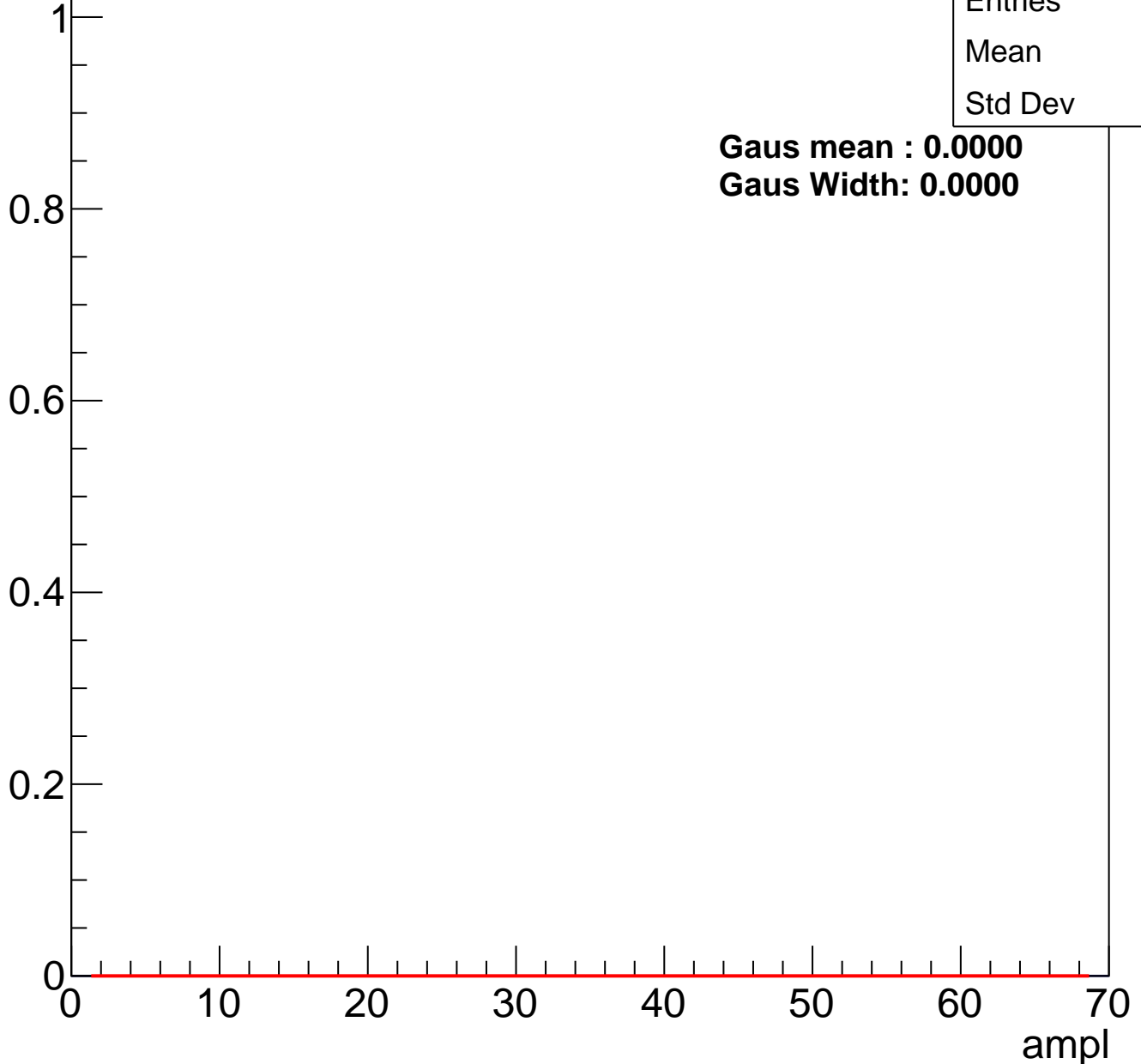
B0L100S, U13-ch100, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch100, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch100, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch100, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch100, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch100, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch100, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch101, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch101, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch101, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50.33
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000
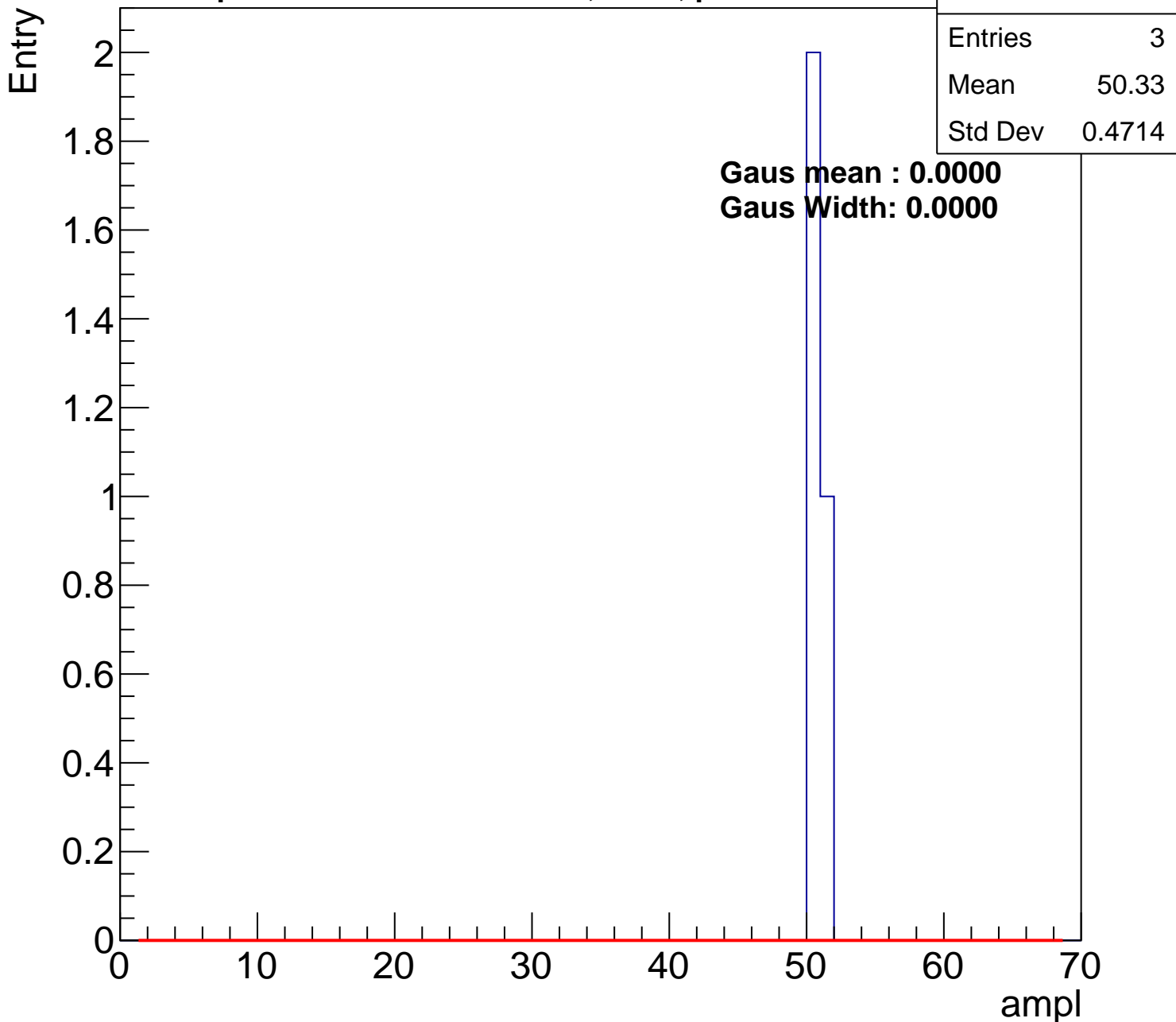
B0L100S, U13-ch101, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50.67
Std Dev	0.4714

Entry

ampl

B0L100S, U13-ch101, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch101, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch101, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch101, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch102, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch102, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

B0L100S, U13-ch102, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch102, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch102, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch102, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch102, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch102, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch103, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch103, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
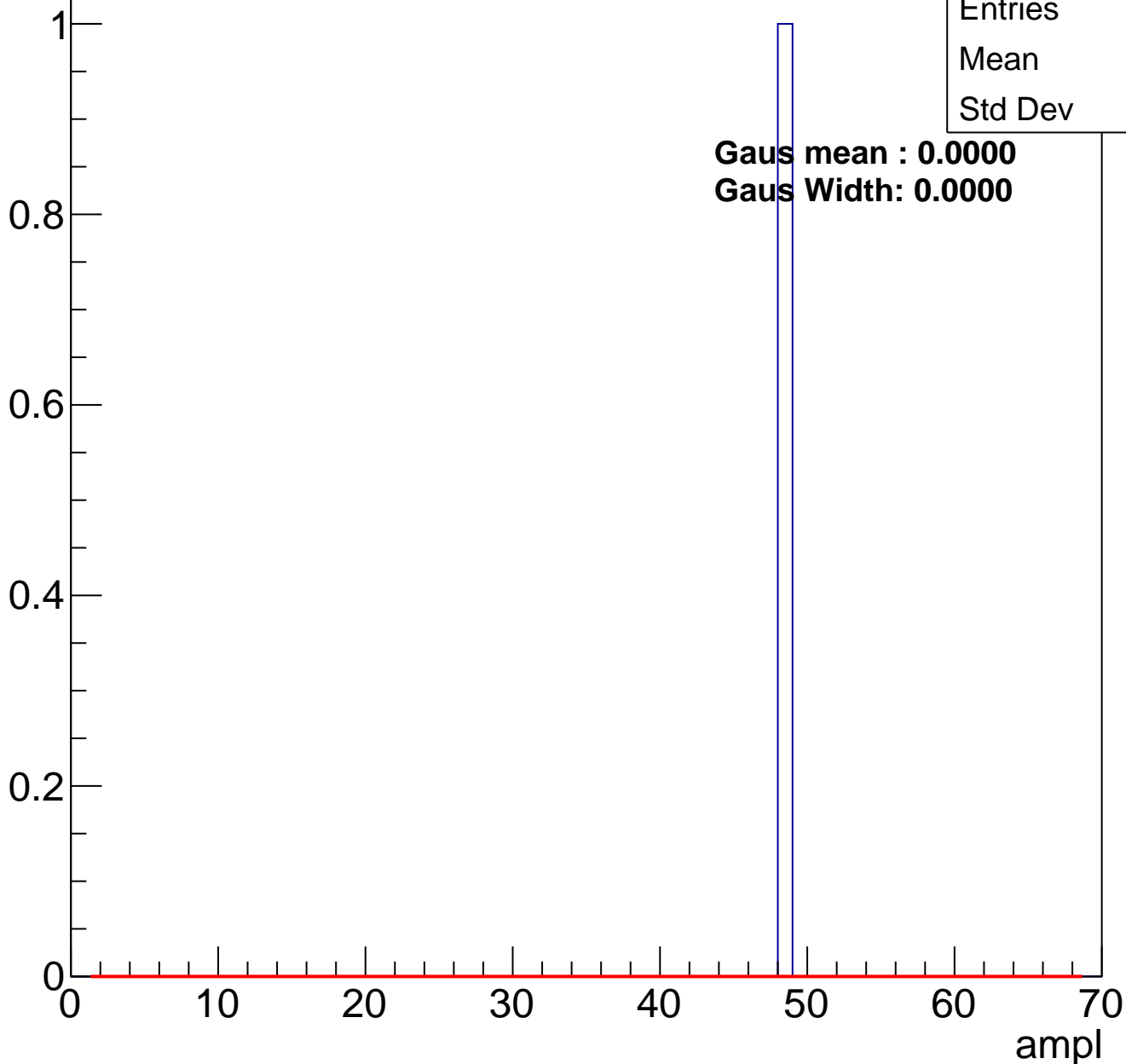
B0L100S, U13-ch103, adc2

calib_packv5_042523_0143.root, FC#6, port A1
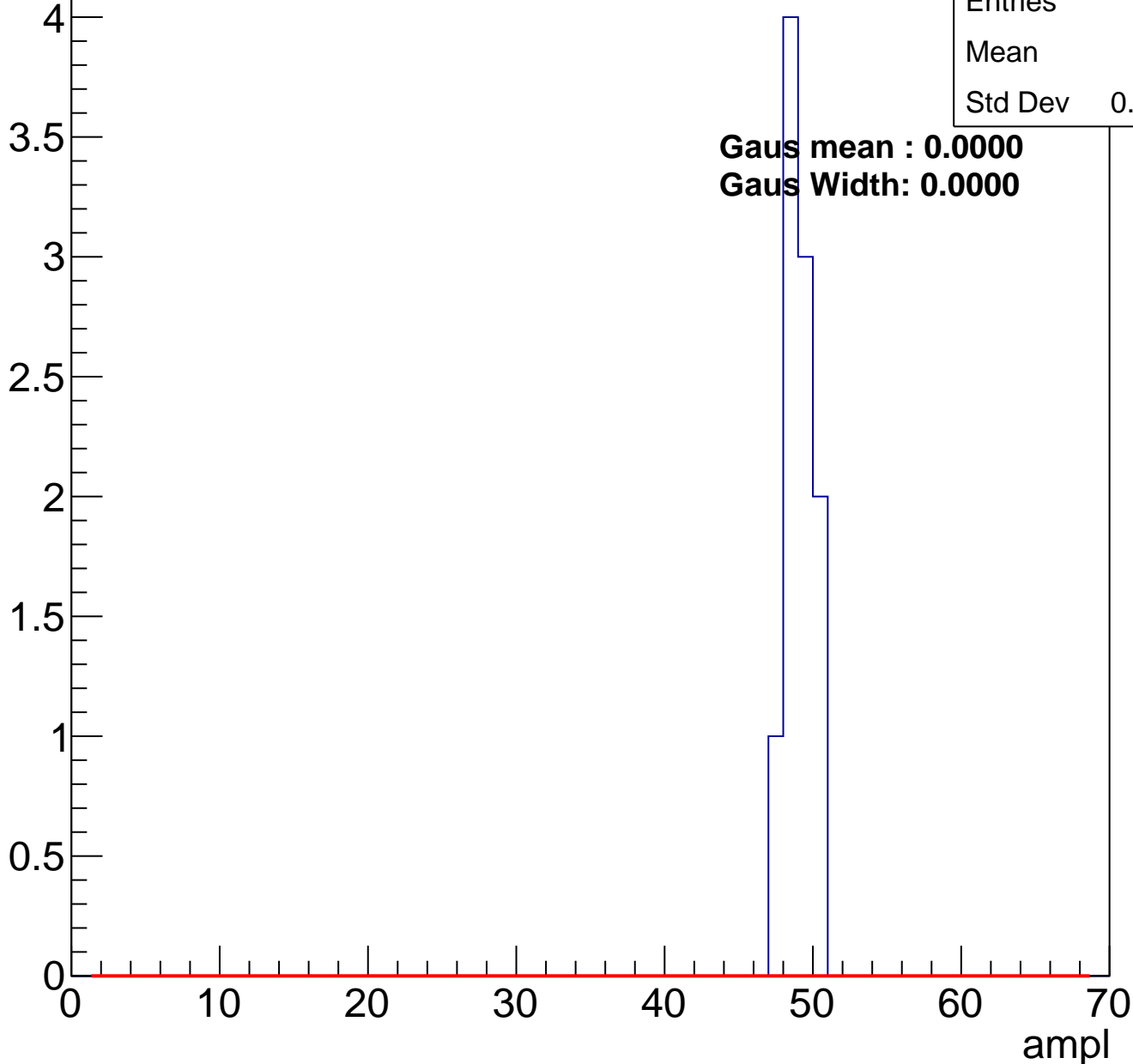
Entries	10
Mean	48.6
Std Dev	0.9165

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch103, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	50.8
Std Dev	1.833

Entry

ampl
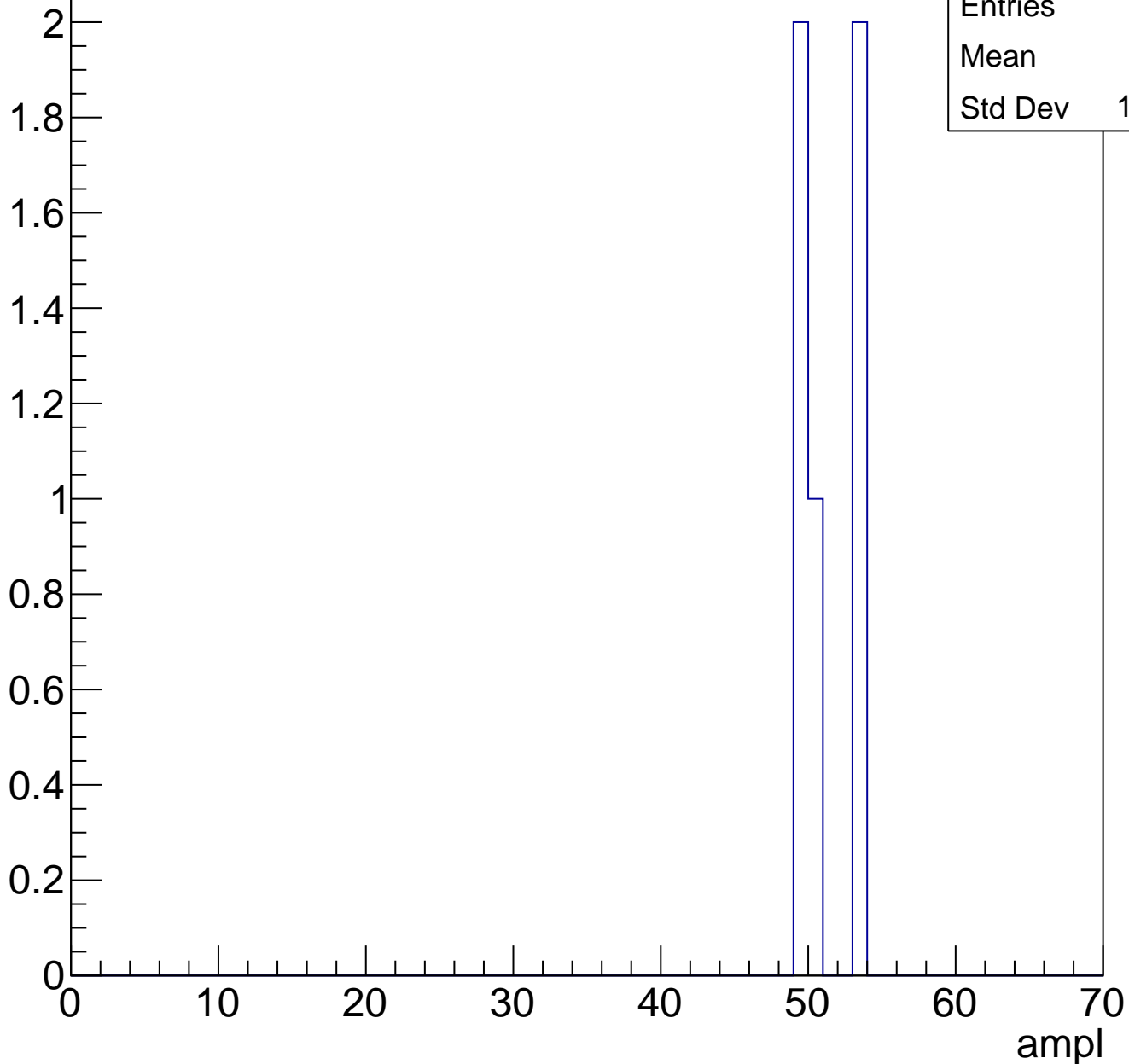
B0L100S, U13-ch103, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch103, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch103, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch103, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch104, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch104, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch104, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
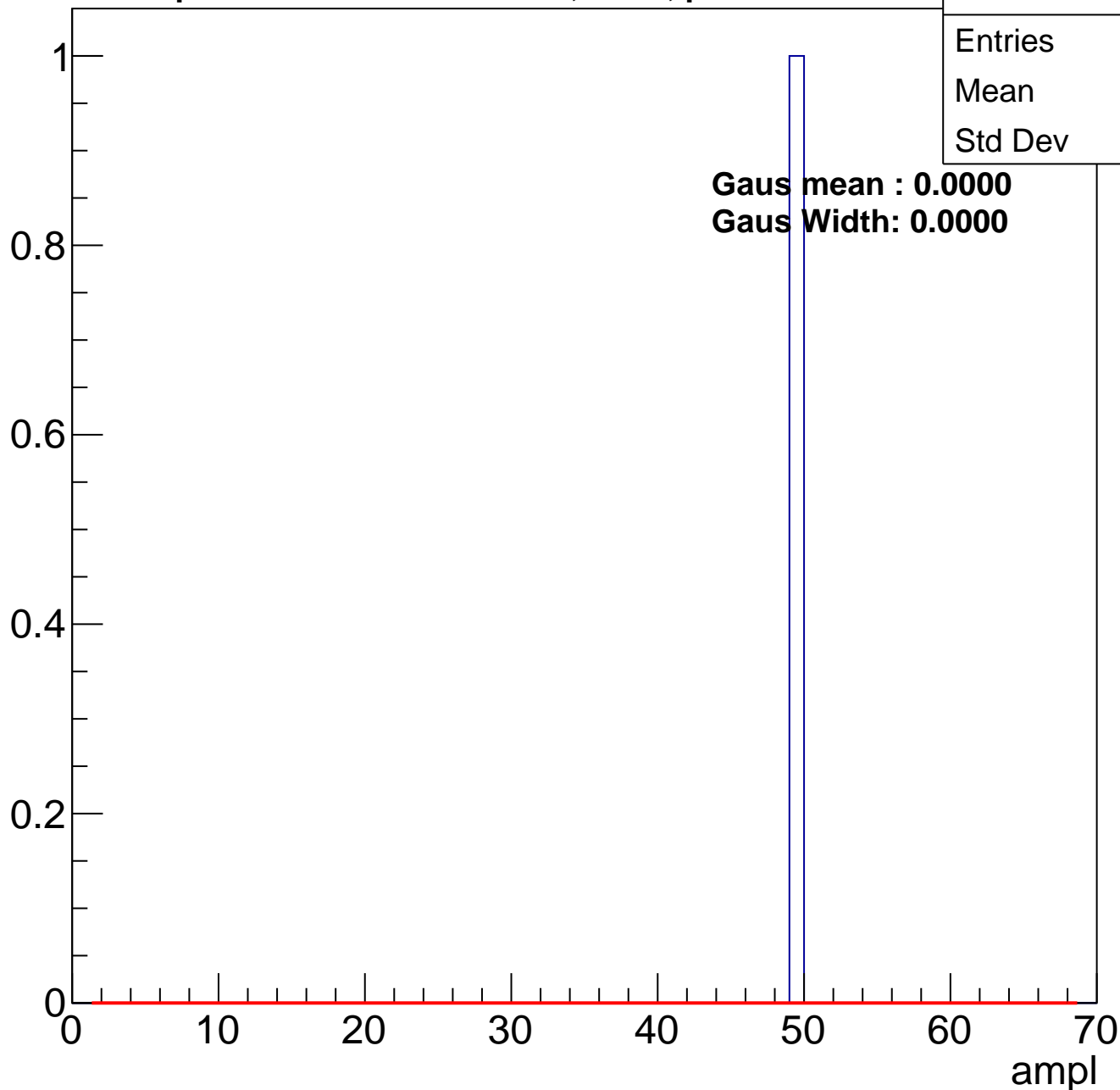
B0L100S, U13-ch104, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch104, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch104, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch104, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch104, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch105, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch105, adc1

calib_packv5_042523_0143.root, FC#6, port A1

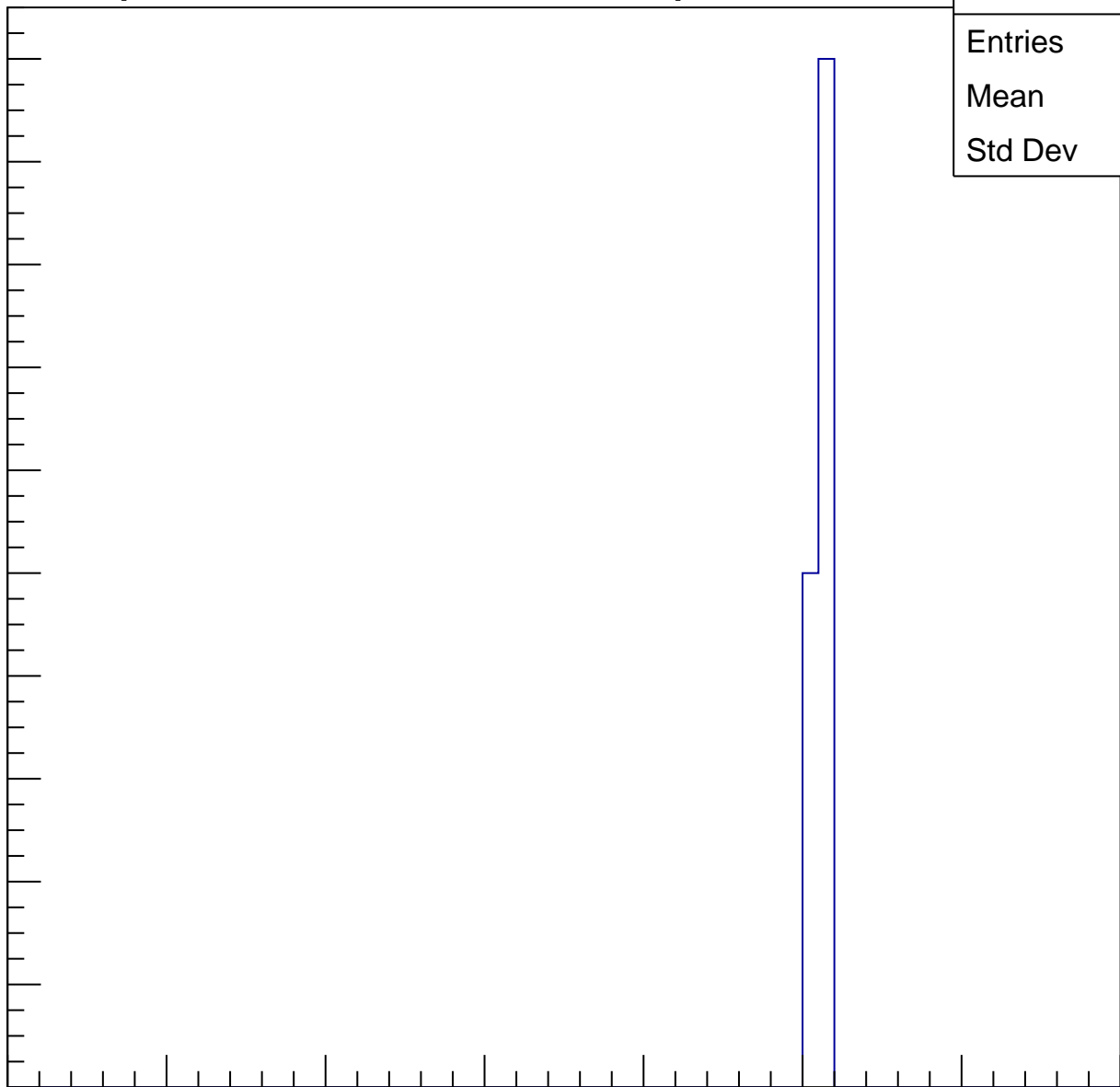| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch105, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch105, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch105, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch105, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch105, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch105, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch106, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch106, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch106, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	49.55
Std Dev	0.8907

Gaus mean : 50.0717
Gaus Width: 1.0202
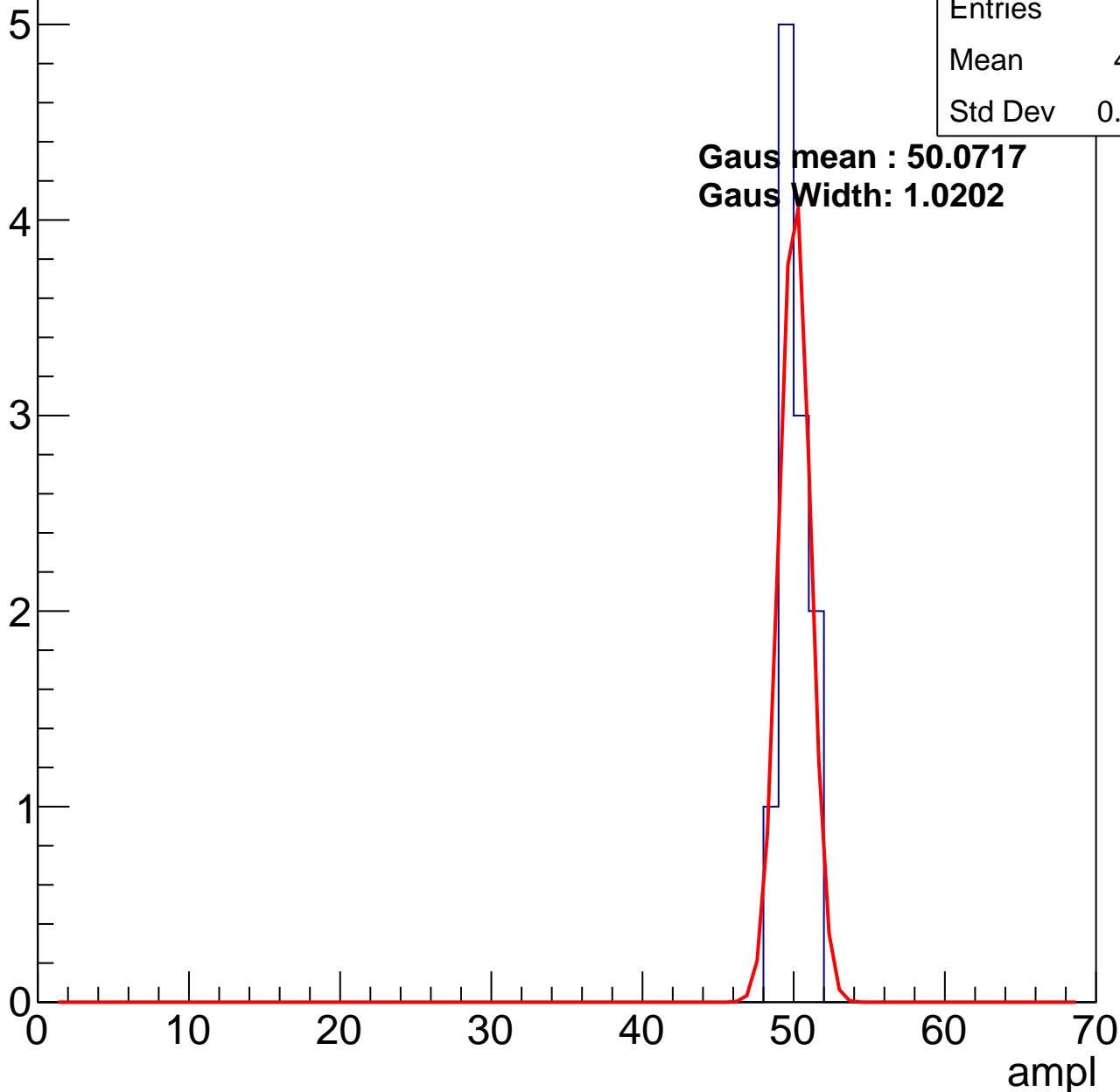
B0L100S, U13-ch106, adc3

calib_packv5_042523_0143.root, FC#6, port A1
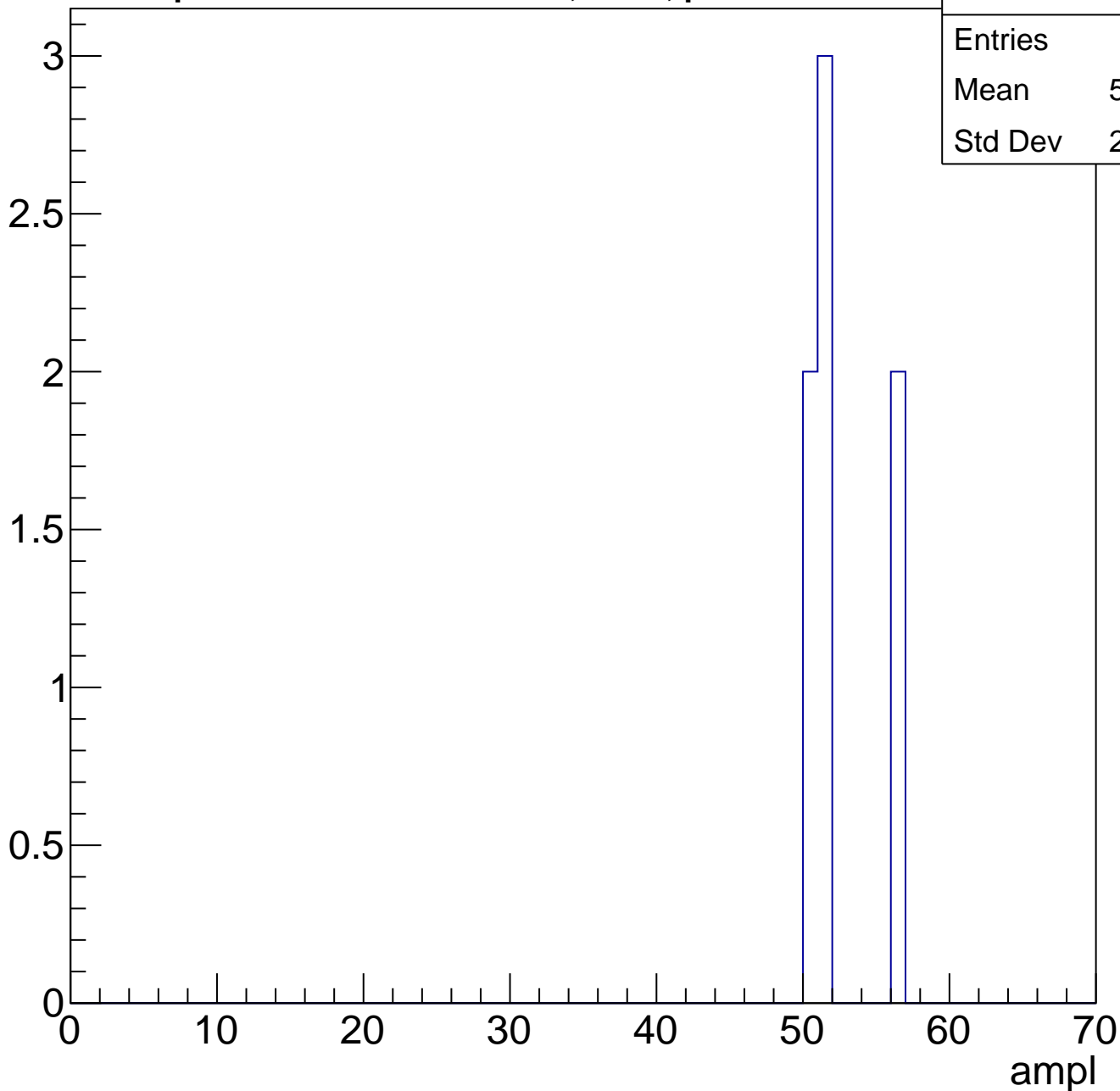
Entries	7
Mean	52.14
Std Dev	2.474

B0L100S, U13-ch106, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch106, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch106, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch106, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch107, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
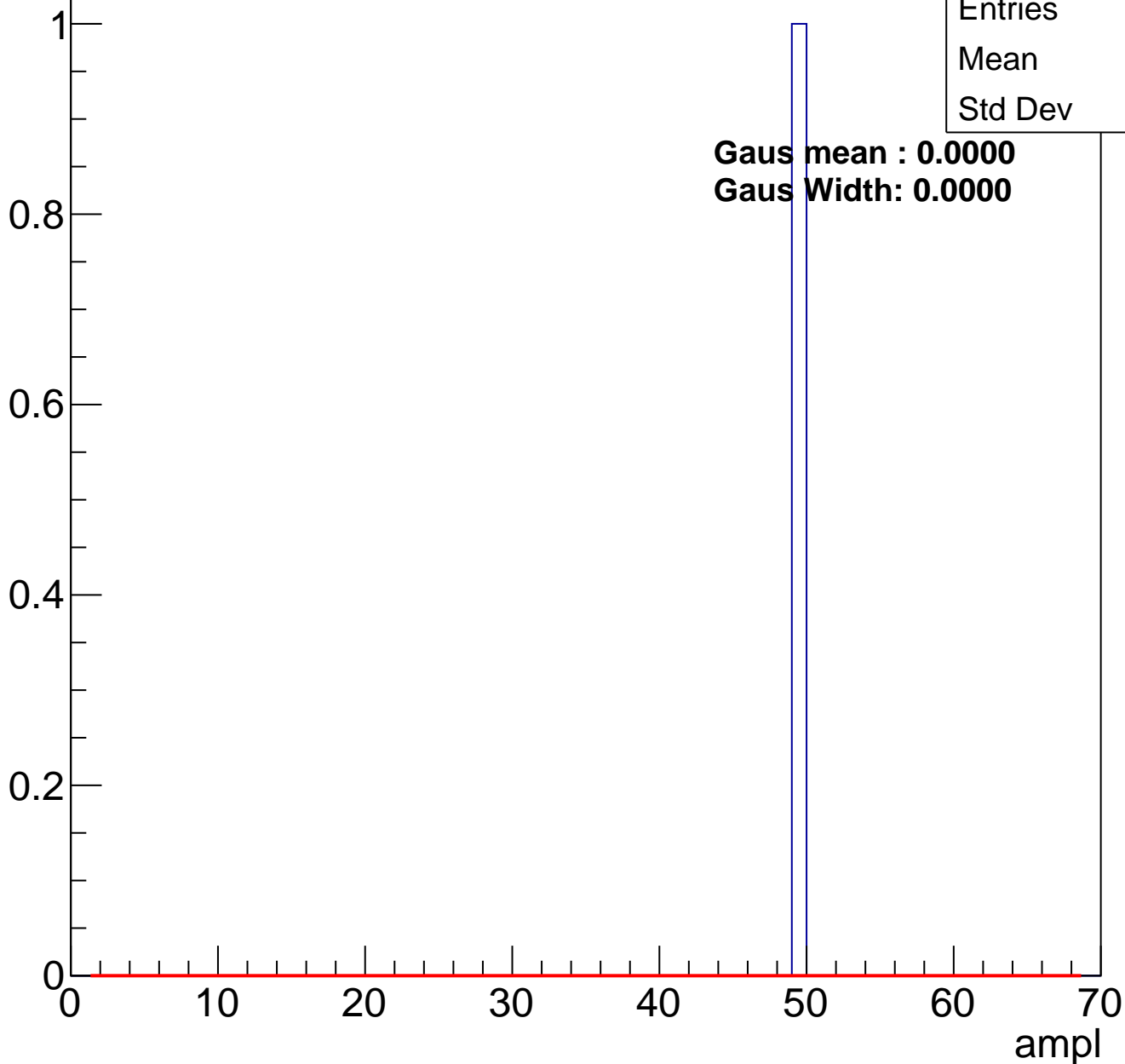
ampl

B0L100S, U13-ch107, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	4
Mean	52
Std Dev	1.225
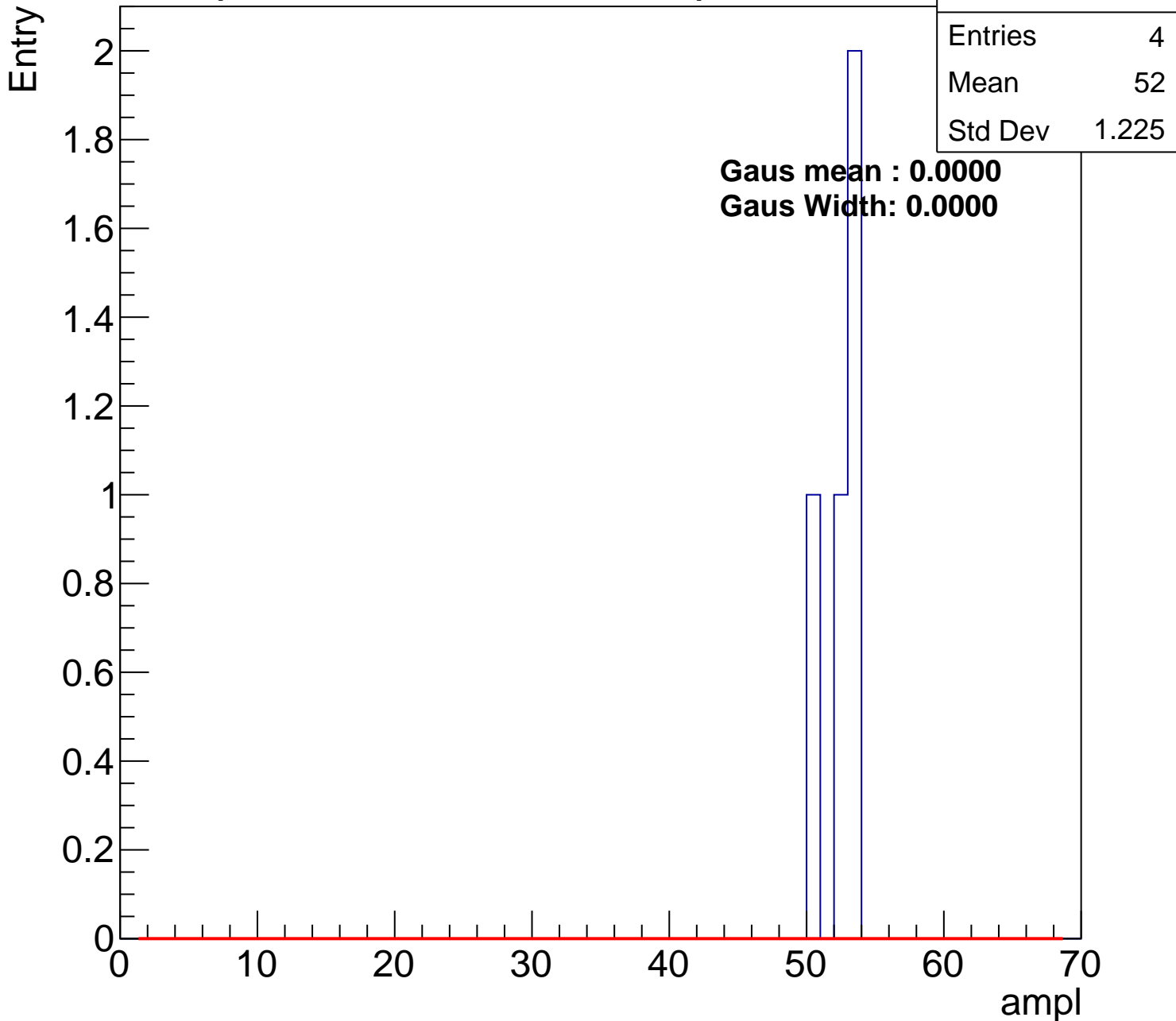
Entry

ampl

B0L100S, U13-ch107, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	52.5
Std Dev	0.5

Entry

ampl

B0L100S, U13-ch107, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch107, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch107, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch107, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch108, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch108, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch108, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

	Entries	0
	Mean	0
	Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch108, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch108, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch108, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch108, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch108, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch109, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch109, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch109, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch109, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch109, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U13-ch109, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch109, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch109, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch110, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch110, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
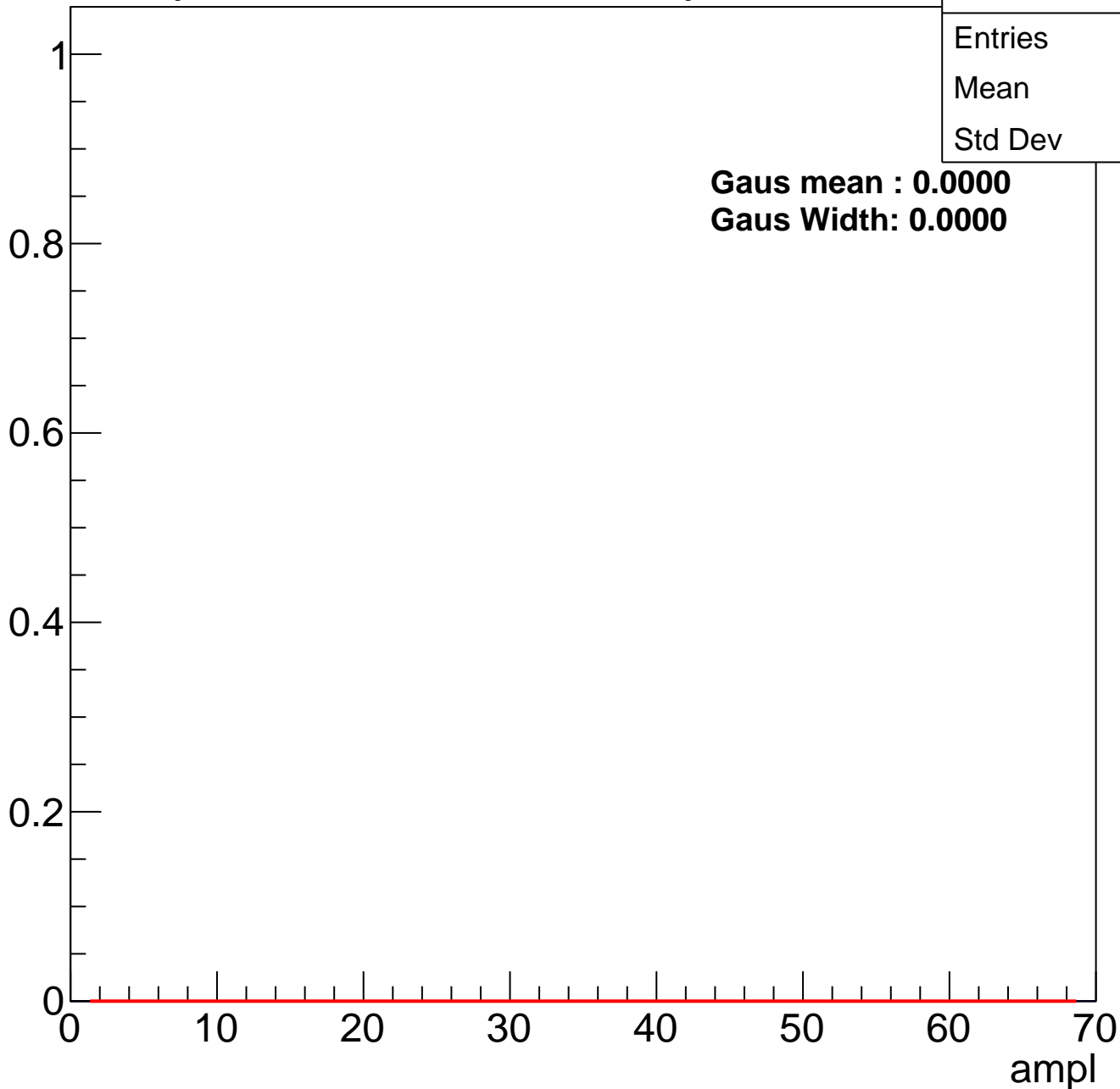
B0L100S, U13-ch110, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch110, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch110, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch110, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch110, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch110, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch111, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch111, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch111, adc2
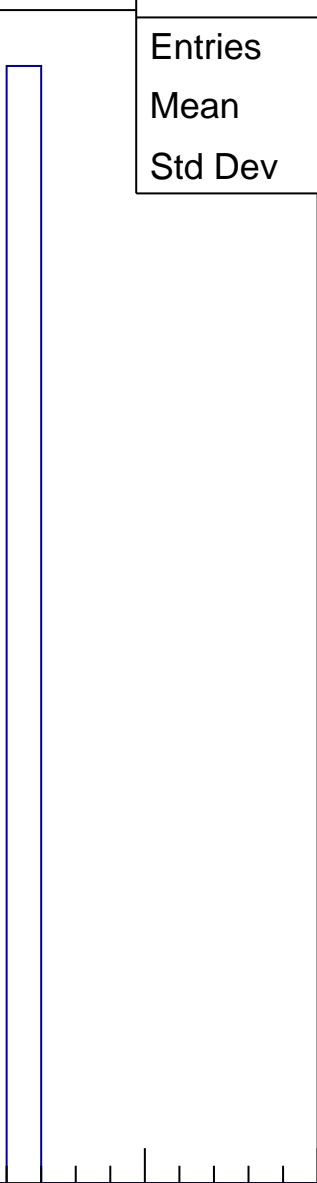
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch111, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch111, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch111, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch111, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch111, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch112, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch112, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch112, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

B0L100S, U13-ch112, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch112, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch112, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch112, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch112, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch113, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch113, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch113, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch113, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch113, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch113, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch113, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch113, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch114, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch114, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch114, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch114, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch114, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch114, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch114, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch114, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch115, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch115, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch115, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch115, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch115, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch115, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch115, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch115, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch116, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch116, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch116, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch116, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch116, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch116, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

ampl
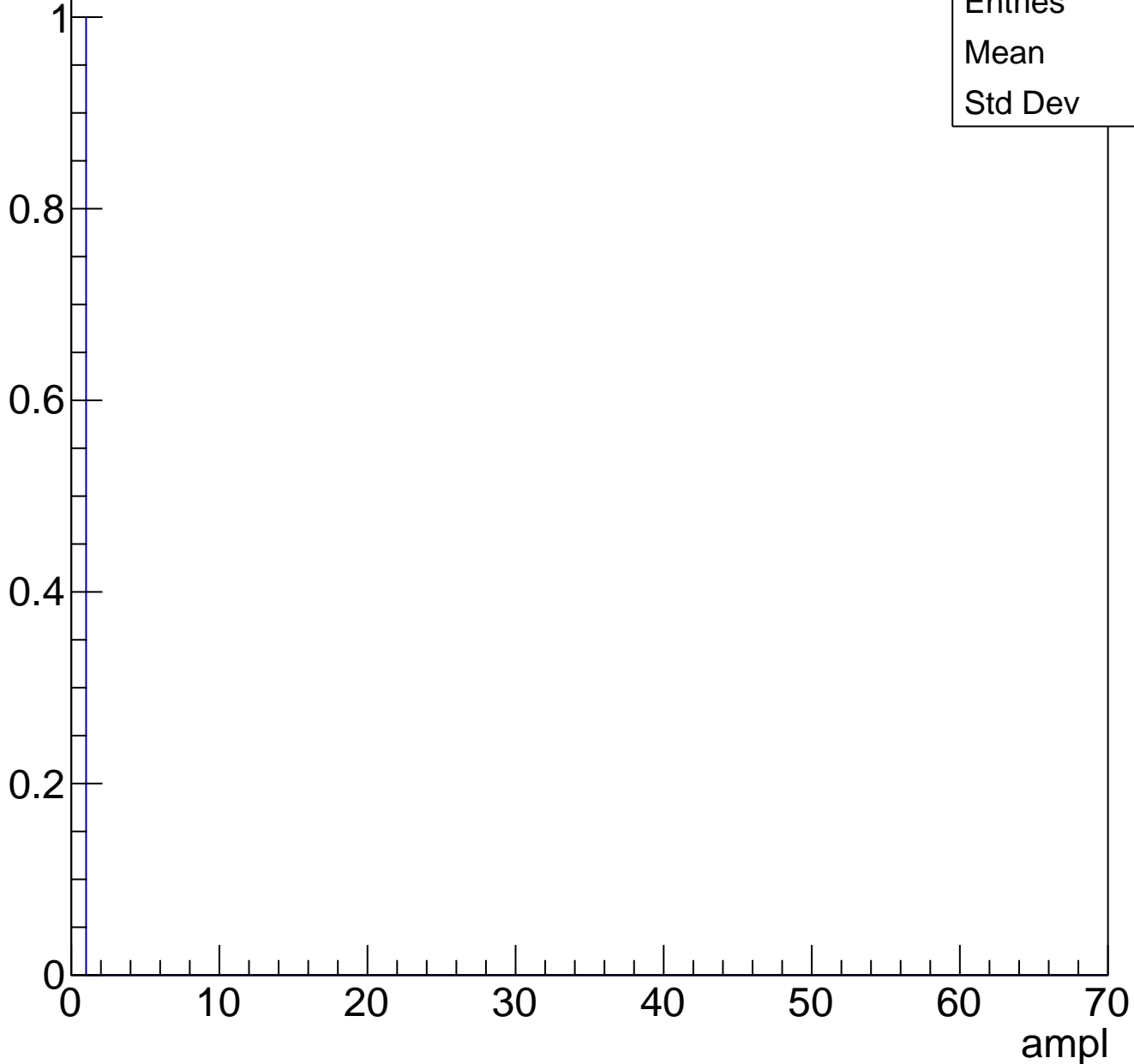
Entry

B0L100S, U13-ch116, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch116, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch117, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch117, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch117, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch117, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch117, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch117, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch117, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch117, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch118, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	47
Std Dev	1

Gaus mean : 0.0000
Gaus Width: 0.0000
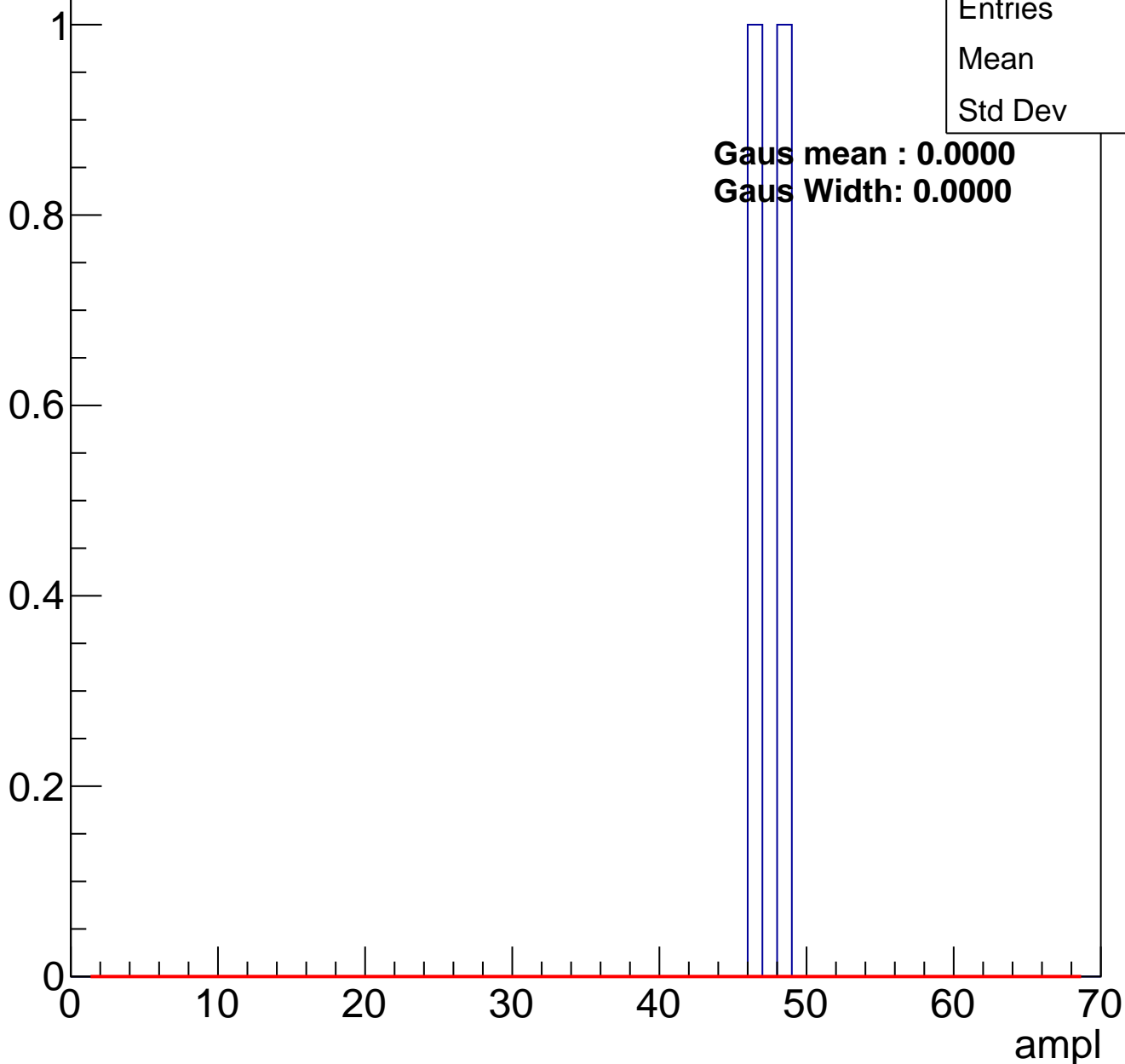
Entry

ampl

B0L100S, U13-ch118, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	47.5
Std Dev	1.239

Gaus mean : 48.8812
Gaus Width: 2.1418

Entry

ampl

B0L100S, U13-ch118, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
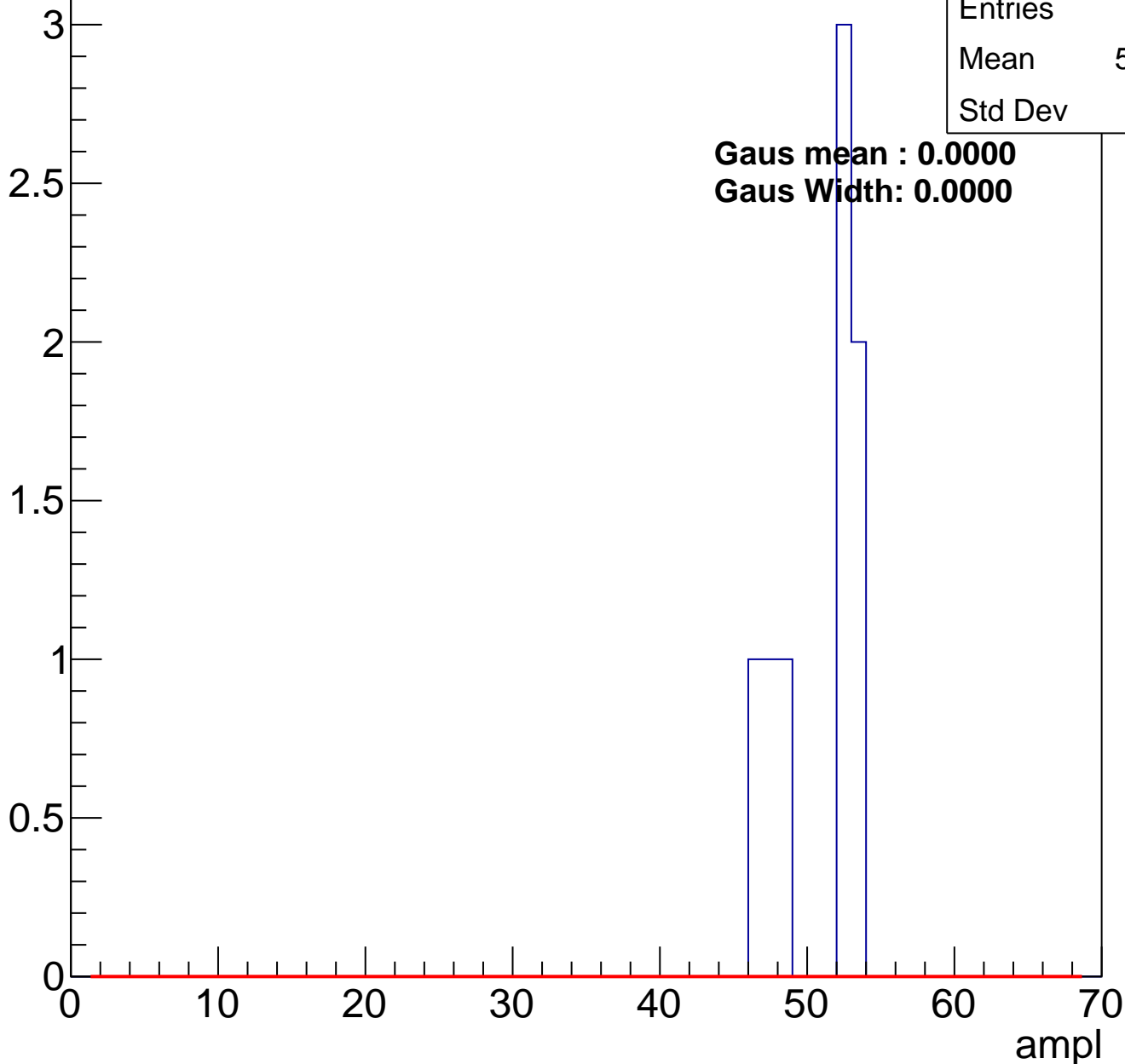
Entries	8
Mean	50.38
Std Dev	2.69

Entry

ampl

B0L100S, U13-ch118, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50.75
Std Dev	2.773

Entry

ampl
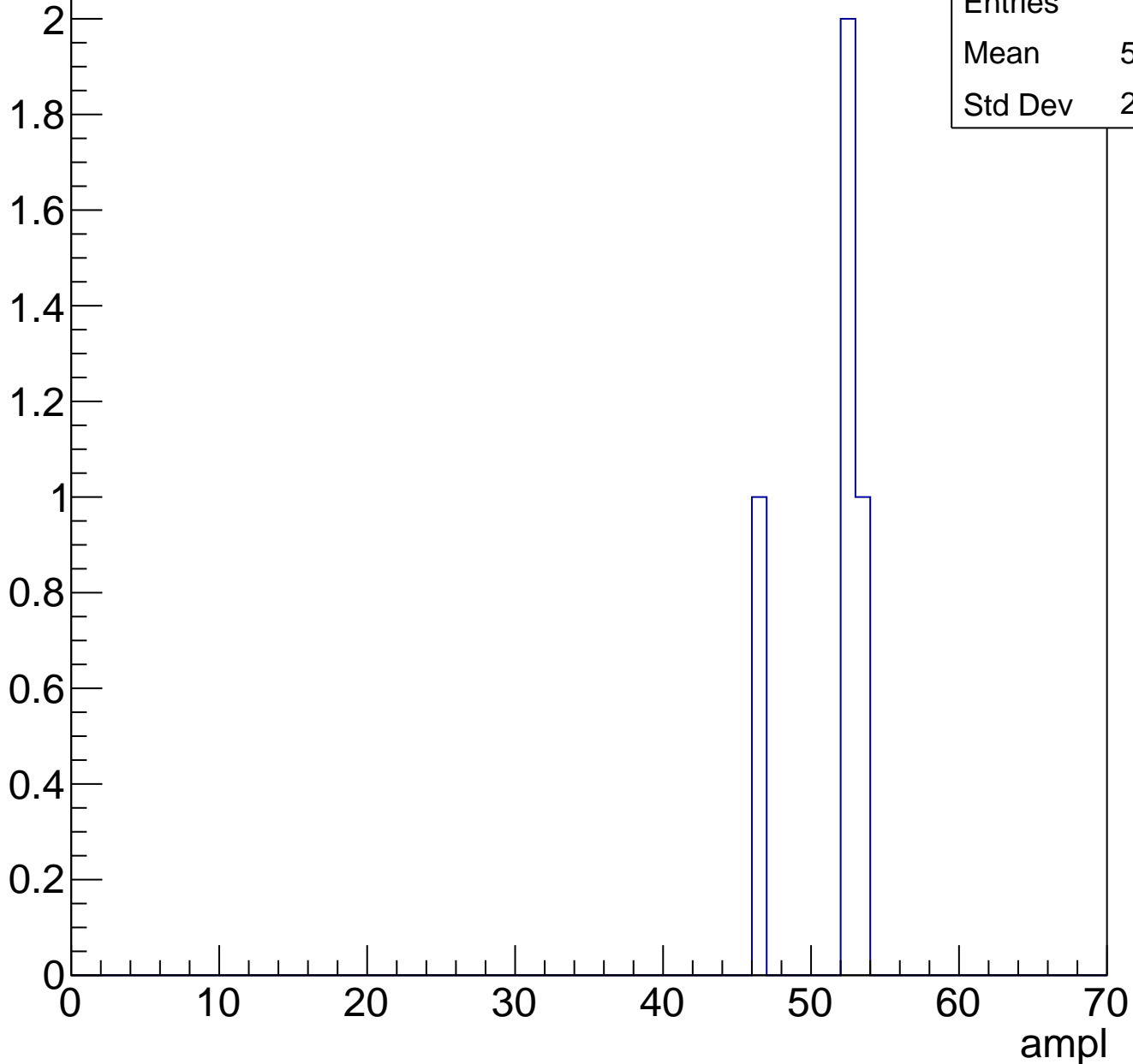
B0L100S, U13-ch118, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch118, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch118, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch118, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch119, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch119, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	48.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
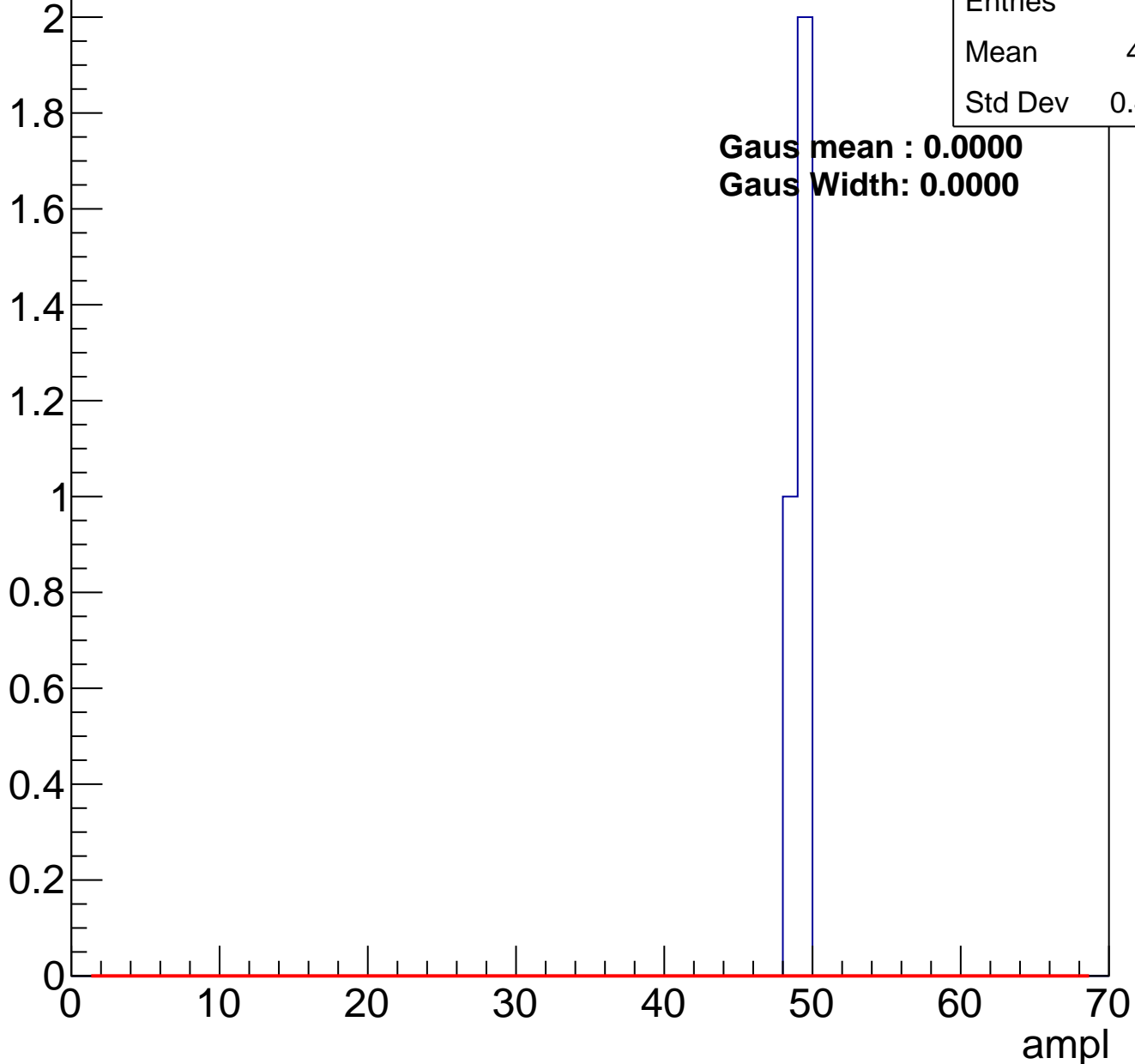
B0L100S, U13-ch119, adc2

calib_packv5_042523_0143.root, FC#6, port A1
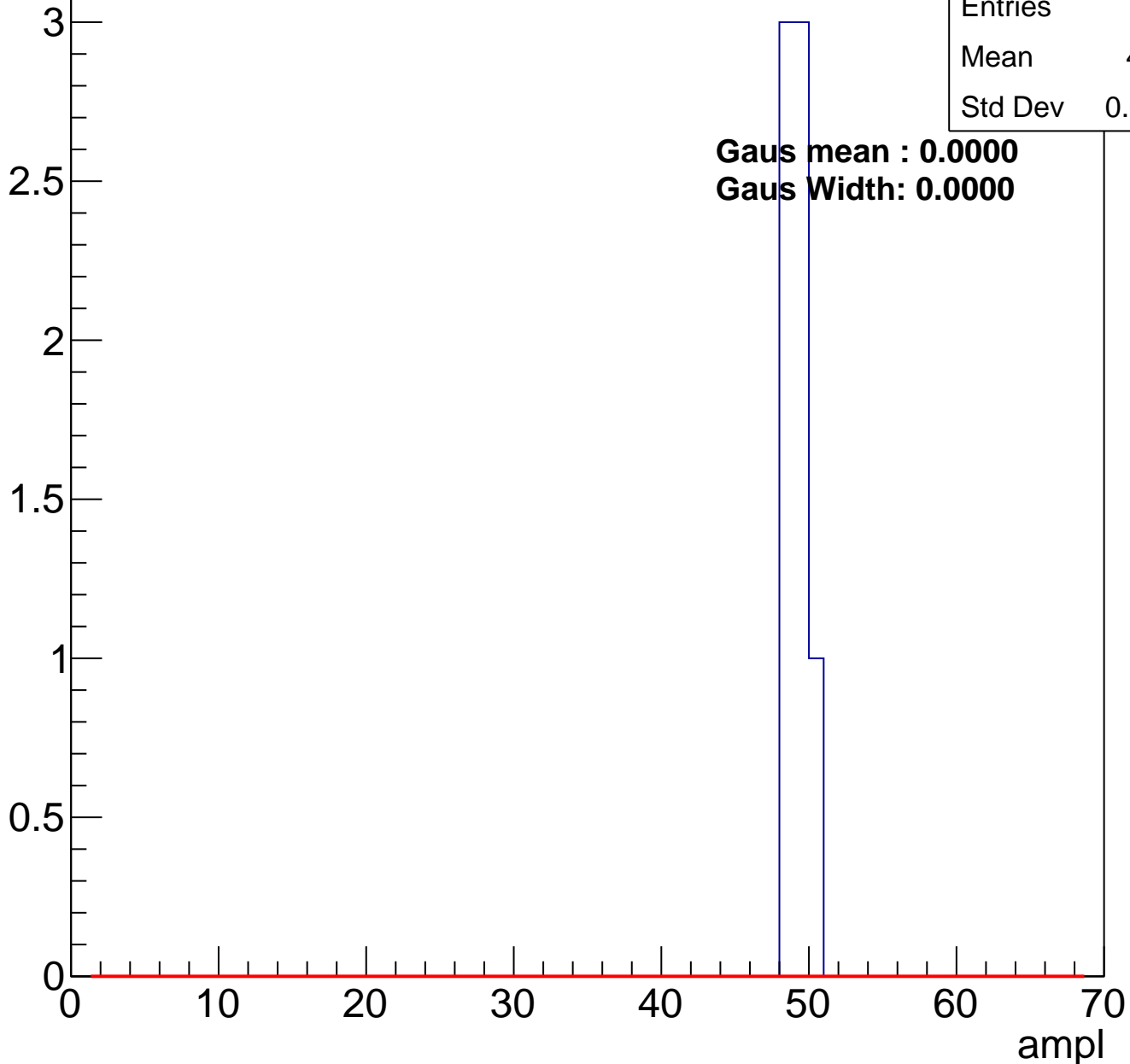
Entries	7
Mean	48.71
Std Dev	0.6999

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch119, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Entry

ampl
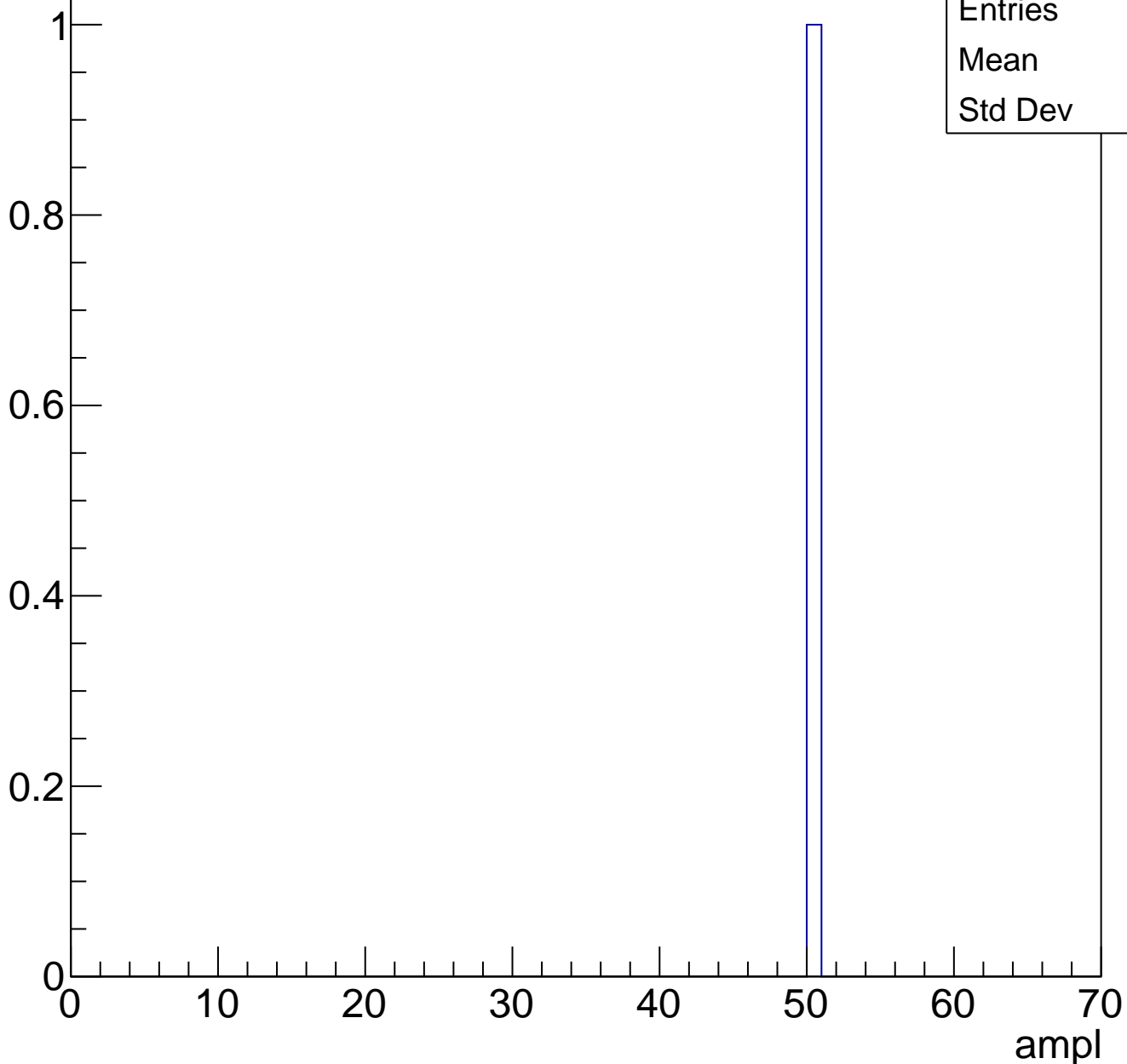
B0L100S, U13-ch119, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch119, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch119, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch119, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch120, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch120, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
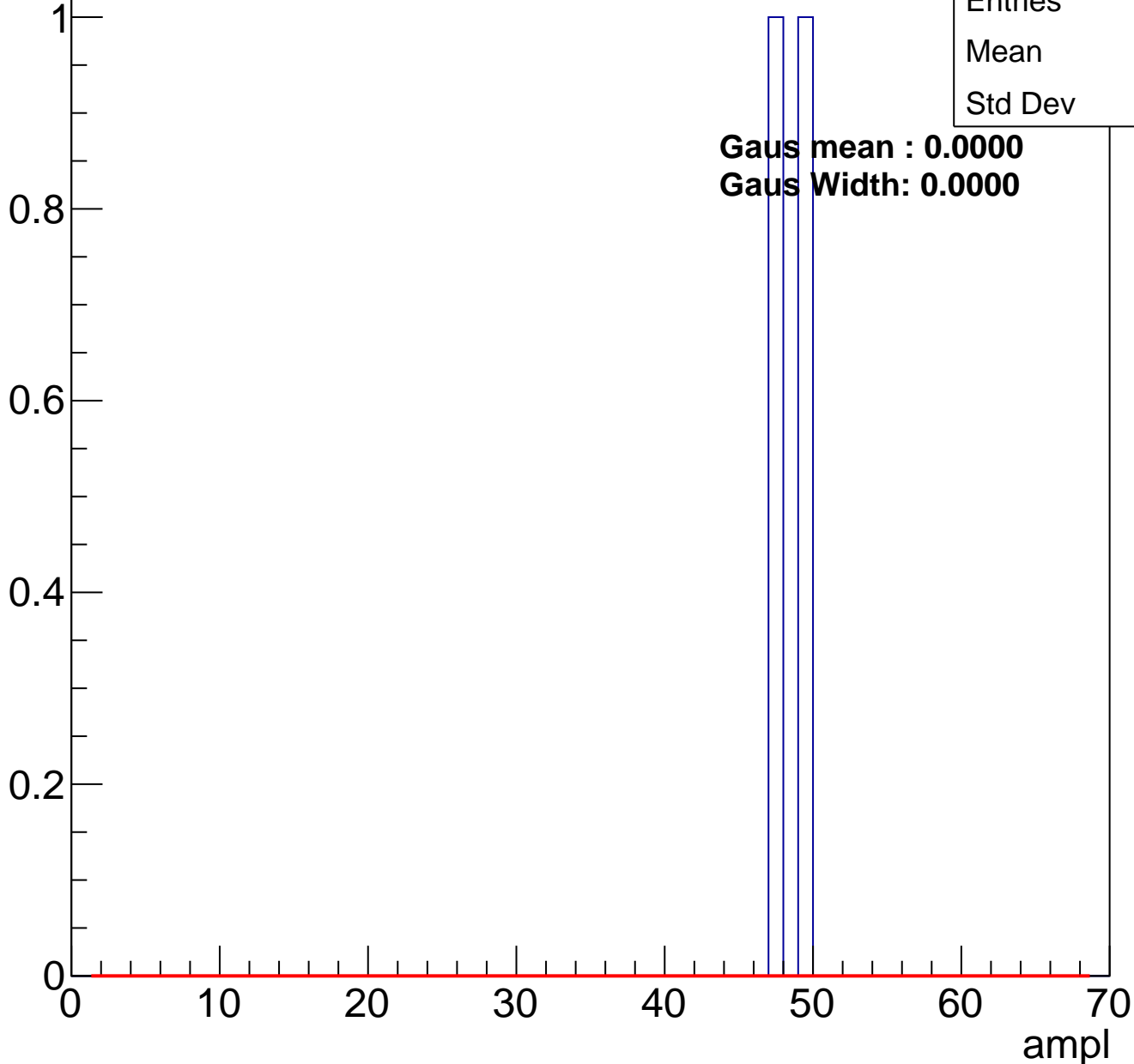
B0L100S, U13-ch120, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	46.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
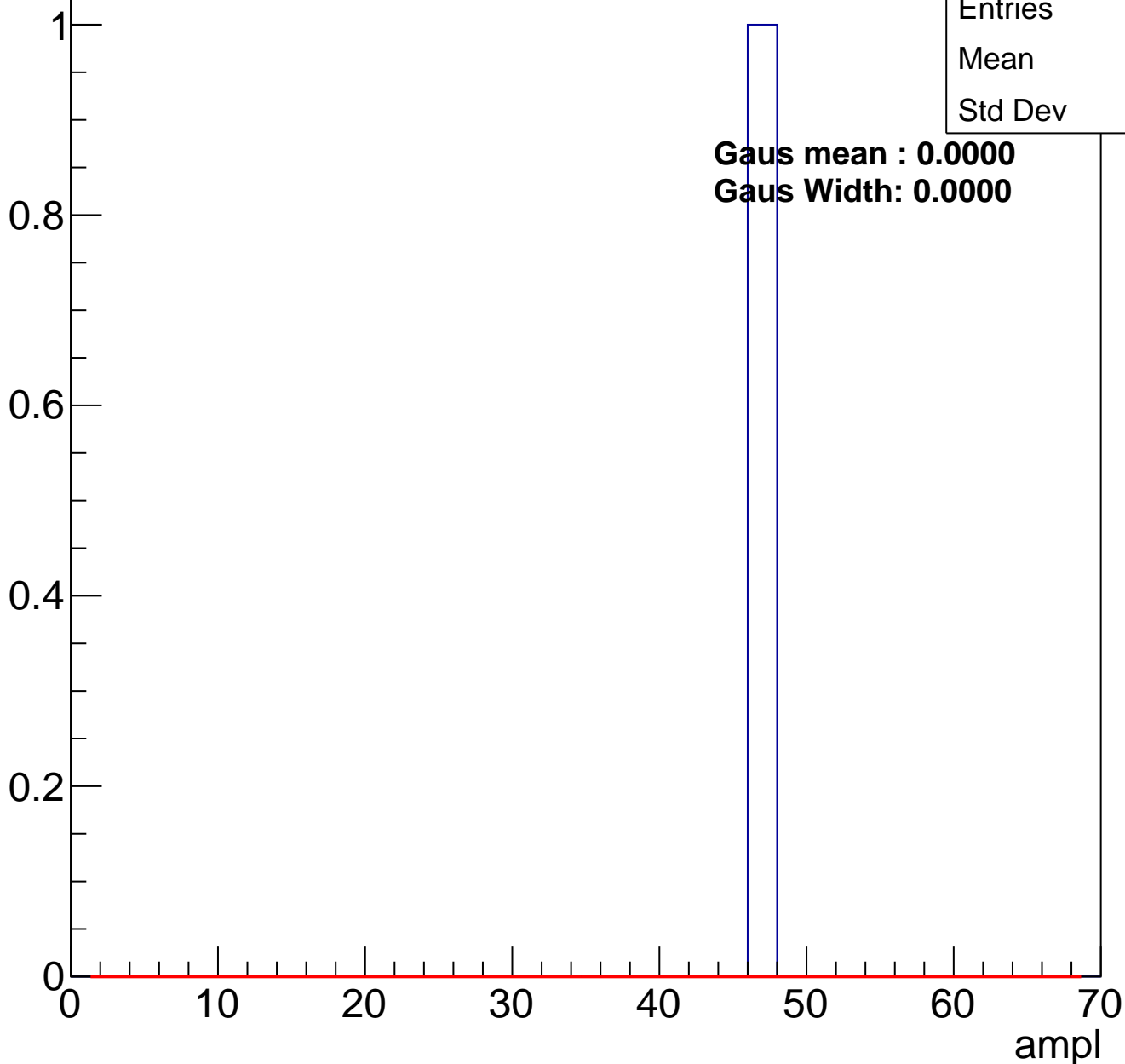
B0L100S, U13-ch120, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch120, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl
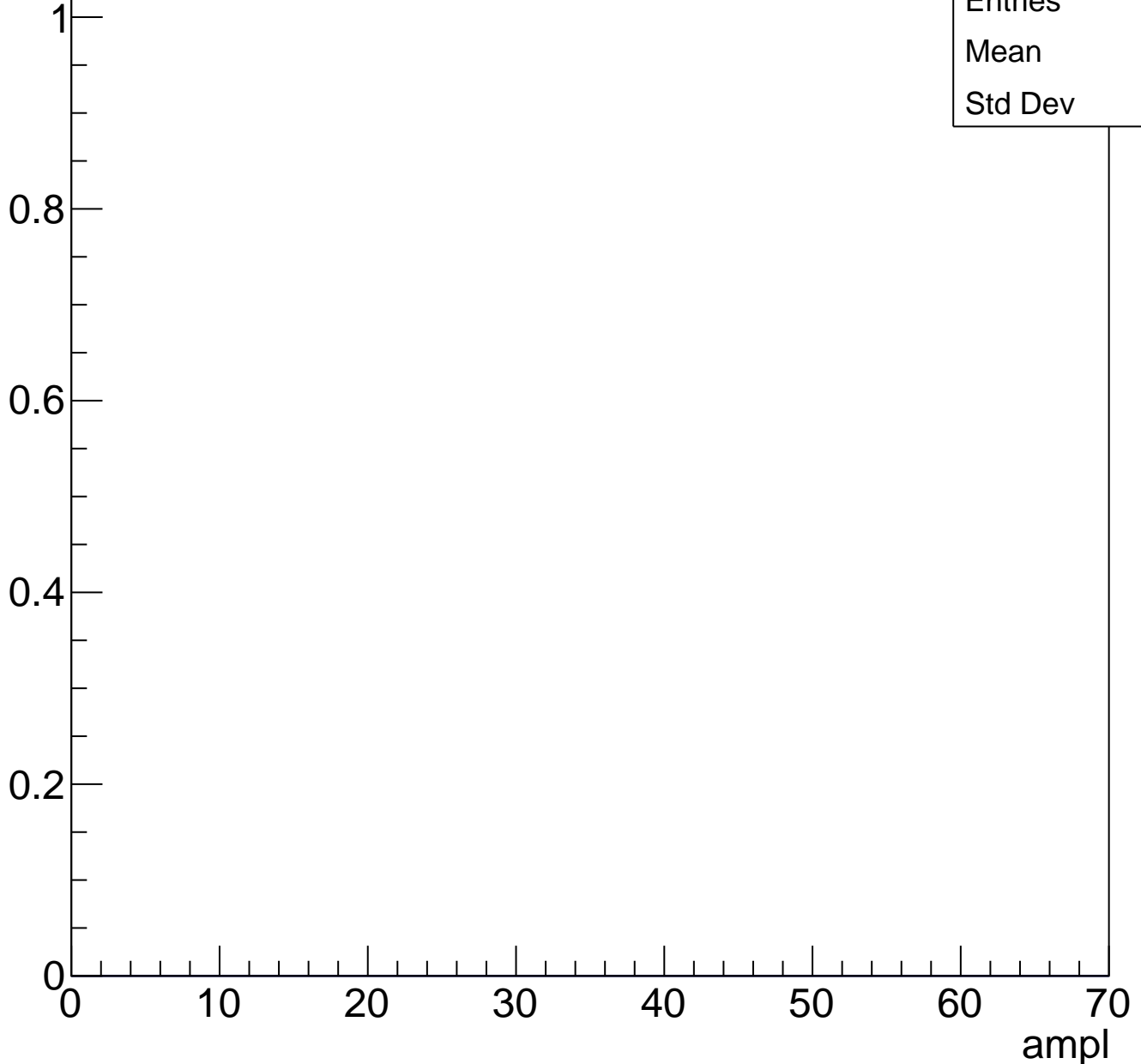
B0L100S, U13-ch120, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch120, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch120, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch121, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch121, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch121, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
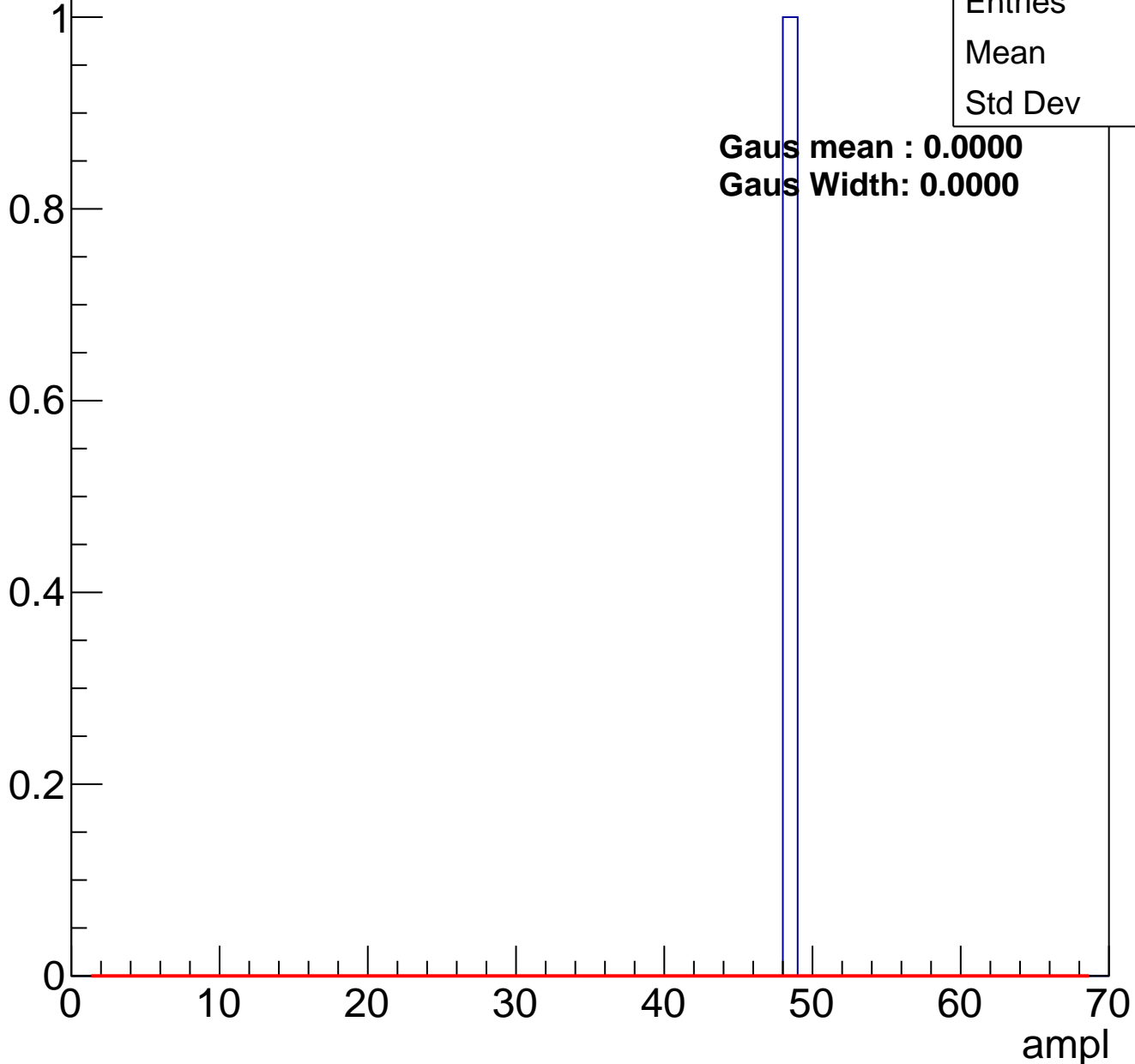
B0L100S, U13-ch121, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch121, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch121, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch121, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch121, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch122, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch122, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch122, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch122, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch122, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch122, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch122, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch122, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch123, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch123, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

B0L100S, U13-ch123, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch123, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch123, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch123, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch123, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch123, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch124, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U13-ch124, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch124, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch124, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch124, adc5

calib_packv5_042523_0143.root, FC#6, port A1
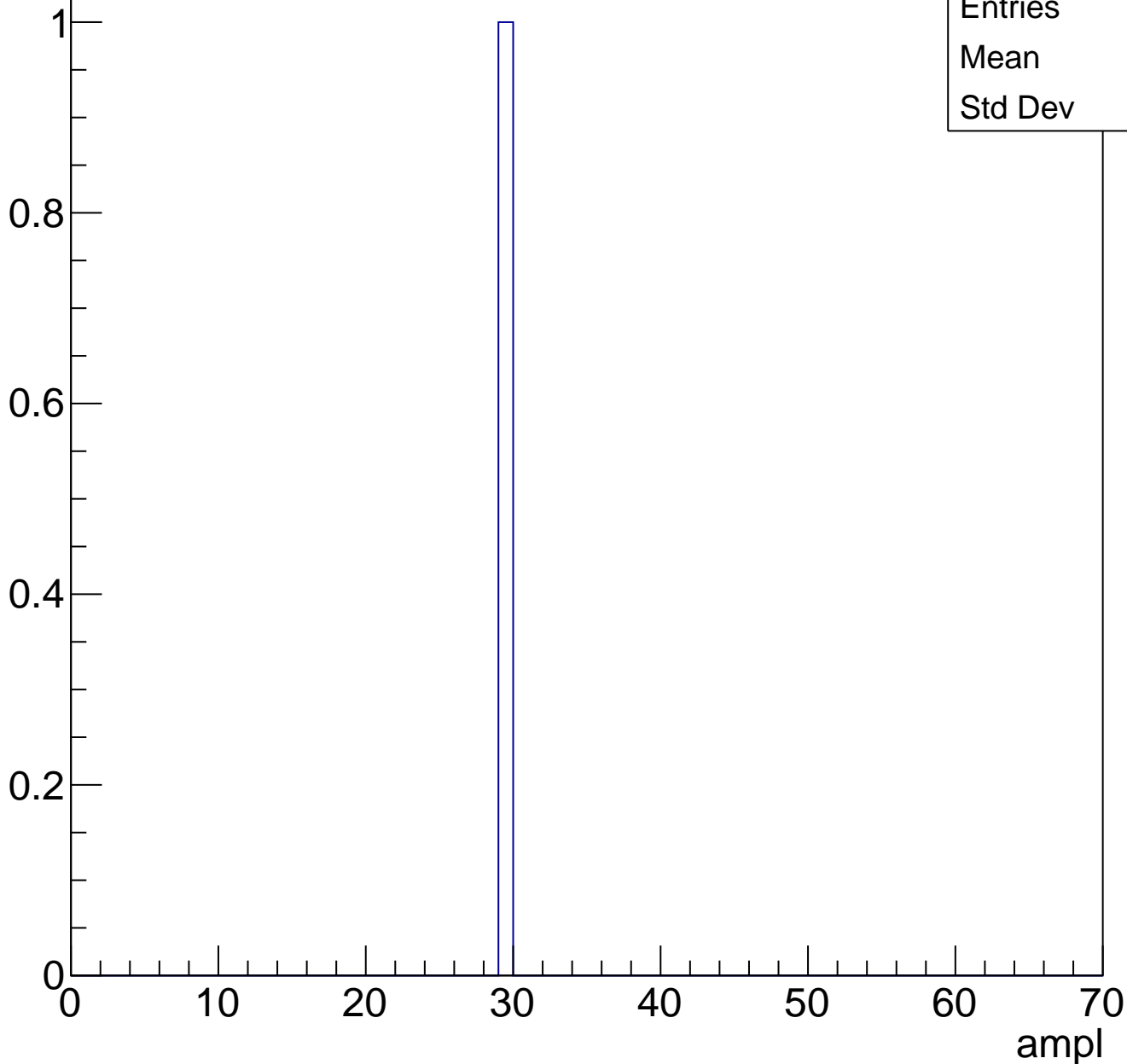
Entries	1
Mean	29
Std Dev	0

Entry

ampl

B0L100S, U13-ch124, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch124, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch125, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch125, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U13-ch125, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch125, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch125, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch125, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch125, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch125, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch126, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch126, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch126, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000
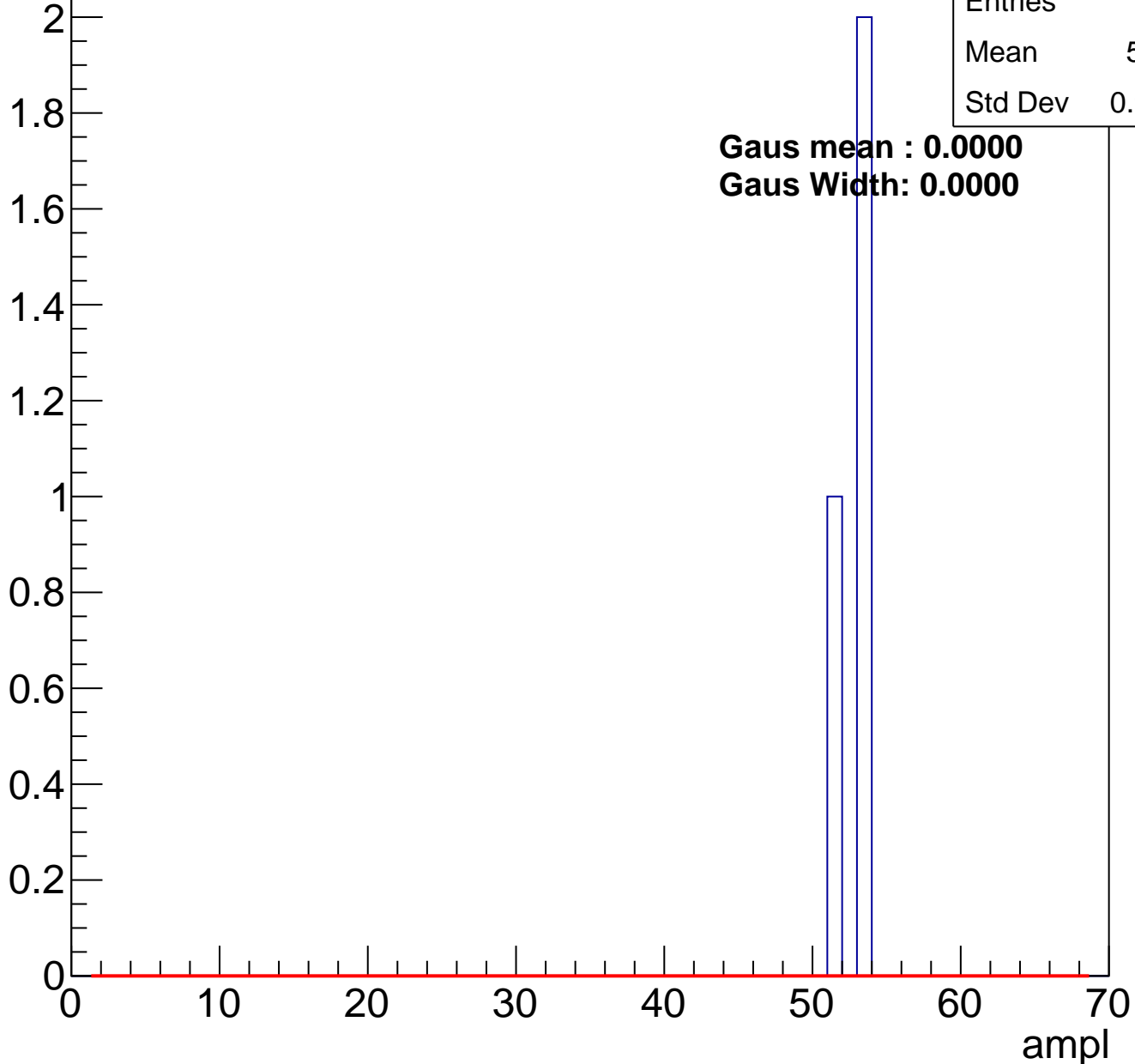
Entries	3
Mean	52.33
Std Dev	0.9428

ampl

B0L100S, U13-ch126, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	53
Std Dev	0

Entry

ampl
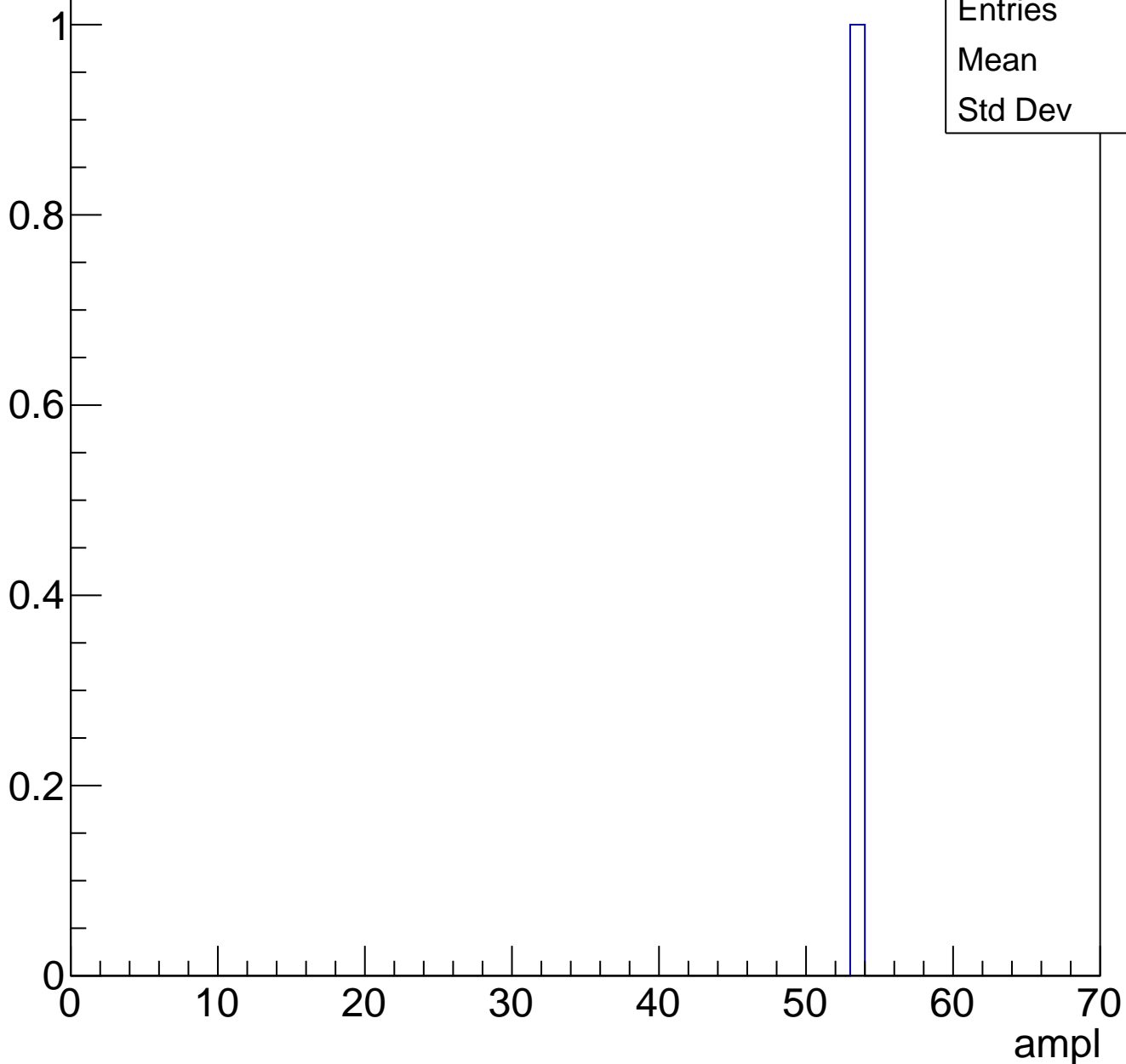
B0L100S, U13-ch126, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch126, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch126, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch126, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch127, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	1
Mean	60
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
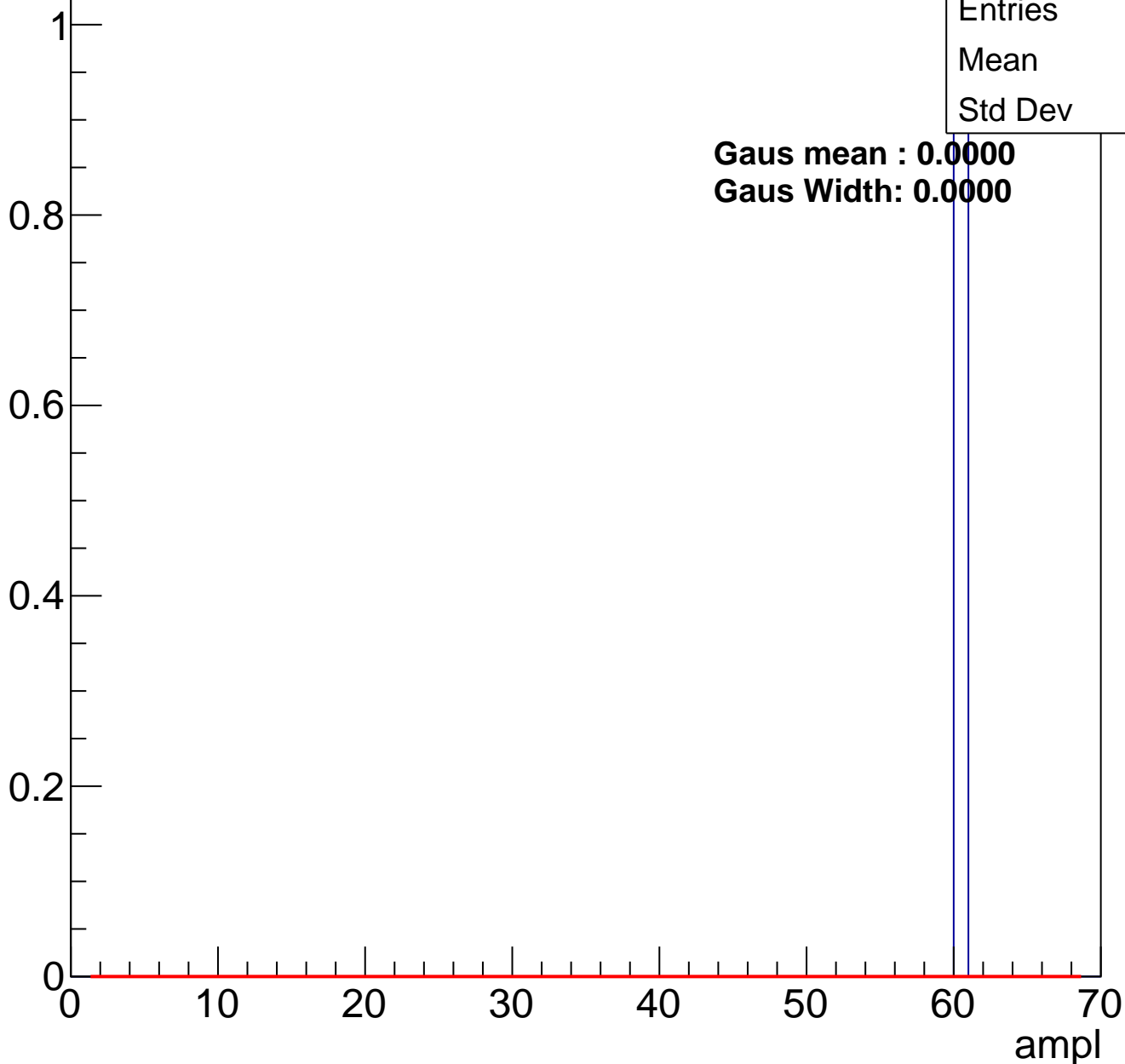
B0L100S, U13-ch127, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch127, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U13-ch127, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch127, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch127, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch127, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl