

B1L103S, U24-ch0, adc0

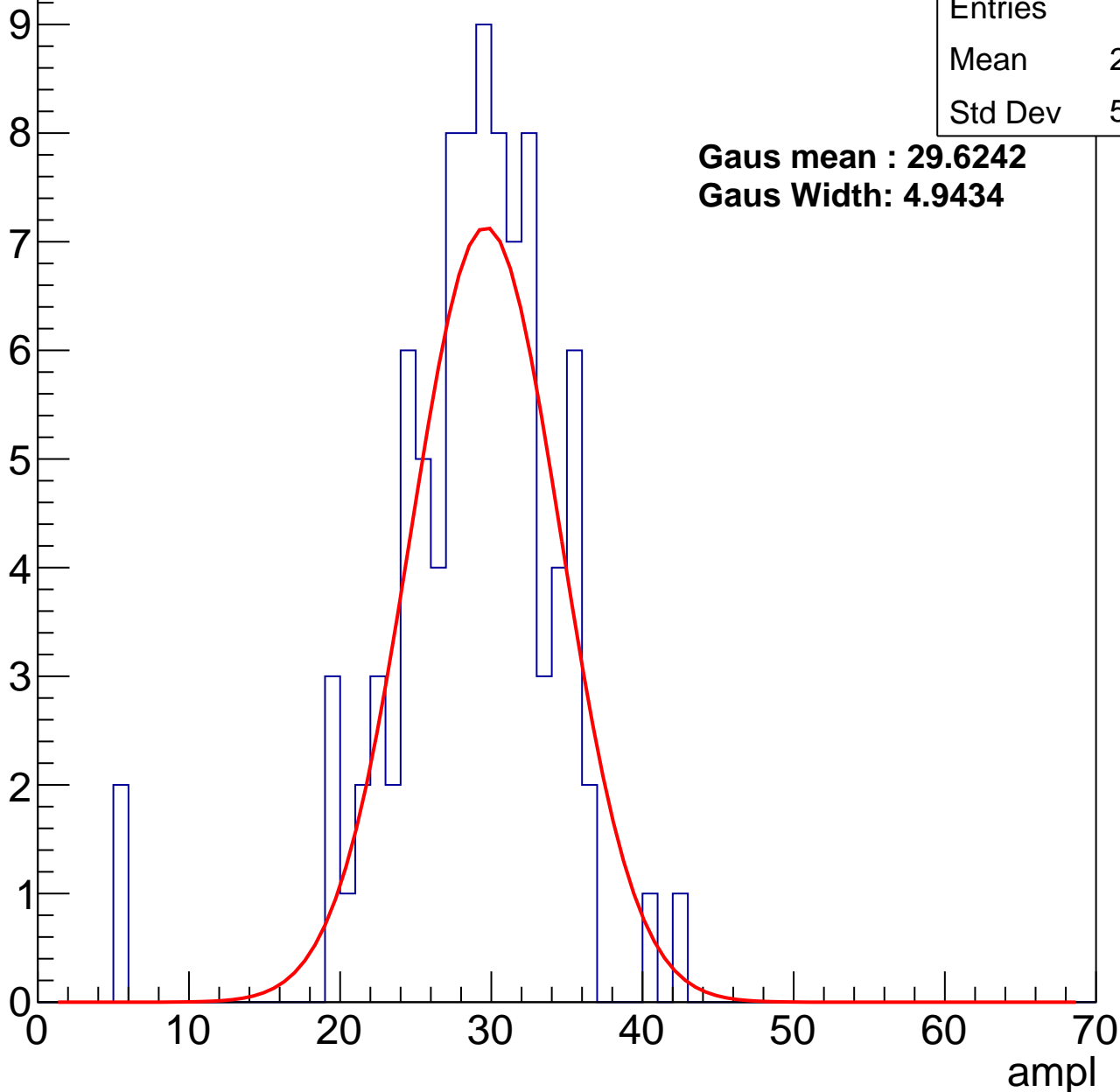
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	93
Mean	28.25
Std Dev	5.673

Gaus mean : 29.6242

Gaus Width: 4.9434



B1L103S, U24-ch0, adc1

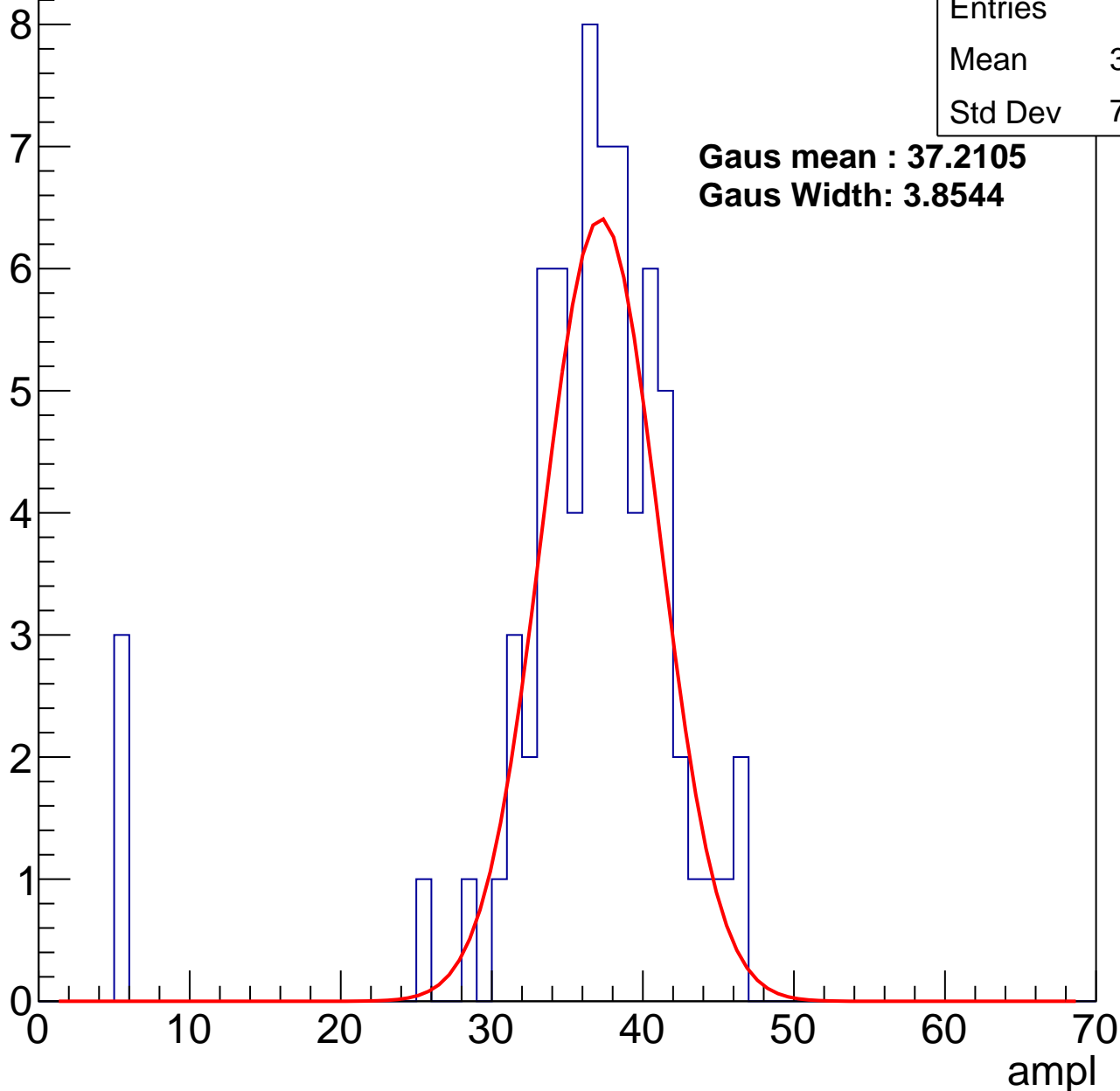
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.48
Std Dev	7.536

Gaus mean : 37.2105

Gaus Width: 3.8544



B1L103S, U24-ch0, adc2

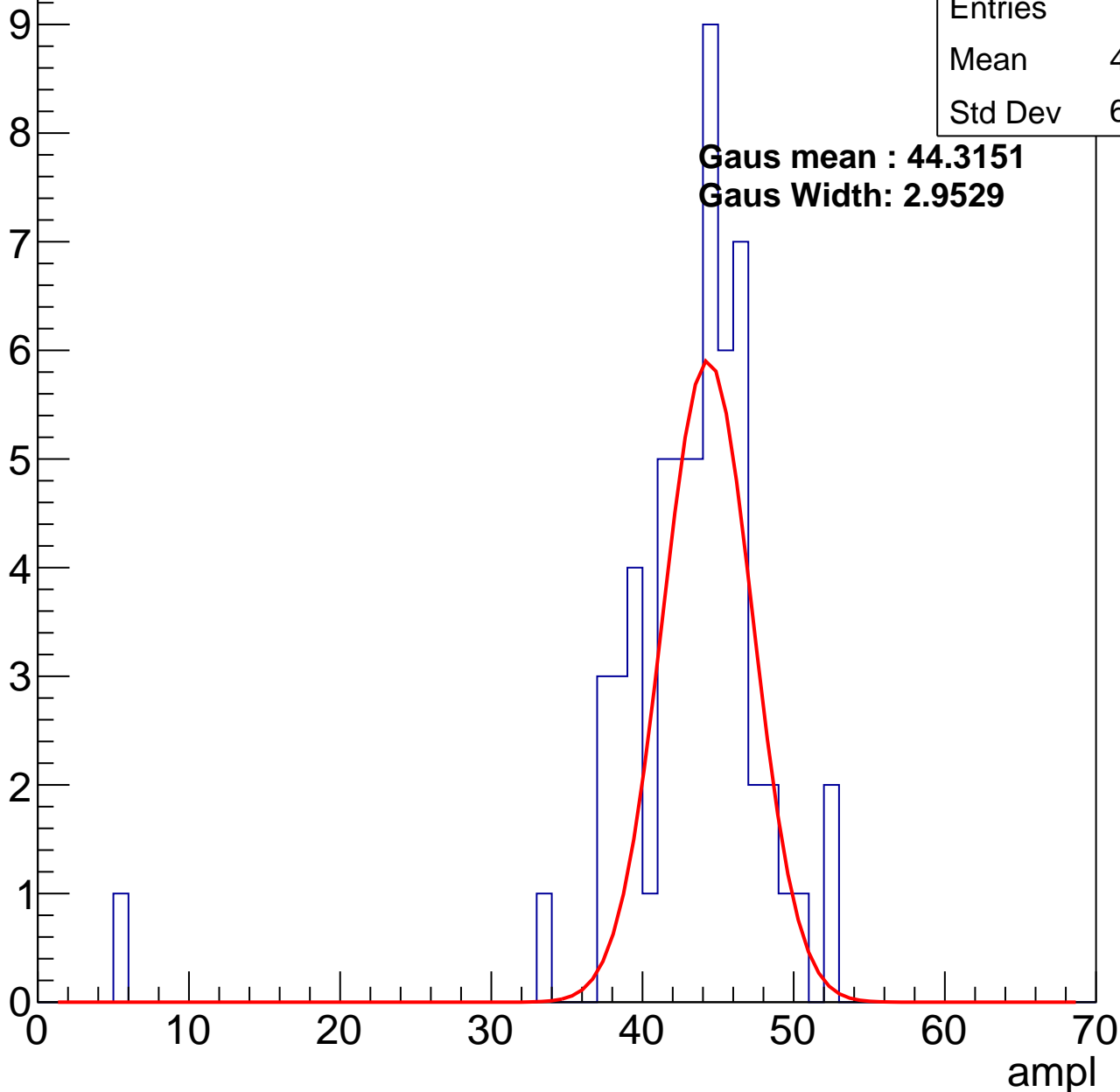
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.59
Std Dev	6.215

Gaus mean : 44.3151

Gaus Width: 2.9529

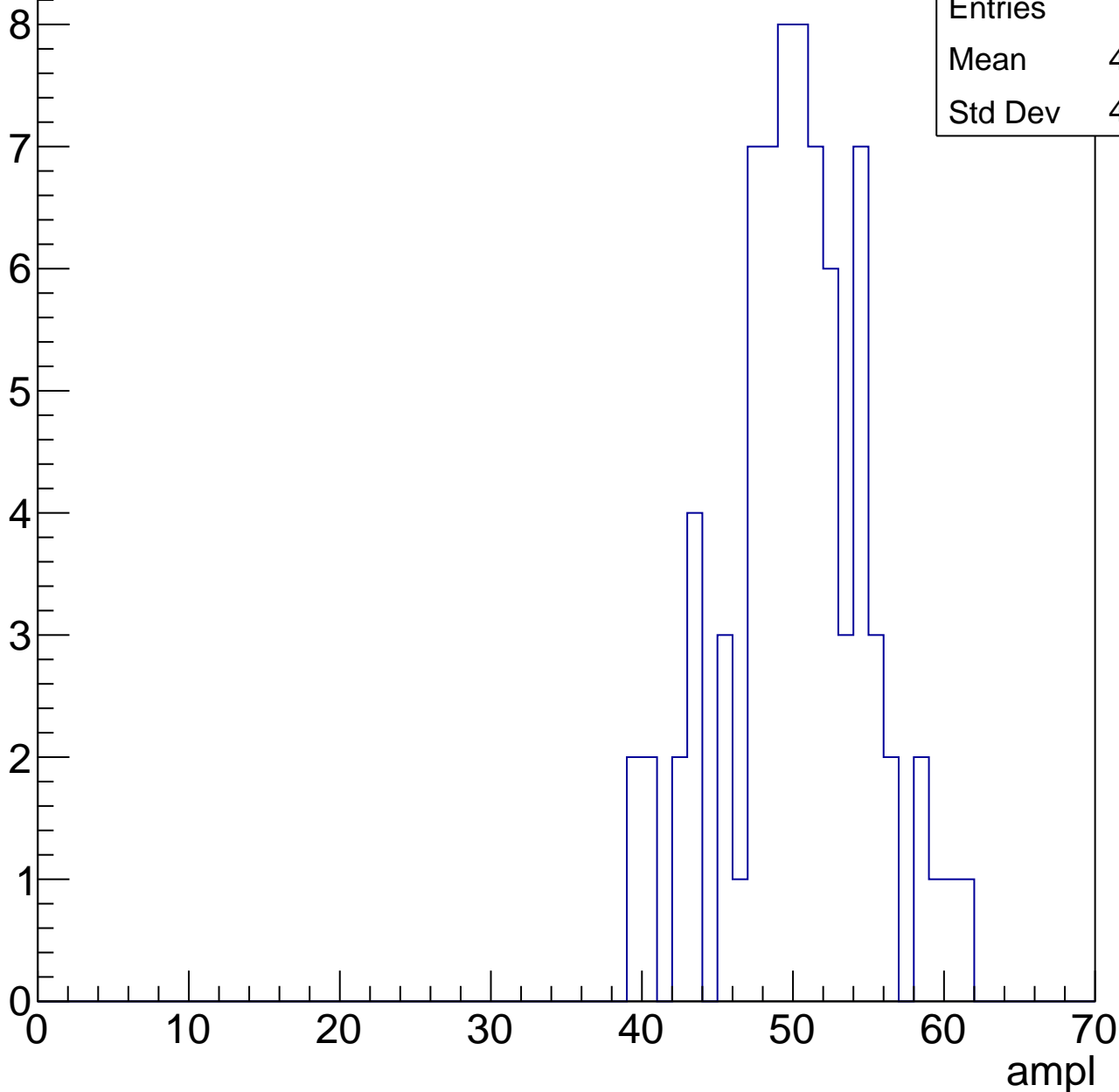


B1L103S, U24-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

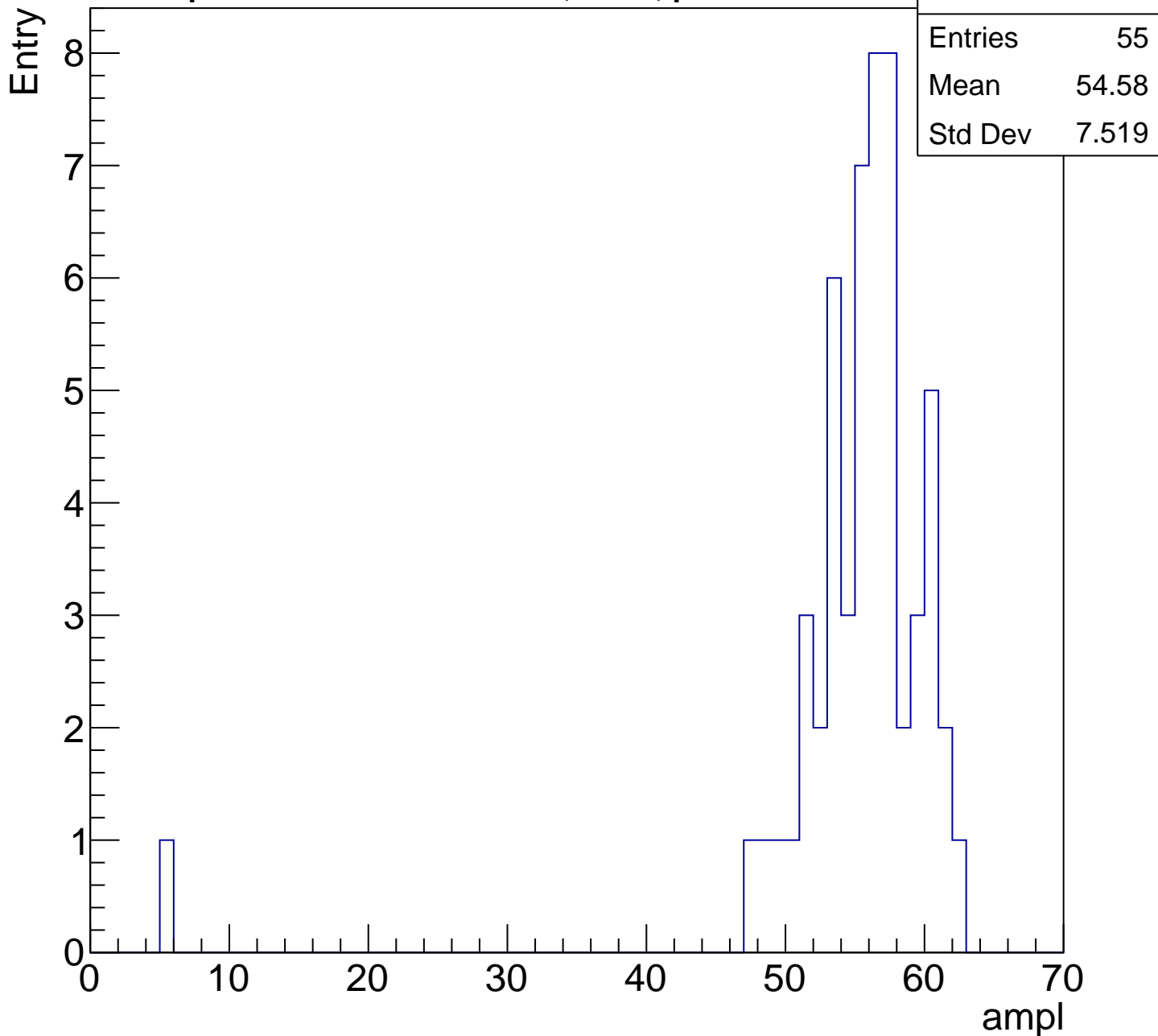
Entry

Entries	77
Mean	49.75
Std Dev	4.696



B1L103S, U24-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

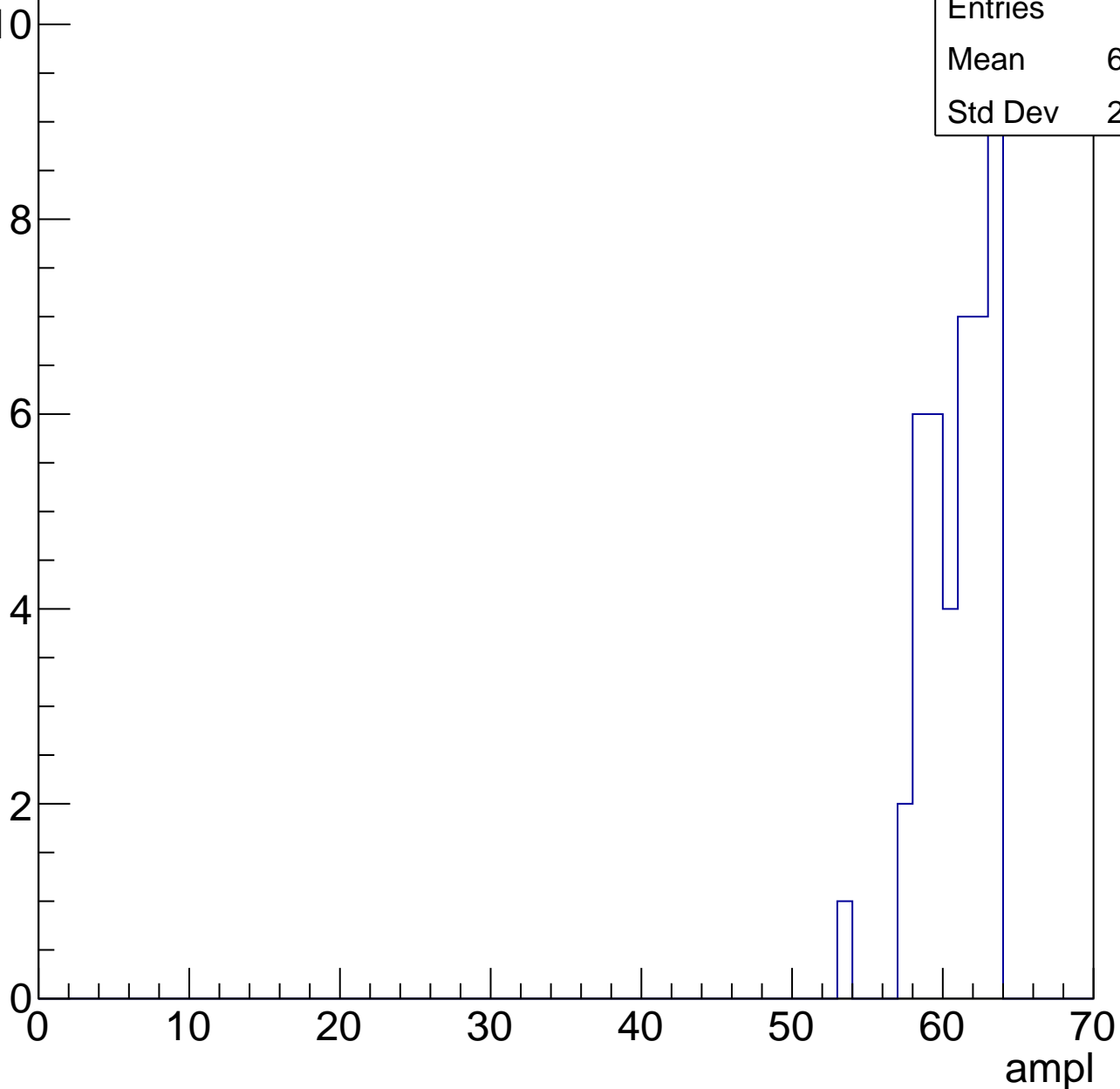


B1L103S, U24-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

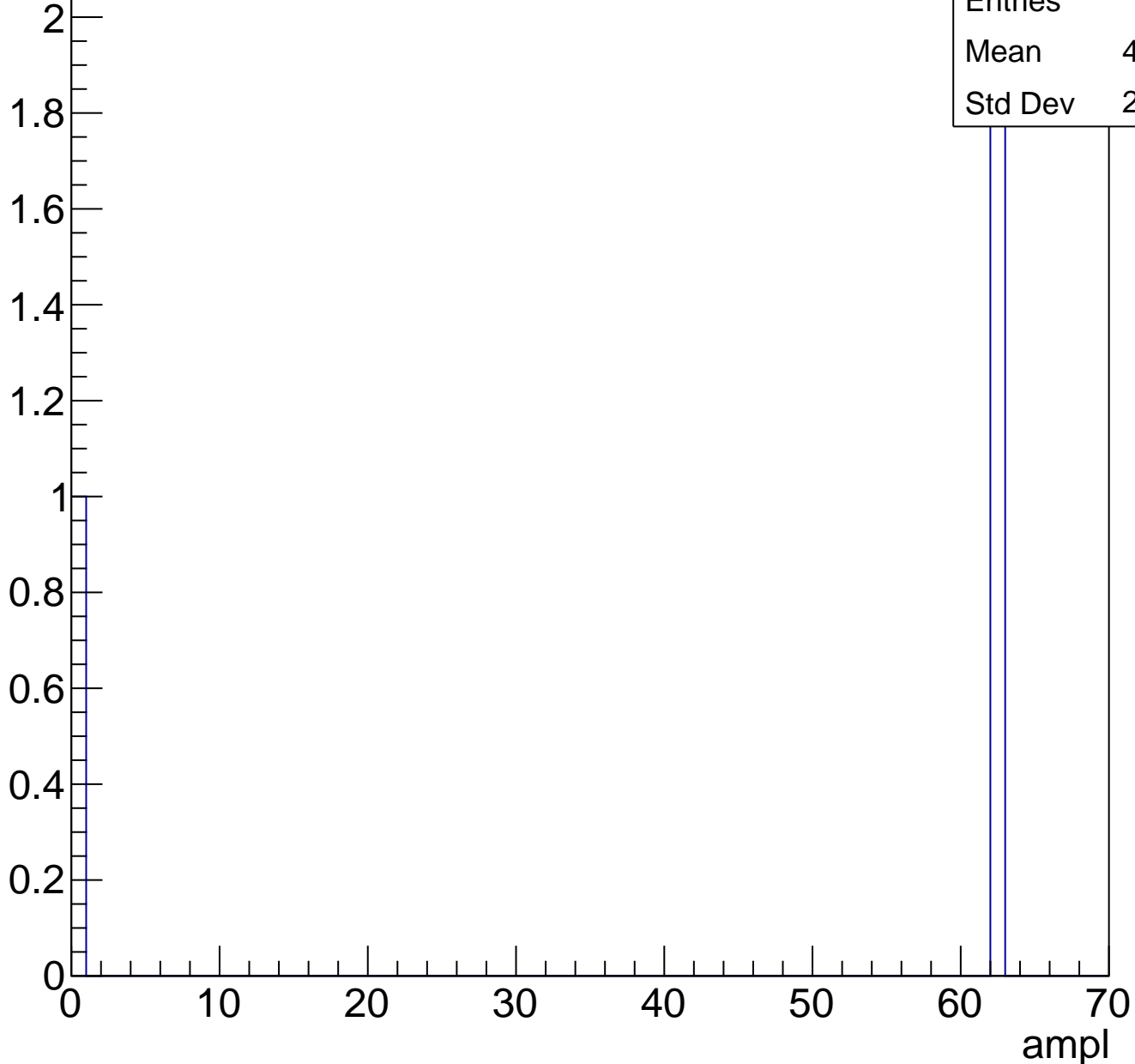
Entries	43
Mean	60.47
Std Dev	2.224



B1L103S, U24-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch1, adc0

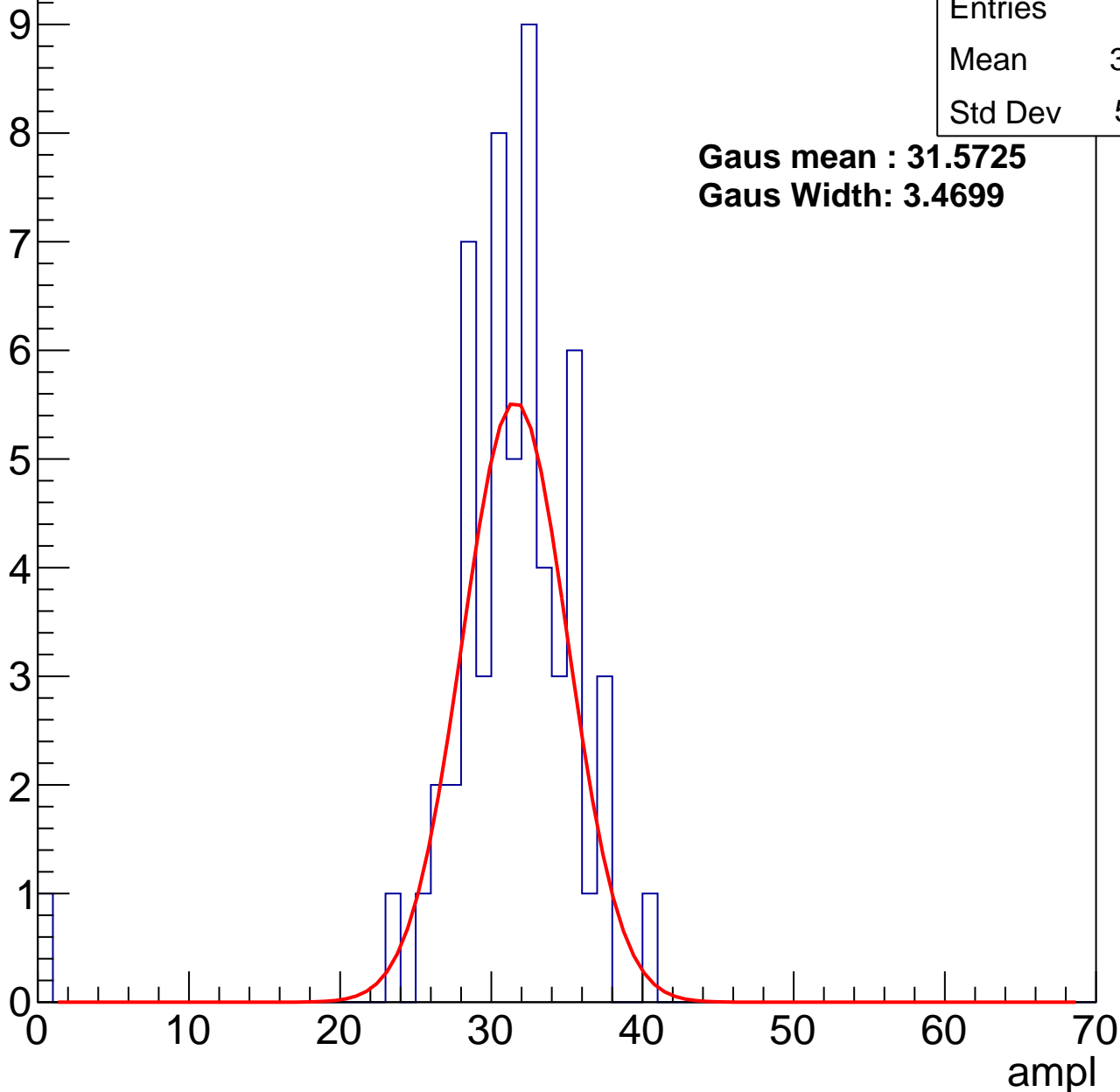
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	30.72
Std Dev	5.271

Gaus mean : 31.5725

Gaus Width: 3.4699



B1L103S, U24-ch1, adc1

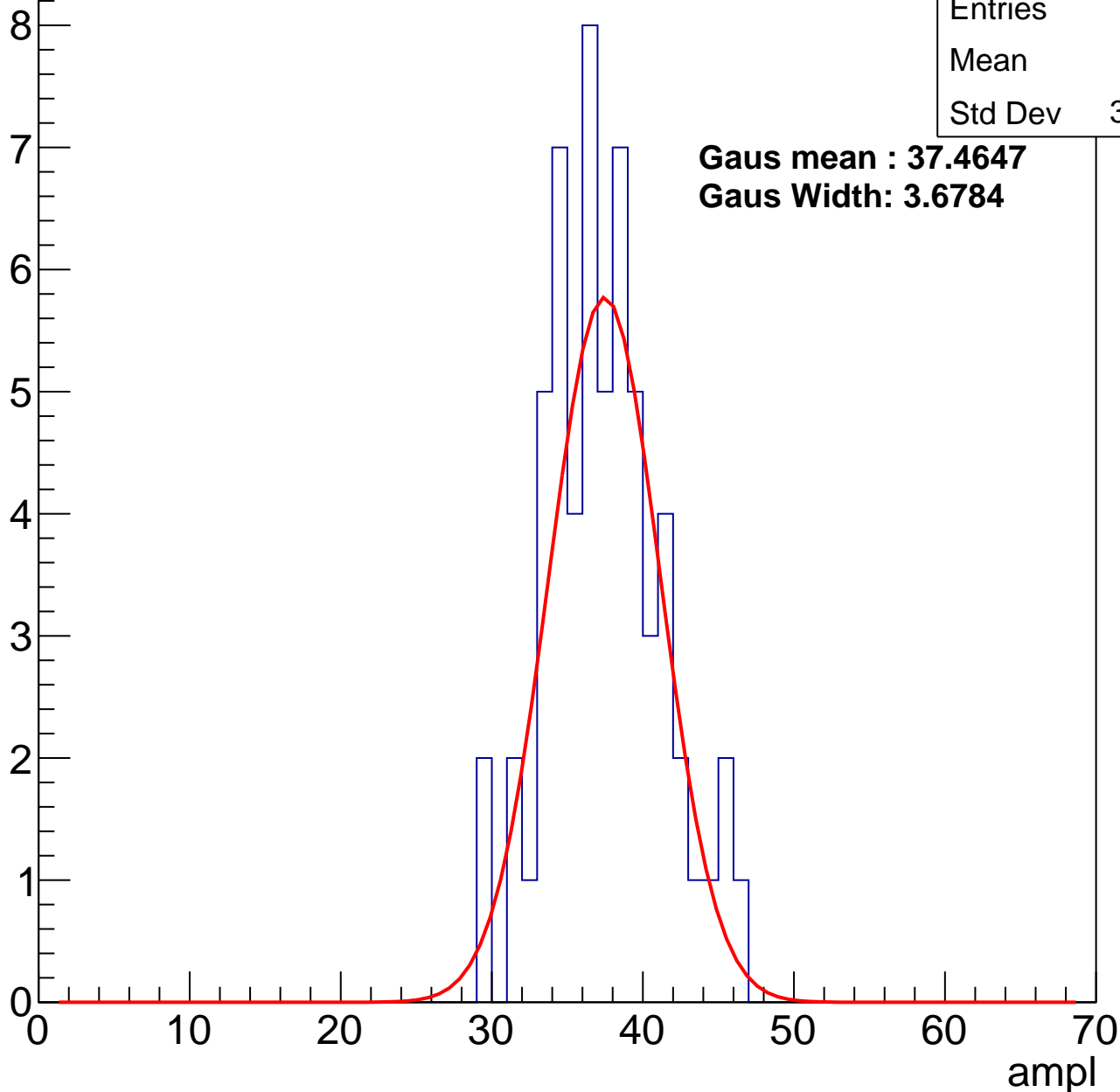
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37
Std Dev	3.773

Gaus mean : 37.4647

Gaus Width: 3.6784



B1L103S, U24-ch1, adc2

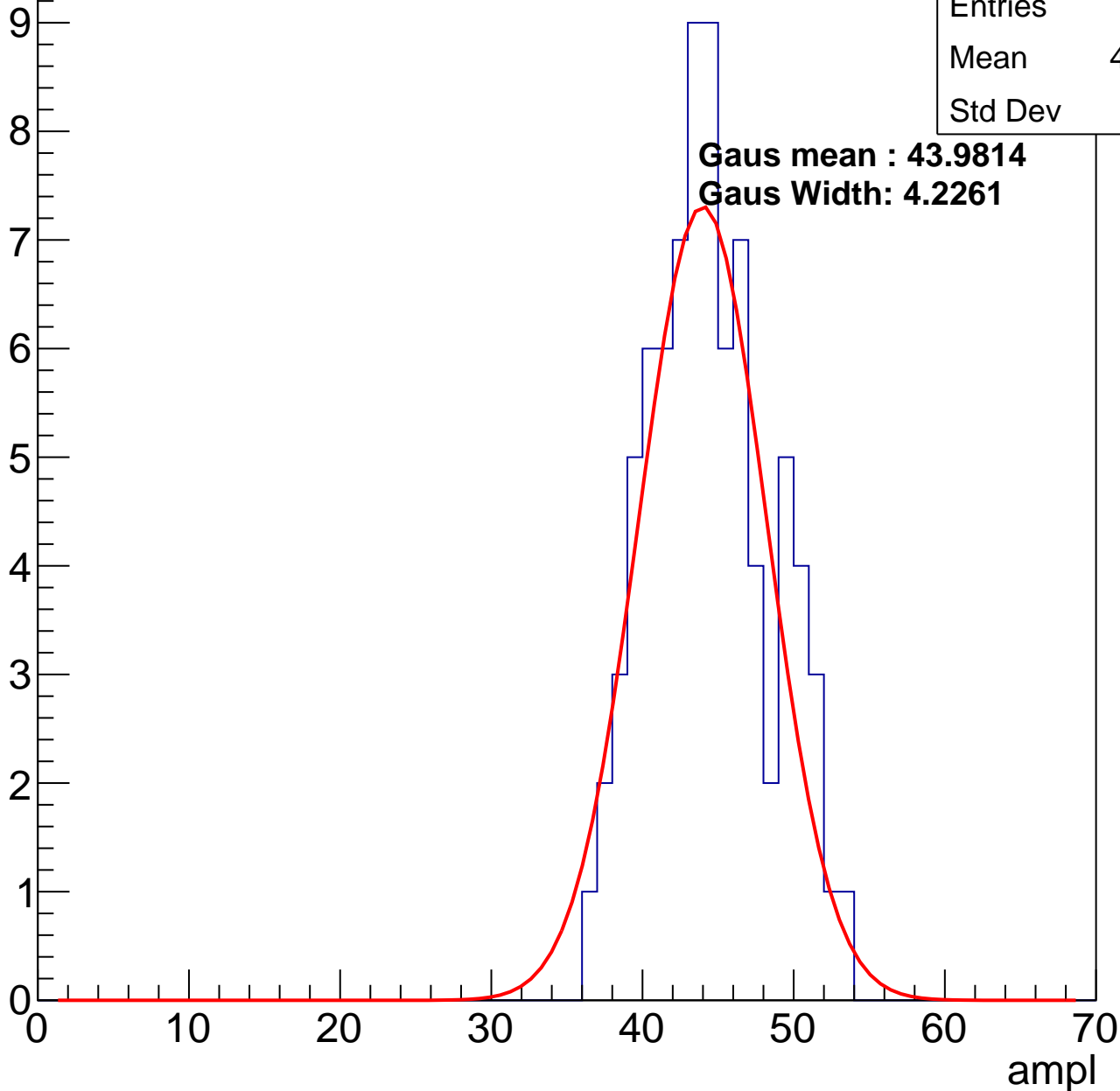
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	43.96
Std Dev	3.92

Gaus mean : 43.9814

Gaus Width: 4.2261

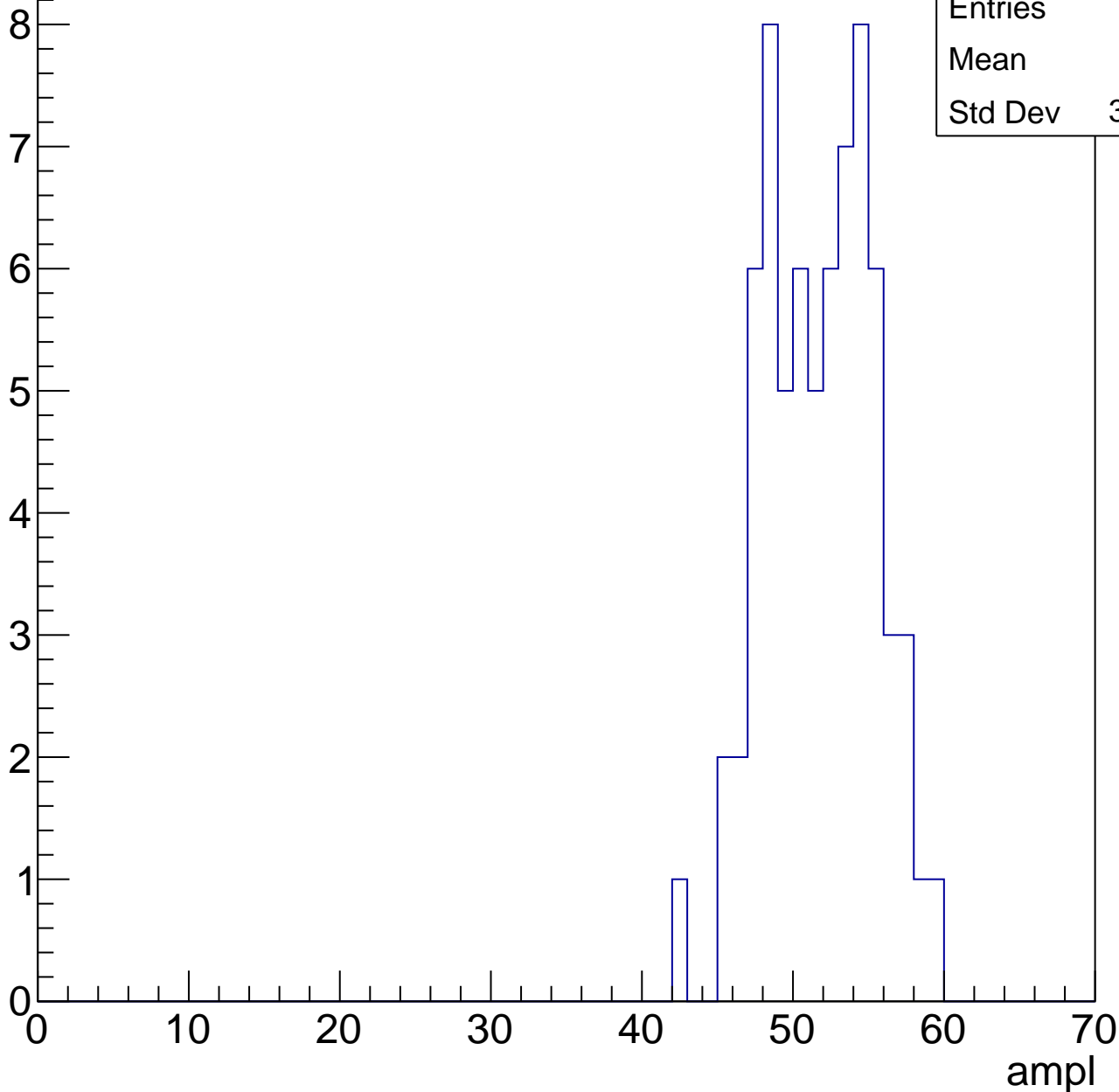


B1L103S, U24-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	51.3
Std Dev	3.563

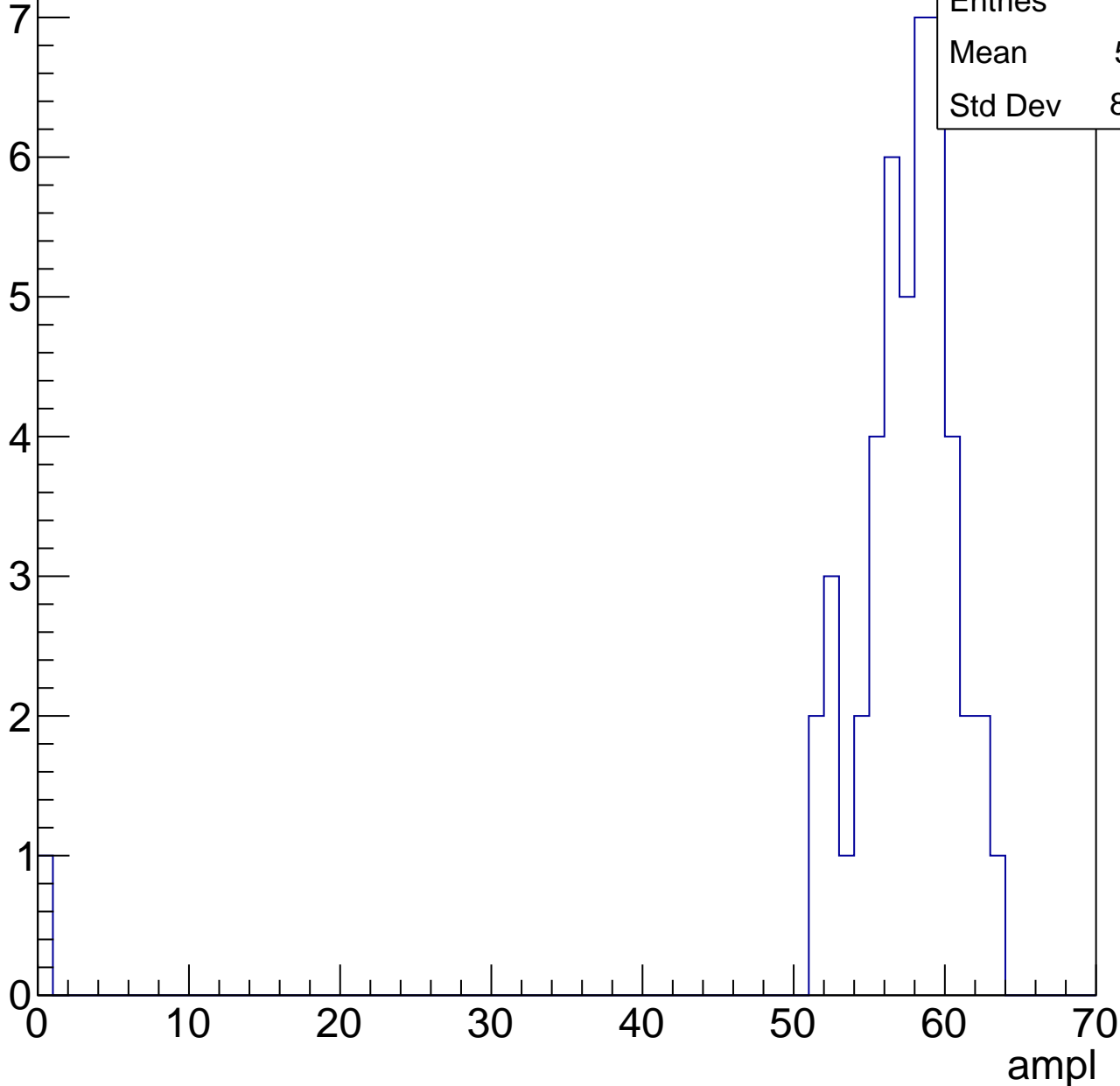


B1L103S, U24-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	55.91
Std Dev	8.734

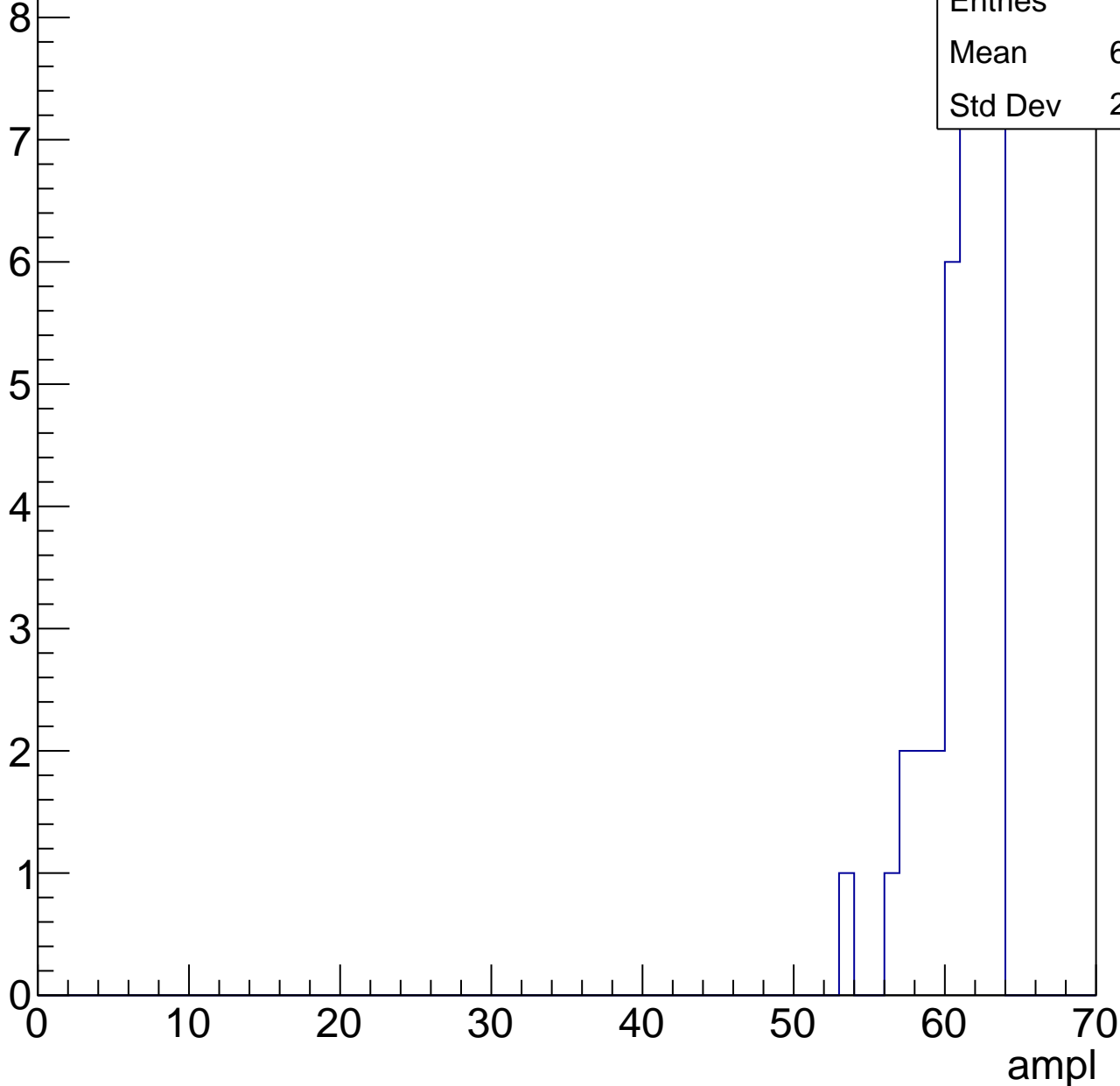


B1L103S, U24-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

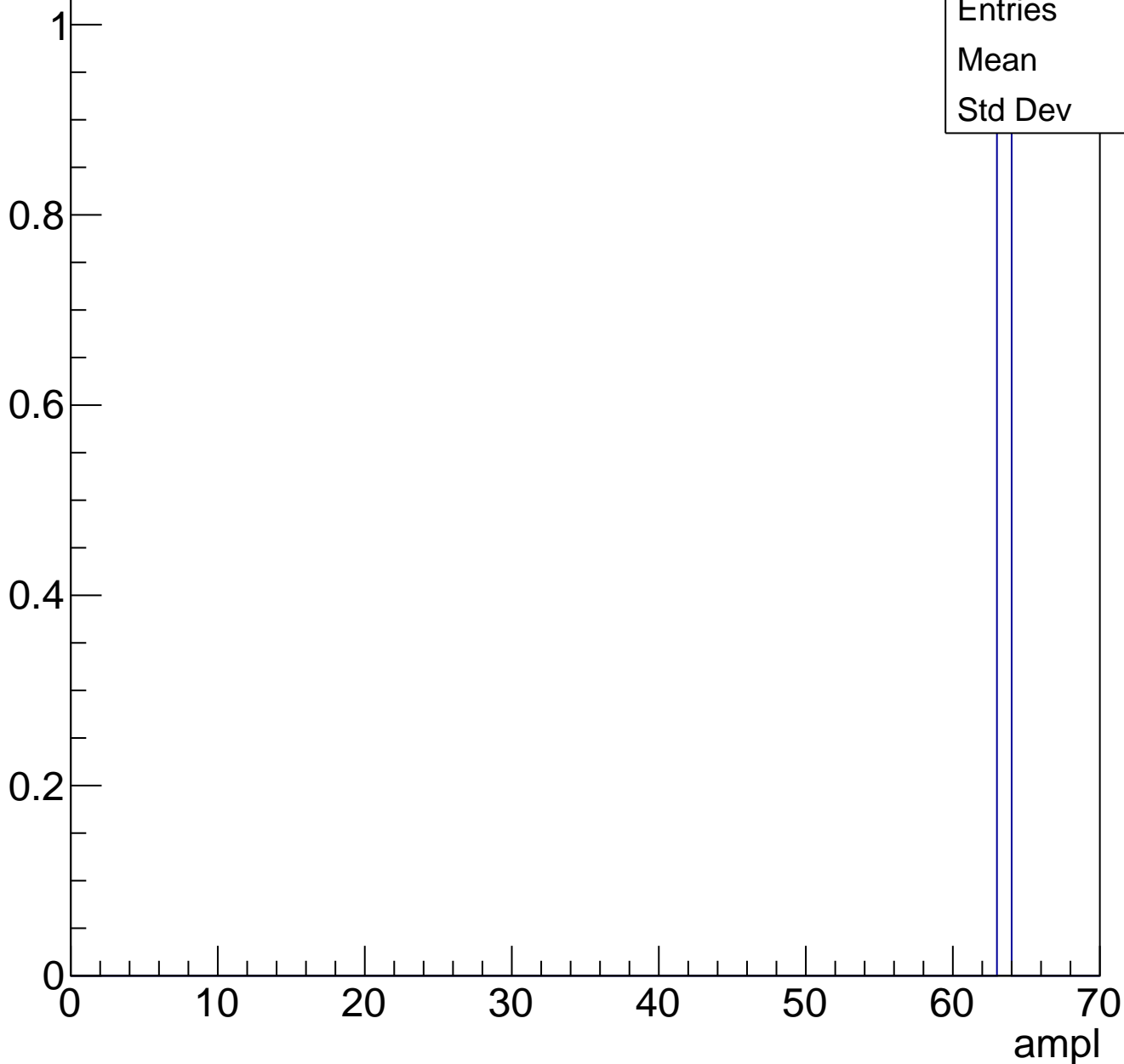
Entries	38
Mean	60.66
Std Dev	2.228



B1L103S, U24-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch2, adc0

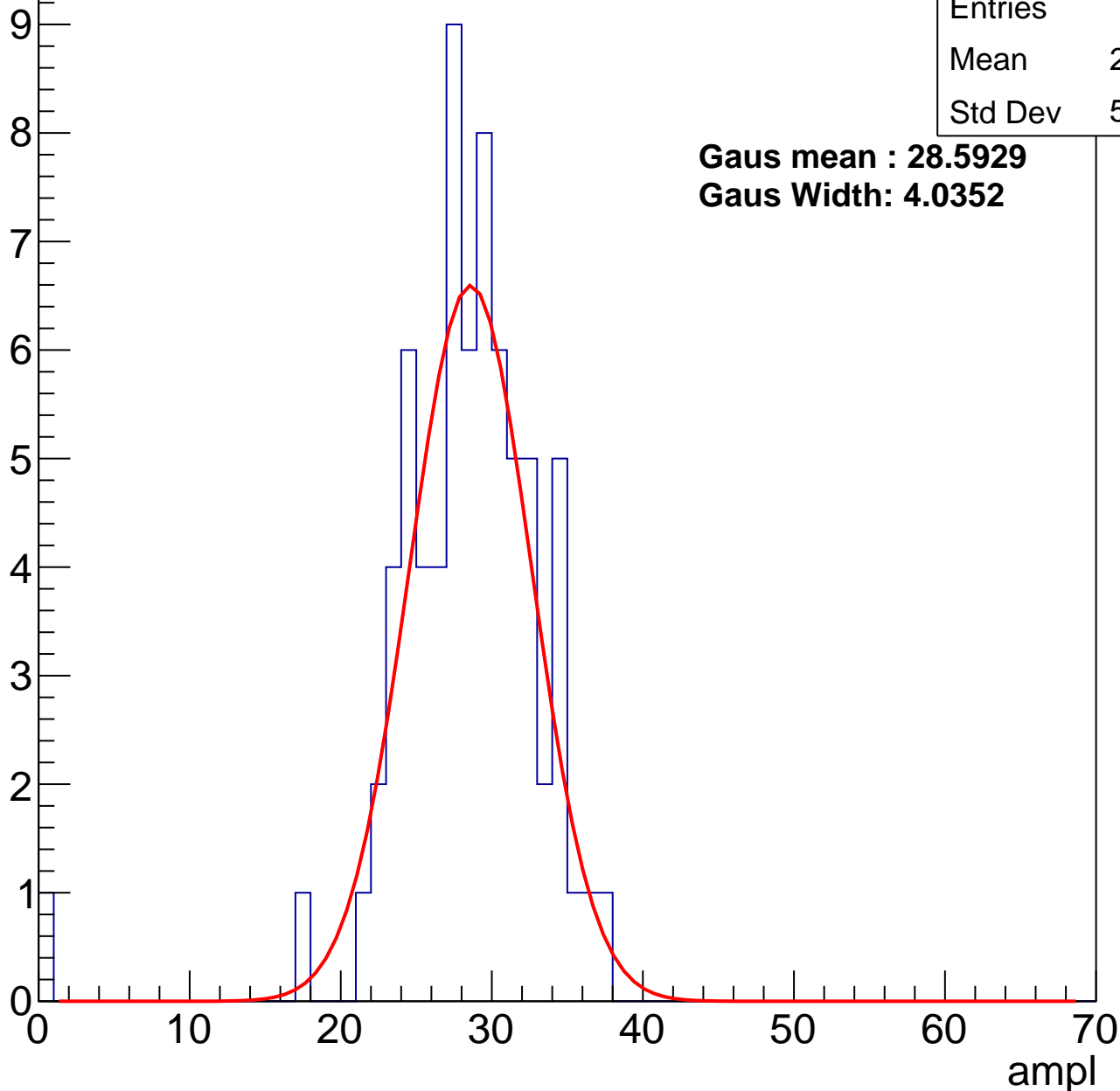
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.83
Std Dev	5.085

Gaus mean : 28.5929

Gaus Width: 4.0352



B1L103S, U24-ch2, adc1

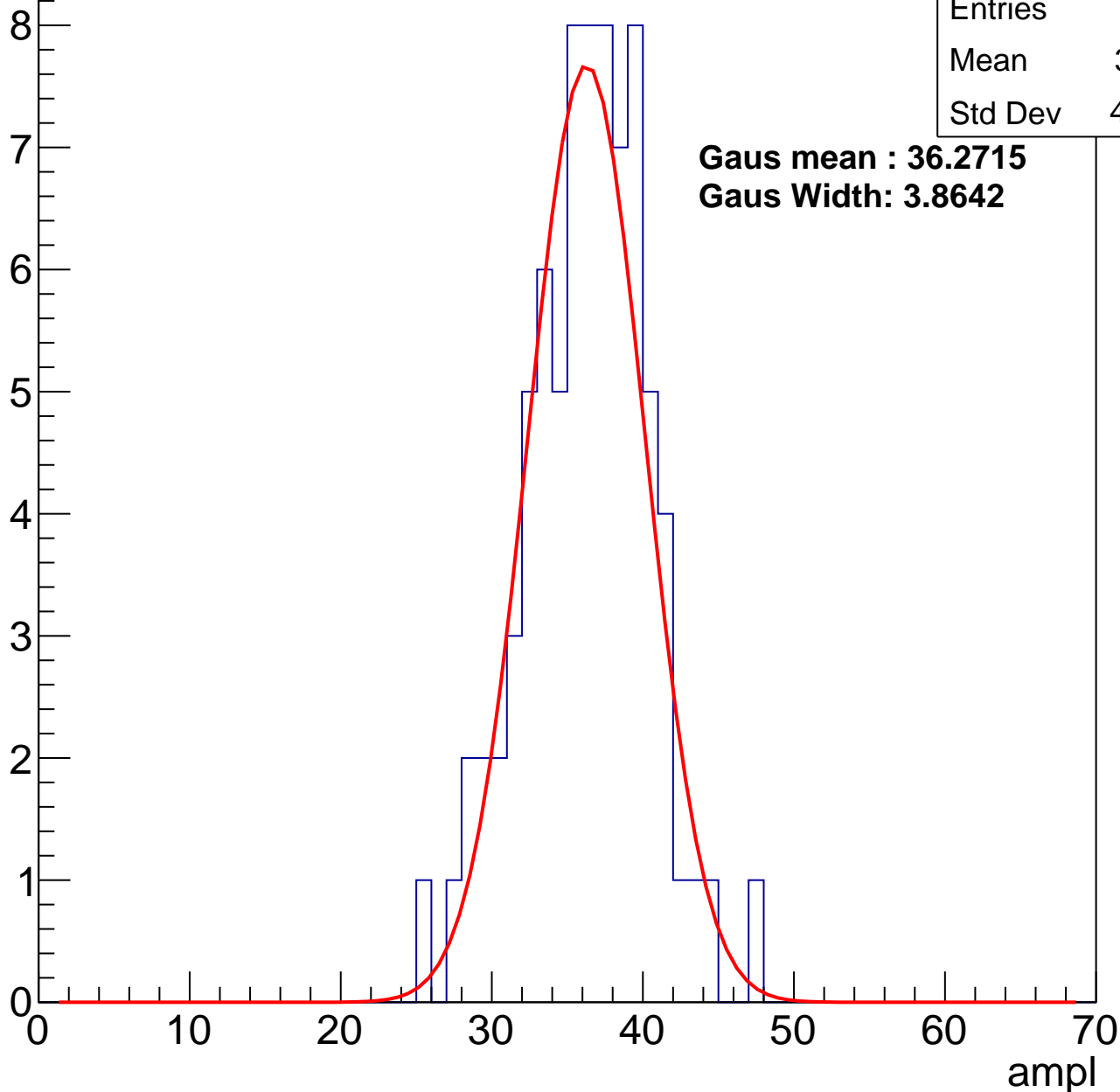
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.81
Std Dev	4.044

Gaus mean : 36.2715

Gaus Width: 3.8642



B1L103S, U24-ch2, adc2

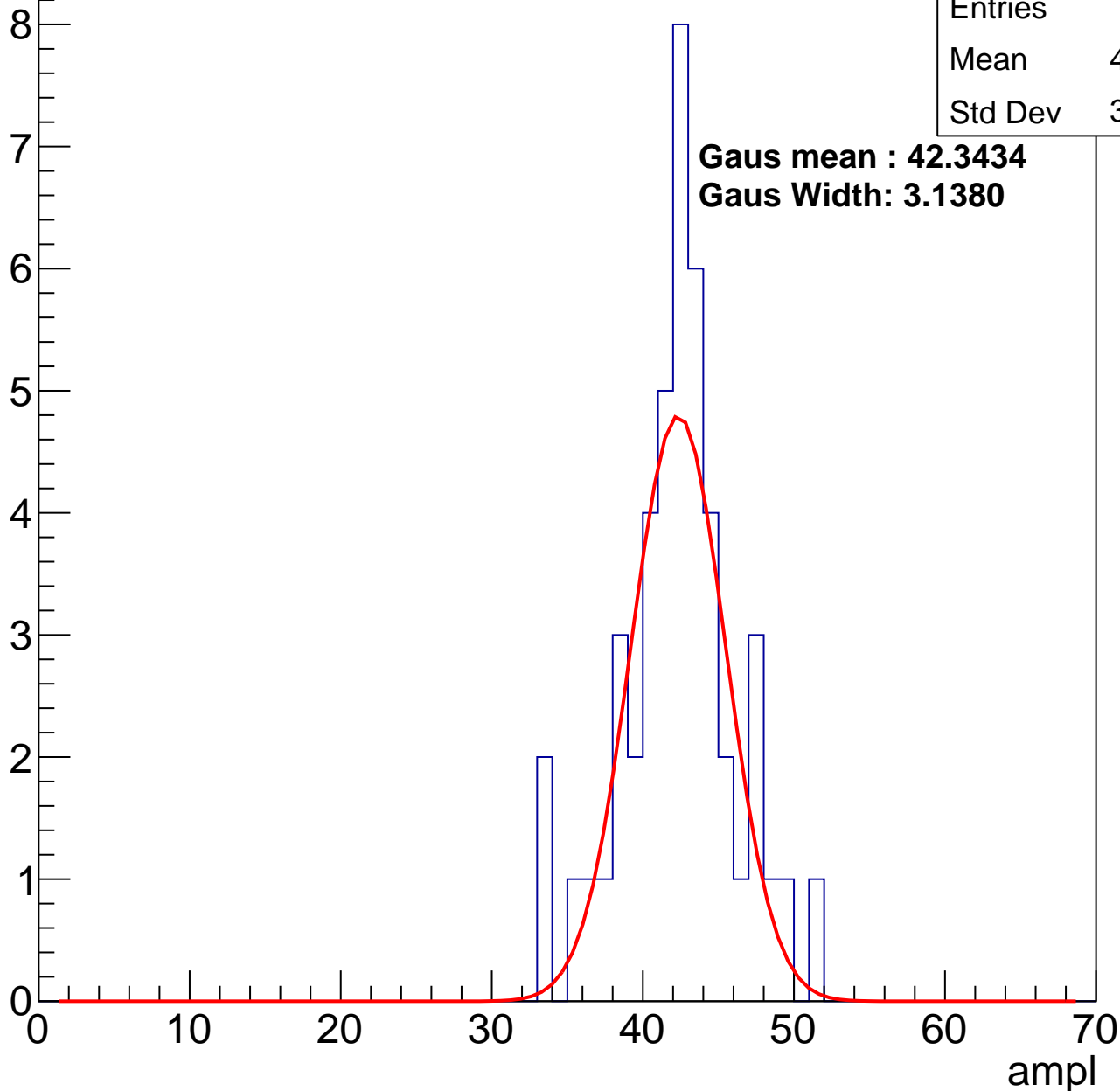
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	41.87
Std Dev	3.774

Gaus mean : 42.3434

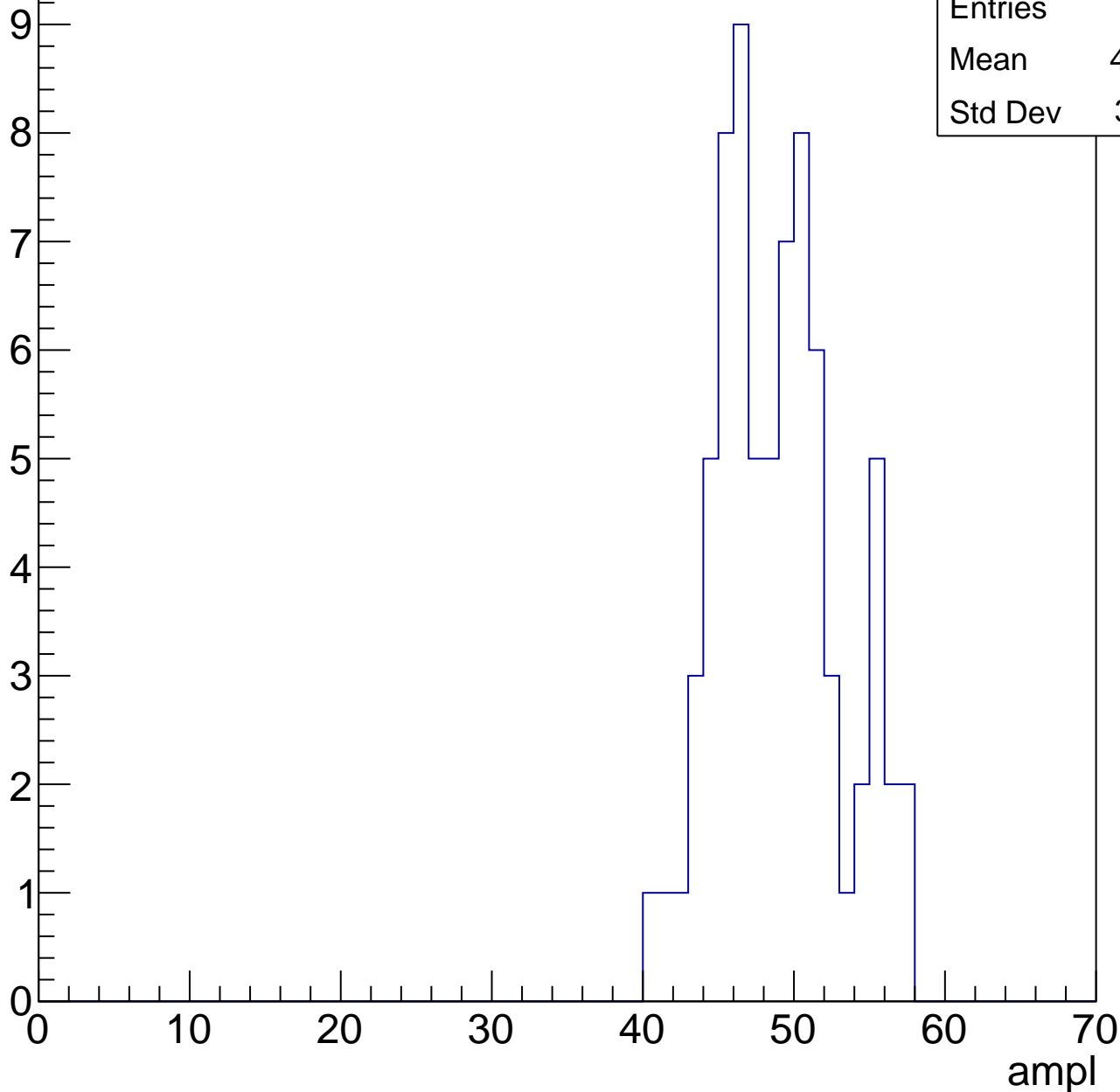
Gaus Width: 3.1380



B1L103S, U24-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



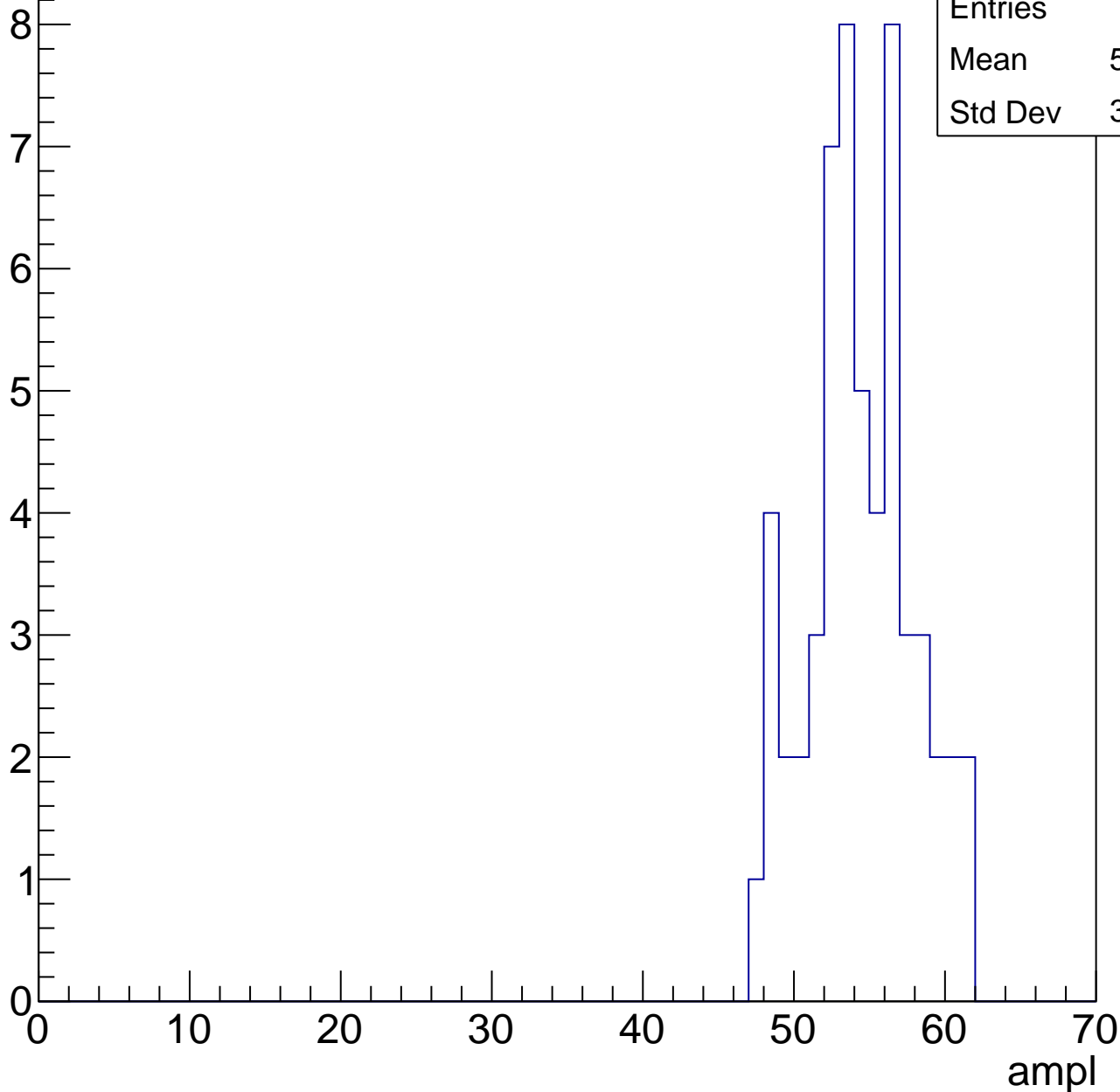
Entries	74
Mean	48.49
Std Dev	3.981

B1L103S, U24-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.95
Std Dev	3.456

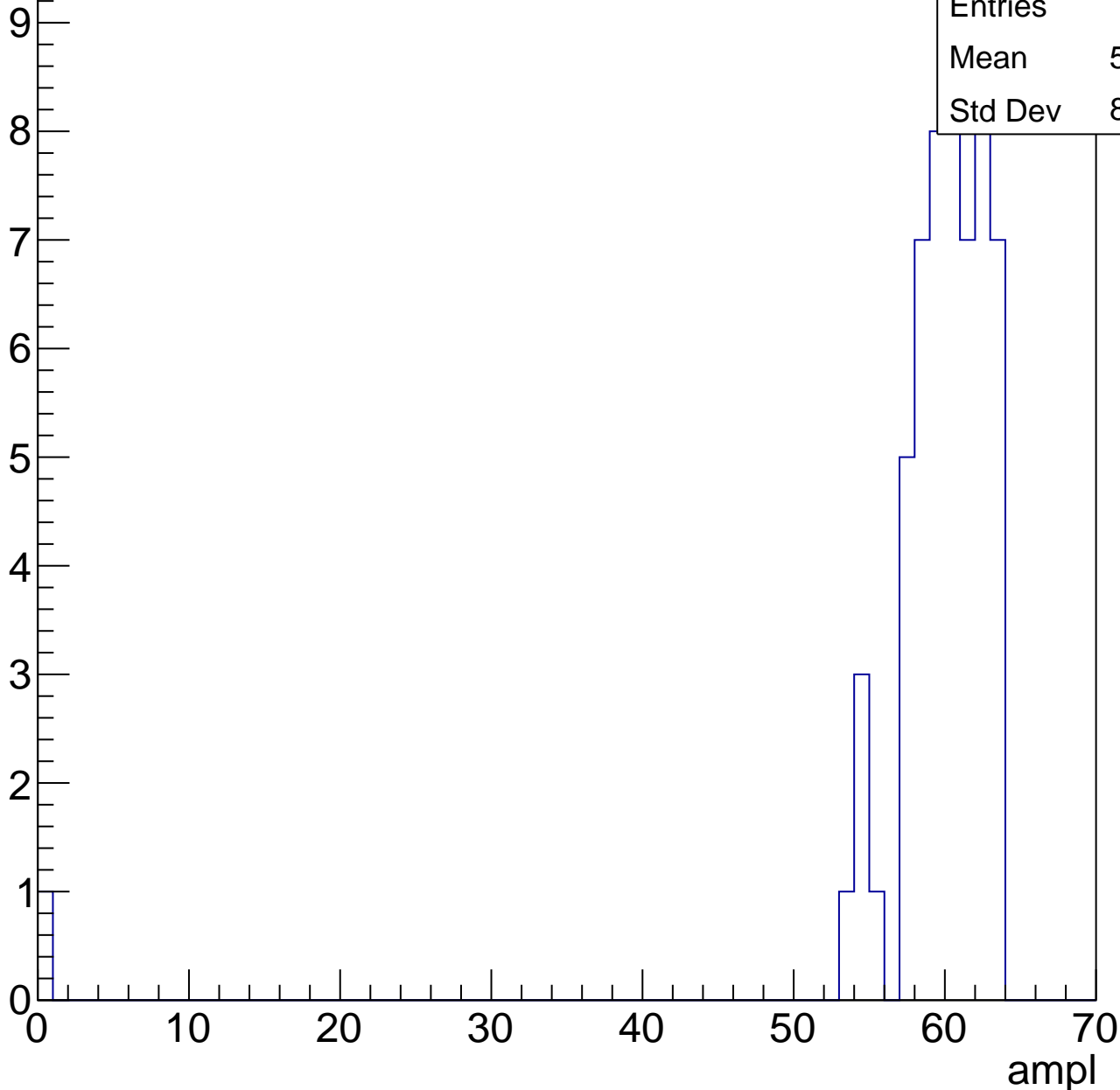


B1L103S, U24-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

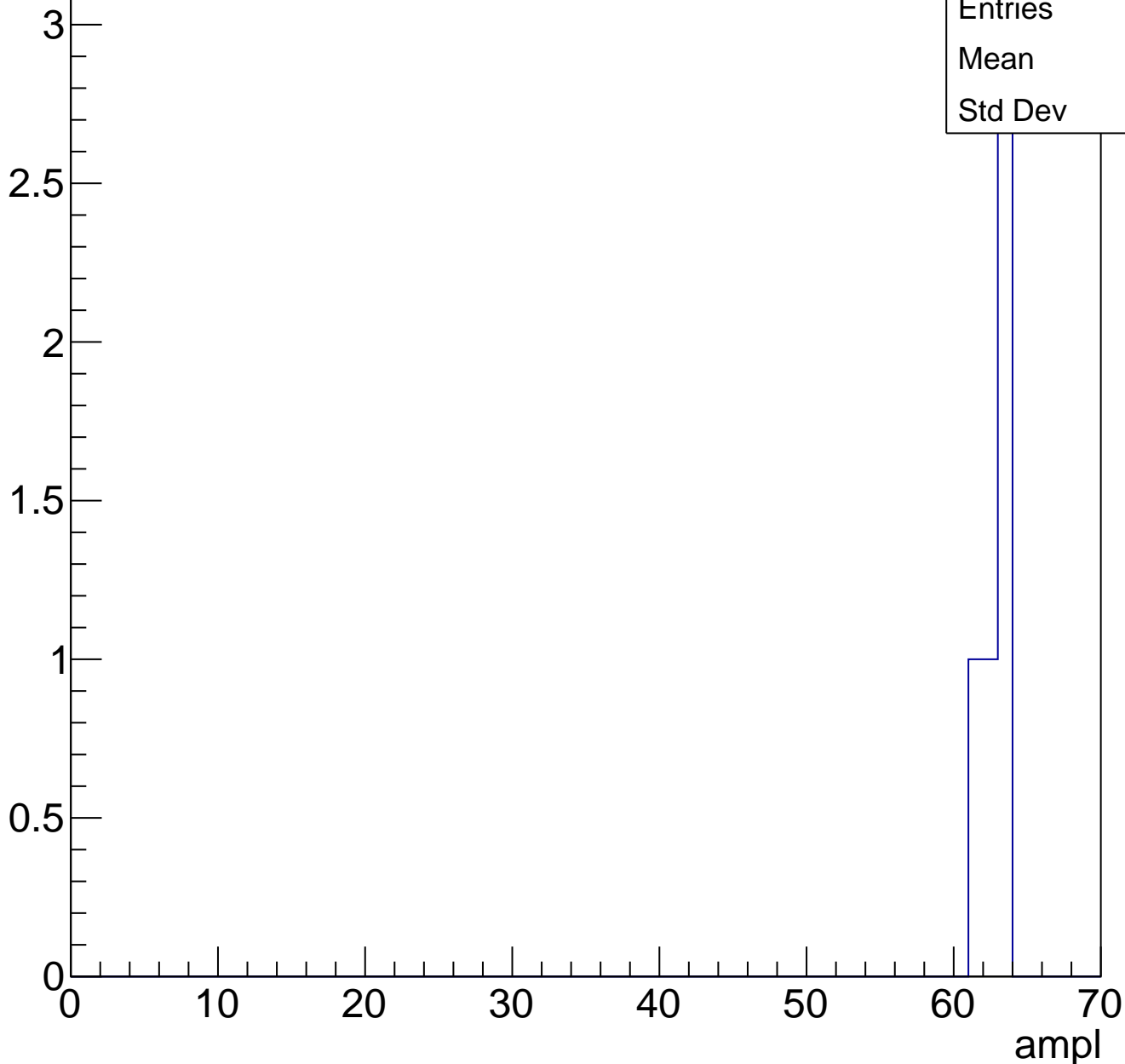
Entries	57
Mean	58.58
Std Dev	8.223



B1L103S, U24-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch3, adc0

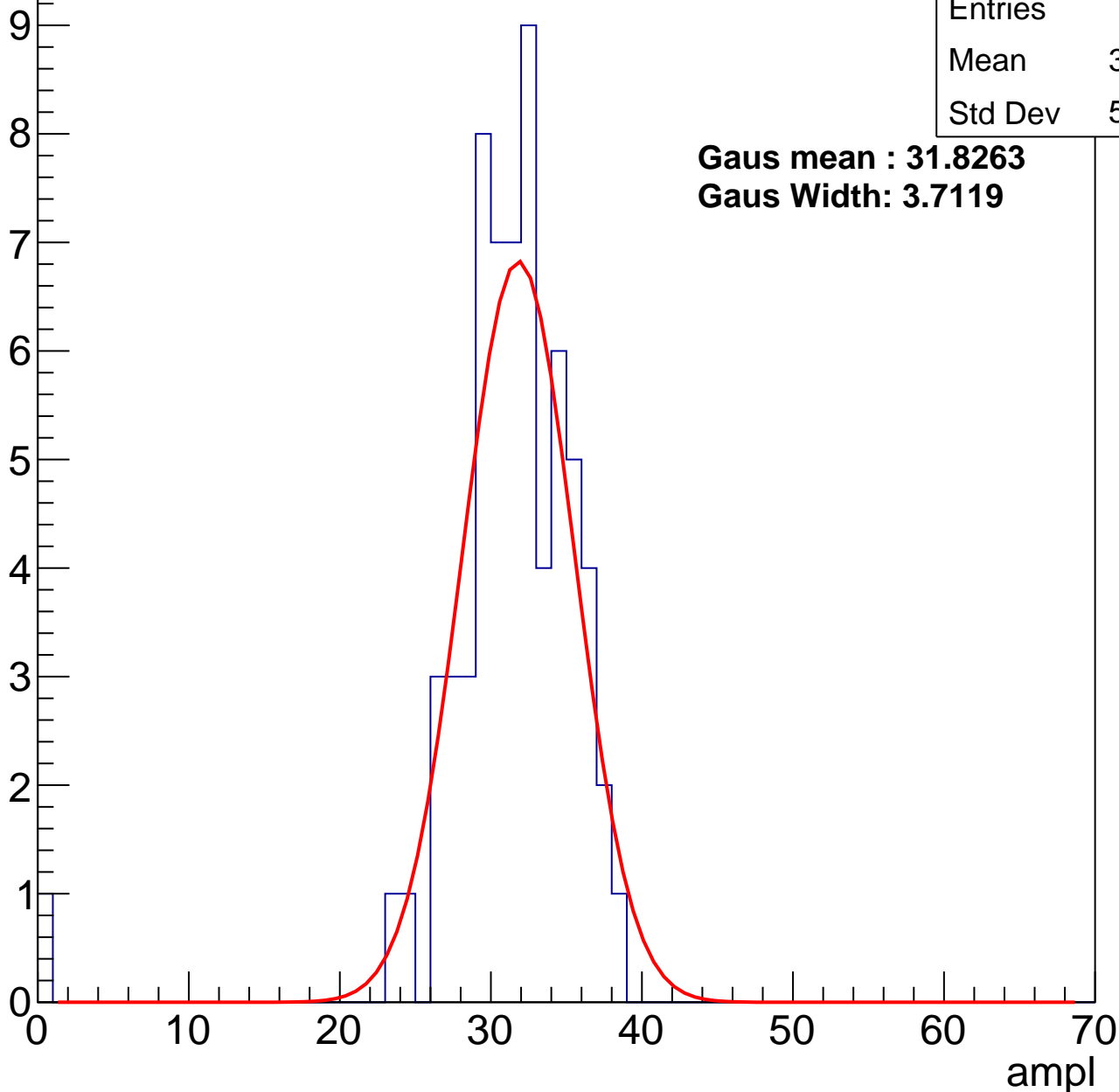
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.83
Std Dev	5.025

Gaus mean : 31.8263

Gaus Width: 3.7119



B1L103S, U24-ch3, adc1

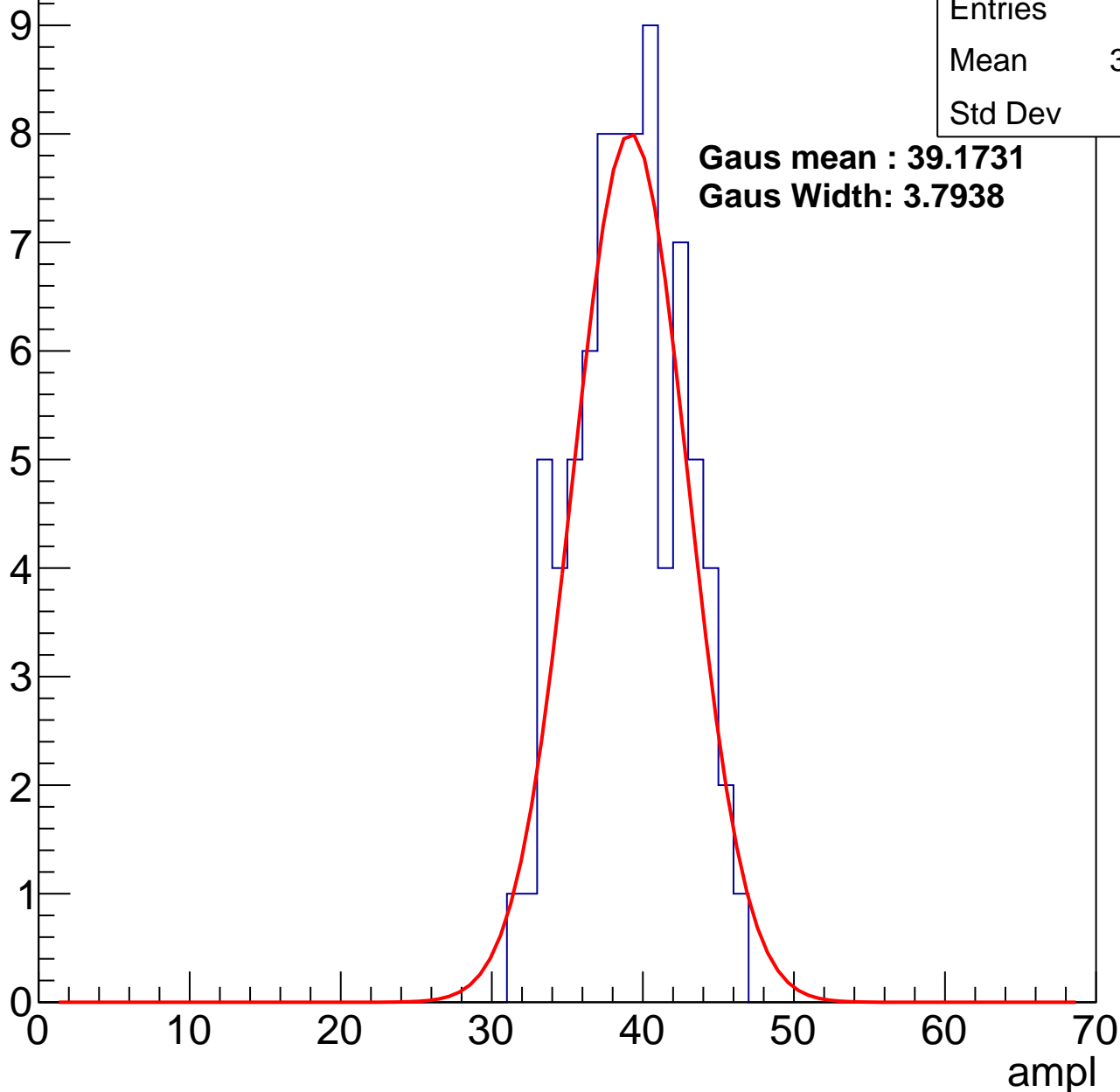
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	38.62
Std Dev	3.48

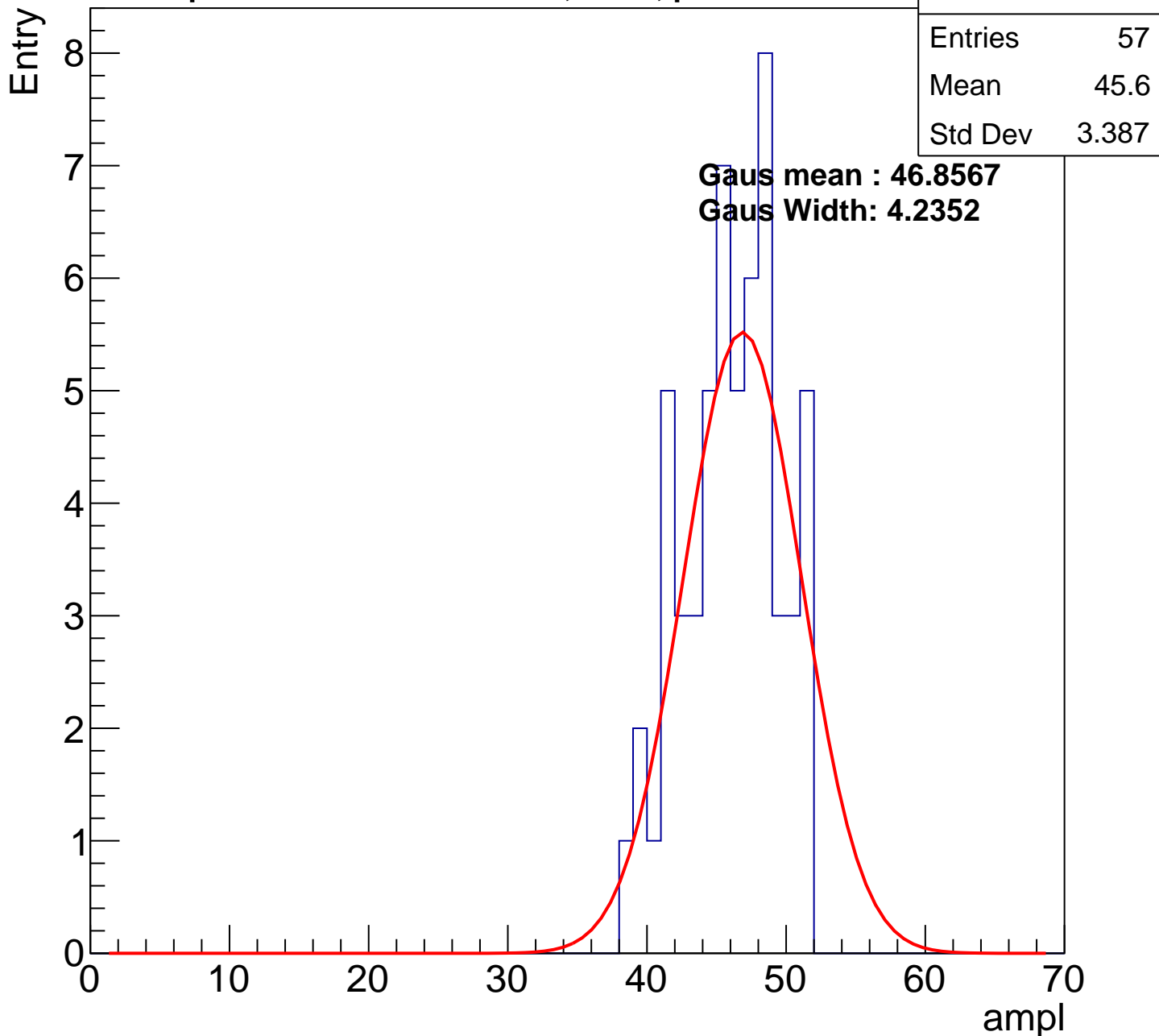
Gaus mean : 39.1731

Gaus Width: 3.7938



B1L103S, U24-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

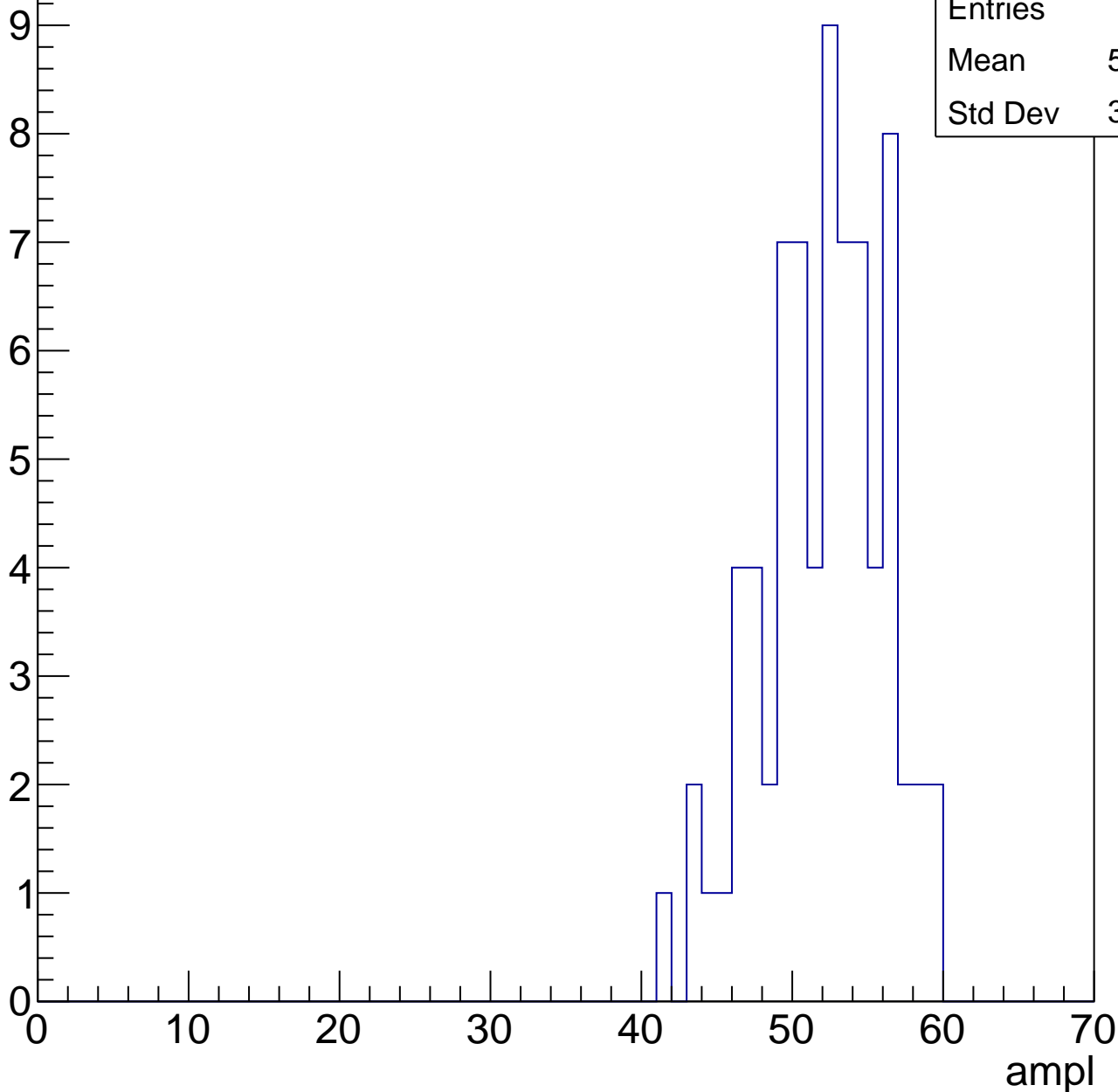


B1L103S, U24-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	51.54
Std Dev	3.977

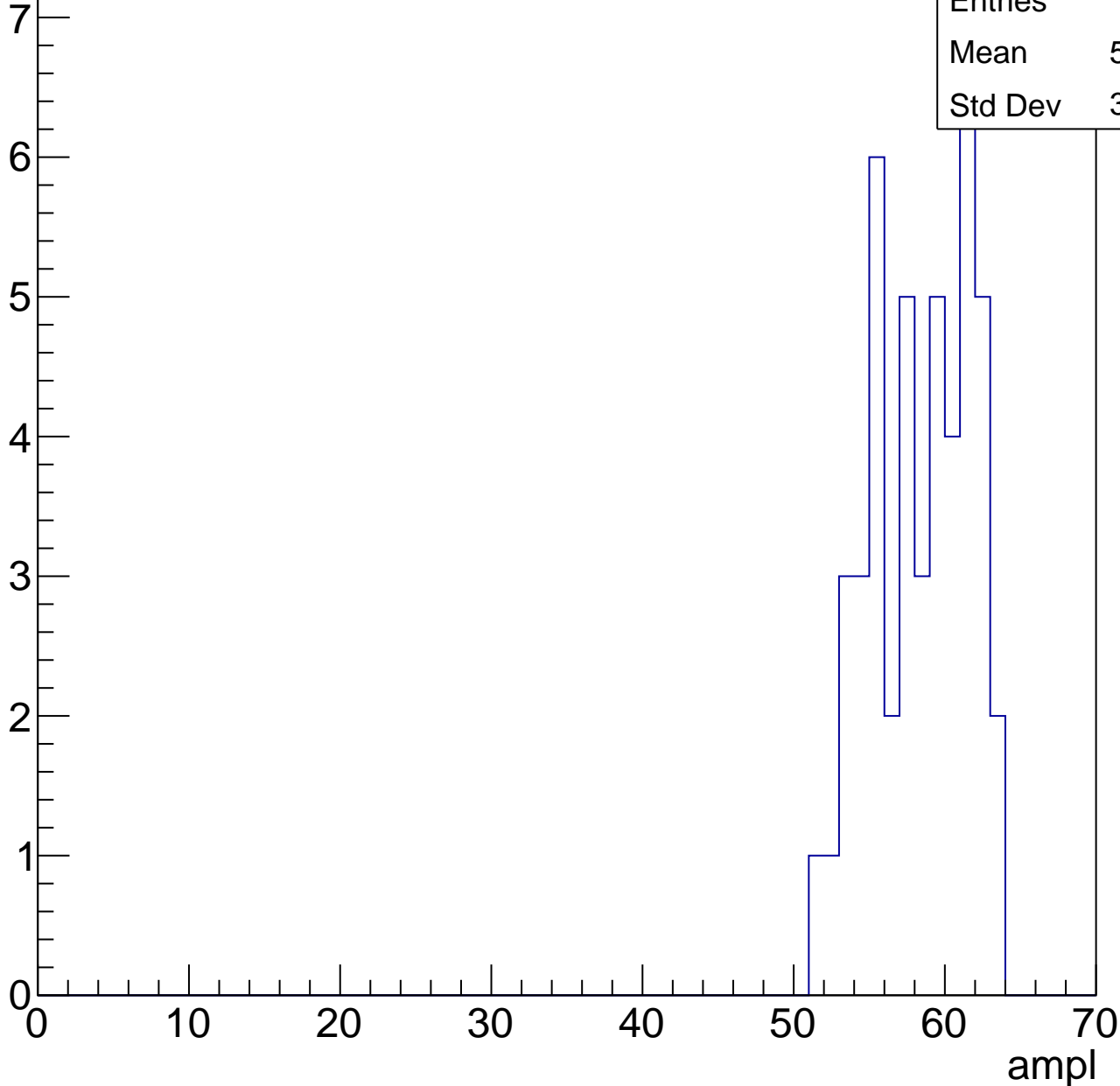


B1L103S, U24-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	57.94
Std Dev	3.225

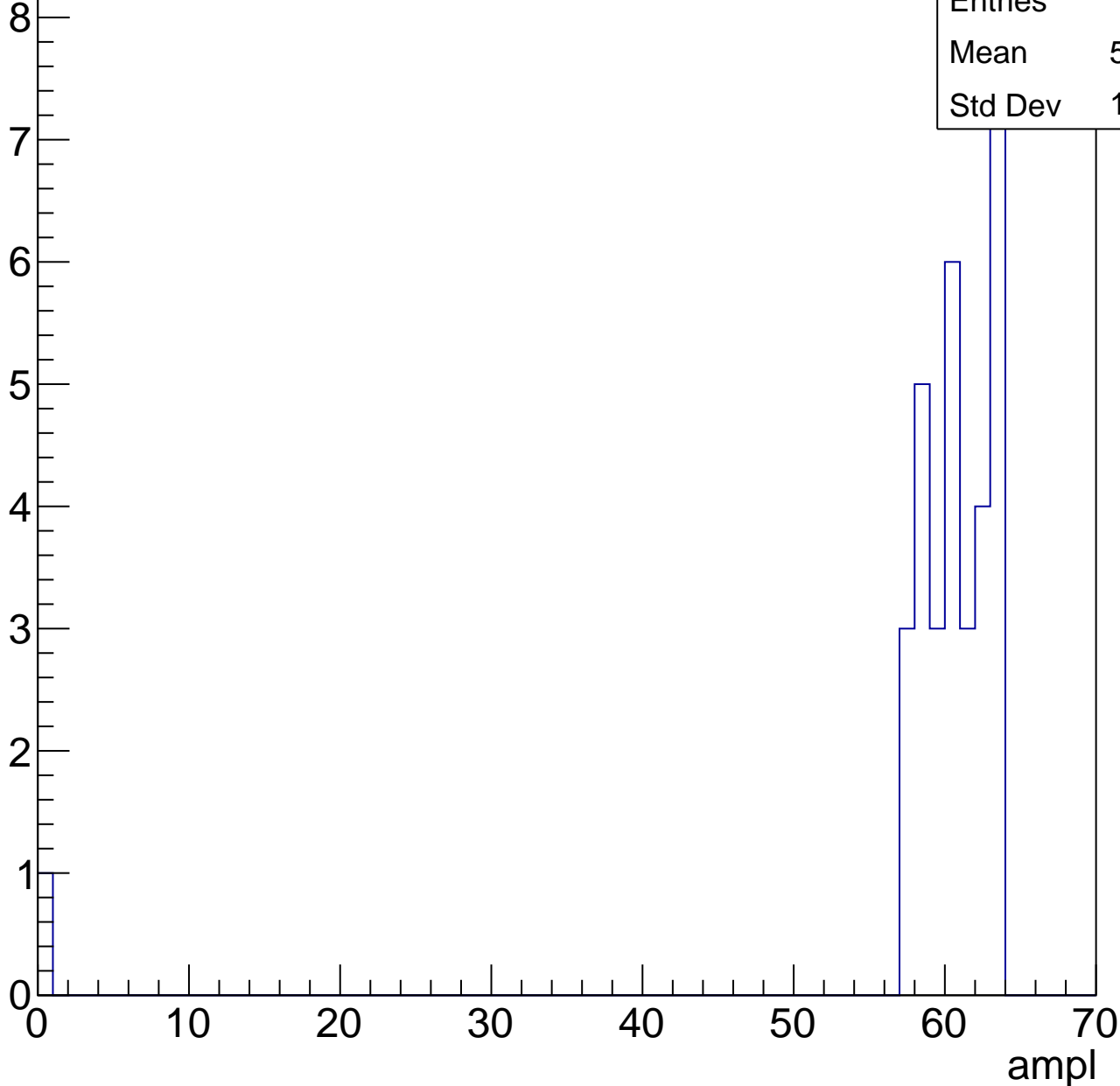


B1L103S, U24-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.58
Std Dev	10.55



B1L103S, U24-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L103S, U24-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch4, adc0

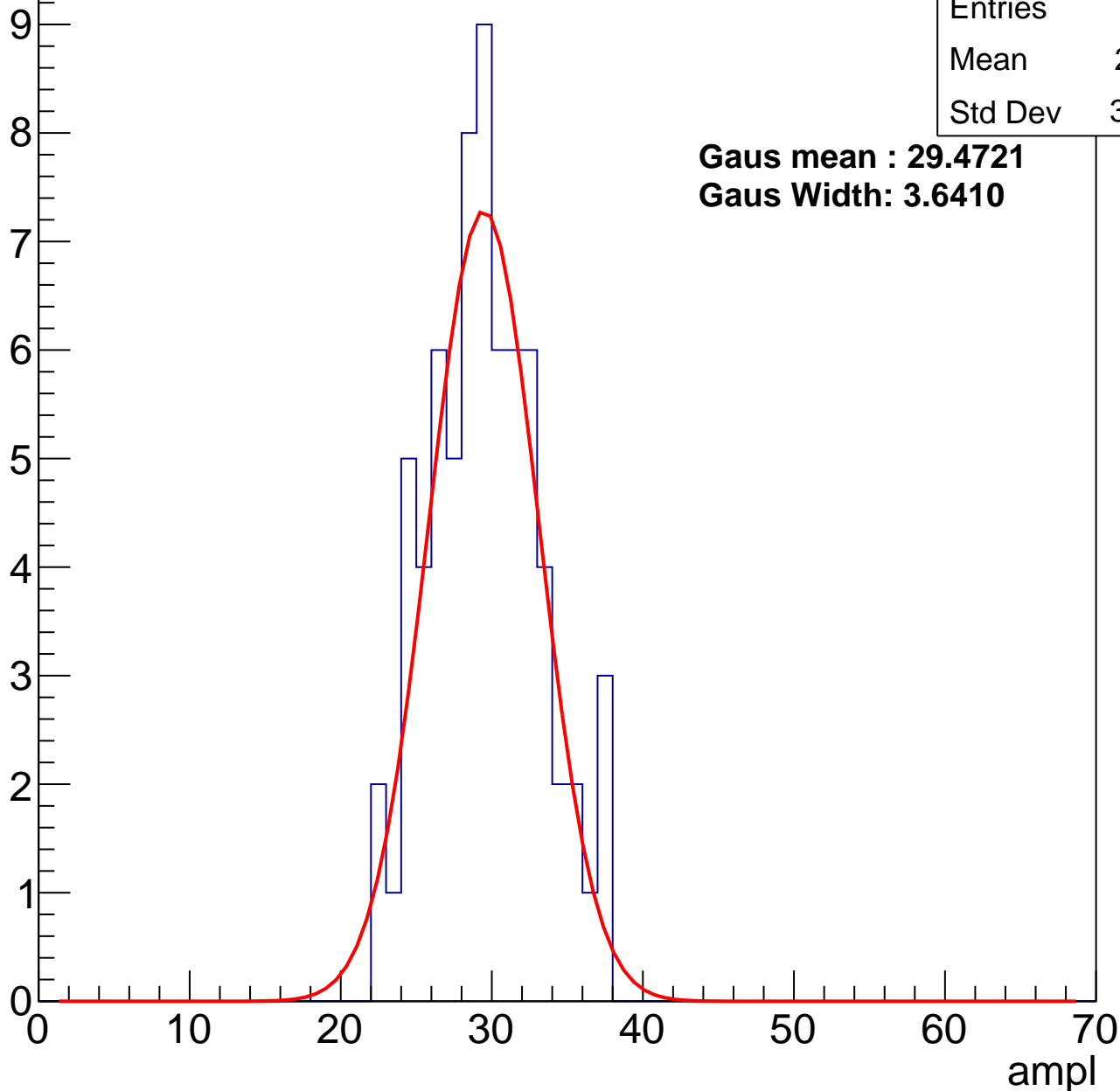
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.11
Std Dev	3.616

Gaus mean : 29.4721

Gaus Width: 3.6410



B1L103S, U24-ch4, adc1

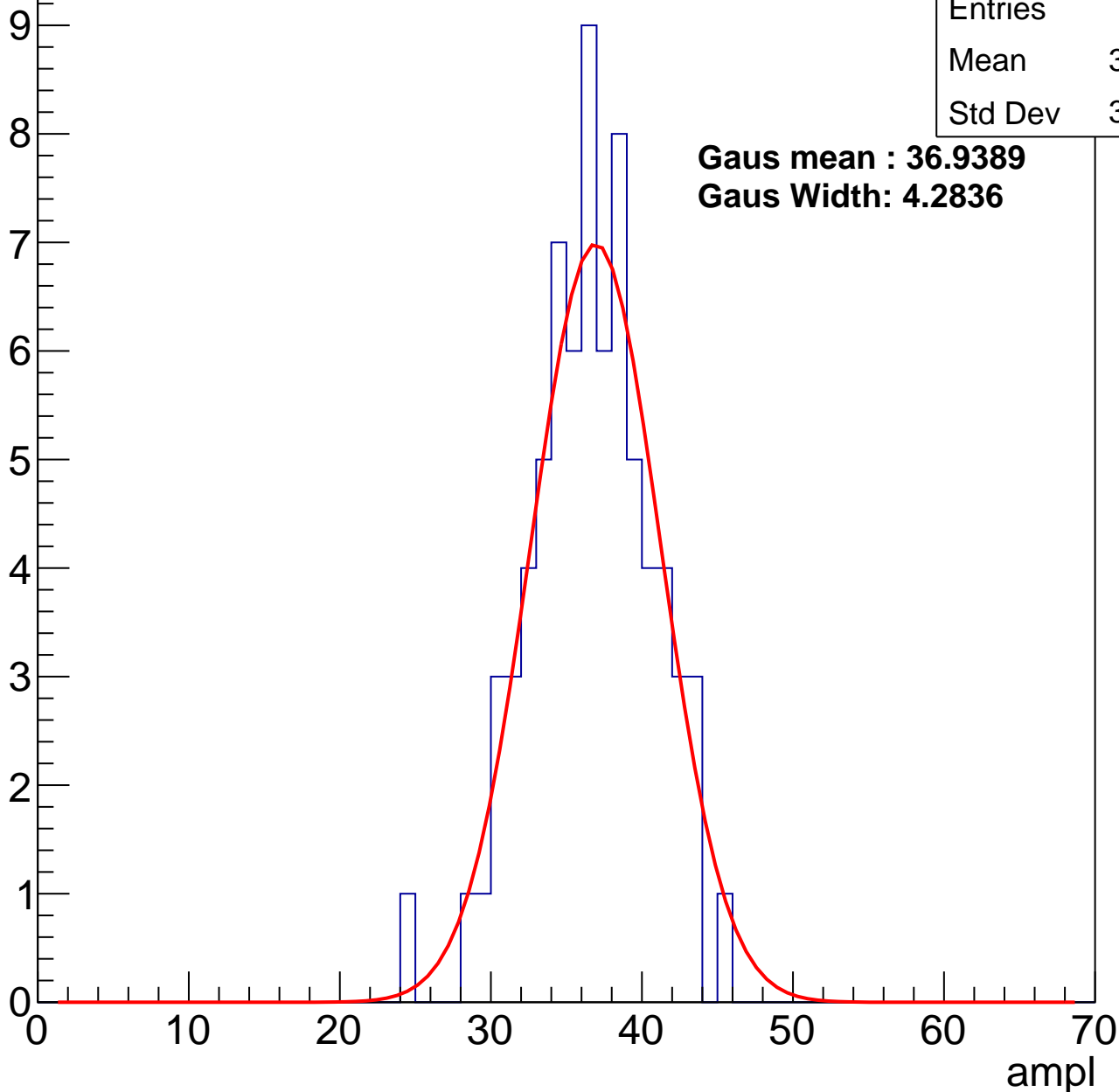
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.14
Std Dev	3.967

Gaus mean : 36.9389

Gaus Width: 4.2836



B1L103S, U24-ch4, adc2

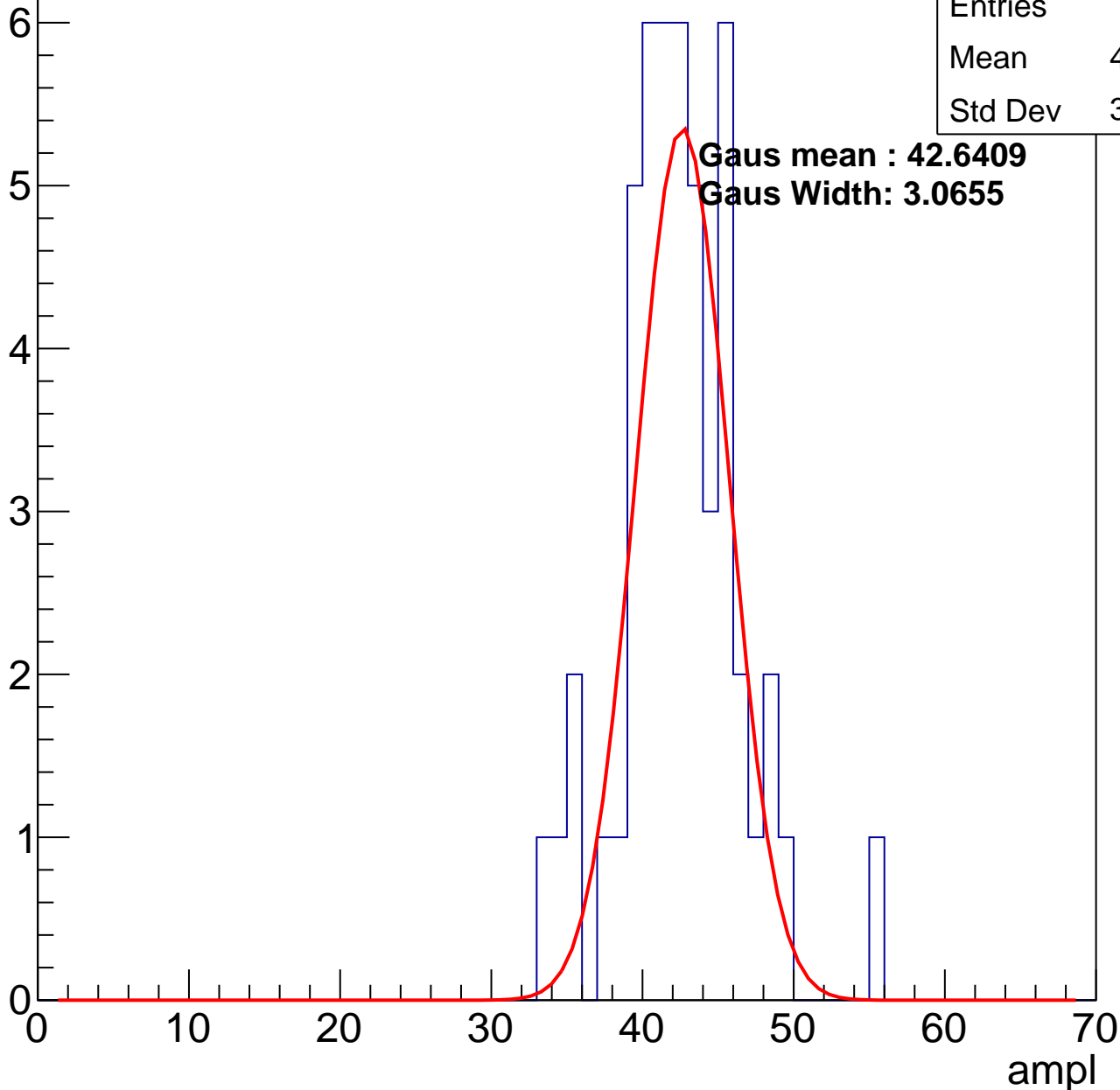
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.02
Std Dev	3.942

Gaus mean : 42.6409

Gaus Width: 3.0655



B1L103S, U24-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

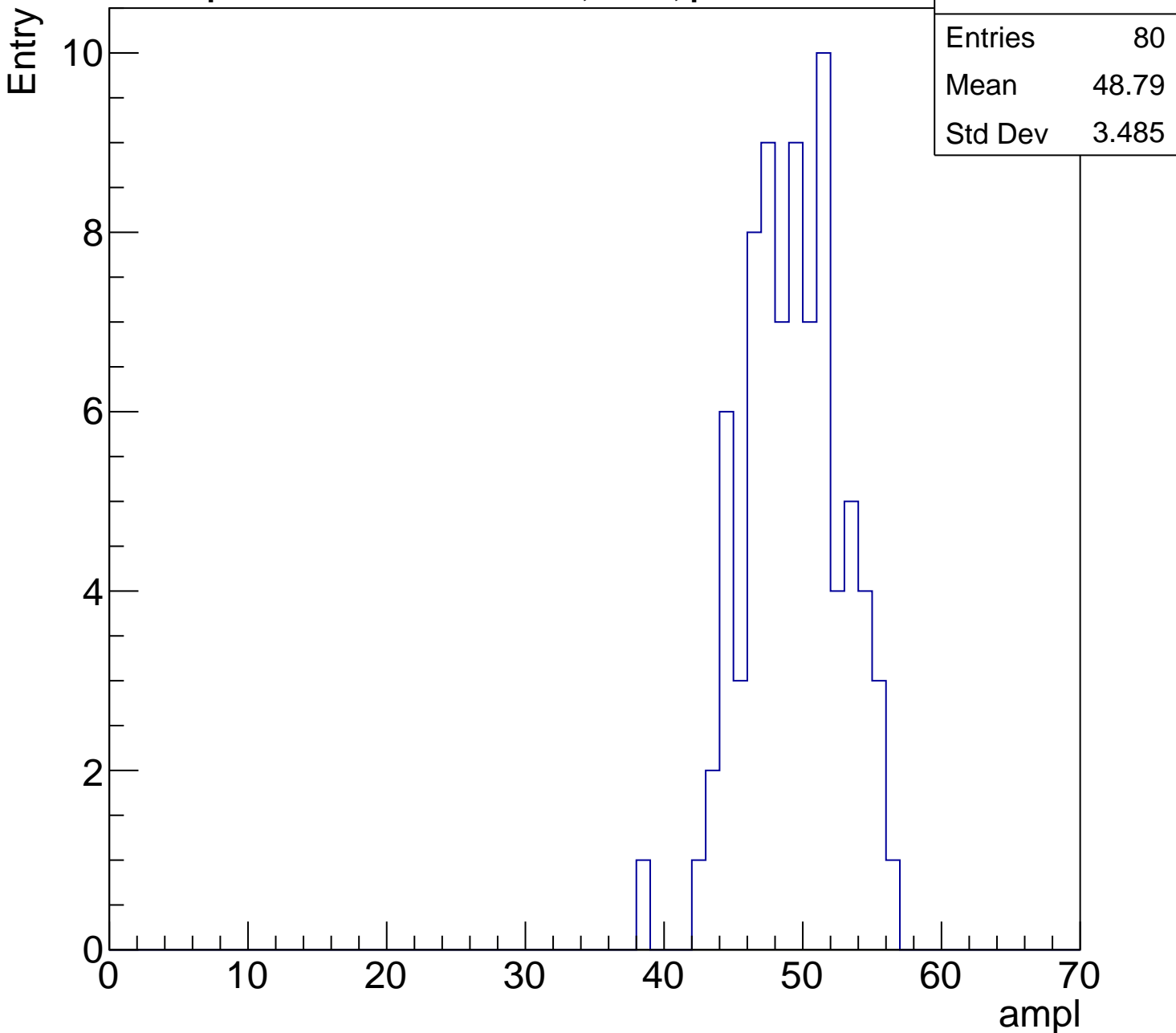
Entries	80
Mean	48.79
Std Dev	3.485

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

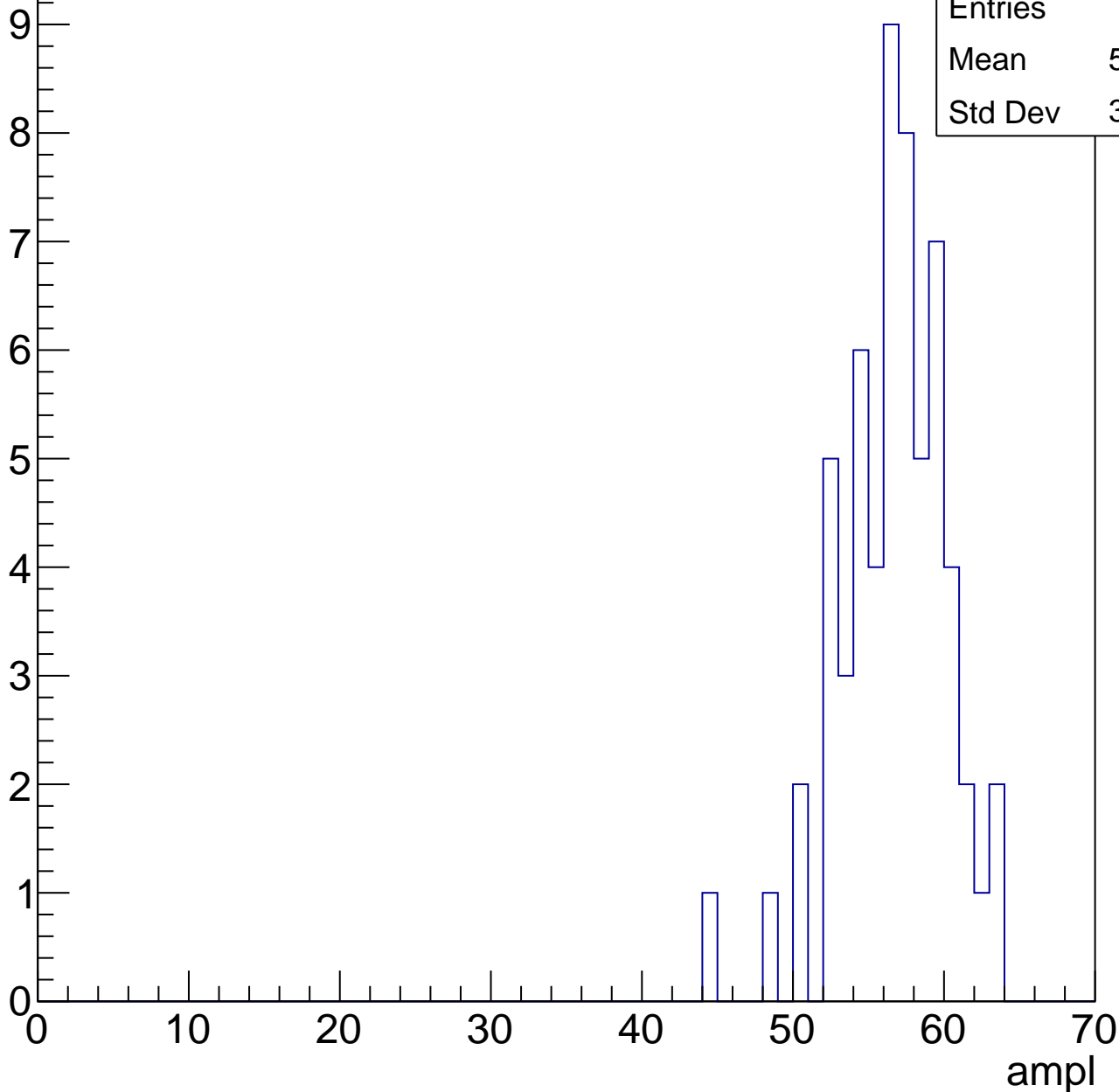


B1L103S, U24-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.13
Std Dev	3.542

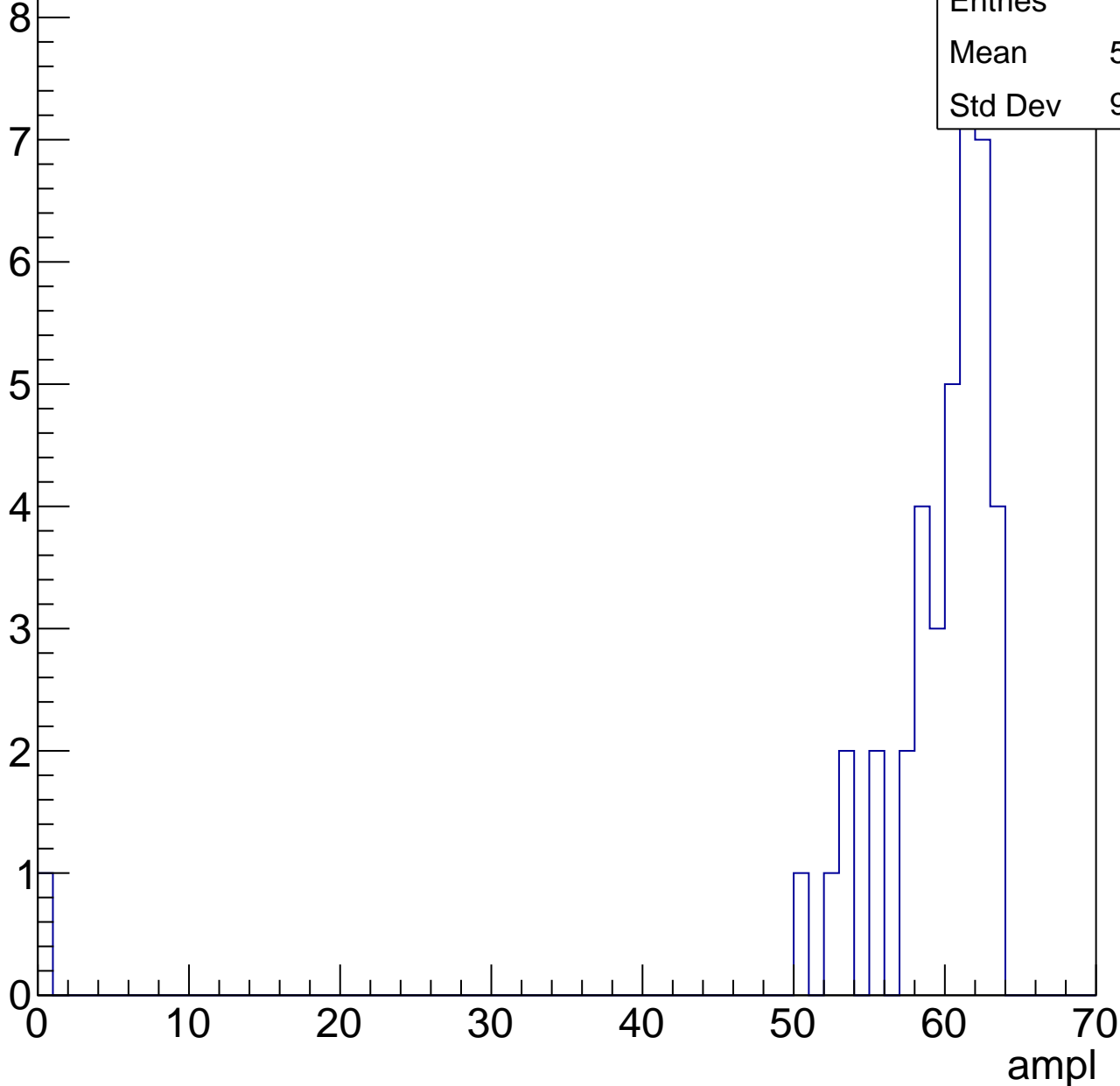


B1L103S, U24-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

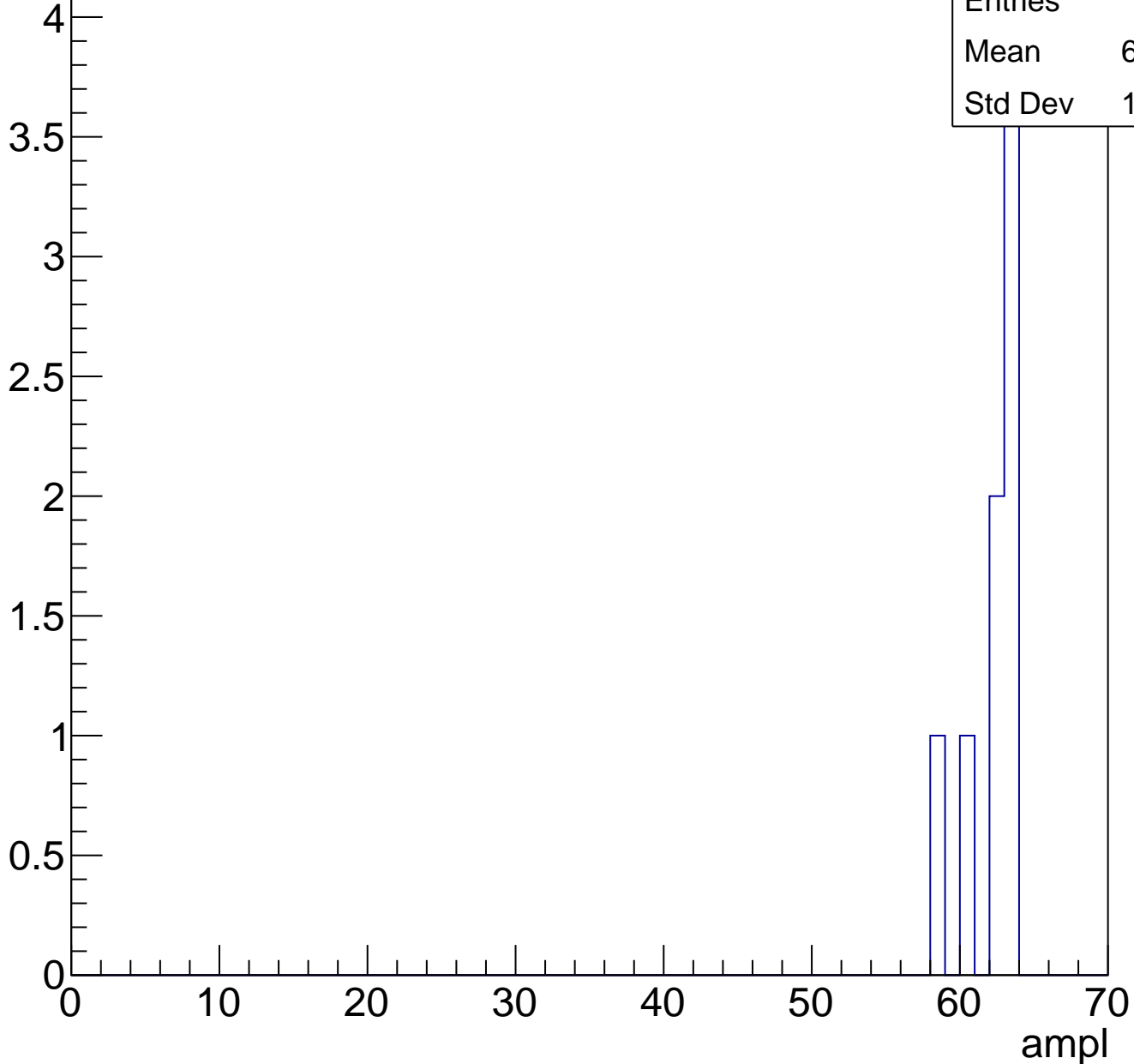
Entries	40
Mean	57.88
Std Dev	9.798



B1L103S, U24-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch5, adc0

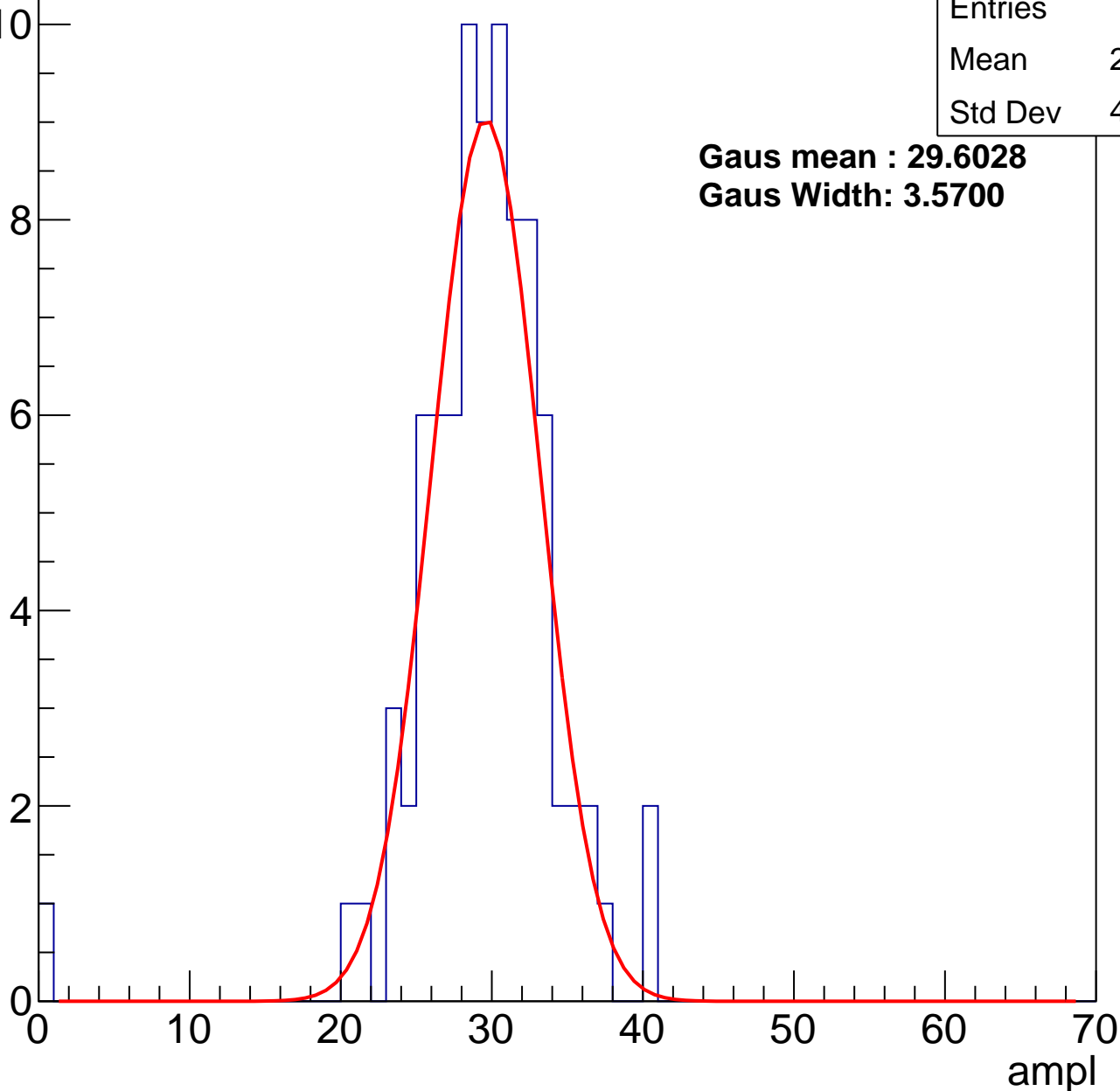
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	29.02
Std Dev	4.899

Gaus mean : 29.6028

Gaus Width: 3.5700



B1L103S, U24-ch5, adc1

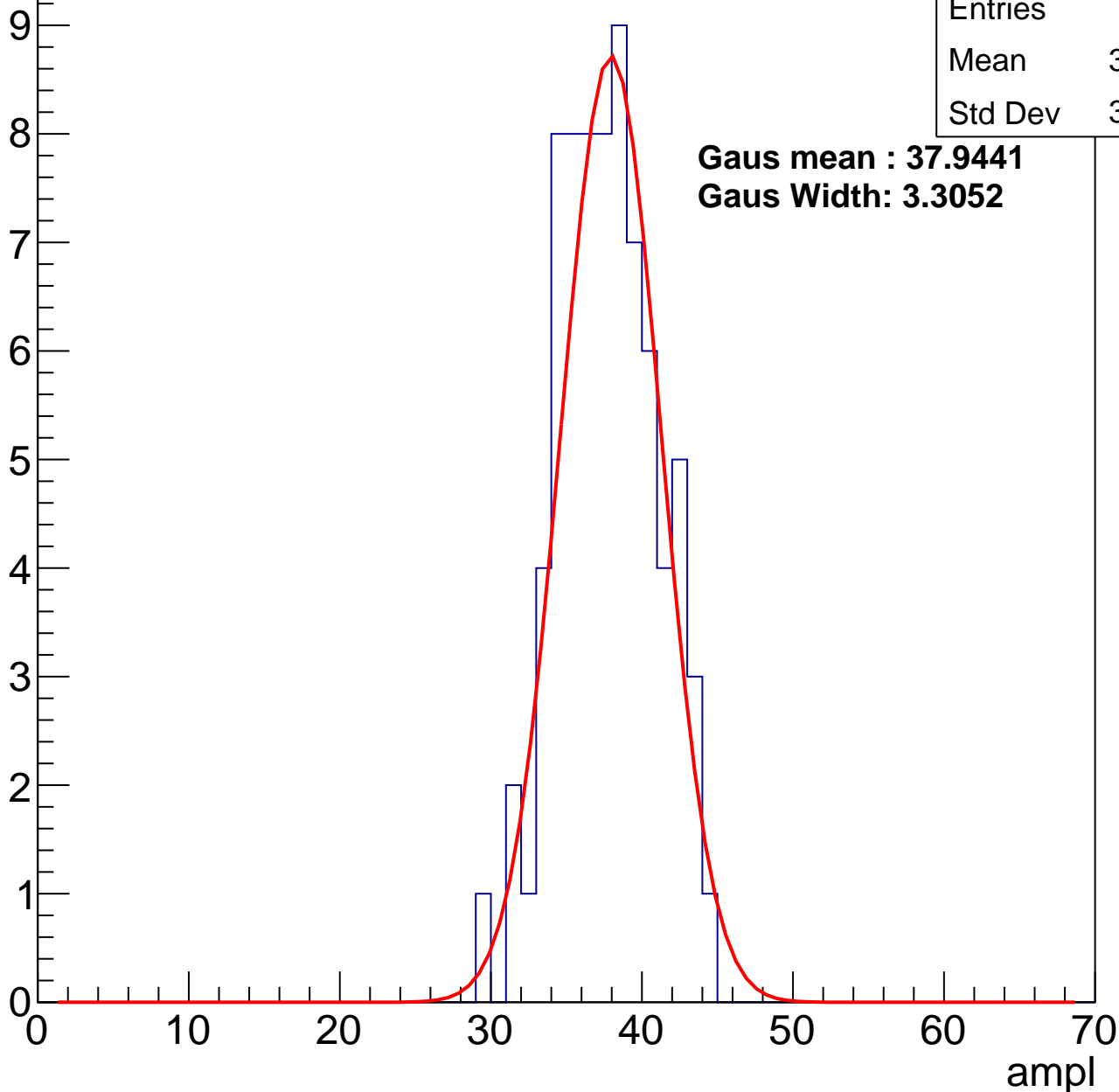
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.24
Std Dev	3.203

Gaus mean : 37.9441

Gaus Width: 3.3052



B1L103S, U24-ch5, adc2

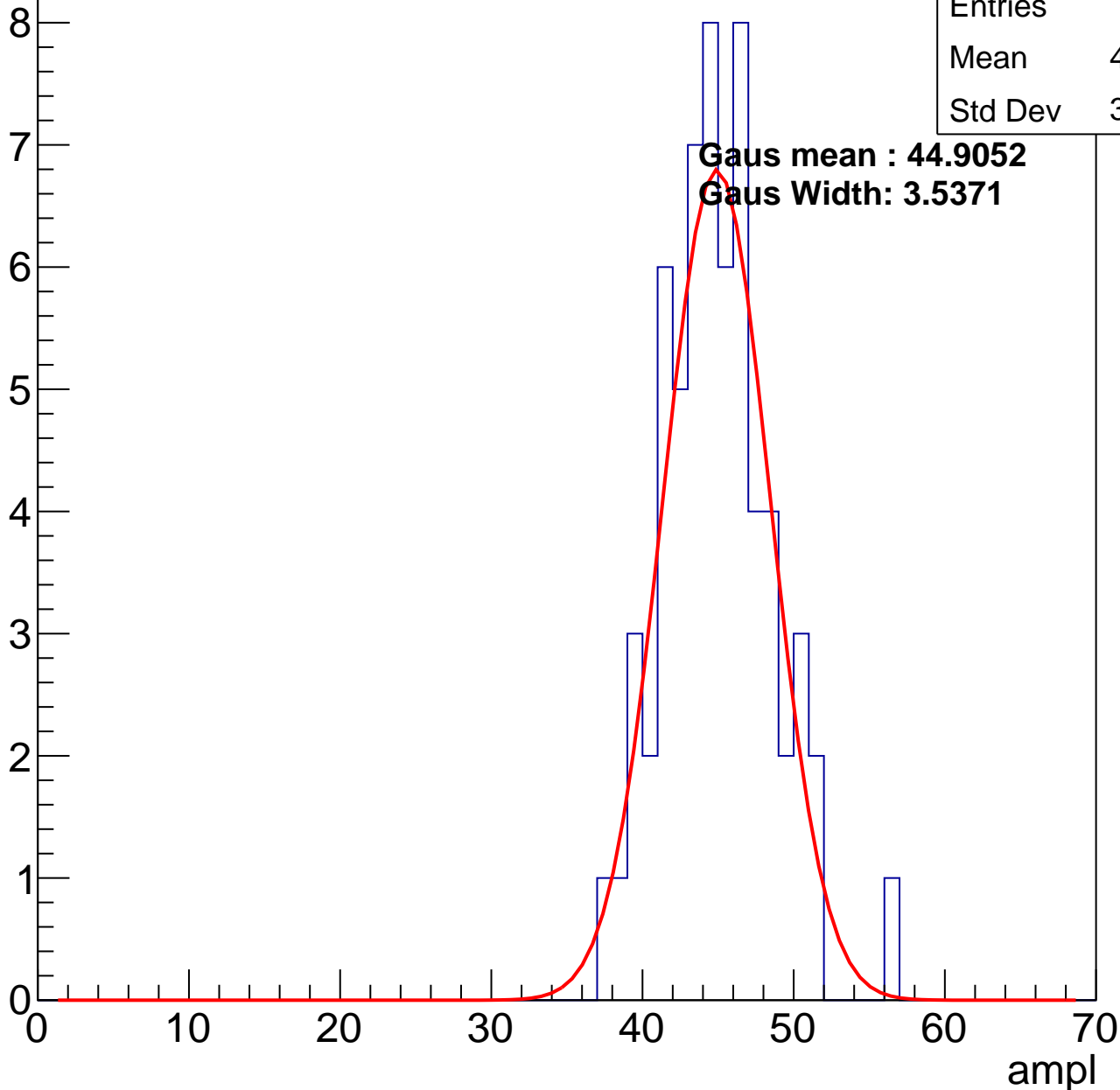
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.52
Std Dev	3.549

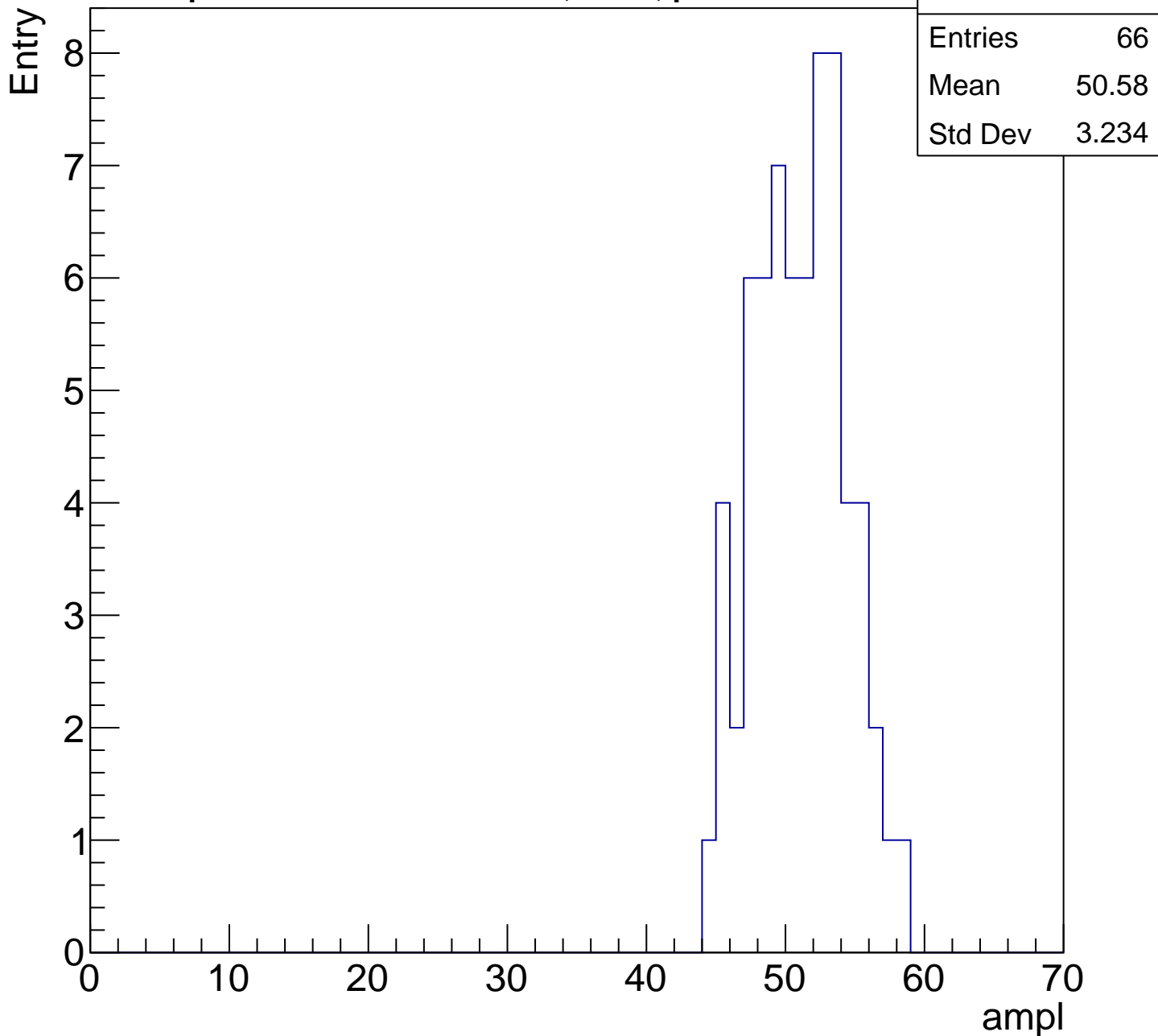
Gaus mean : 44.9052

Gaus Width: 3.5371



B1L103S, U24-ch5, adc3

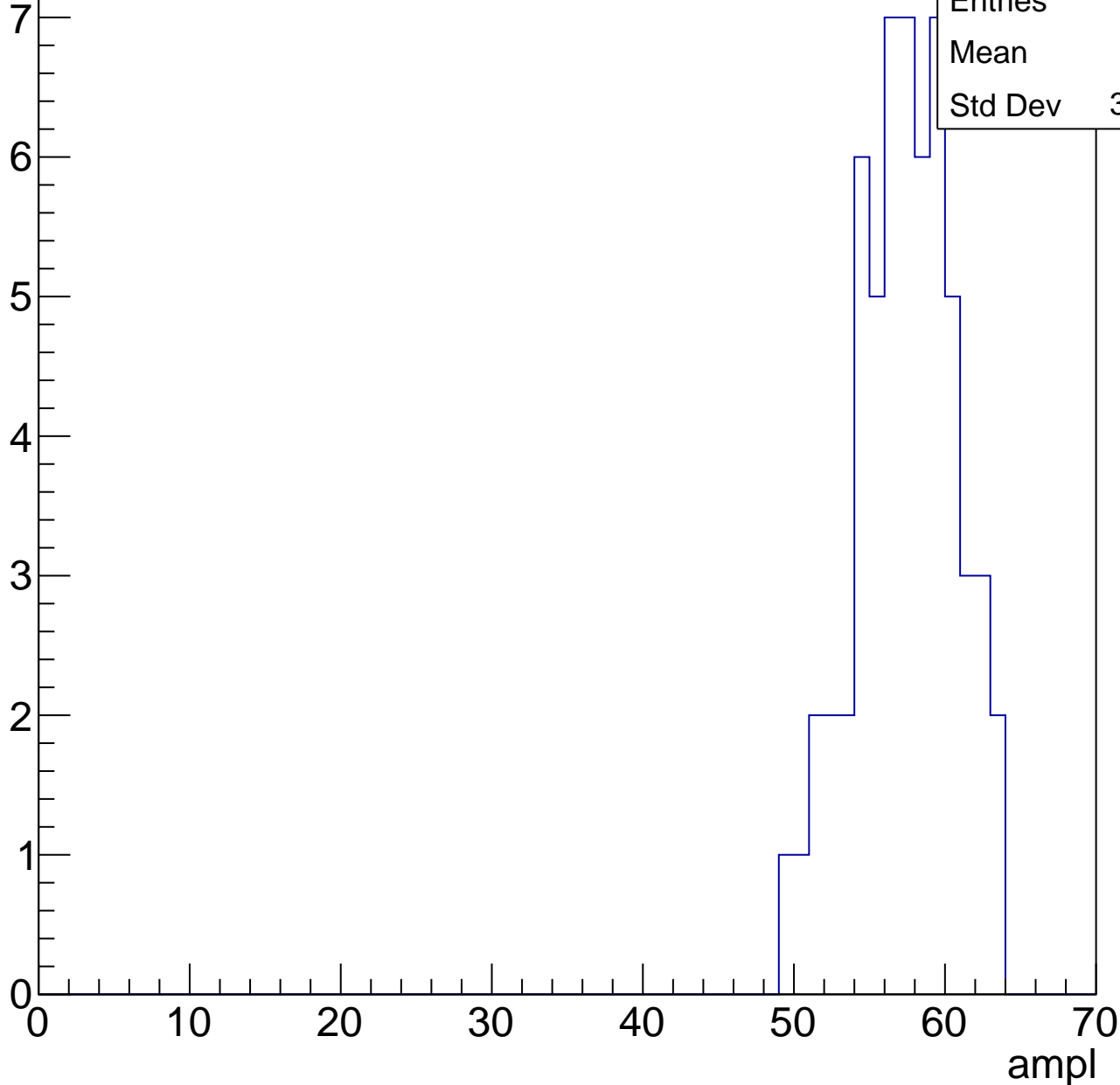
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



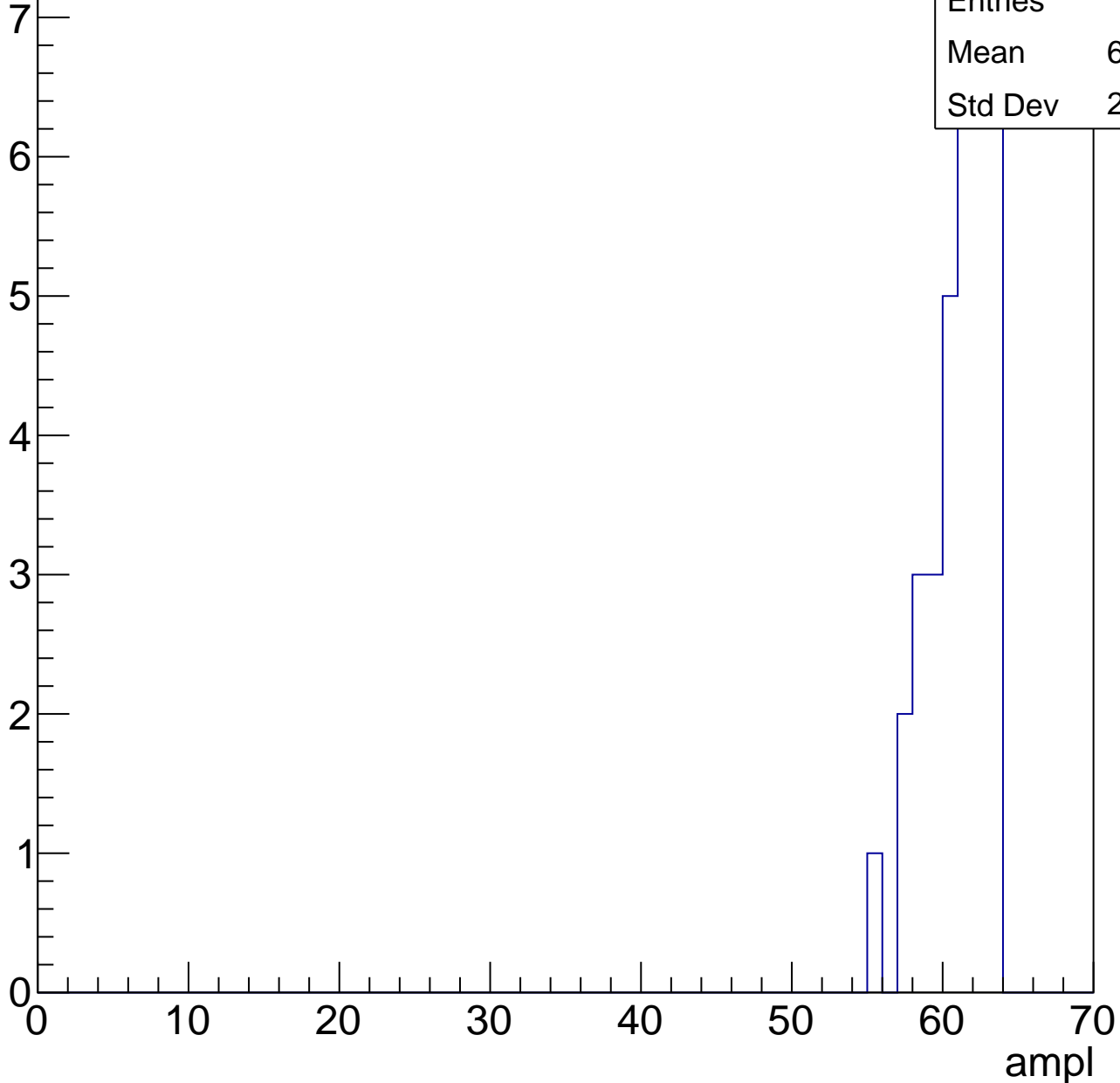
Entries	59
Mean	56.9
Std Dev	3.245

B1L103S, U24-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

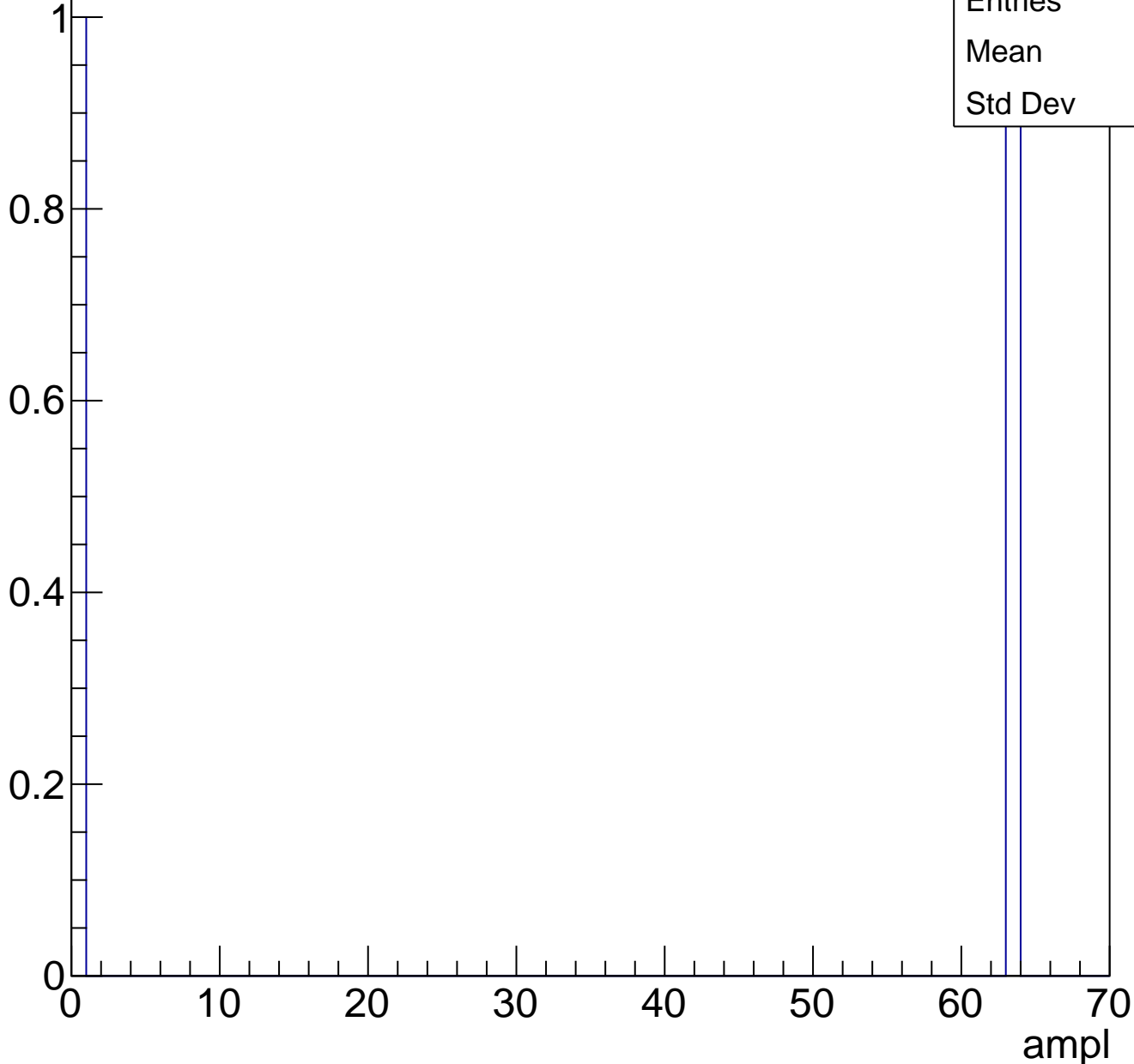
Entries	35
Mean	60.63
Std Dev	2.015



B1L103S, U24-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch6, adc0

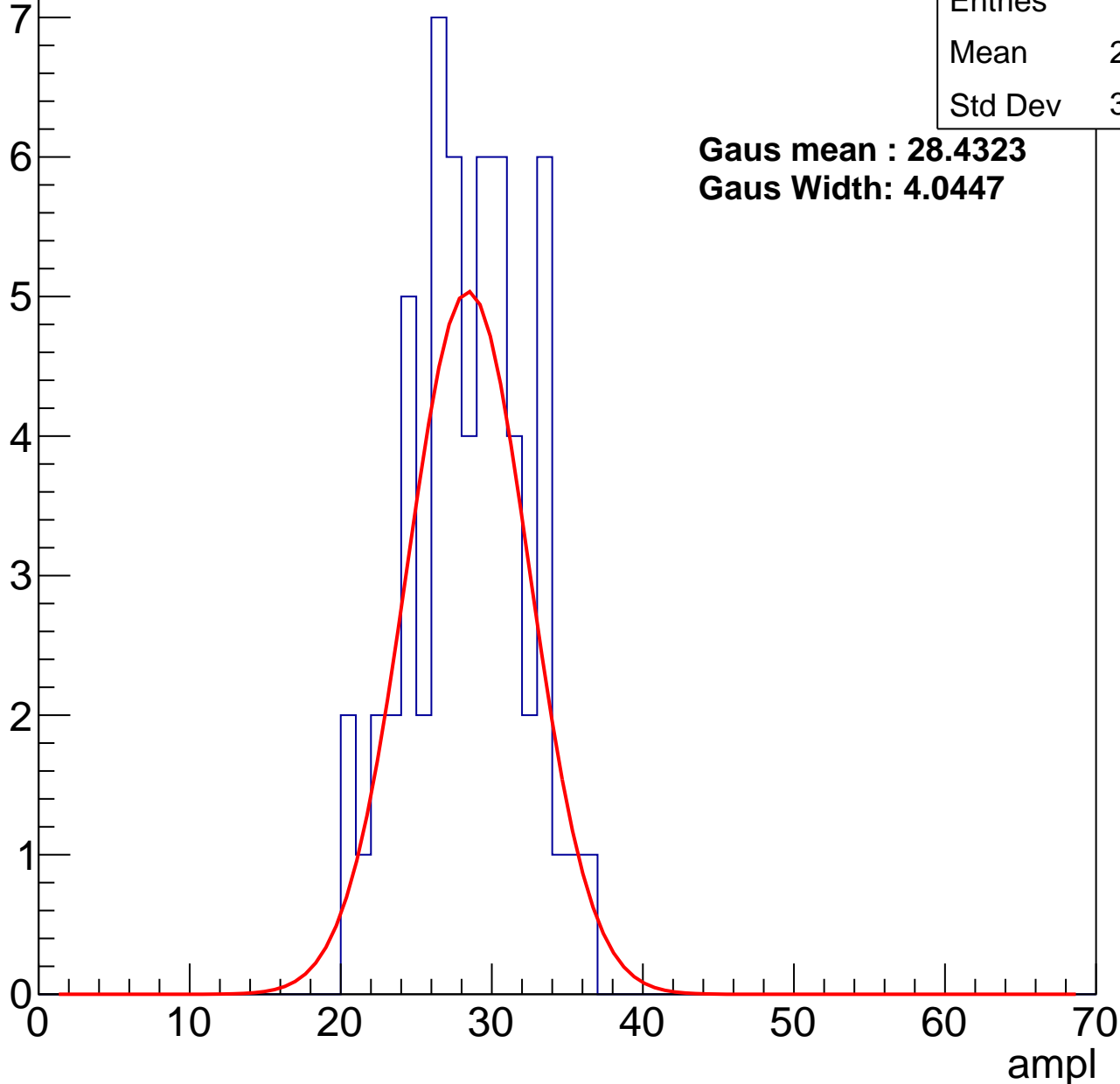
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	27.97
Std Dev	3.778

Gaus mean : 28.4323

Gaus Width: 4.0447



B1L103S, U24-ch6, adc1

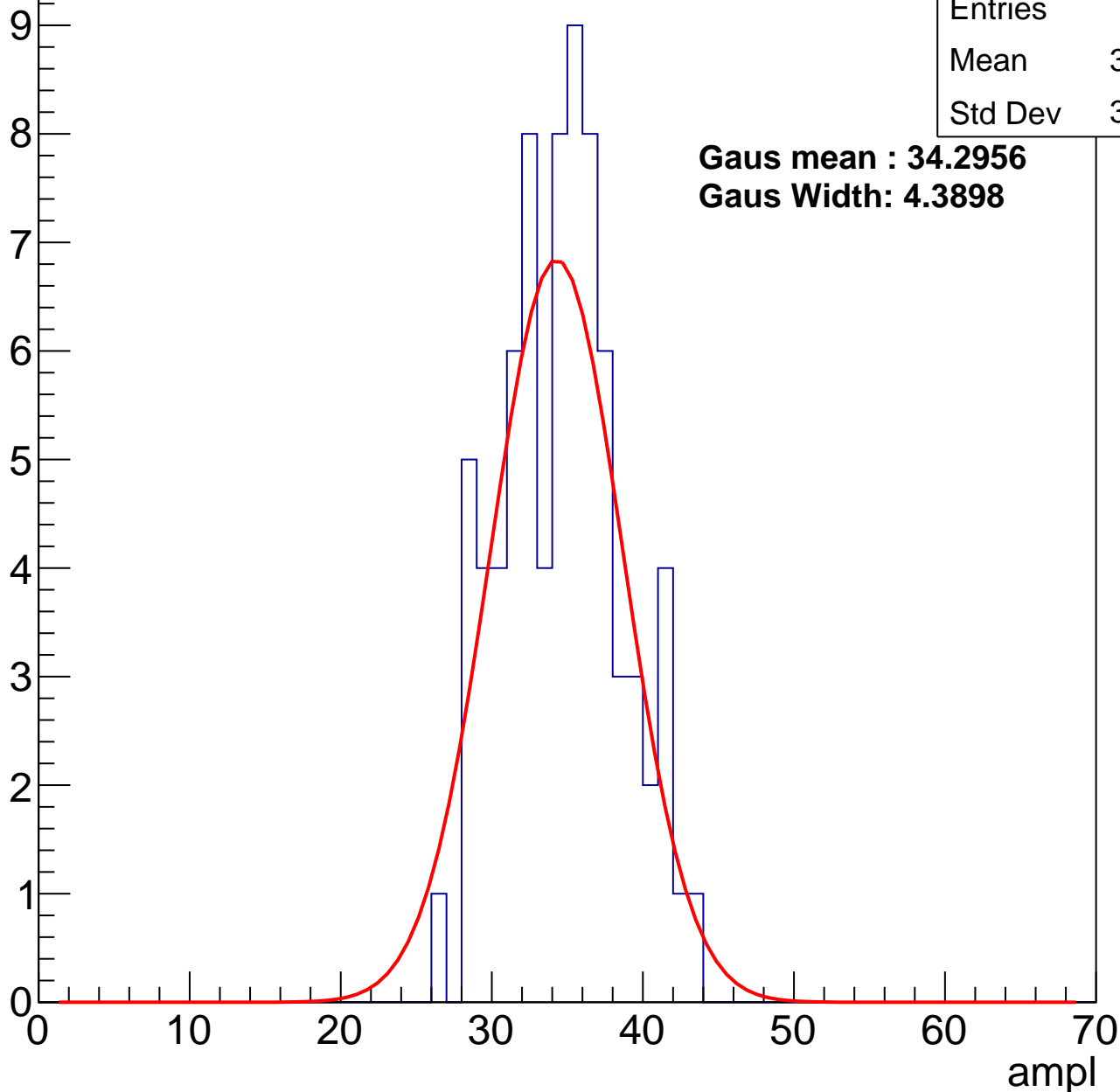
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	34.19
Std Dev	3.824

Gaus mean : 34.2956

Gaus Width: 4.3898



B1L103S, U24-ch6, adc2

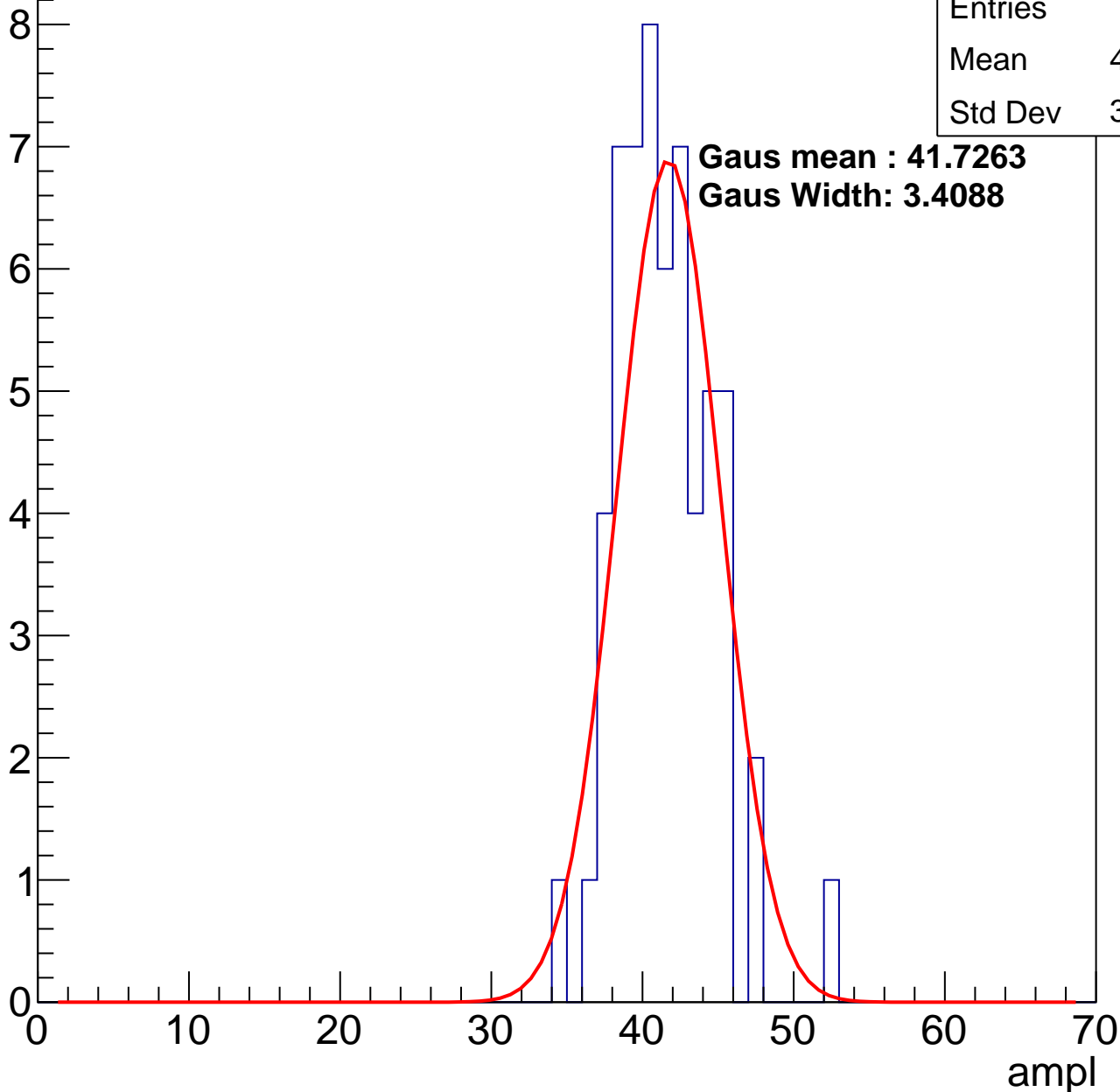
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.03
Std Dev	3.157

Gaus mean : 41.7263

Gaus Width: 3.4088

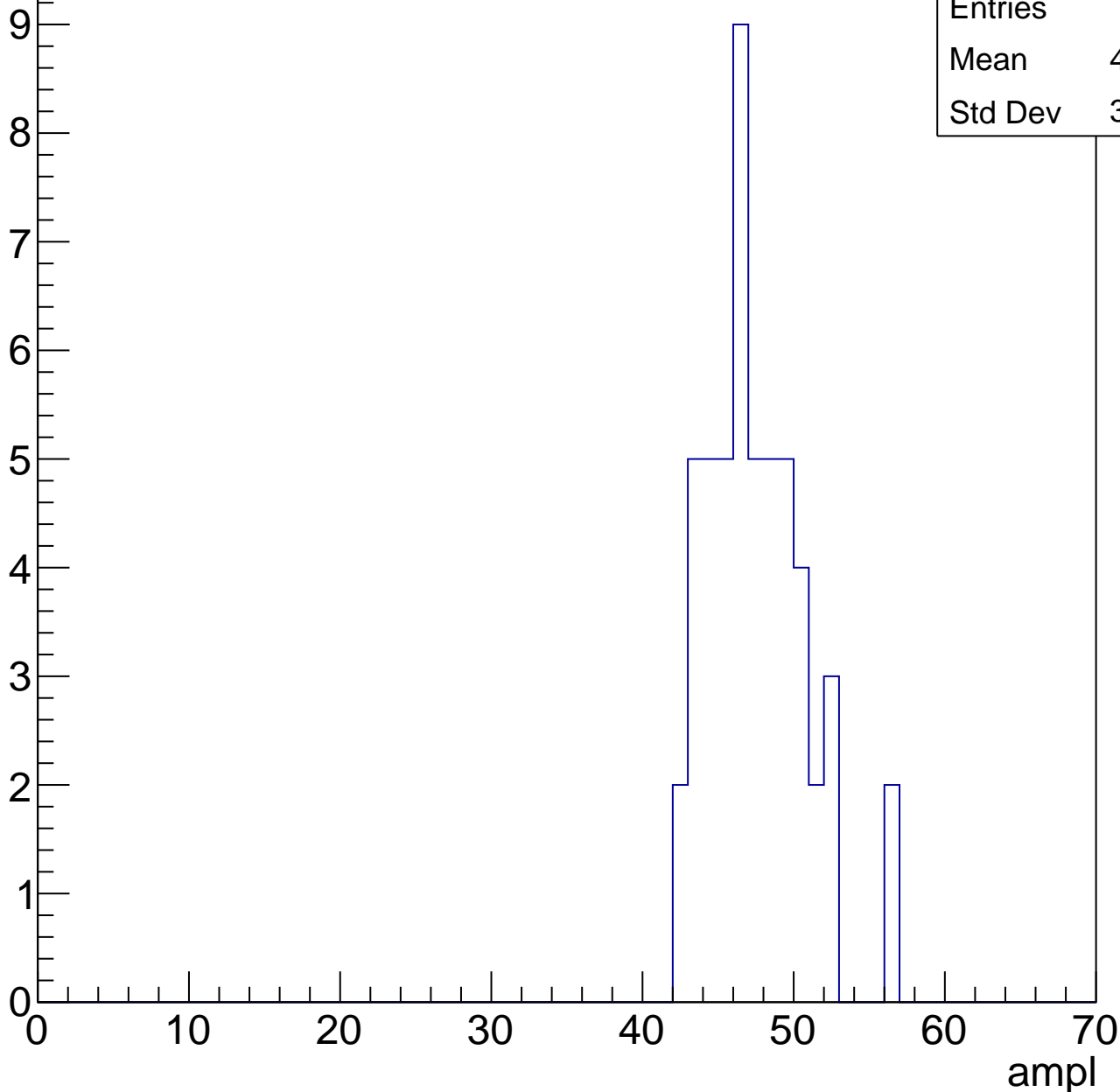


B1L103S, U24-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	47.08
Std Dev	3.204



B1L103S, U24-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	53.51
Std Dev	3.812

Entry

10

8

6

4

2

0

0

10

20

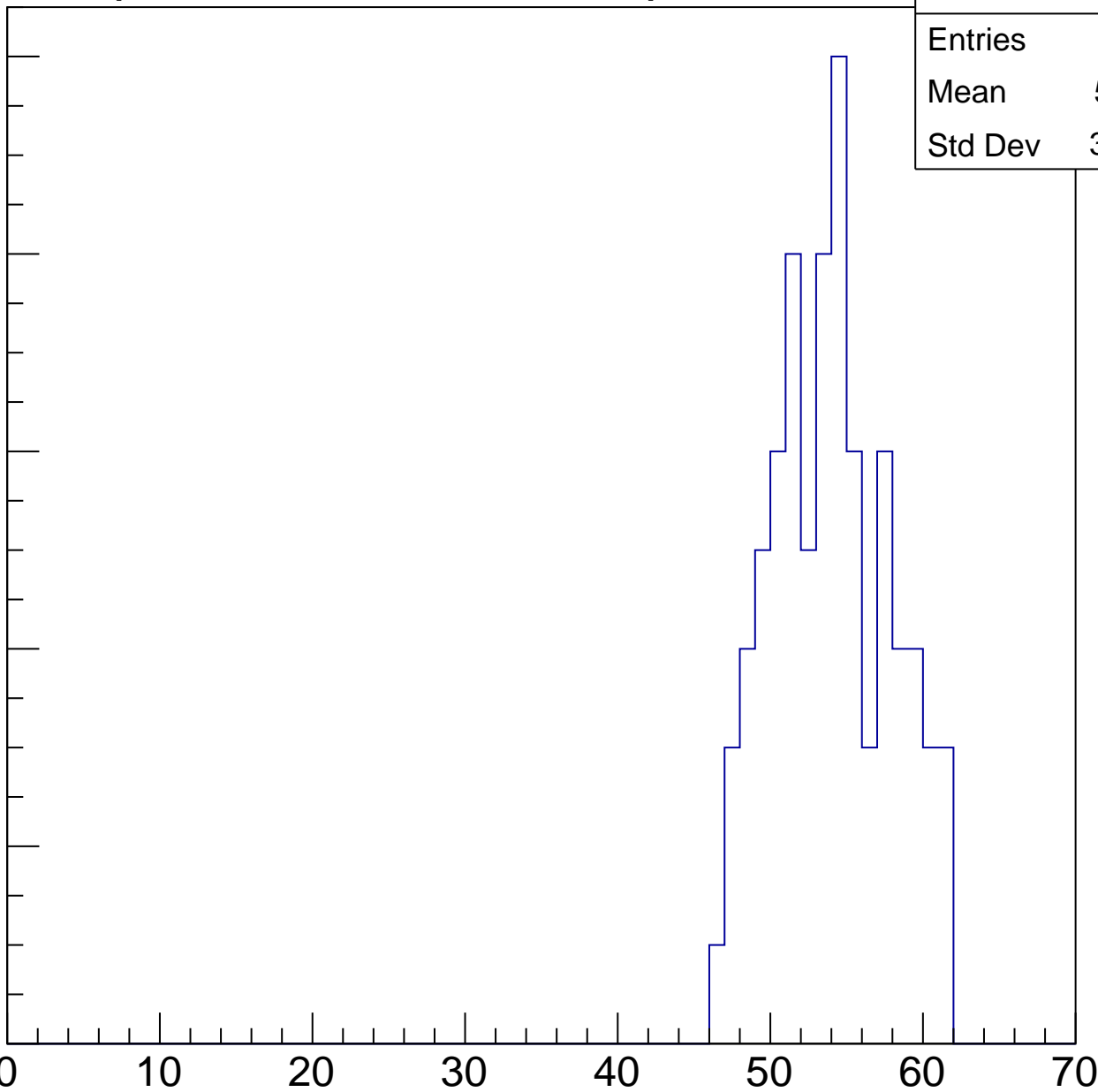
30

40

50

60

ampl

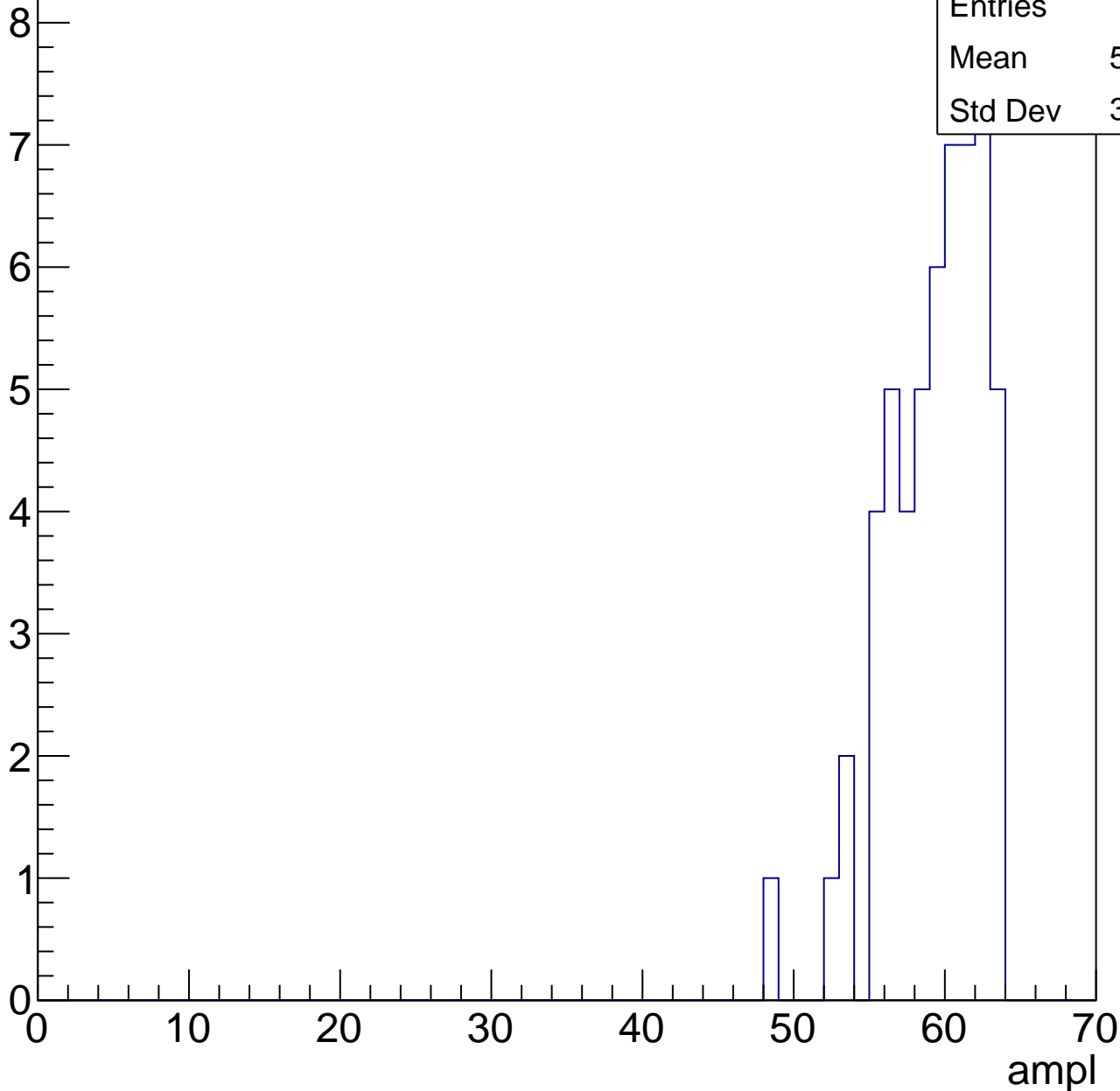


B1L103S, U24-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.84
Std Dev	3.184

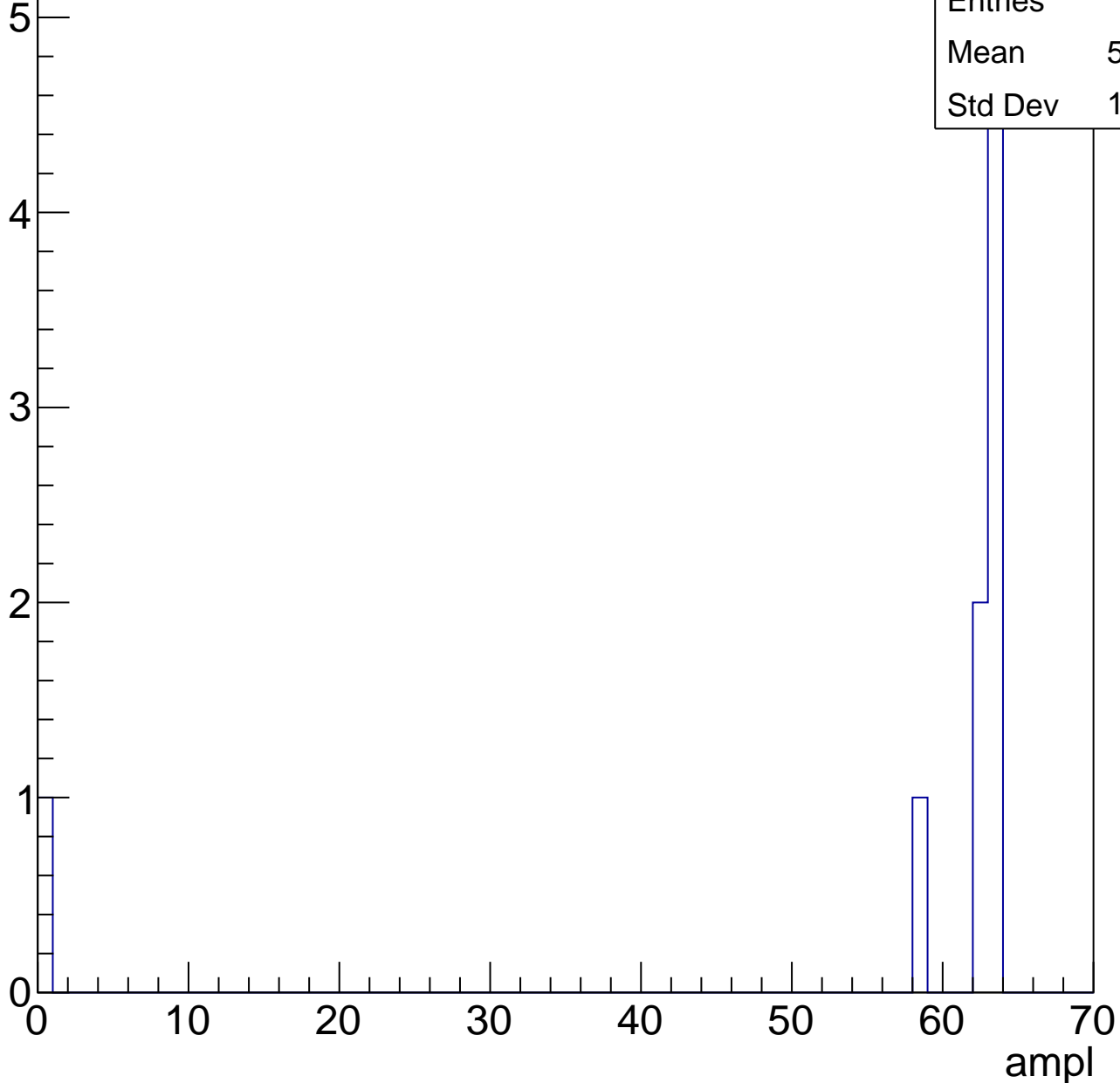


B1L103S, U24-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	55.22
Std Dev	19.58



B1L103S, U24-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch7, adc0

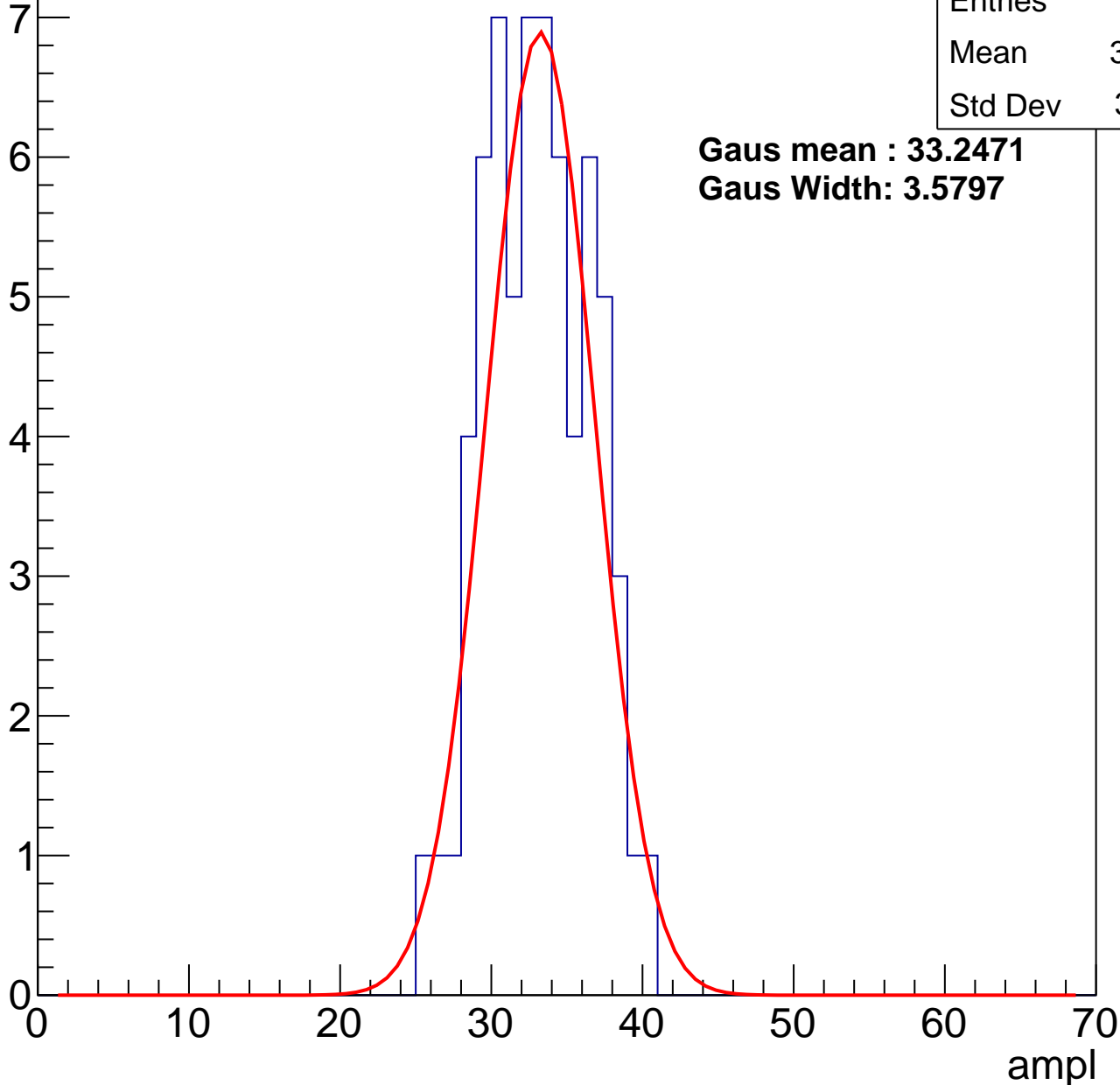
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	32.65
Std Dev	3.381

Gaus mean : 33.2471

Gaus Width: 3.5797



B1L103S, U24-ch7, adc1

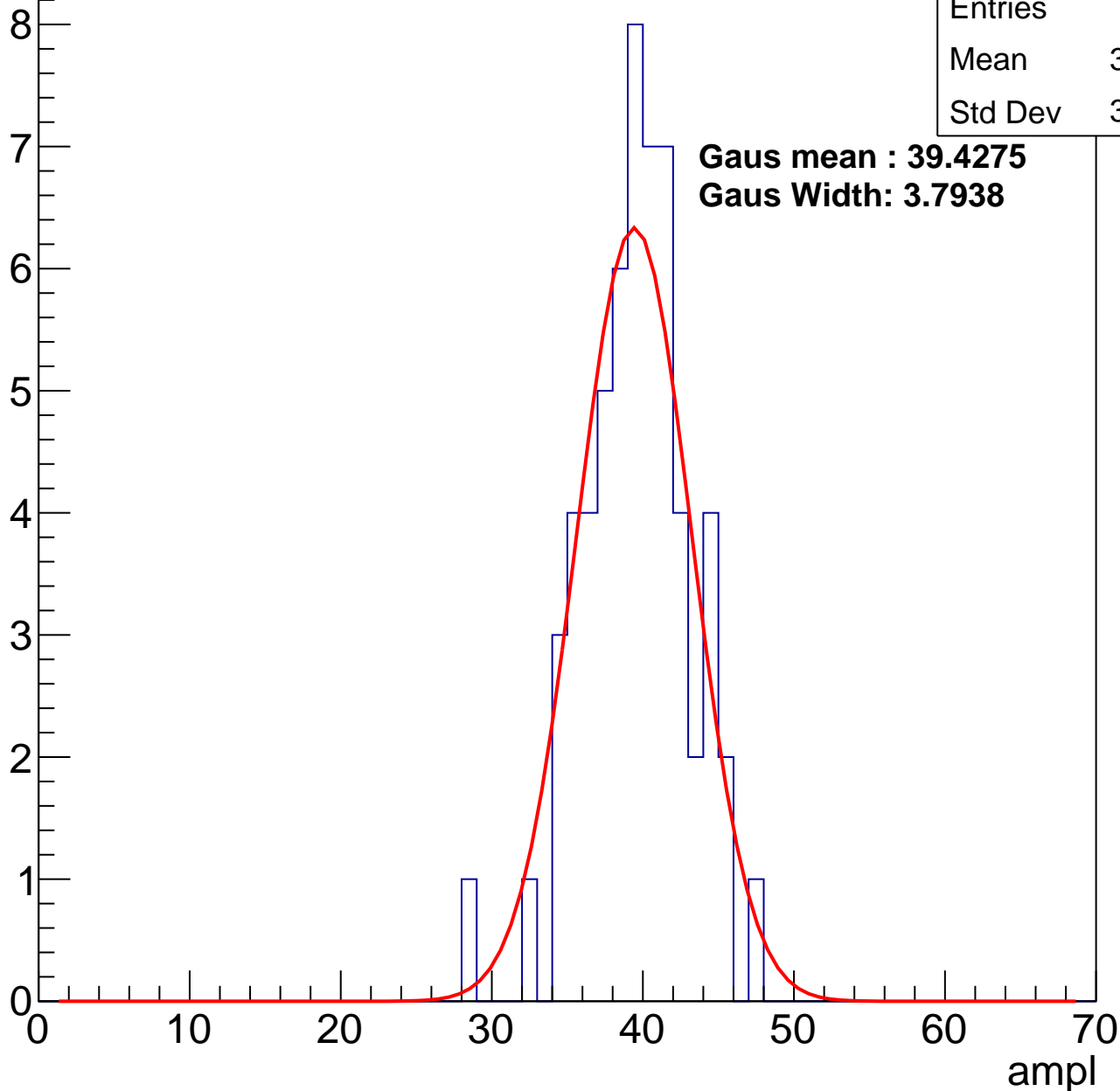
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	39.07
Std Dev	3.468

Gaus mean : 39.4275

Gaus Width: 3.7938



B1L103S, U24-ch7, adc2

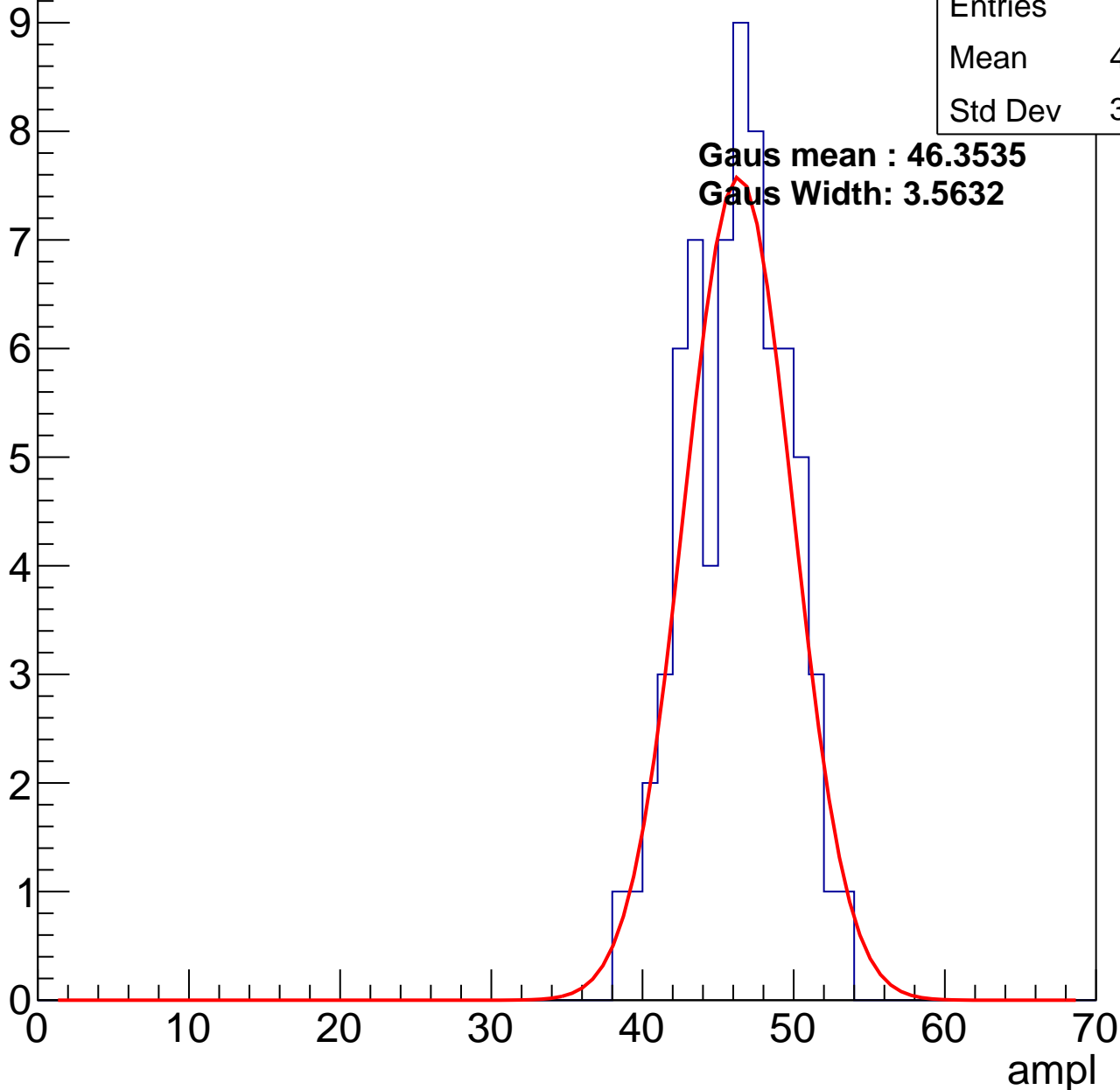
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	45.77
Std Dev	3.304

Gaus mean : 46.3535

Gaus Width: 3.5632

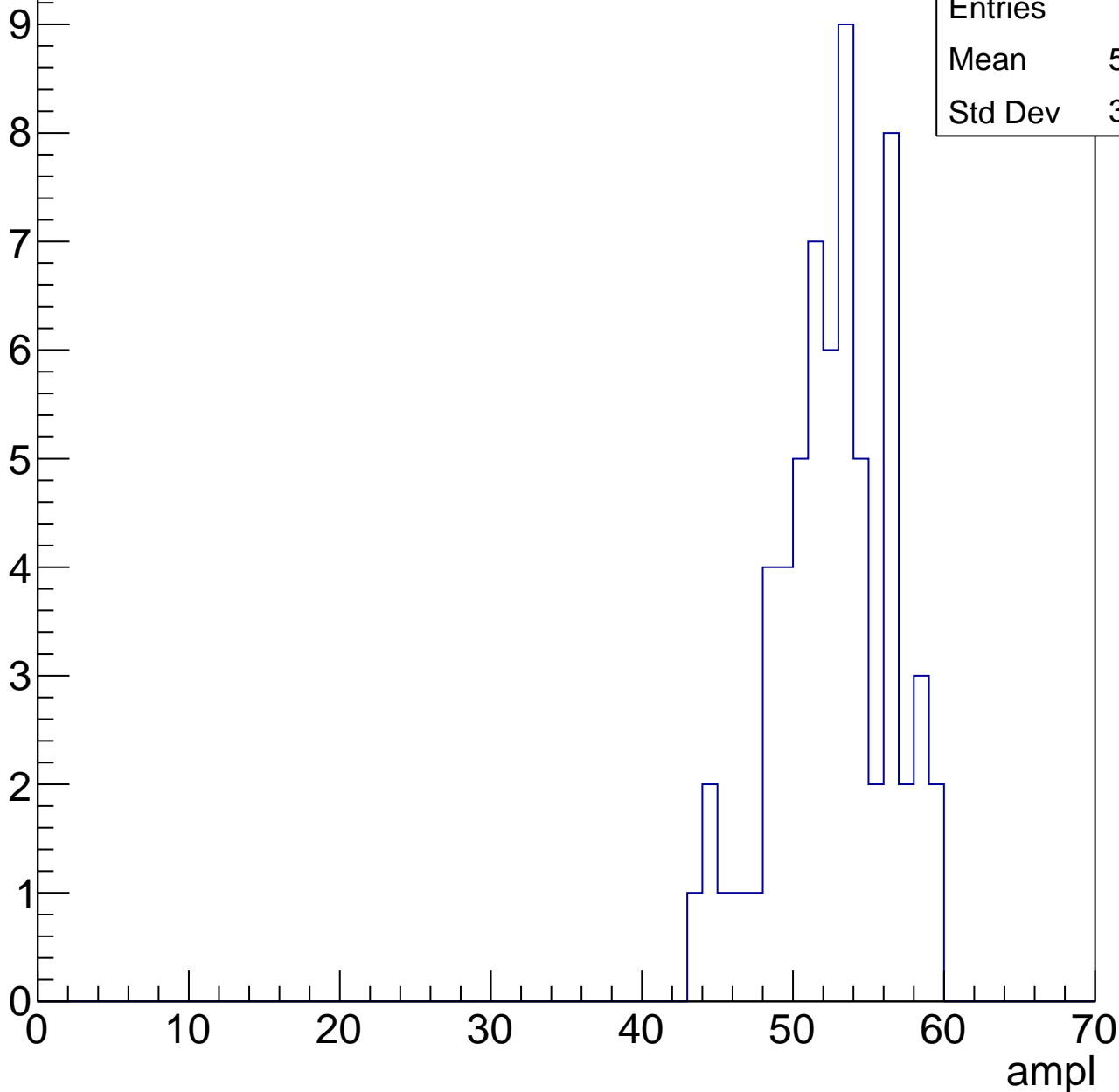


B1L103S, U24-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	52.17
Std Dev	3.727

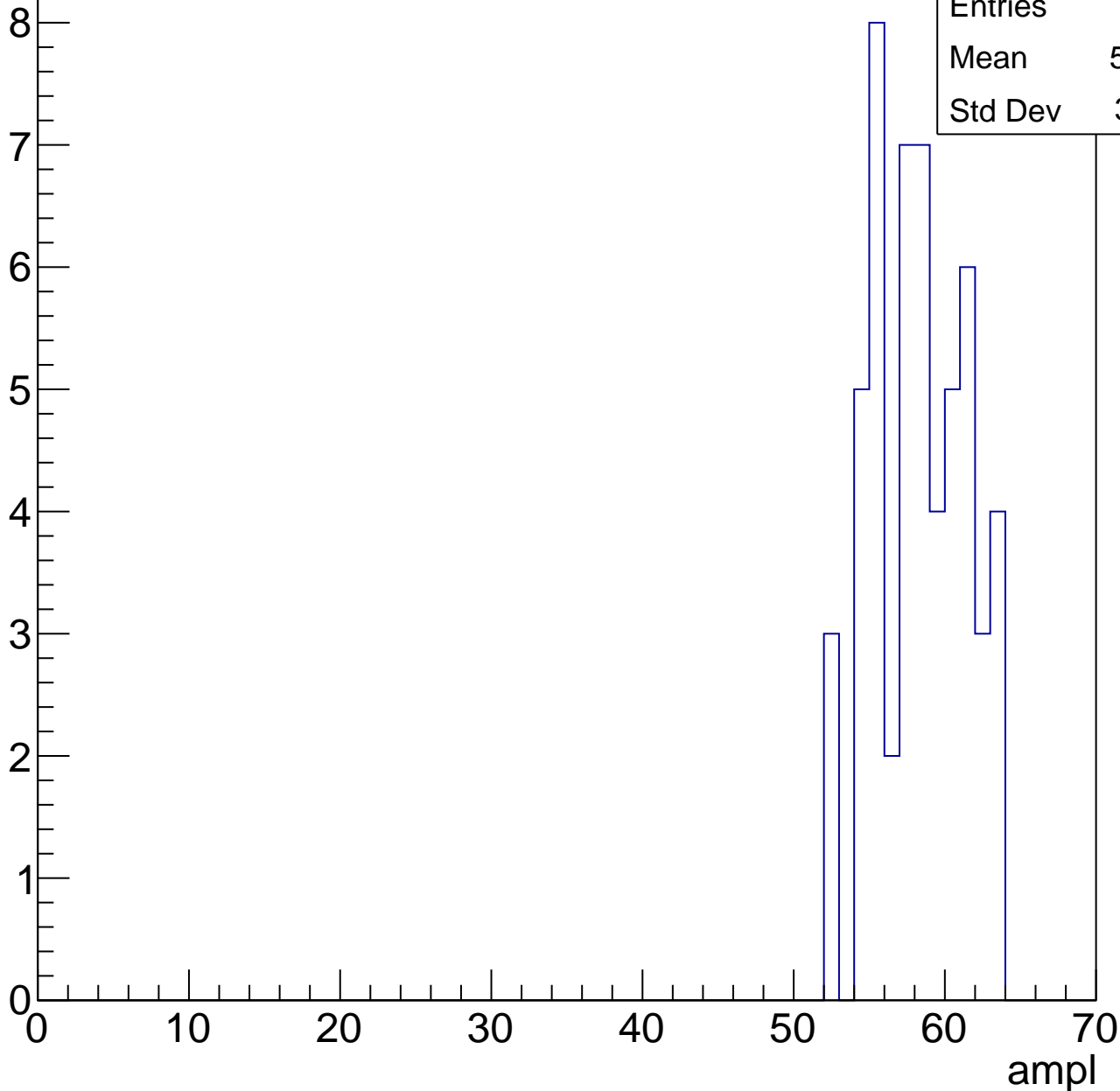


B1L103S, U24-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	57.83
Std Dev	3.041

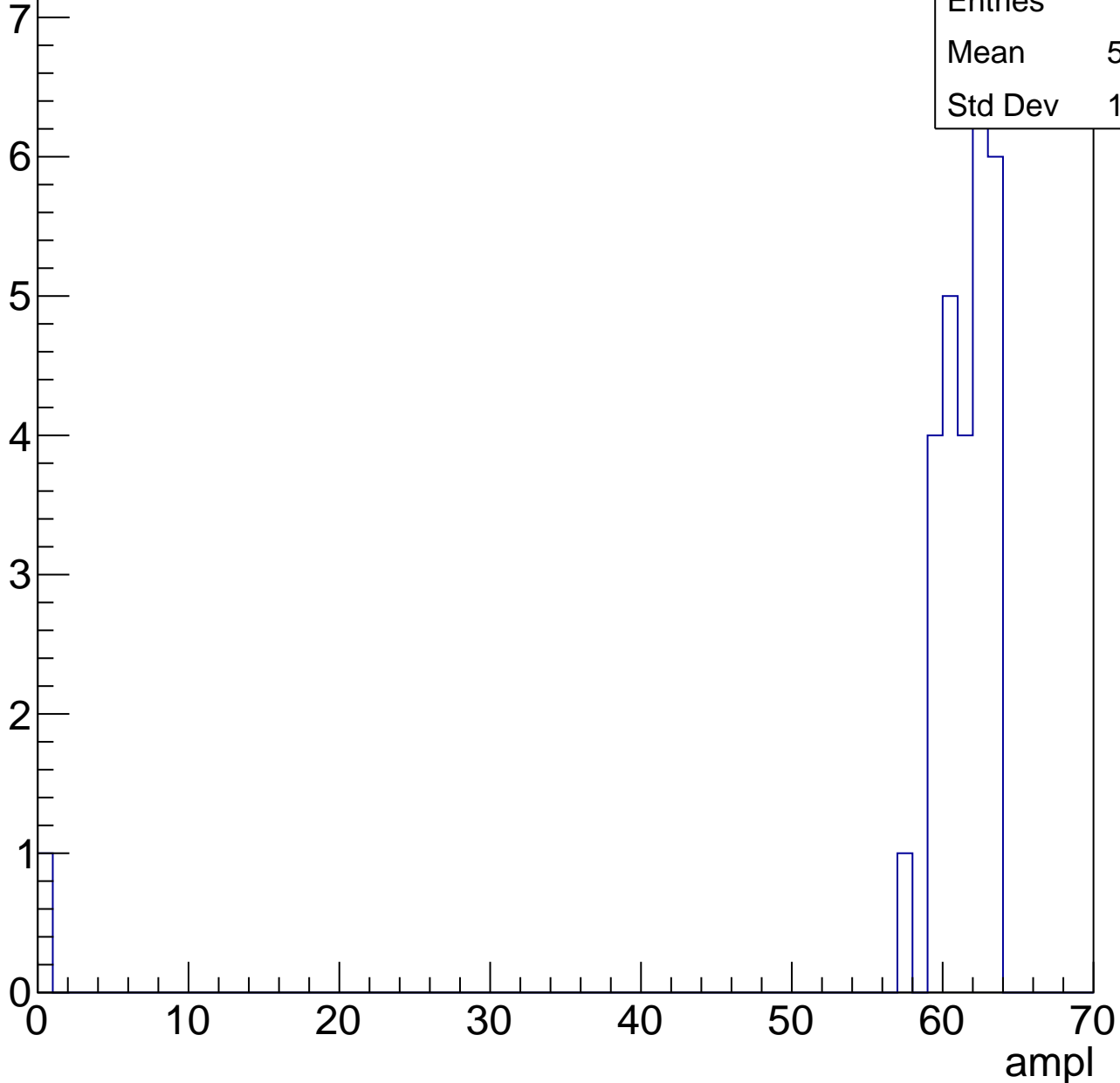


B1L103S, U24-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	58.89
Std Dev	11.44



B1L103S, U24-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L103S, U24-ch8, adc0

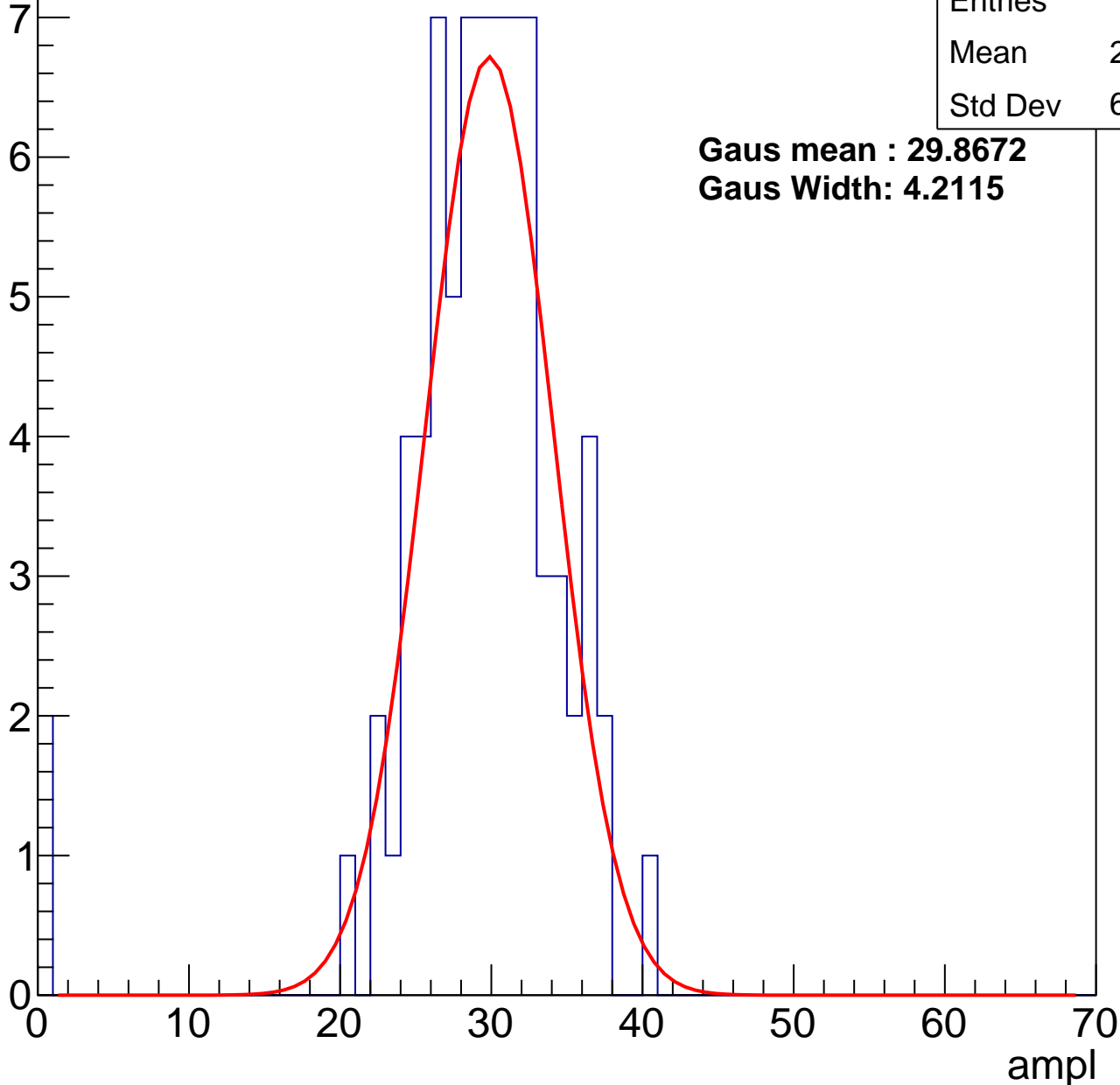
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.67
Std Dev	6.157

Gaus mean : 29.8672

Gaus Width: 4.2115



B1L103S, U24-ch8, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	36.23
Std Dev	3.727

Gaus mean : 36.9714

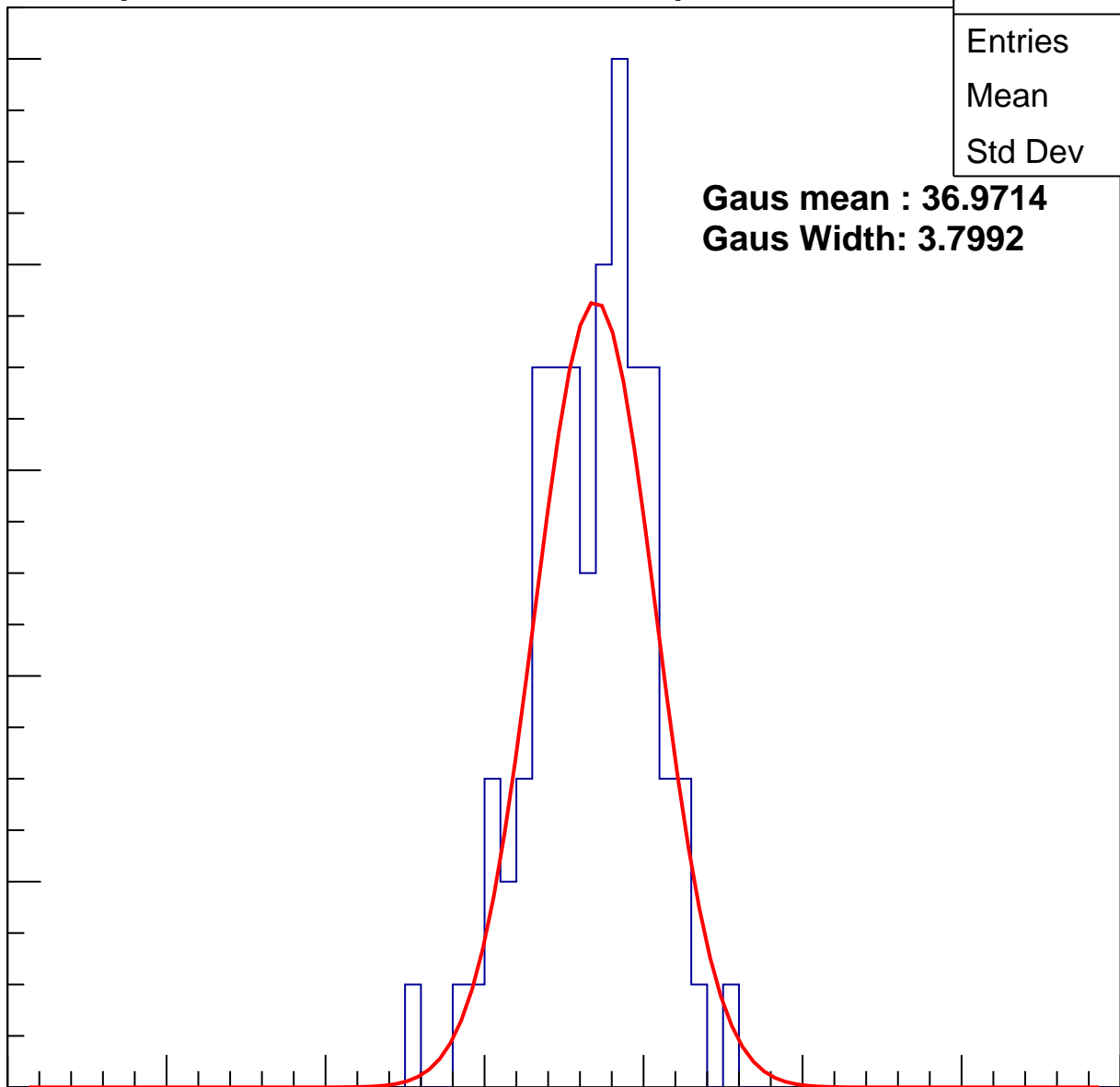
Gaus Width: 3.7992

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch8, adc2

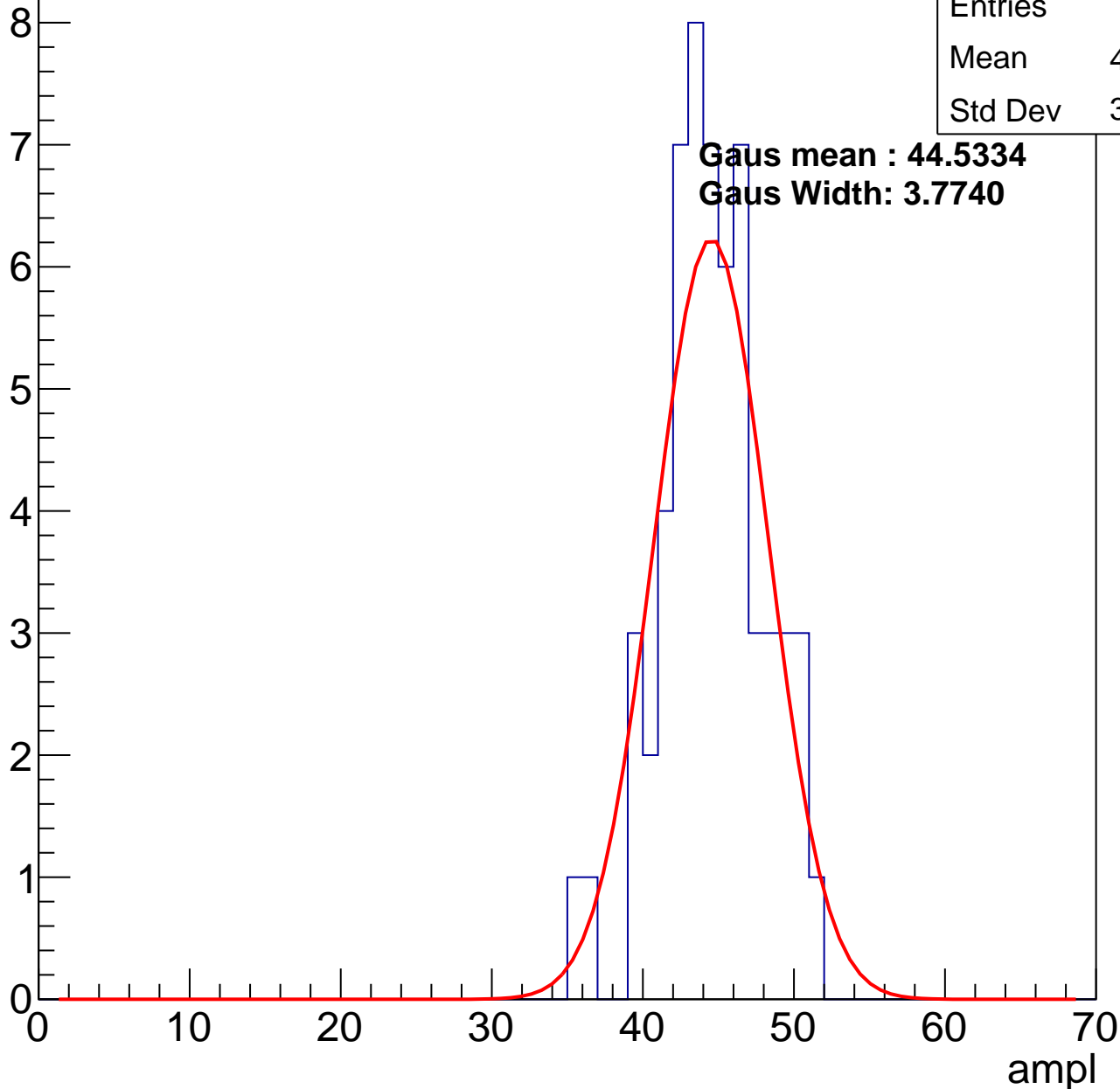
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	44.12
Std Dev	3.355

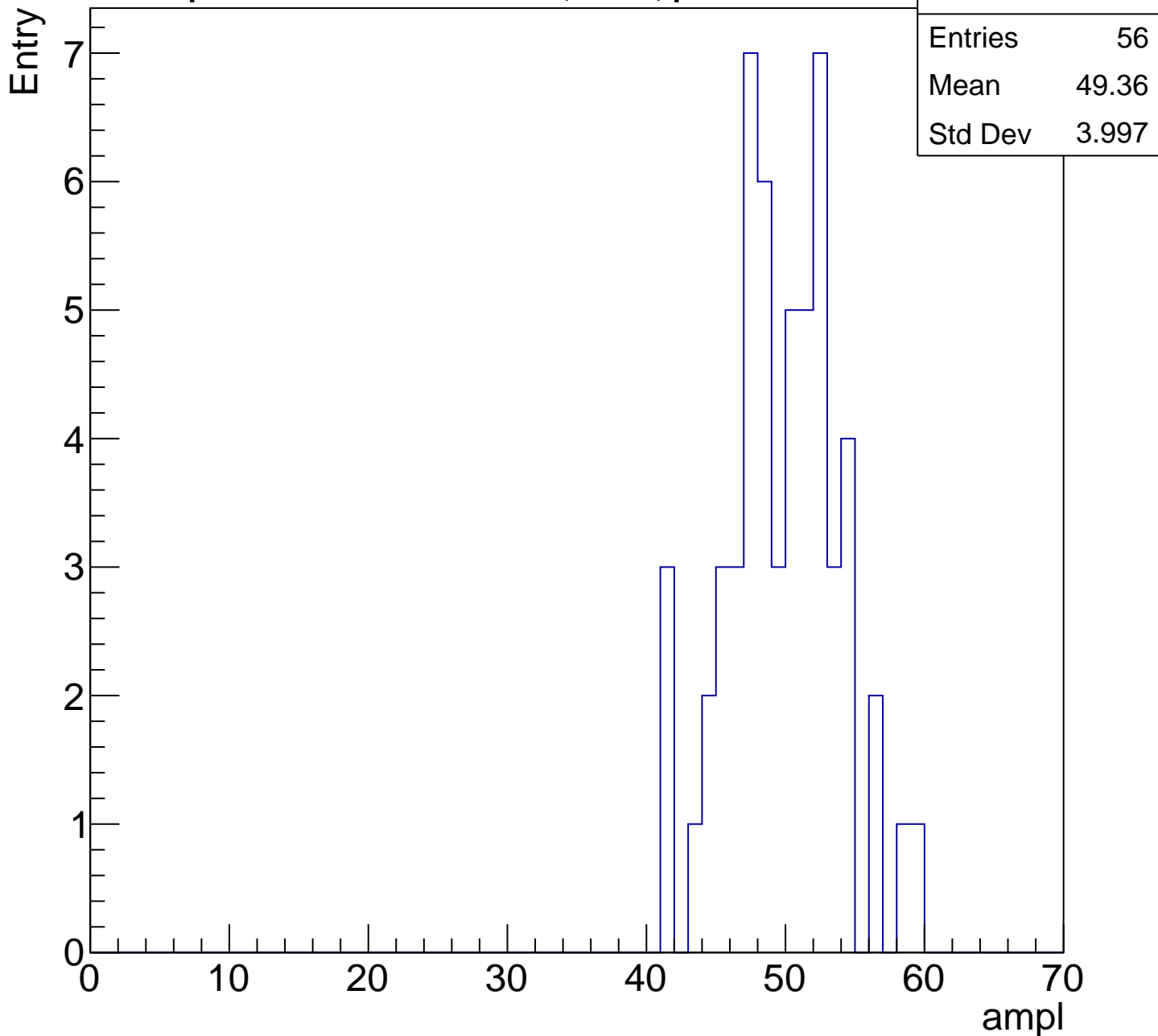
Gaus mean : 44.5334

Gaus Width: 3.7740



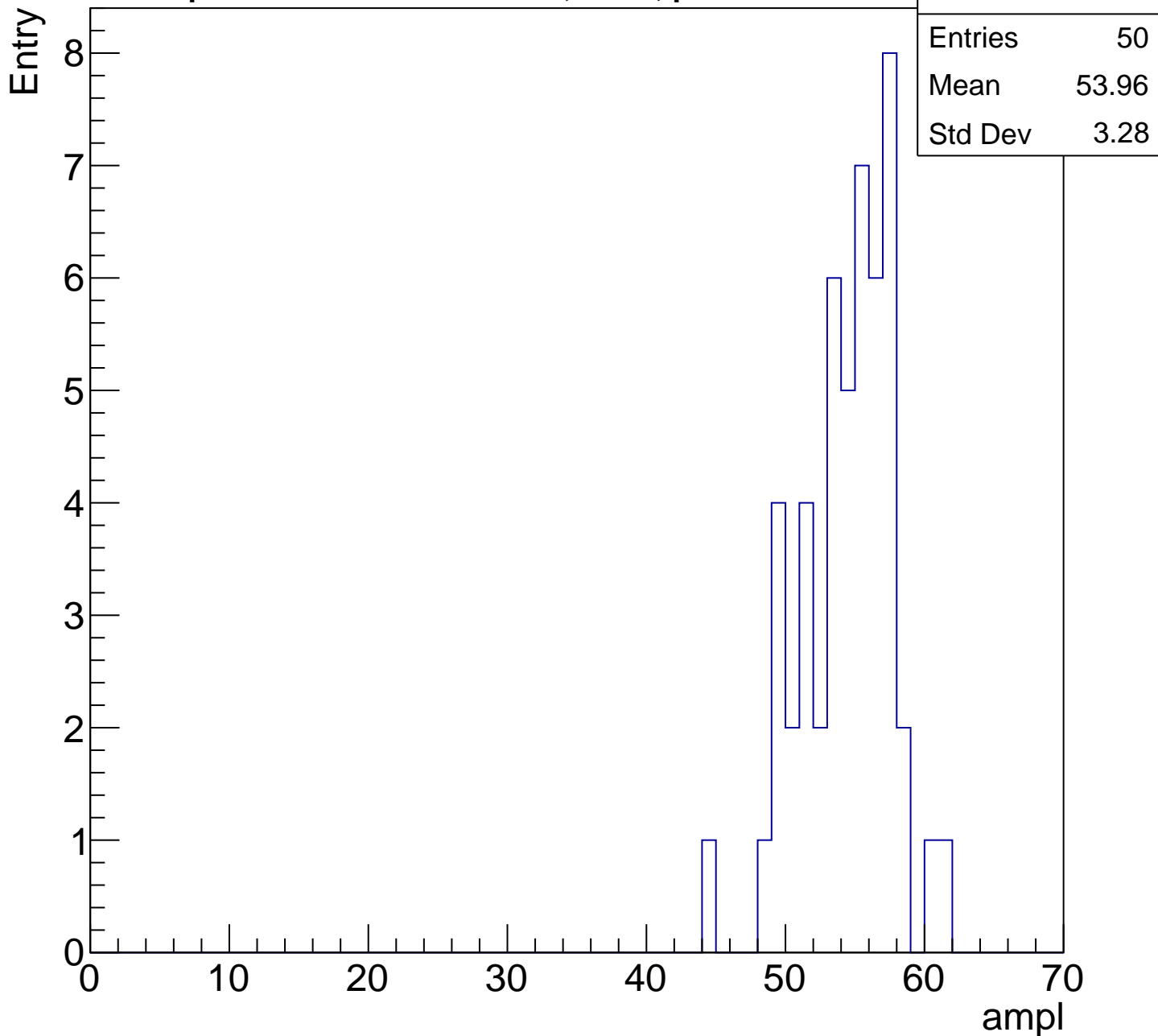
B1L103S, U24-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch8, adc5

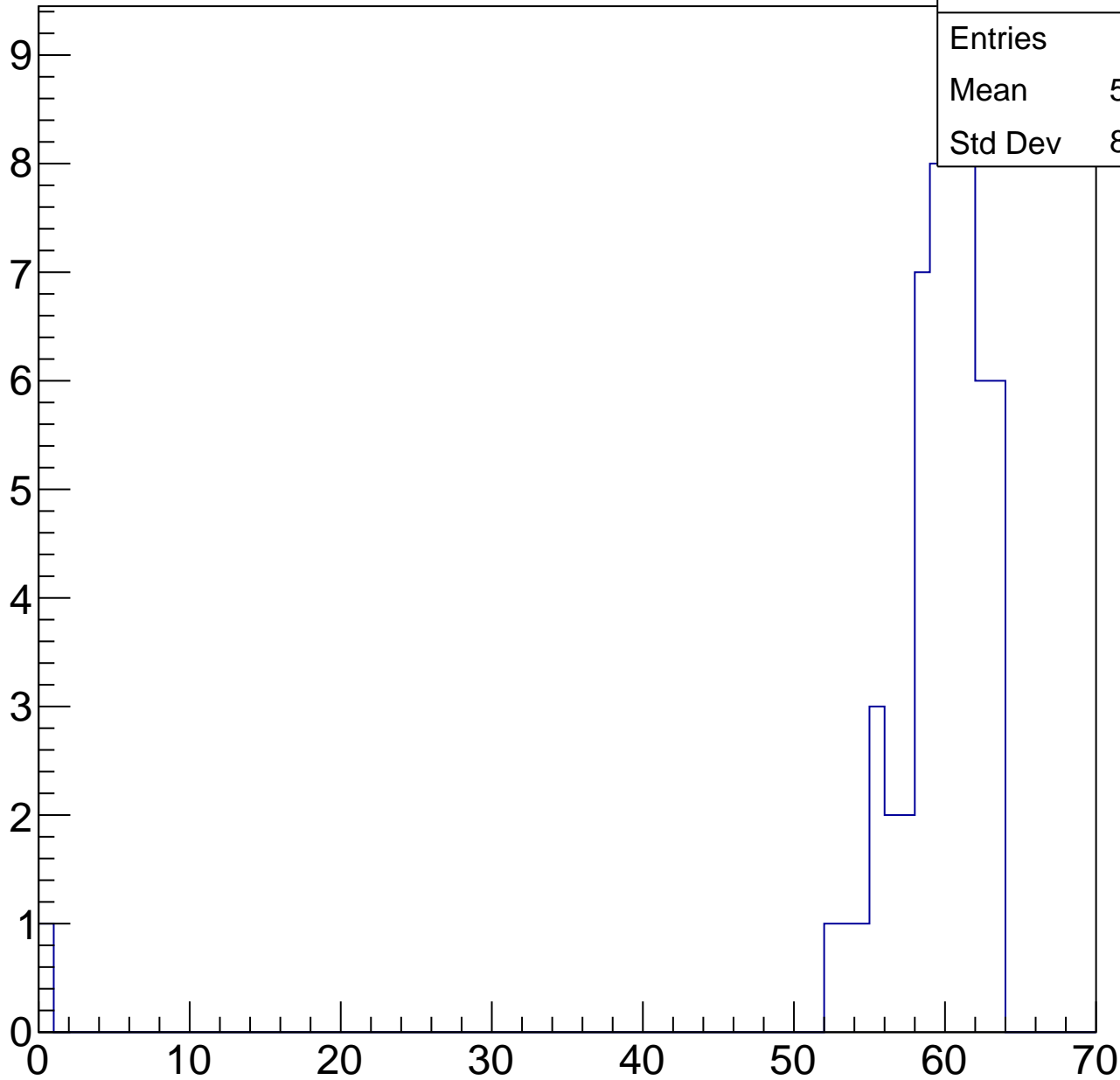
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.34
Std Dev	8.284

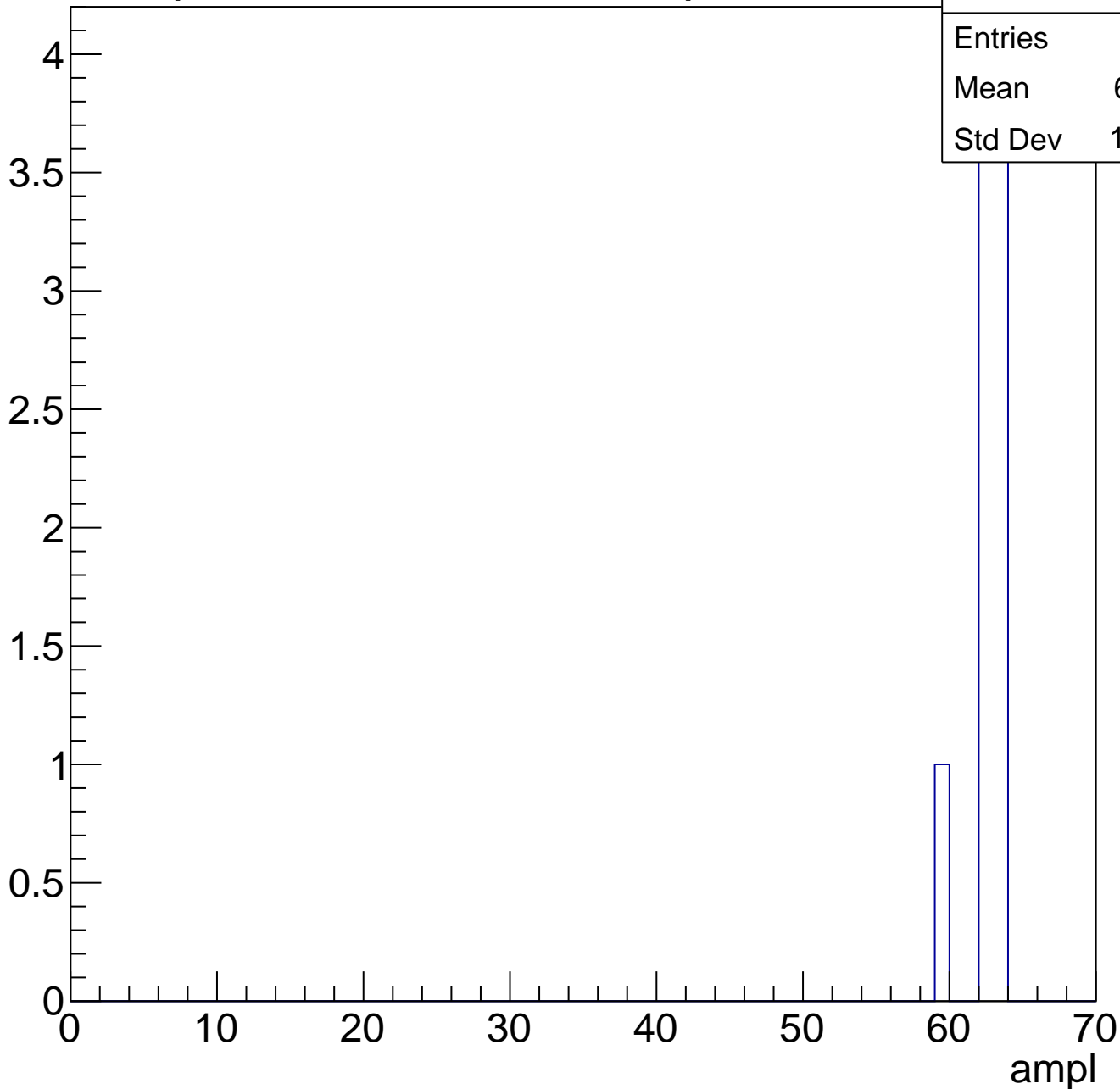
ampl



B1L103S, U24-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch9, adc0

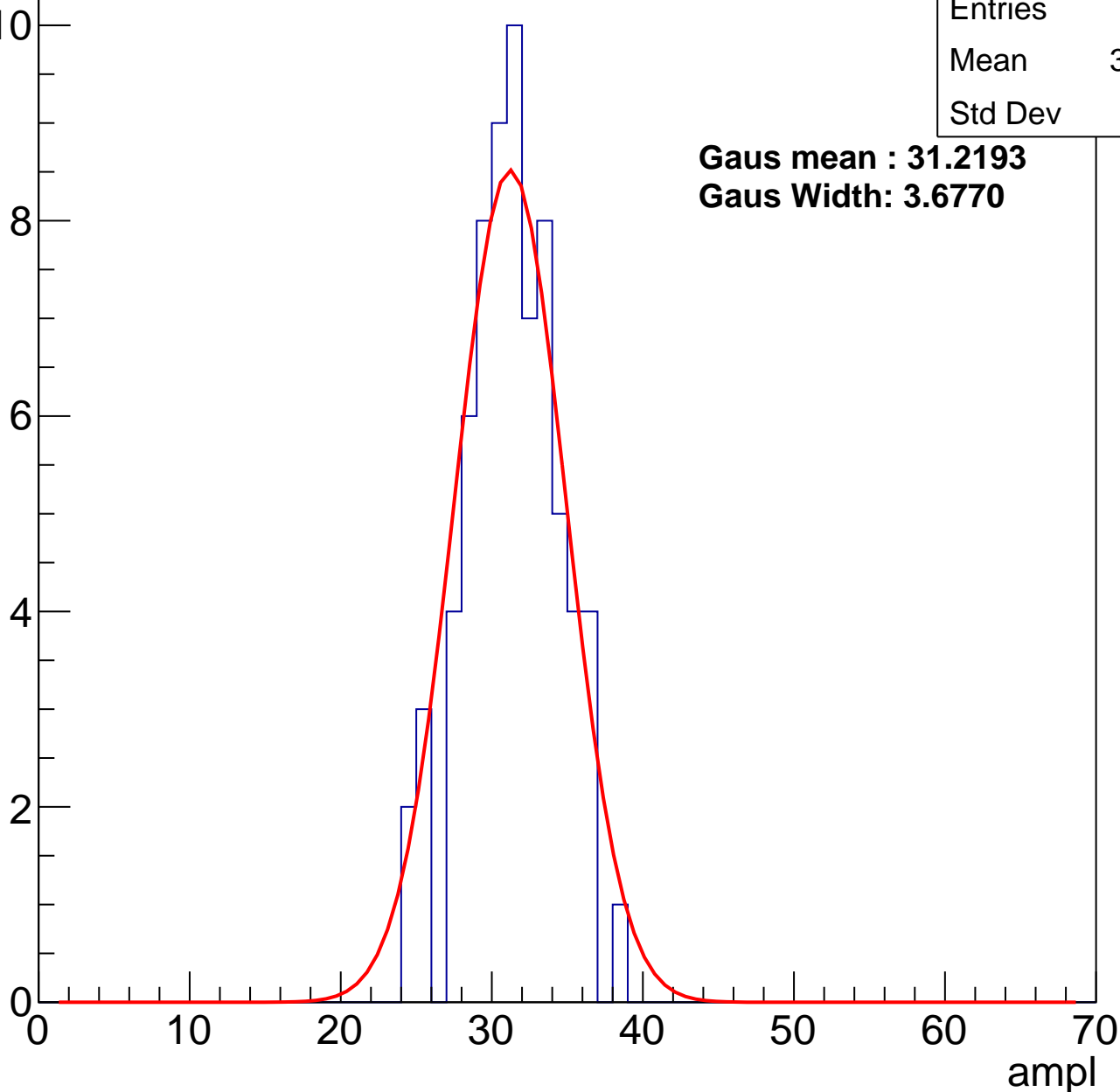
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.86
Std Dev	3.05

Gaus mean : 31.2193

Gaus Width: 3.6770



B1L103S, U24-ch9, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	38.1
Std Dev	3.107

Gaus mean : 38.6609

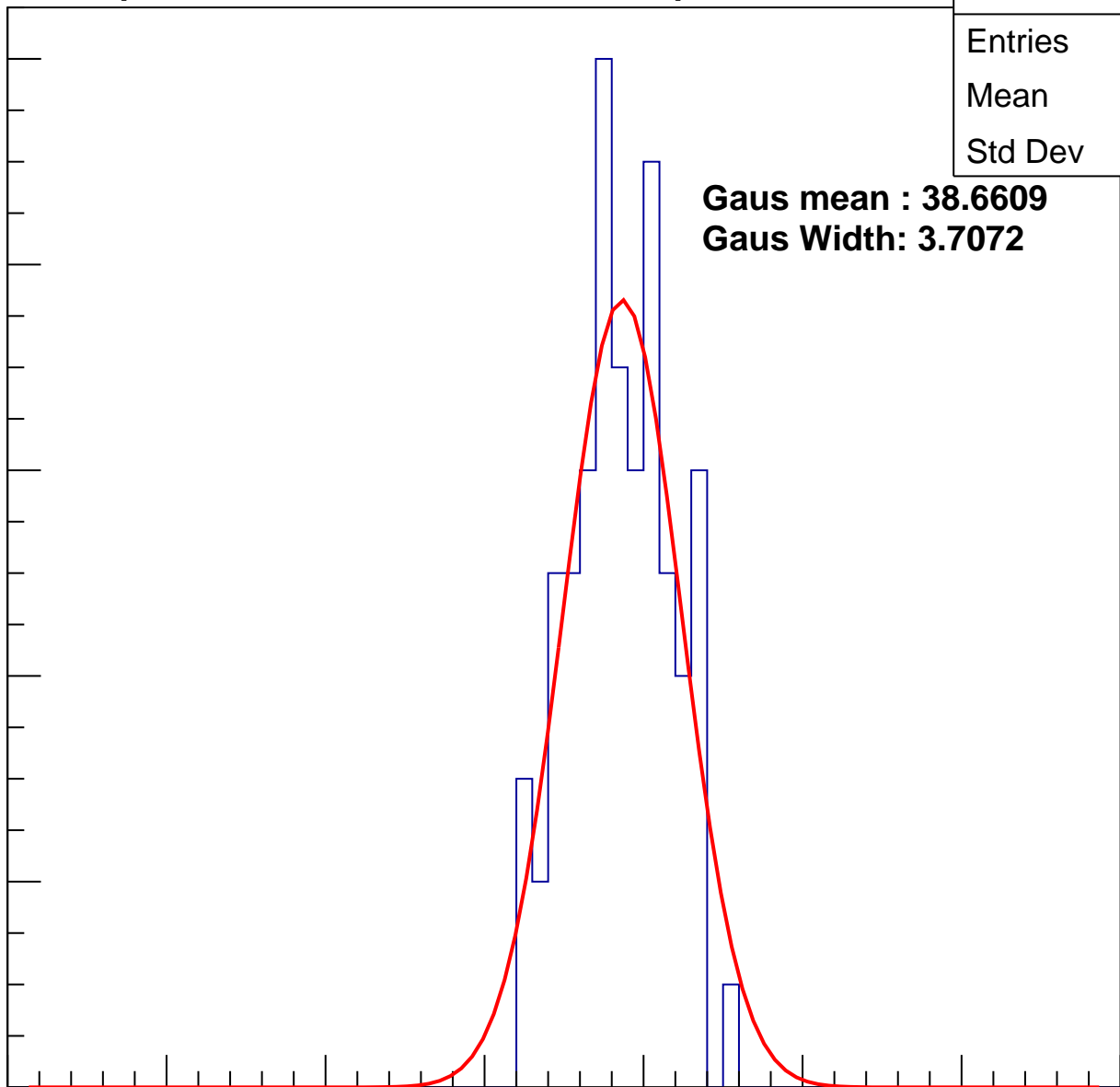
Gaus Width: 3.7072

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch9, adc2

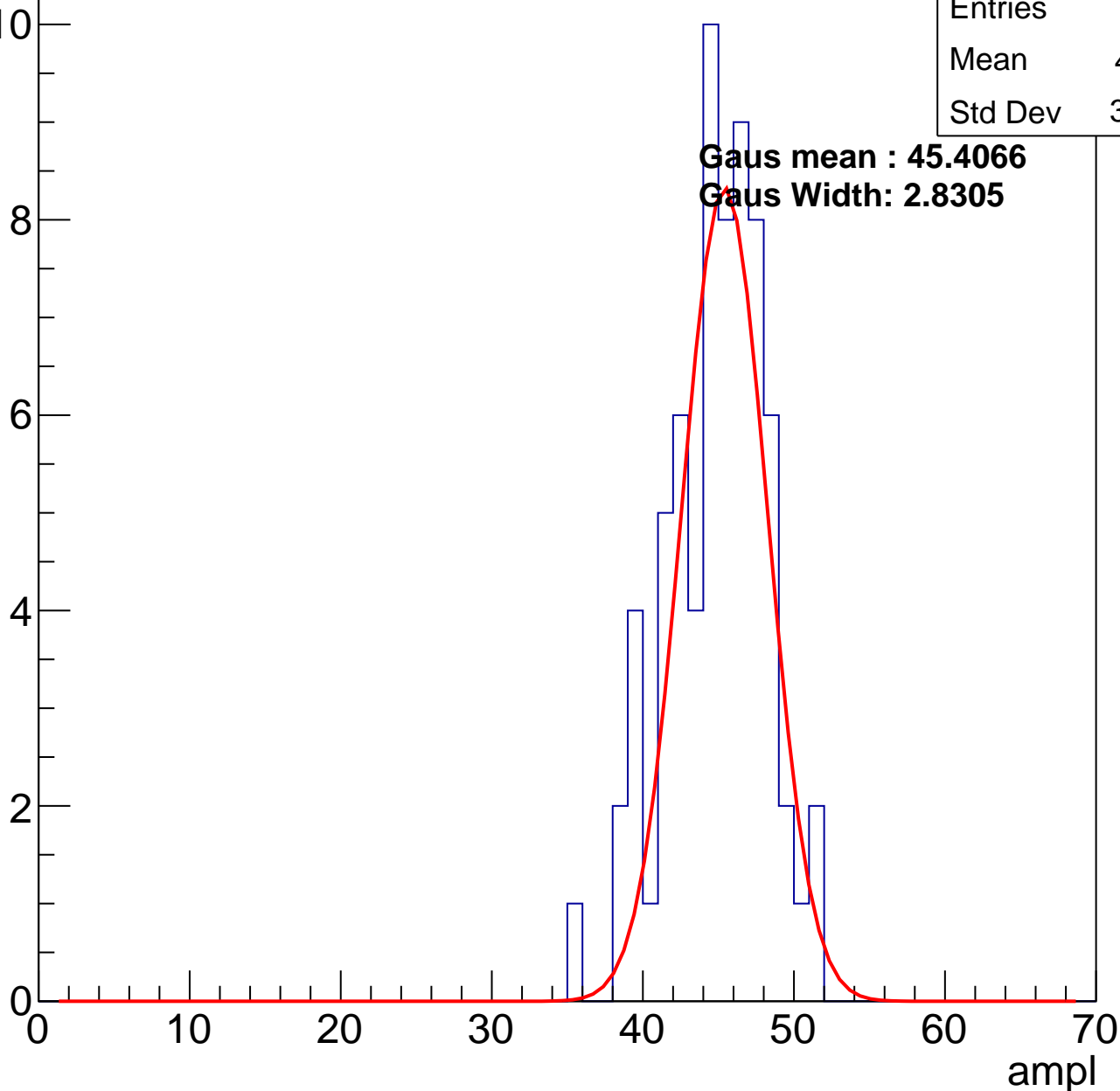
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	44.41
Std Dev	3.245

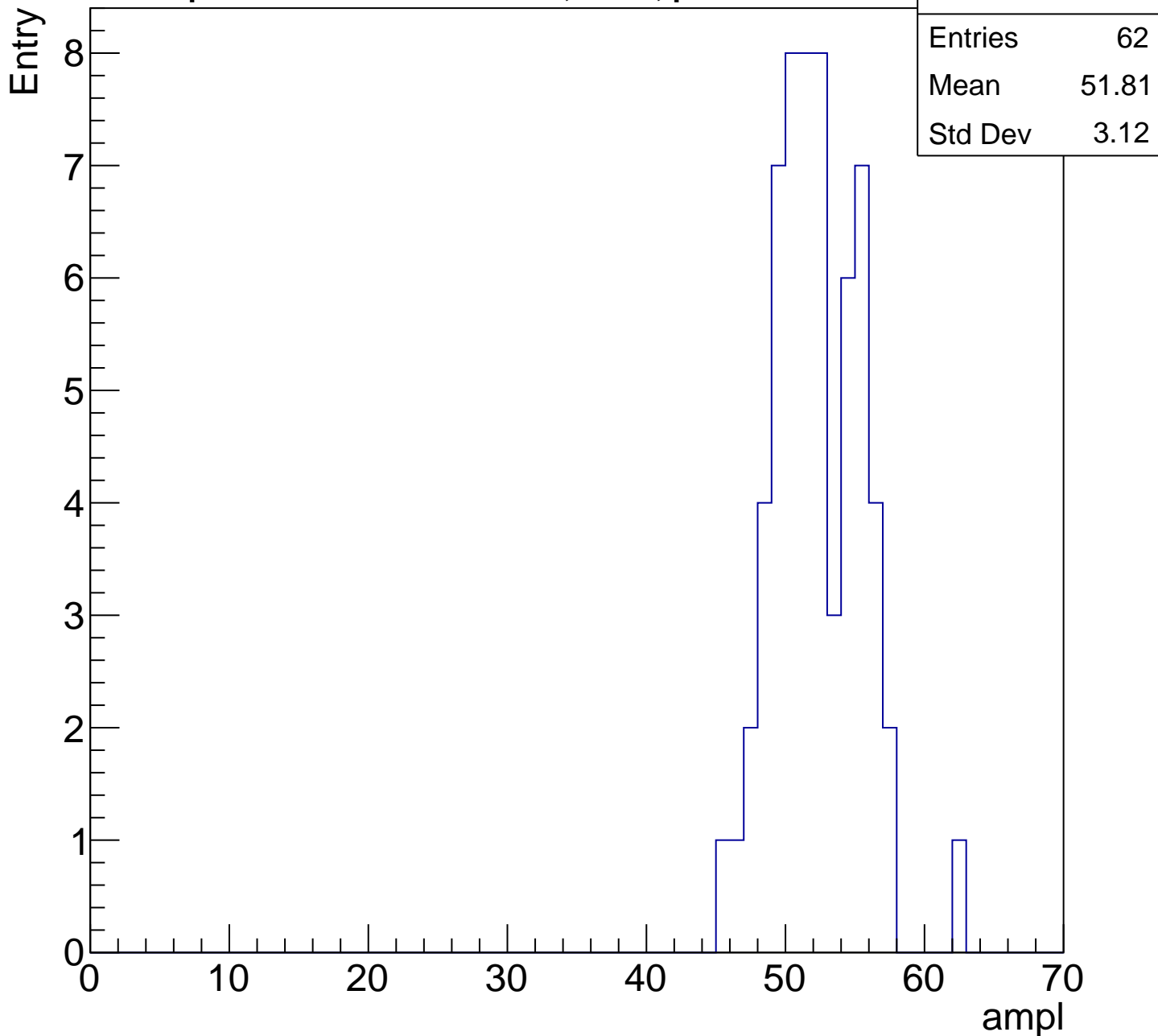
Gaus mean : 45.4066

Gaus Width: 2.8305



B1L103S, U24-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

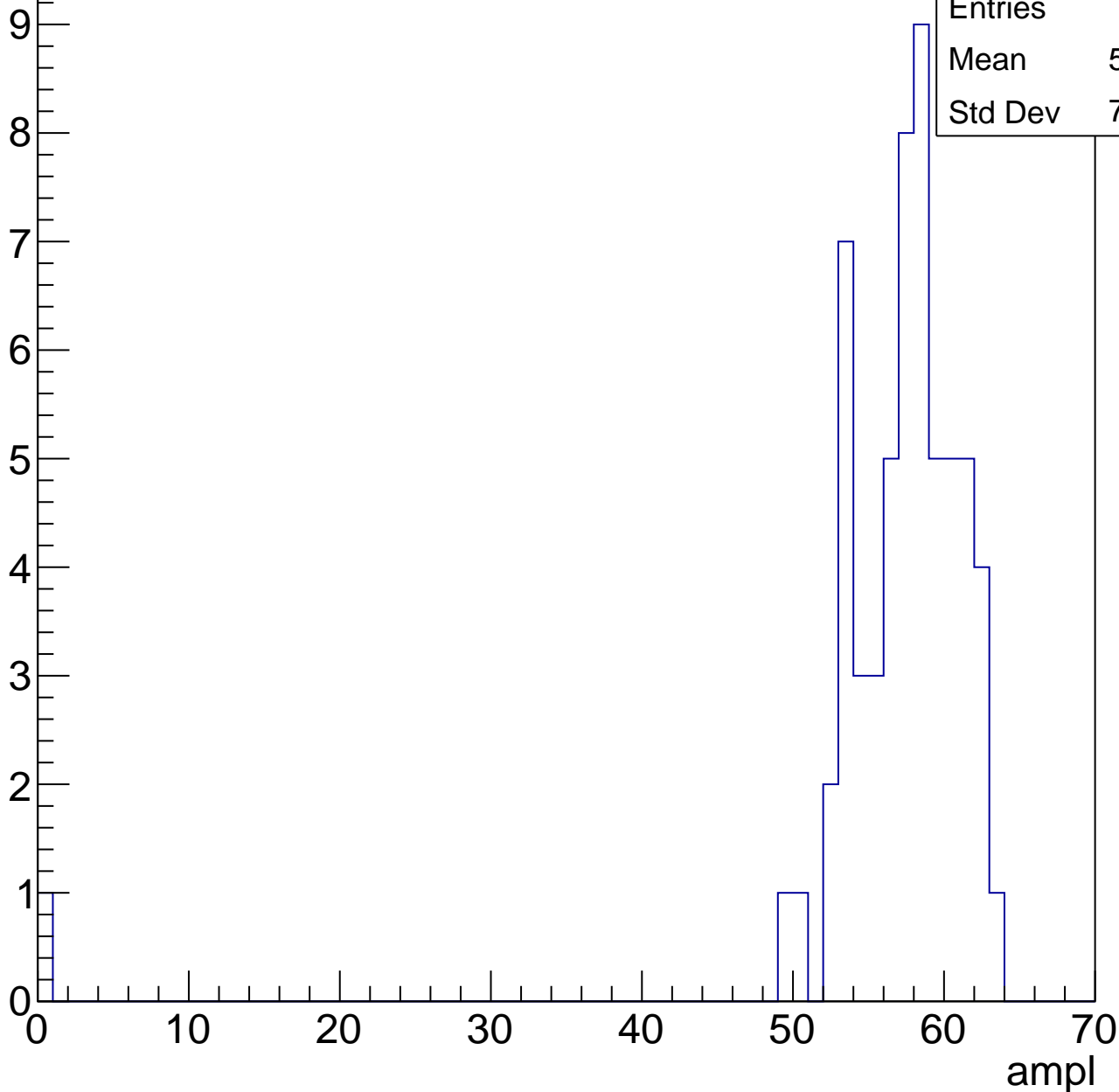


B1L103S, U24-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.17
Std Dev	7.975

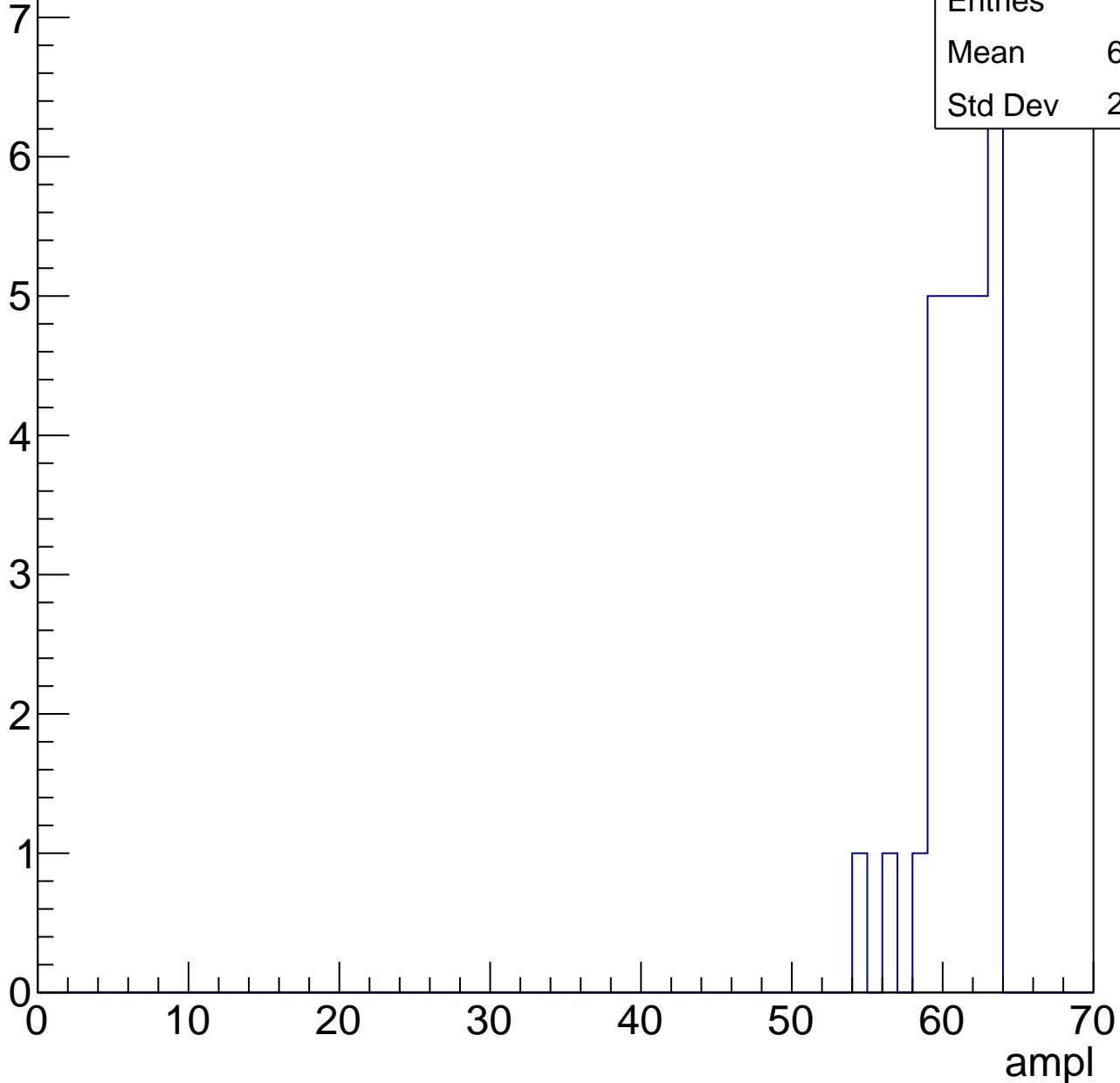


B1L103S, U24-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	60.63
Std Dev	2.137



B1L103S, U24-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L103S, U24-ch10, adc0

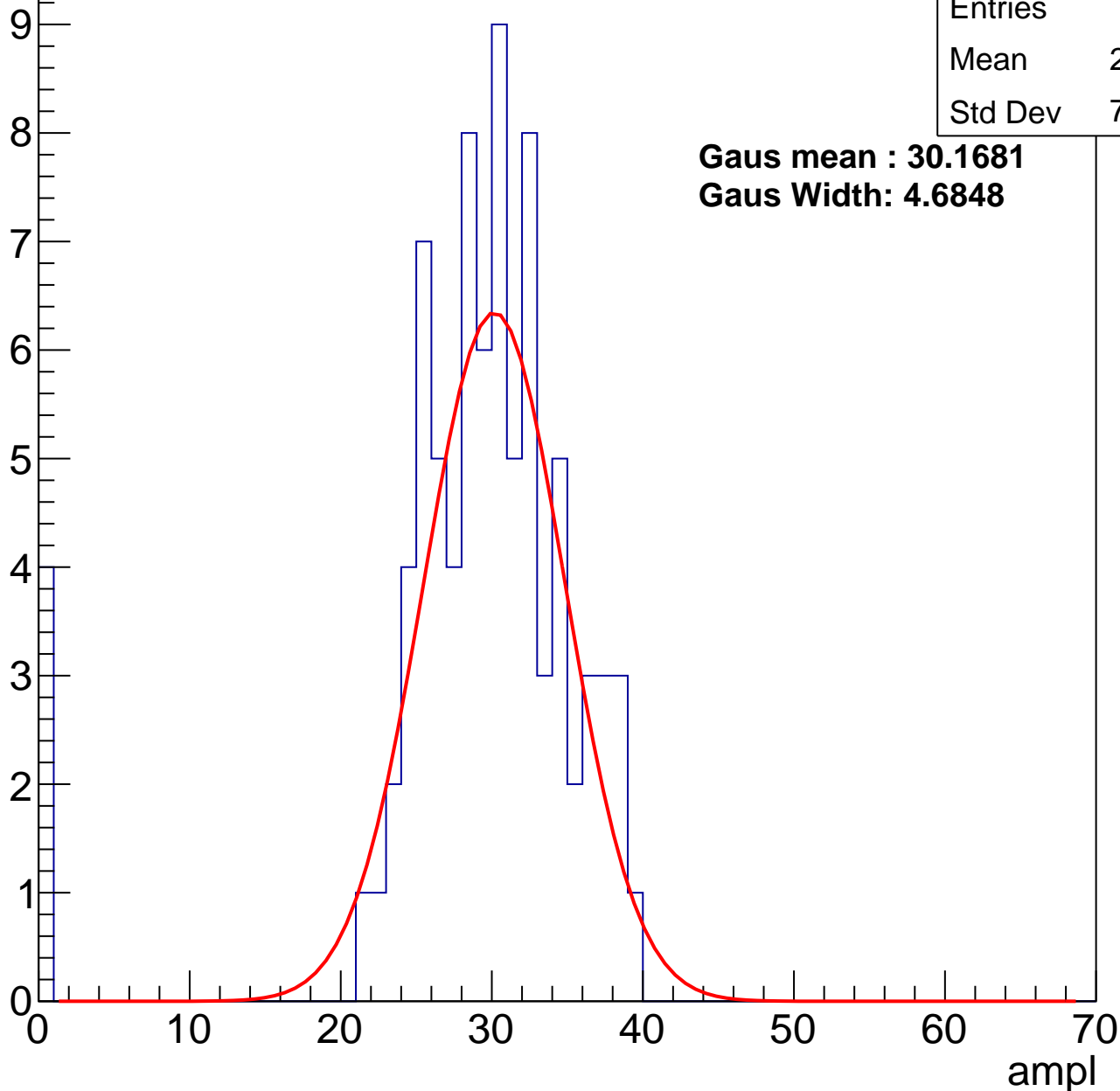
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	28.43
Std Dev	7.583

Gaus mean : 30.1681

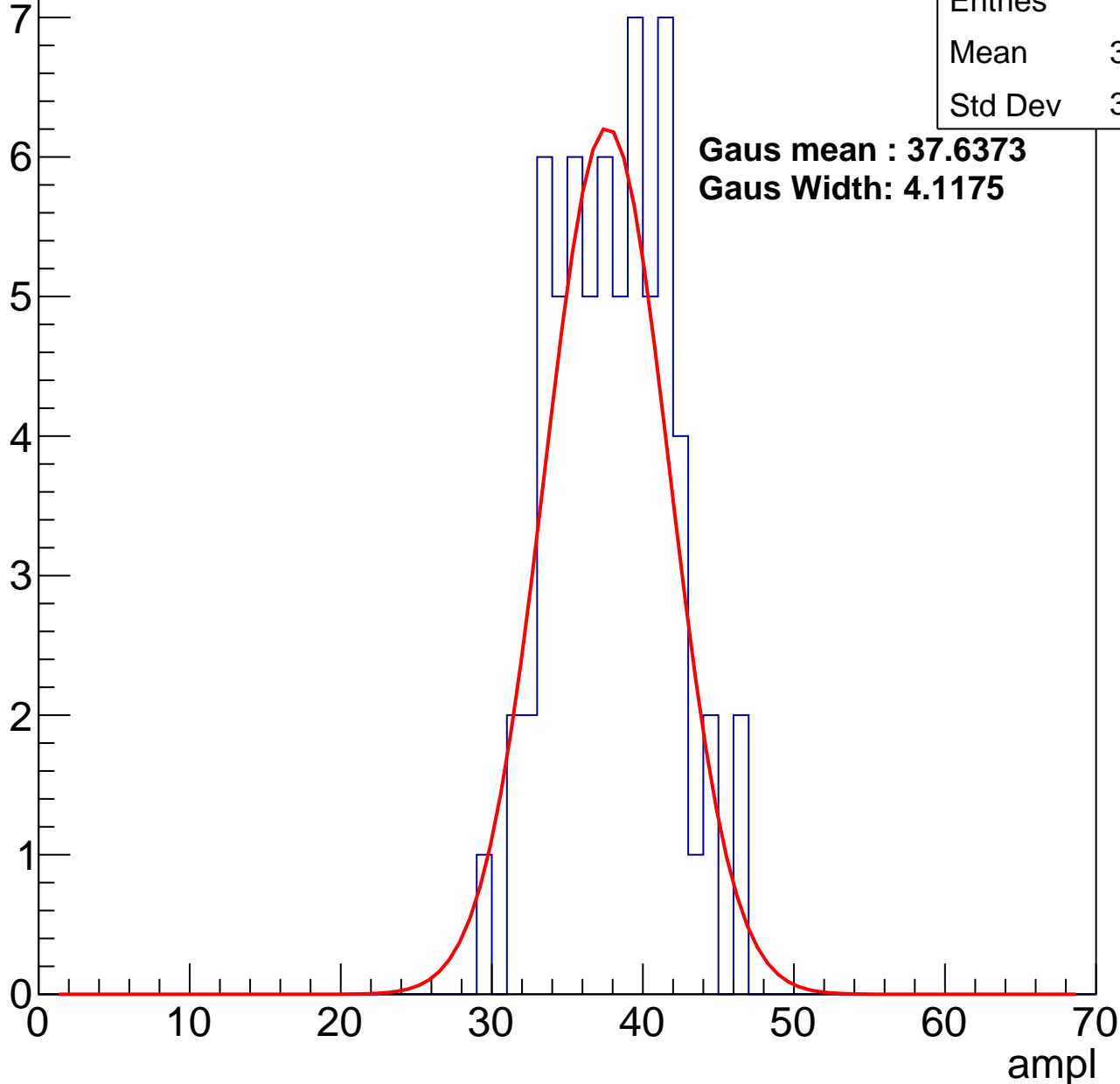
Gaus Width: 4.6848



B1L103S, U24-ch10, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch10, adc2

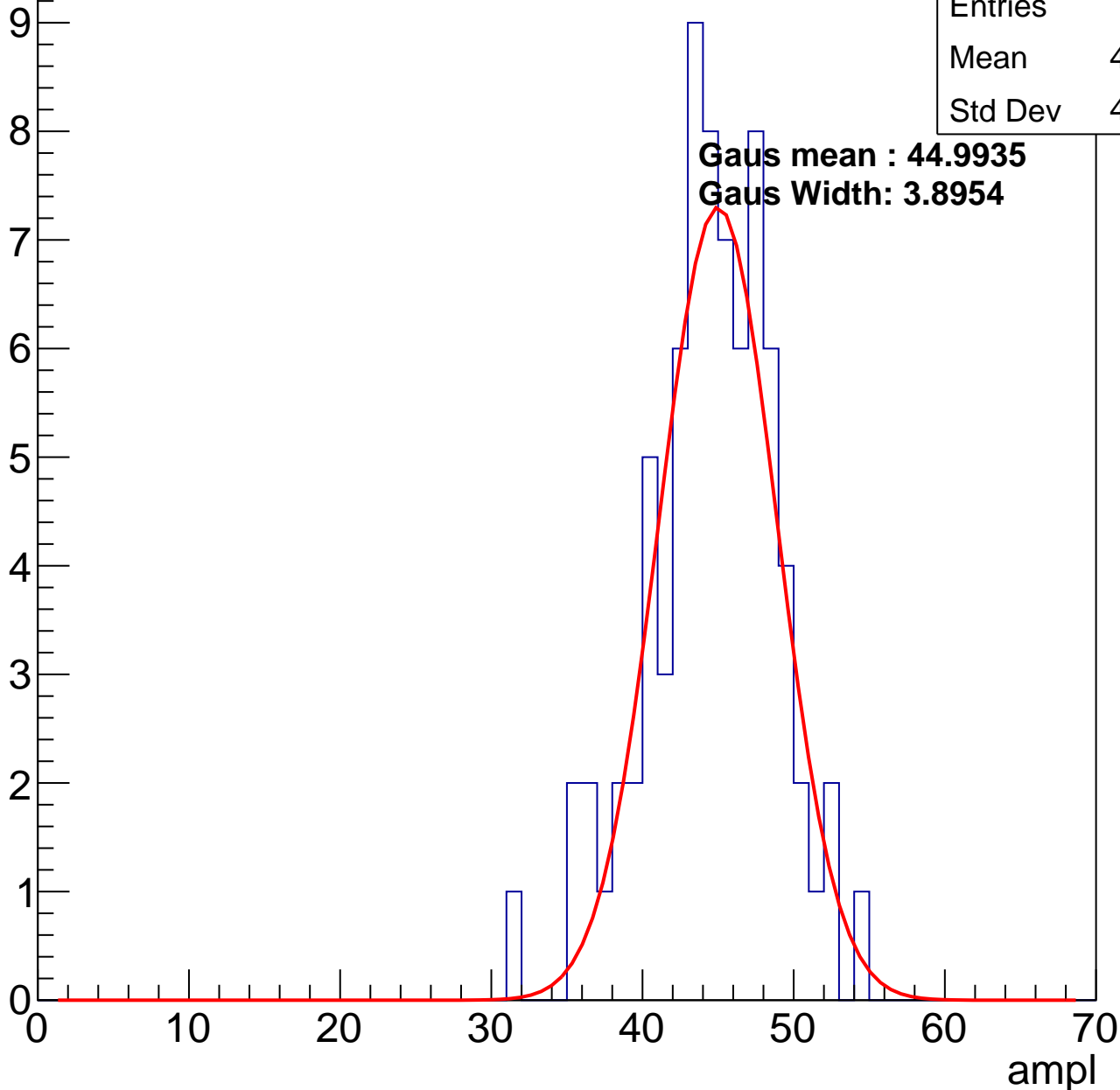
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	44.08
Std Dev	4.263

Gaus mean : 44.9935

Gaus Width: 3.8954

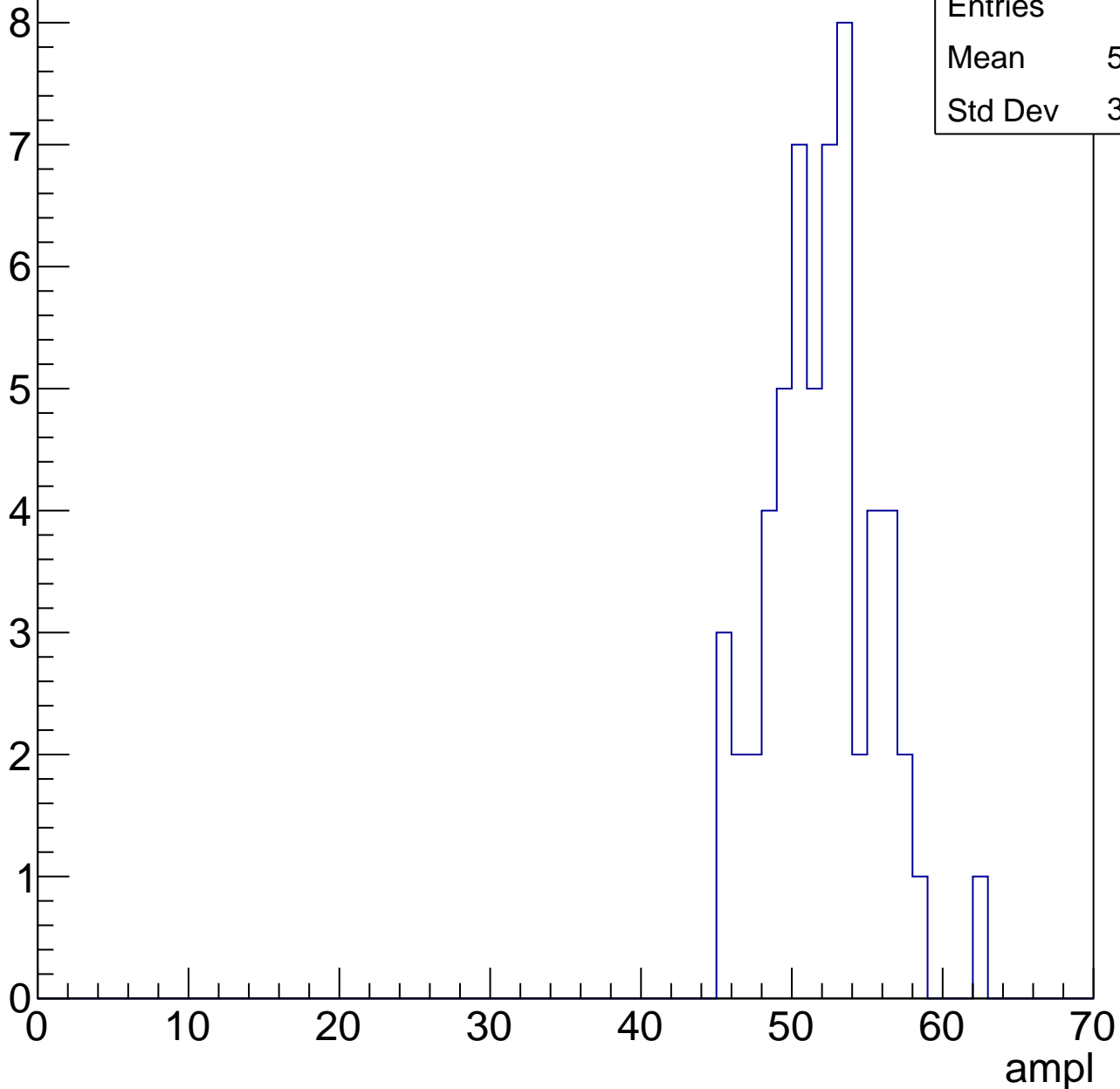


B1L103S, U24-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

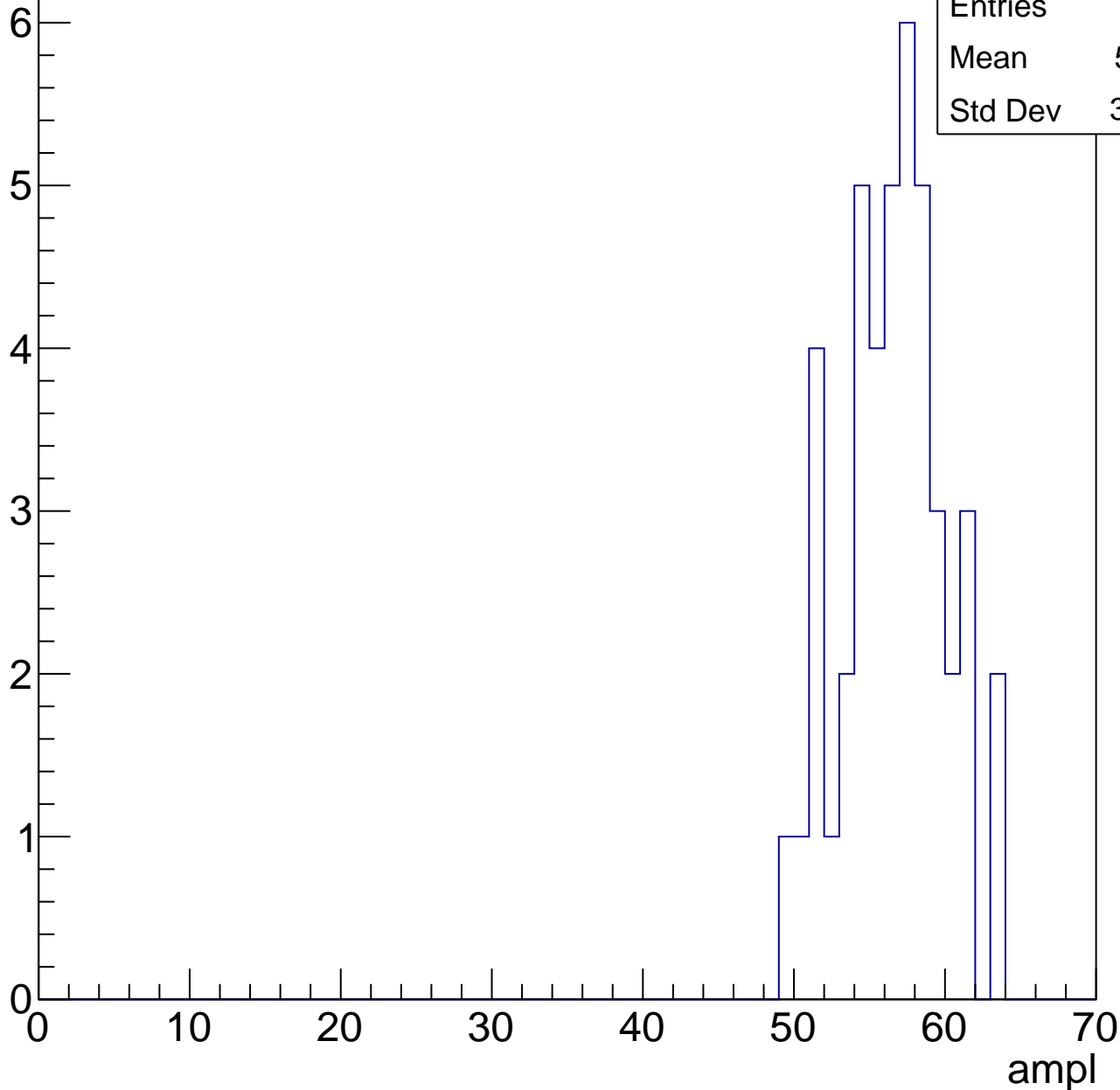
Entries	57
Mean	51.53
Std Dev	3.505



B1L103S, U24-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

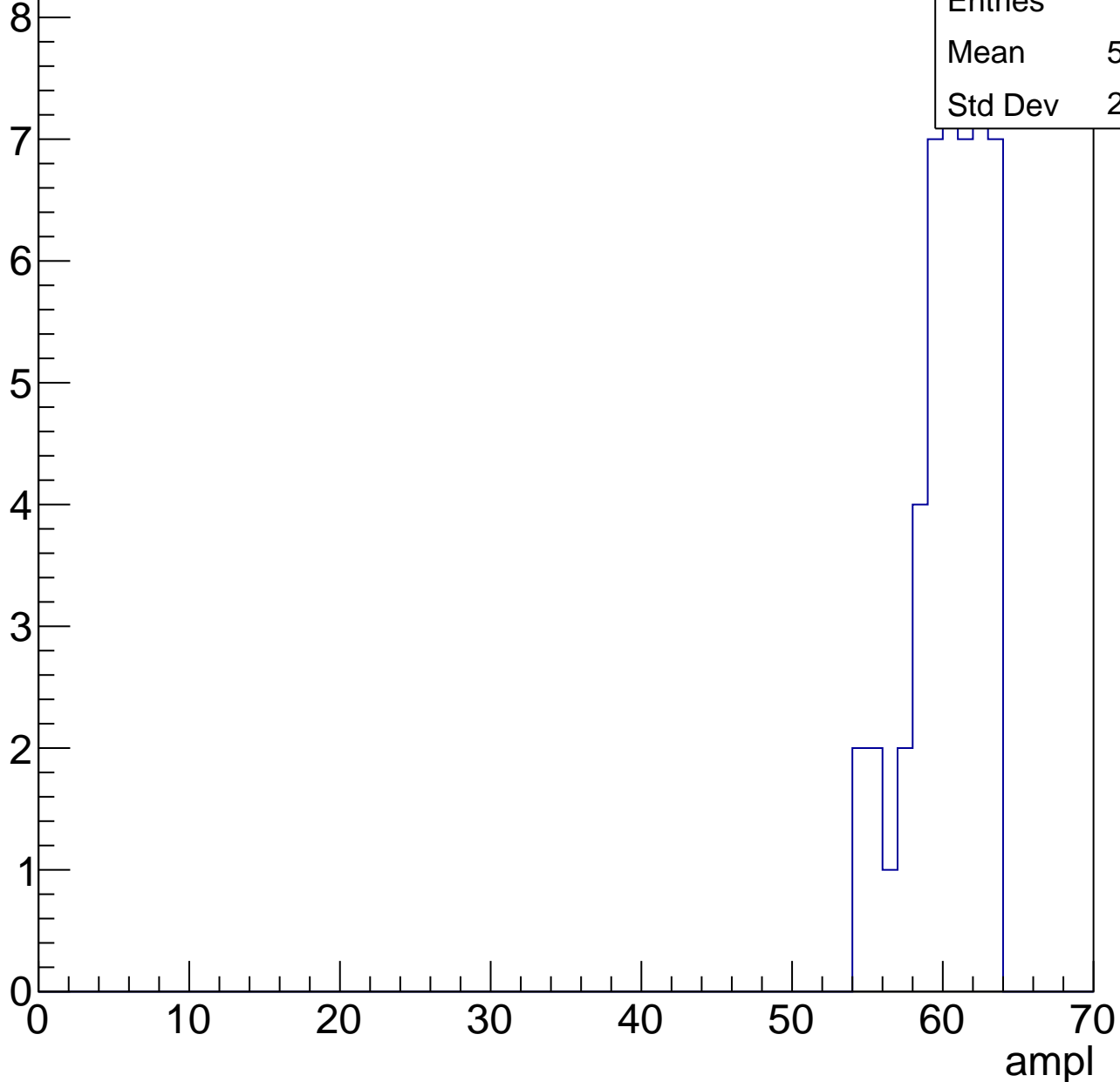


Entries	44
Mean	56.11
Std Dev	3.372

B1L103S, U24-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	48
Mean	59.94
Std Dev	2.419

B1L103S, U24-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch11, adc0

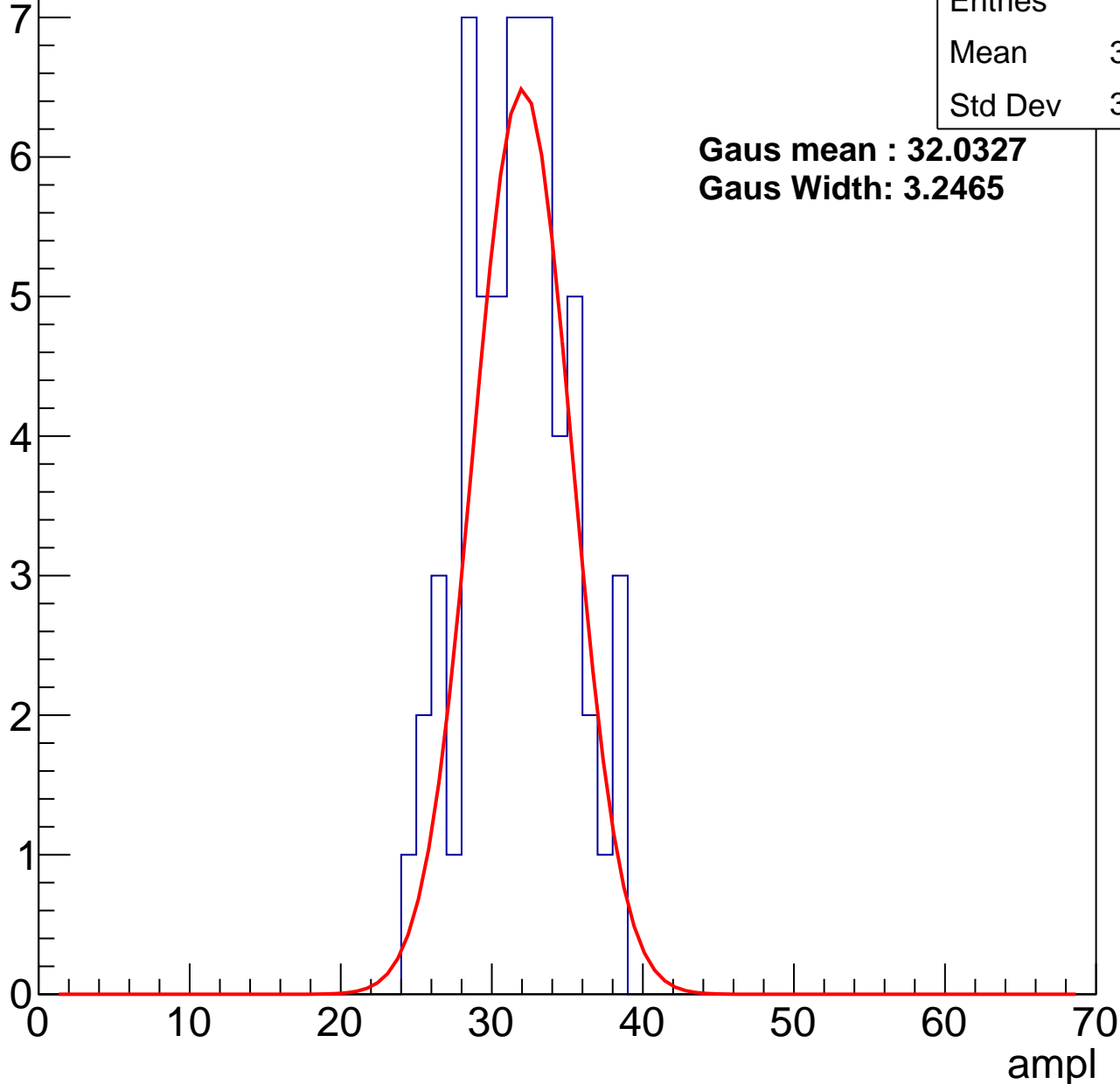
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	31.27
Std Dev	3.366

Gaus mean : 32.0327

Gaus Width: 3.2465



B1L103S, U24-ch11, adc1

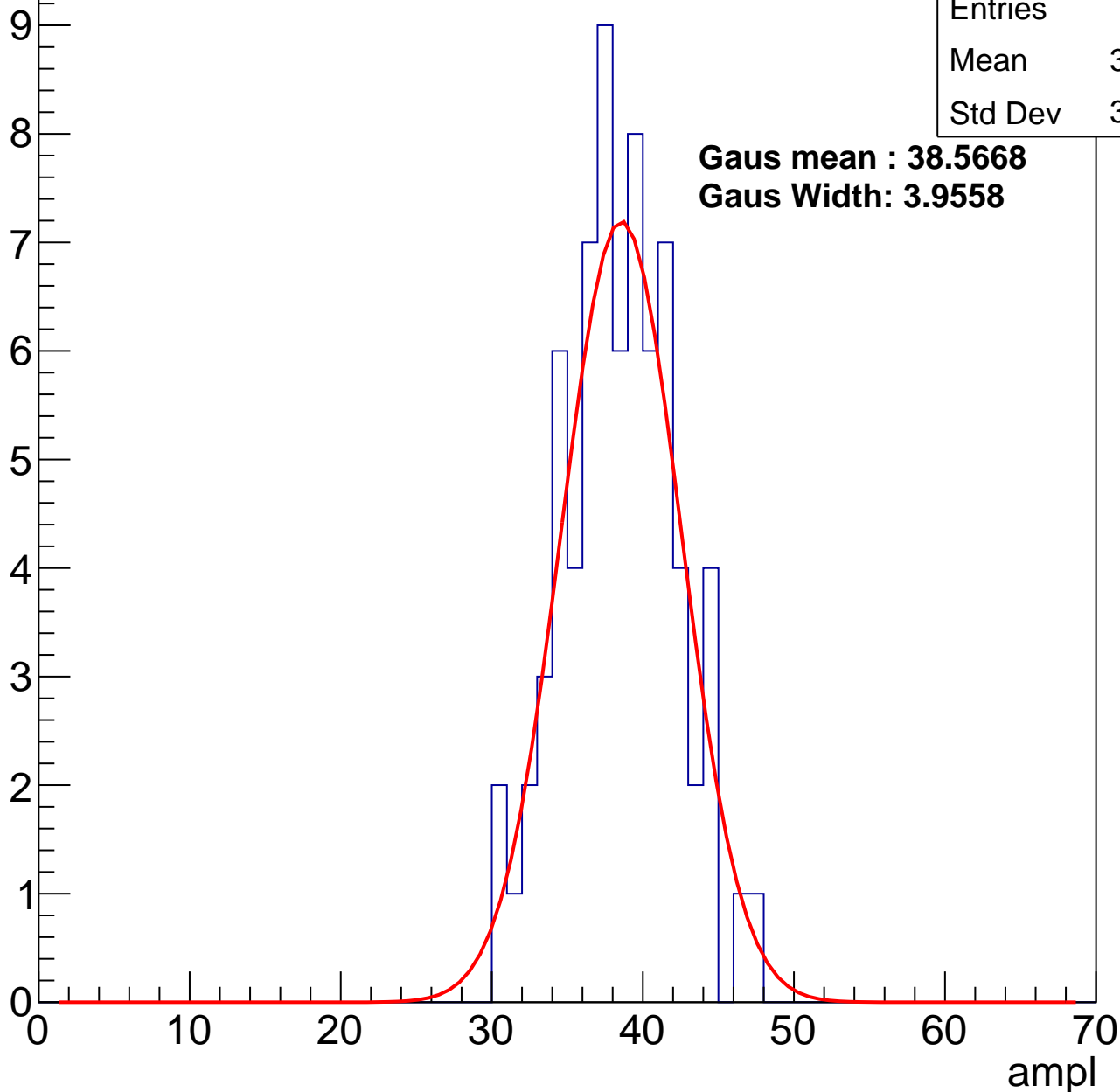
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	37.99
Std Dev	3.684

Gaus mean : 38.5668

Gaus Width: 3.9558



B1L103S, U24-ch11, adc2

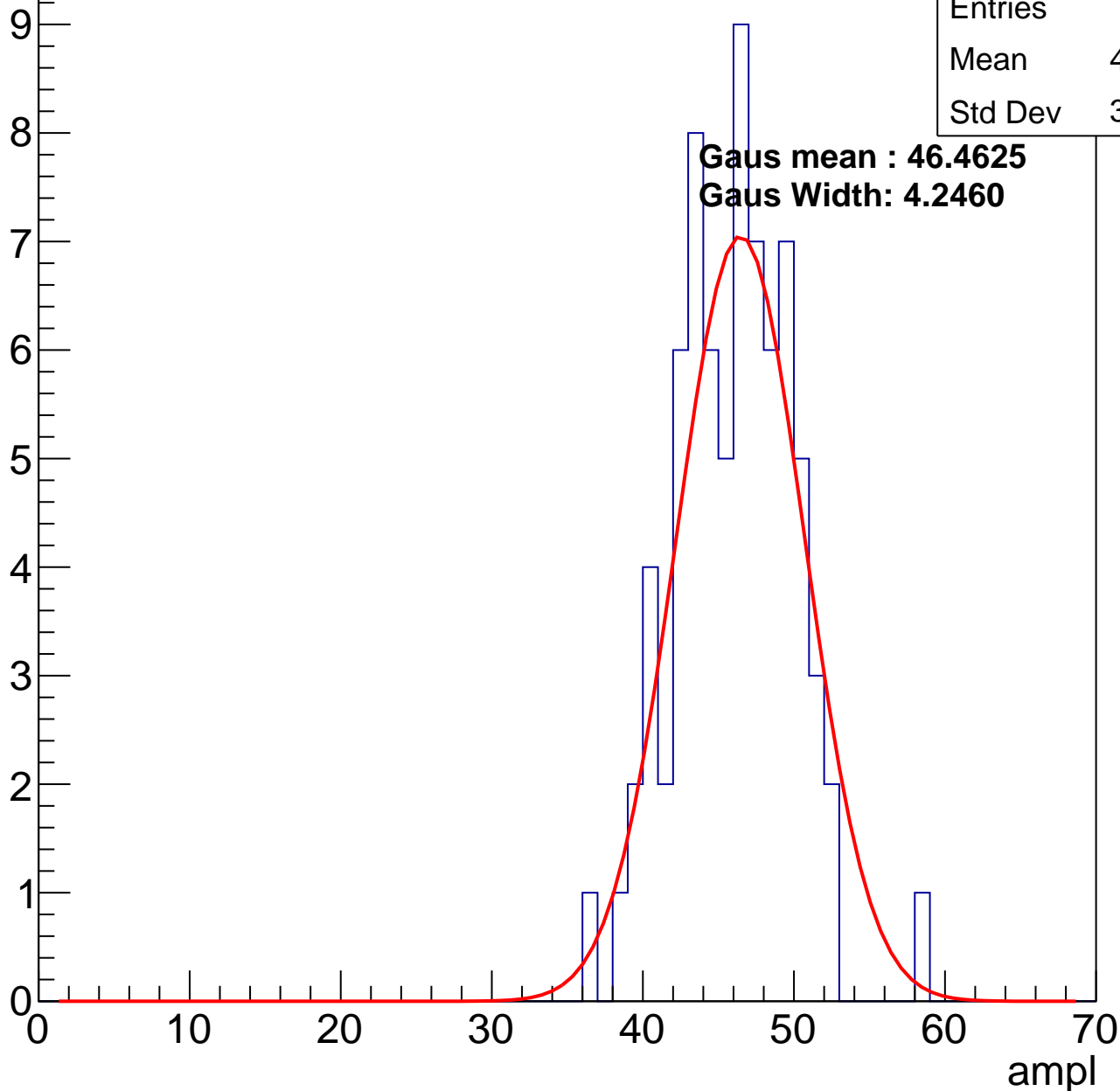
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	45.57
Std Dev	3.837

Gaus mean : 46.4625

Gaus Width: 4.2460

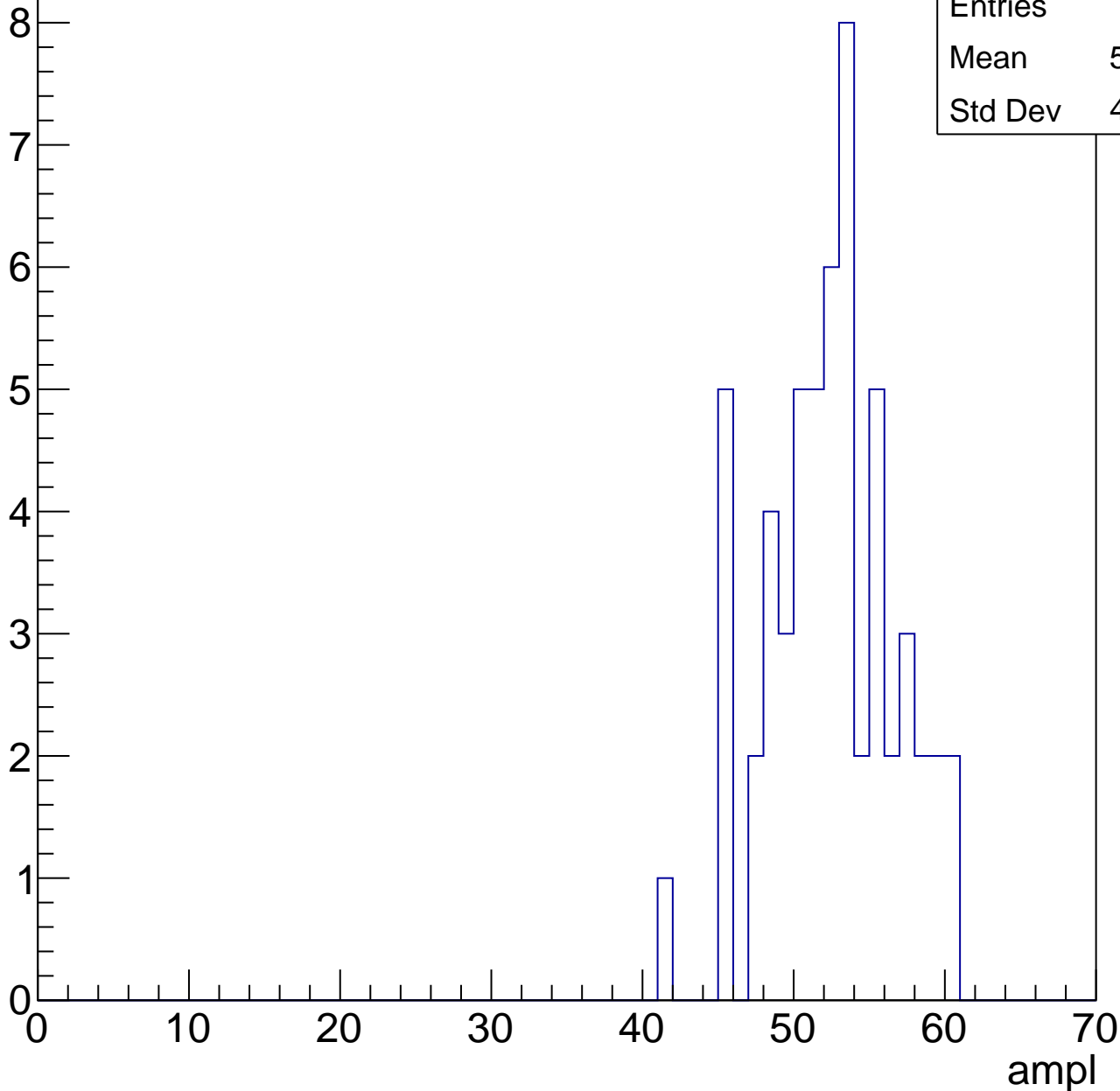


B1L103S, U24-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	51.93
Std Dev	4.184

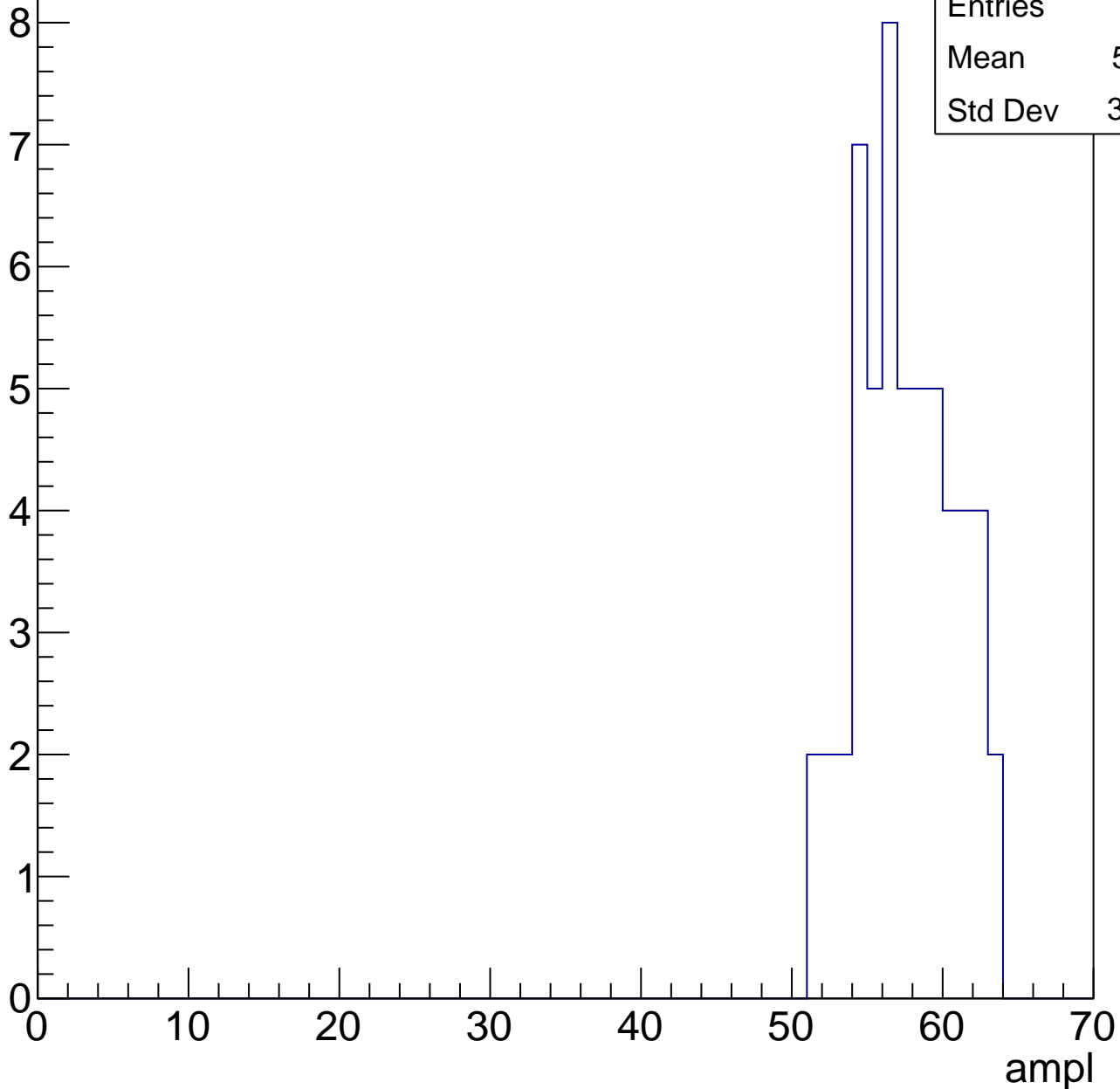


B1L103S, U24-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.11
Std Dev	3.137

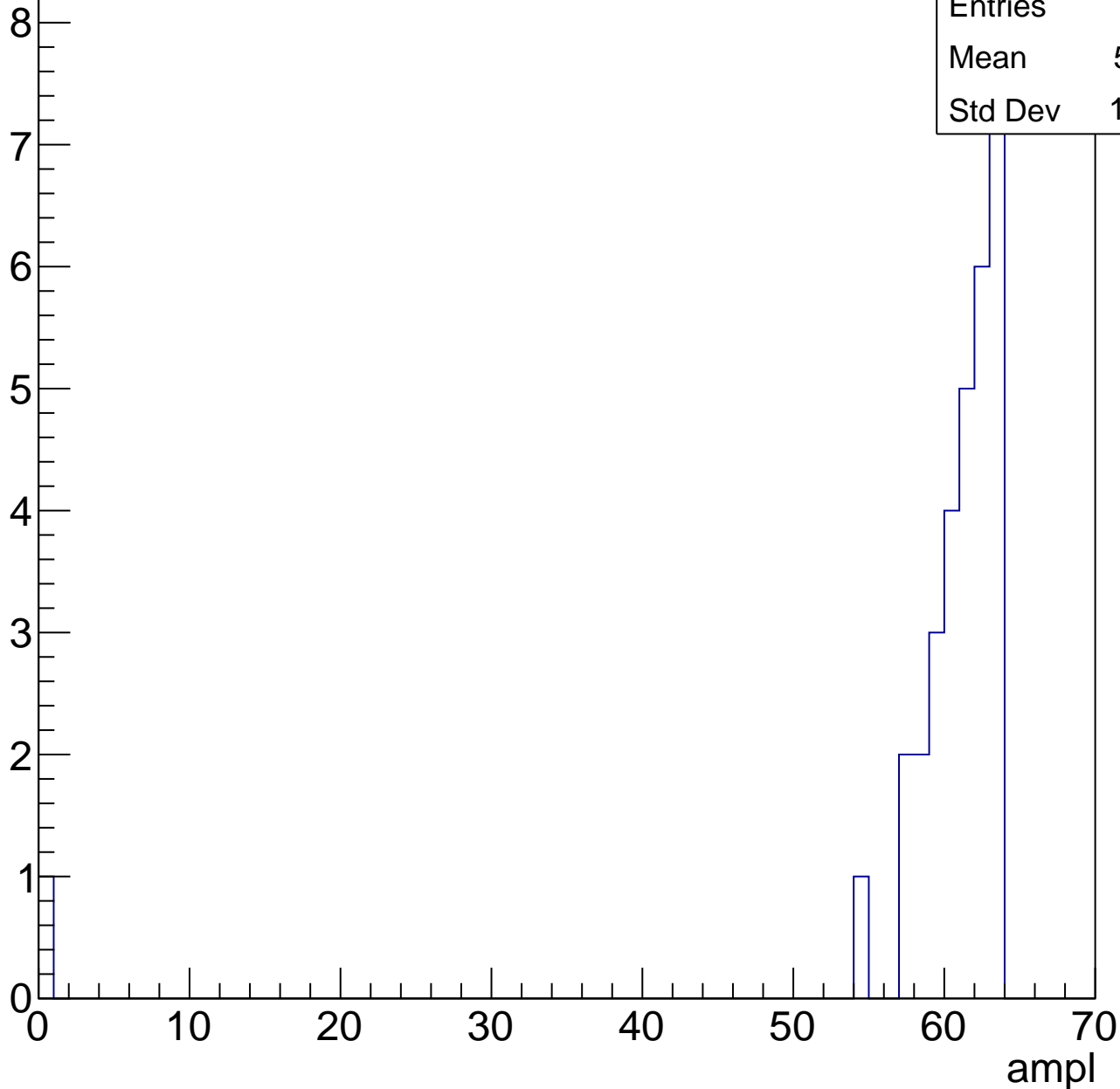


B1L103S, U24-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.81
Std Dev	10.78



B1L103S, U24-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U24-ch12, adc0

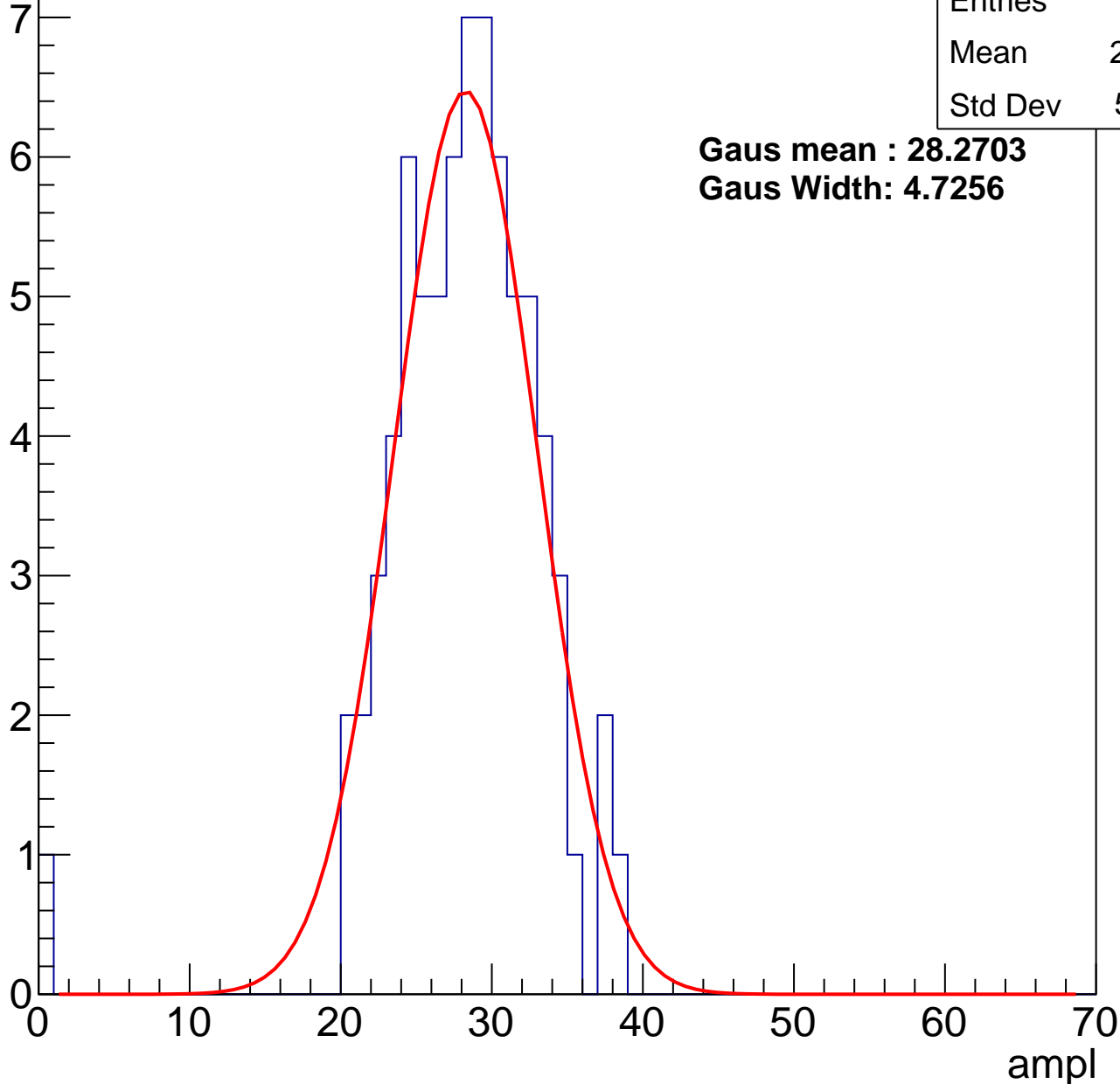
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	27.68
Std Dev	5.231

Gaus mean : 28.2703

Gaus Width: 4.7256



B1L103S, U24-ch12, adc1

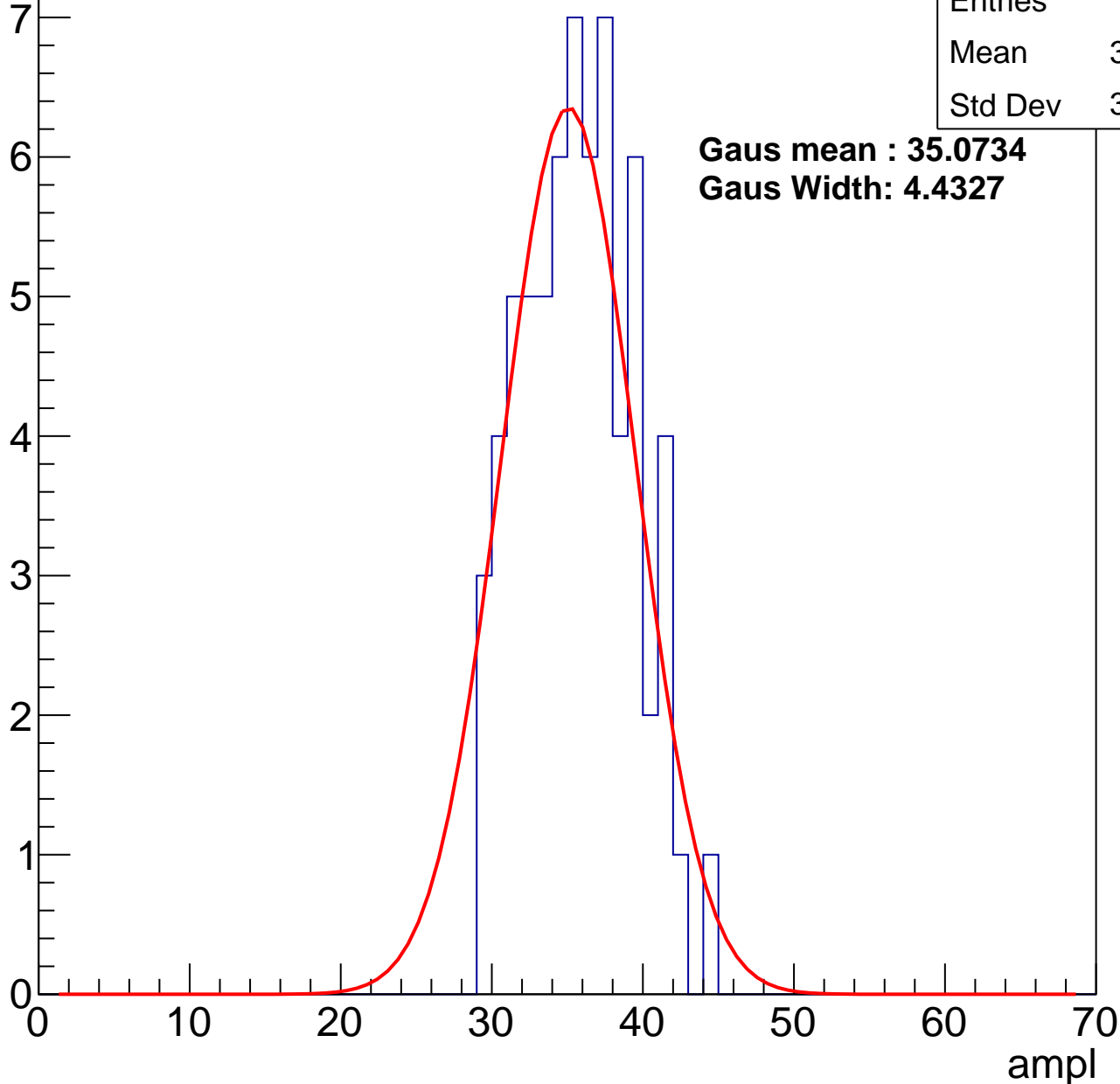
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.26
Std Dev	3.577

Gaus mean : 35.0734

Gaus Width: 4.4327



B1L103S, U24-ch12, adc2

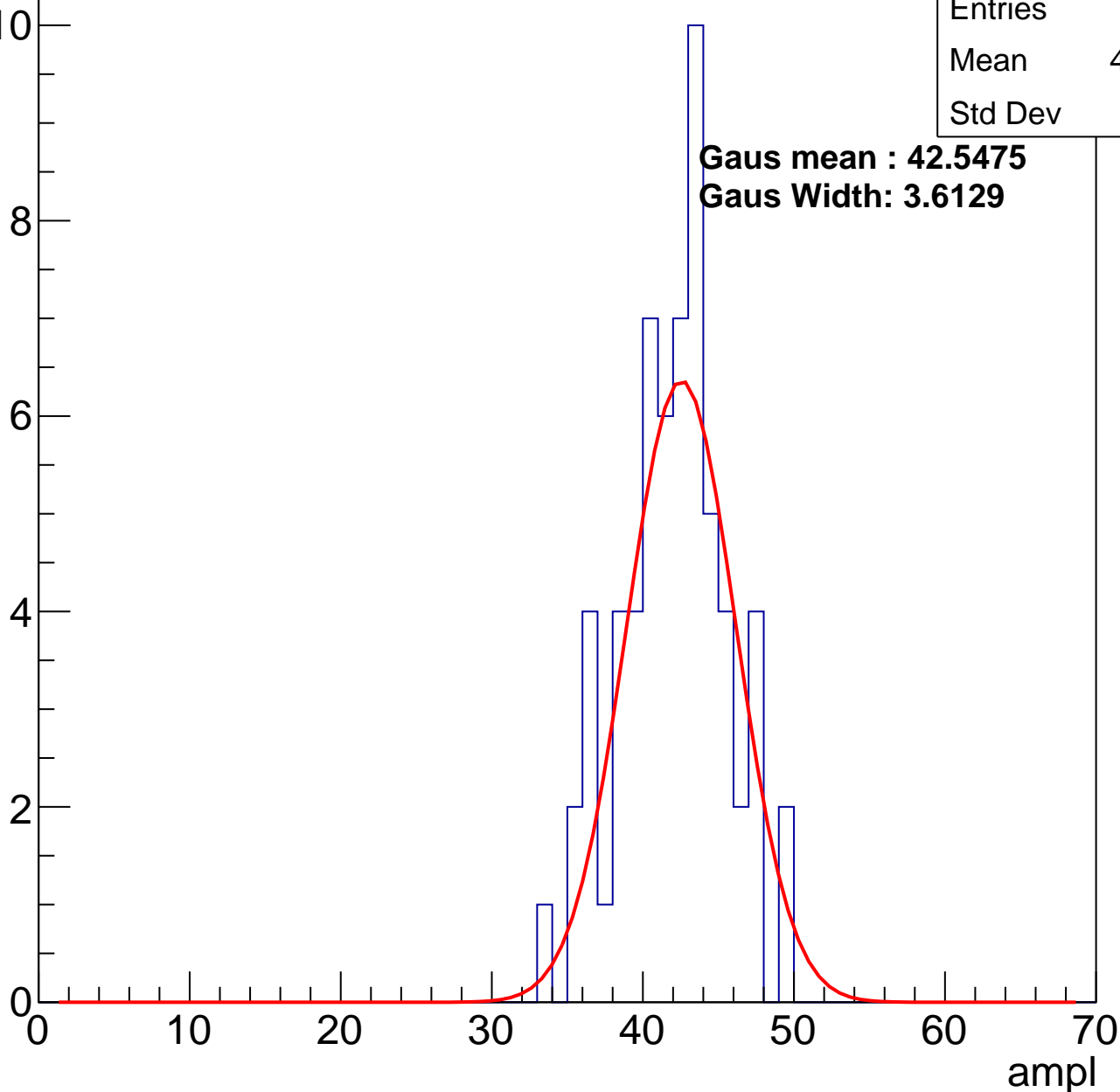
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.59
Std Dev	3.49

Gaus mean : 42.5475

Gaus Width: 3.6129

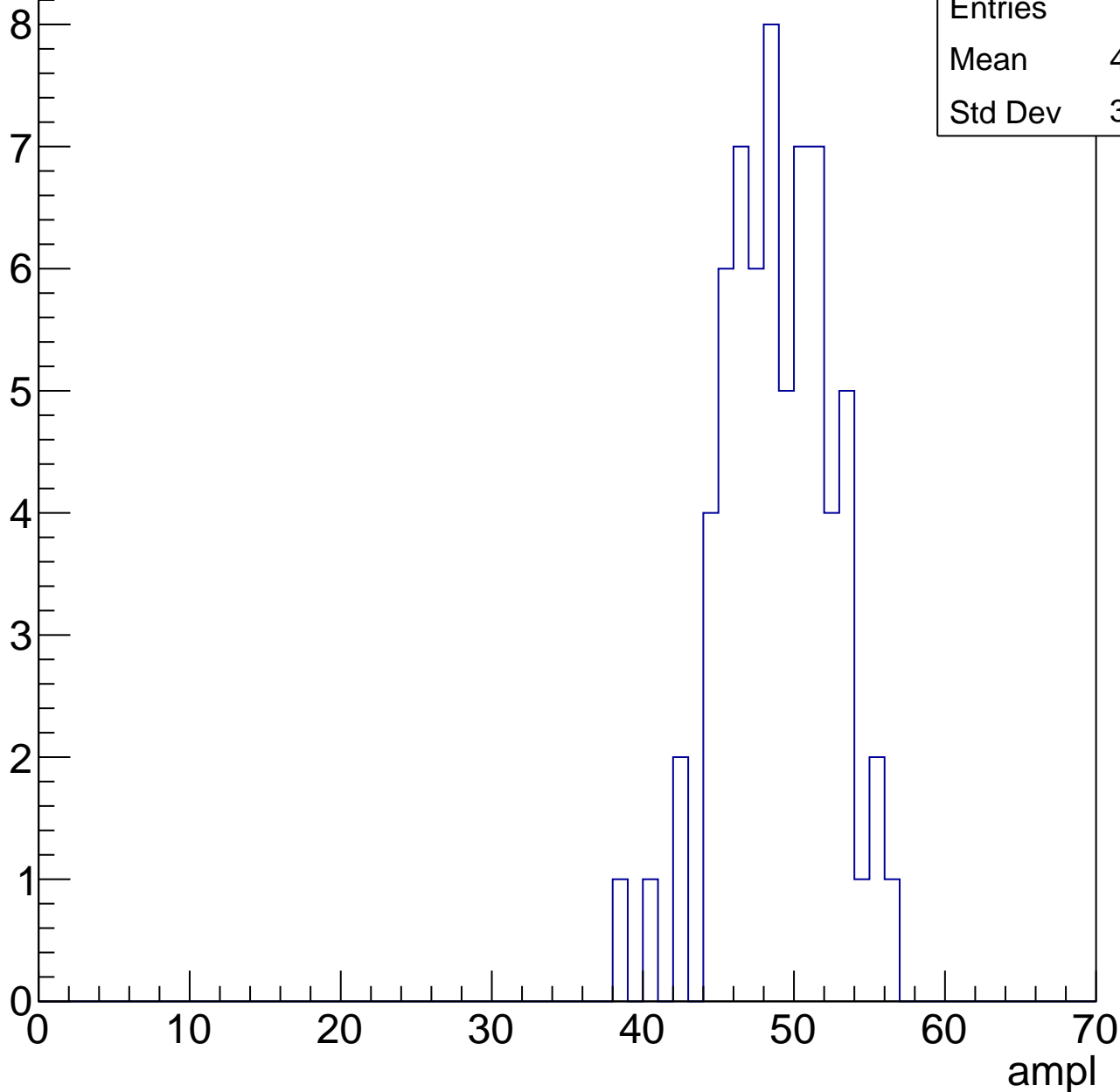


B1L103S, U24-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.37
Std Dev	3.586

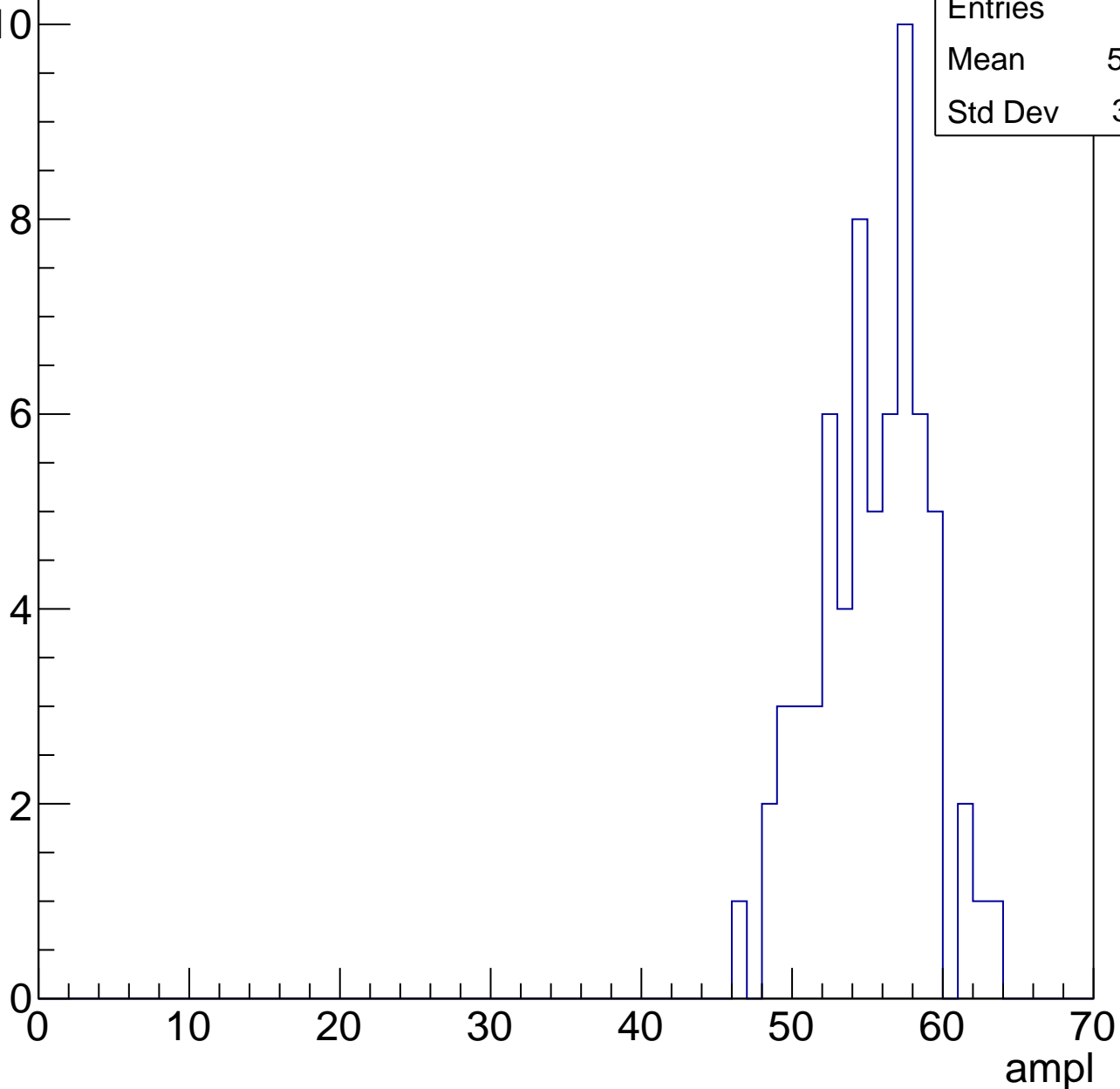


B1L103S, U24-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.83
Std Dev	3.591



B1L103S, U24-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	49
Mean	58.57
Std Dev	8.843

Entry

10

8

6

4

2

0

0

10

20

30

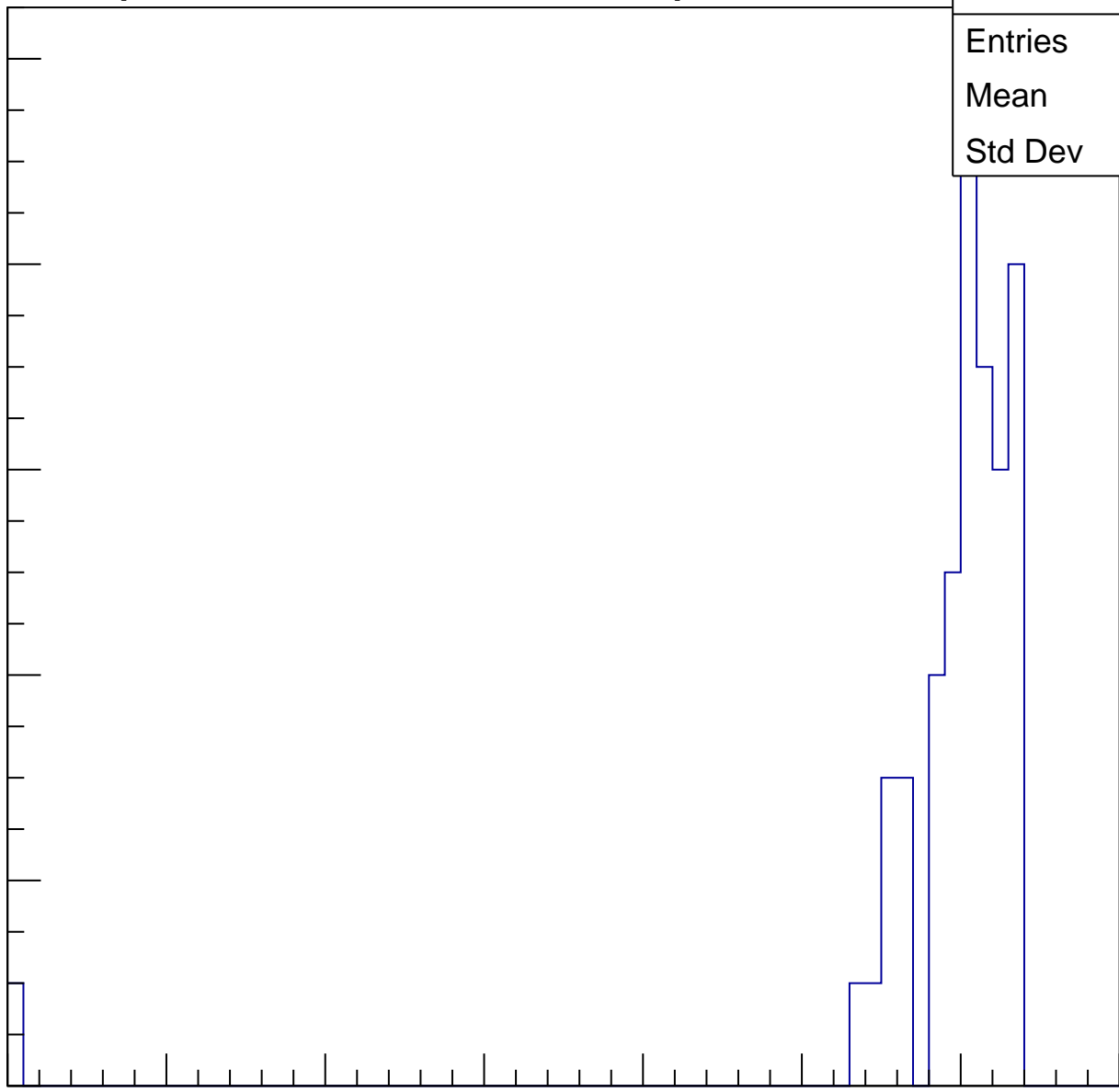
40

50

60

70

ampl



B1L103S, U24-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch13, adc0

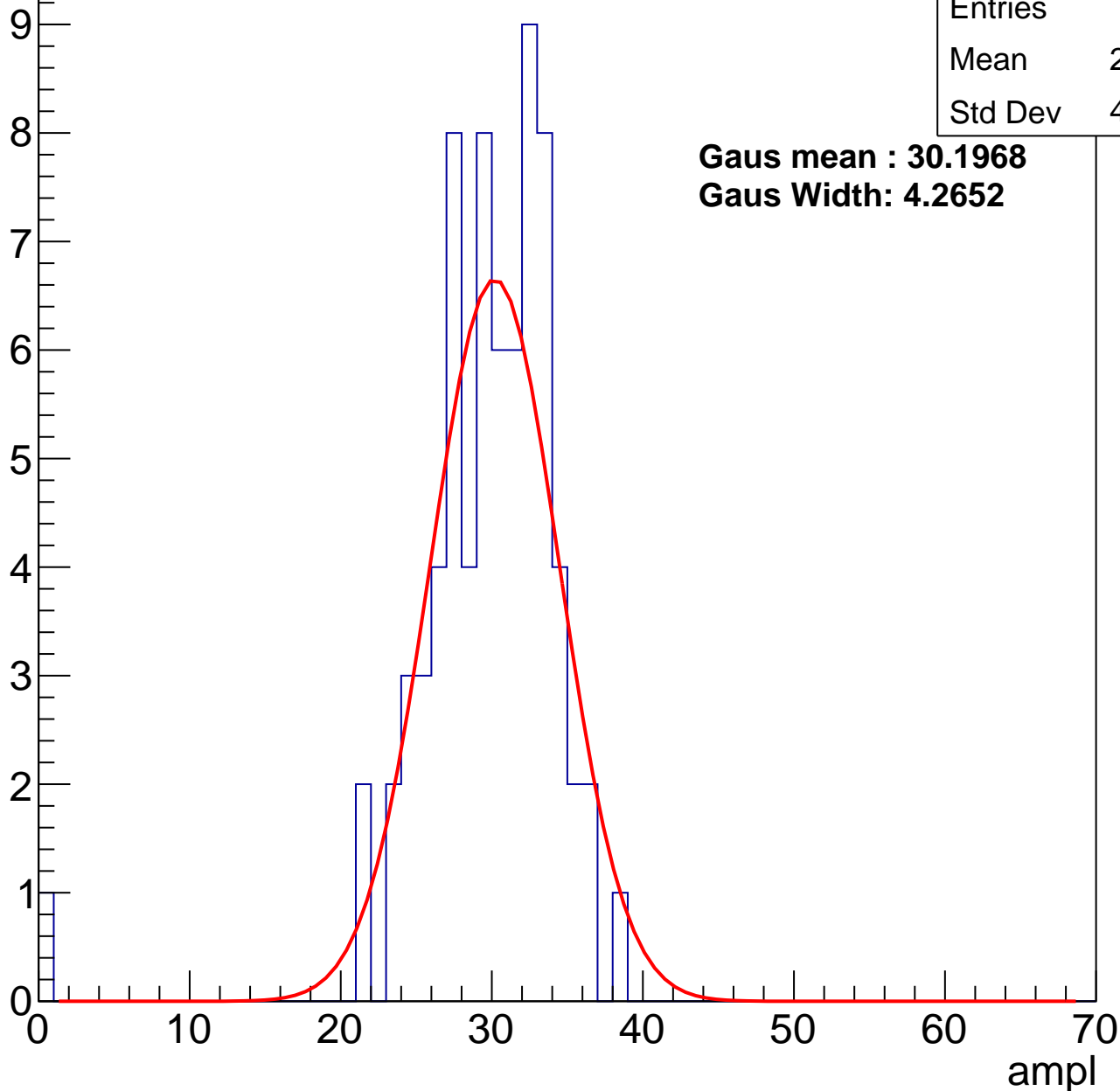
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.22
Std Dev	4.997

Gaus mean : 30.1968

Gaus Width: 4.2652



B1L103S, U24-ch13, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	37.13
Std Dev	3.685

Gaus mean : 37.5546

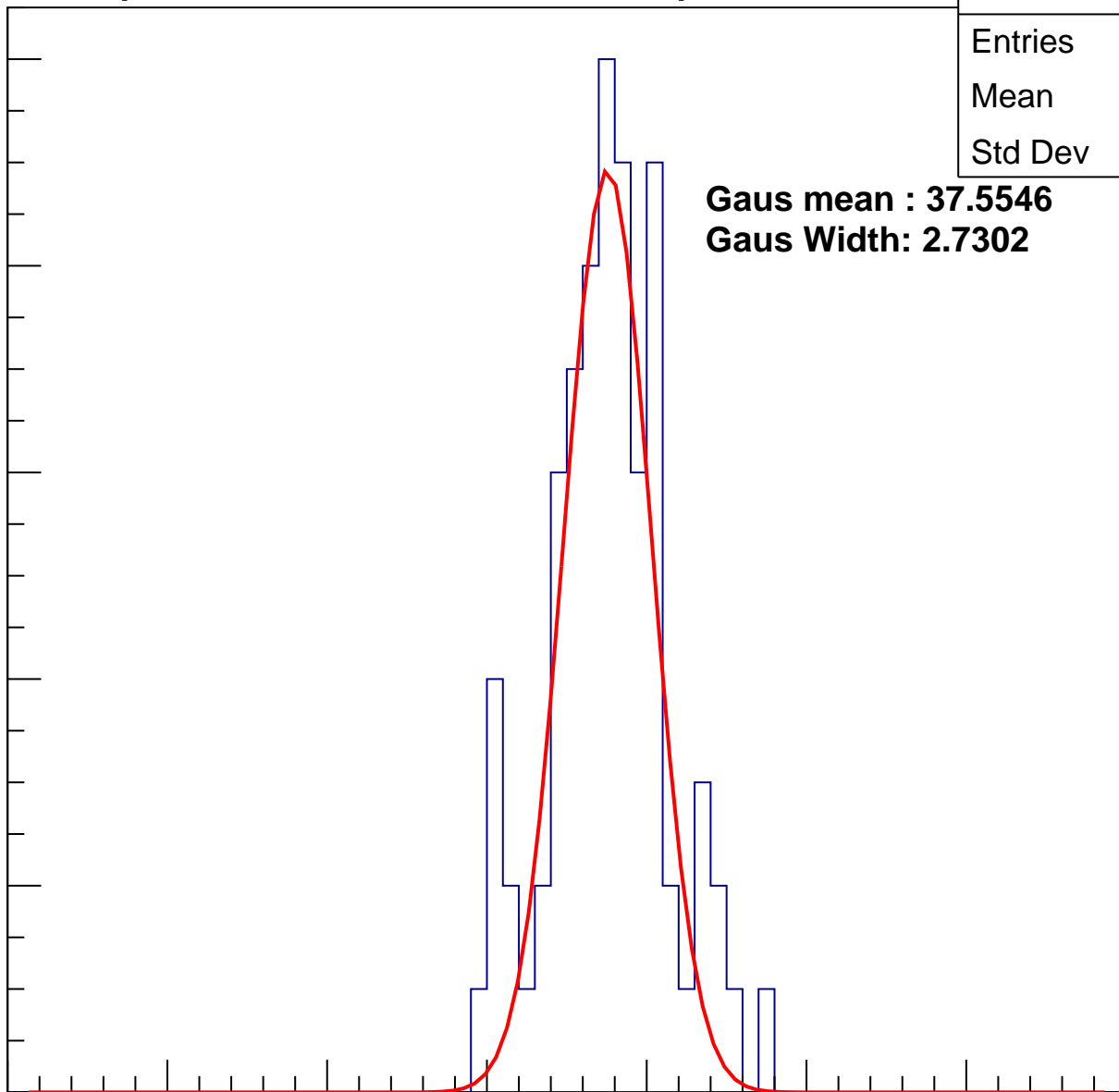
Gaus Width: 2.7302

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch13, adc2

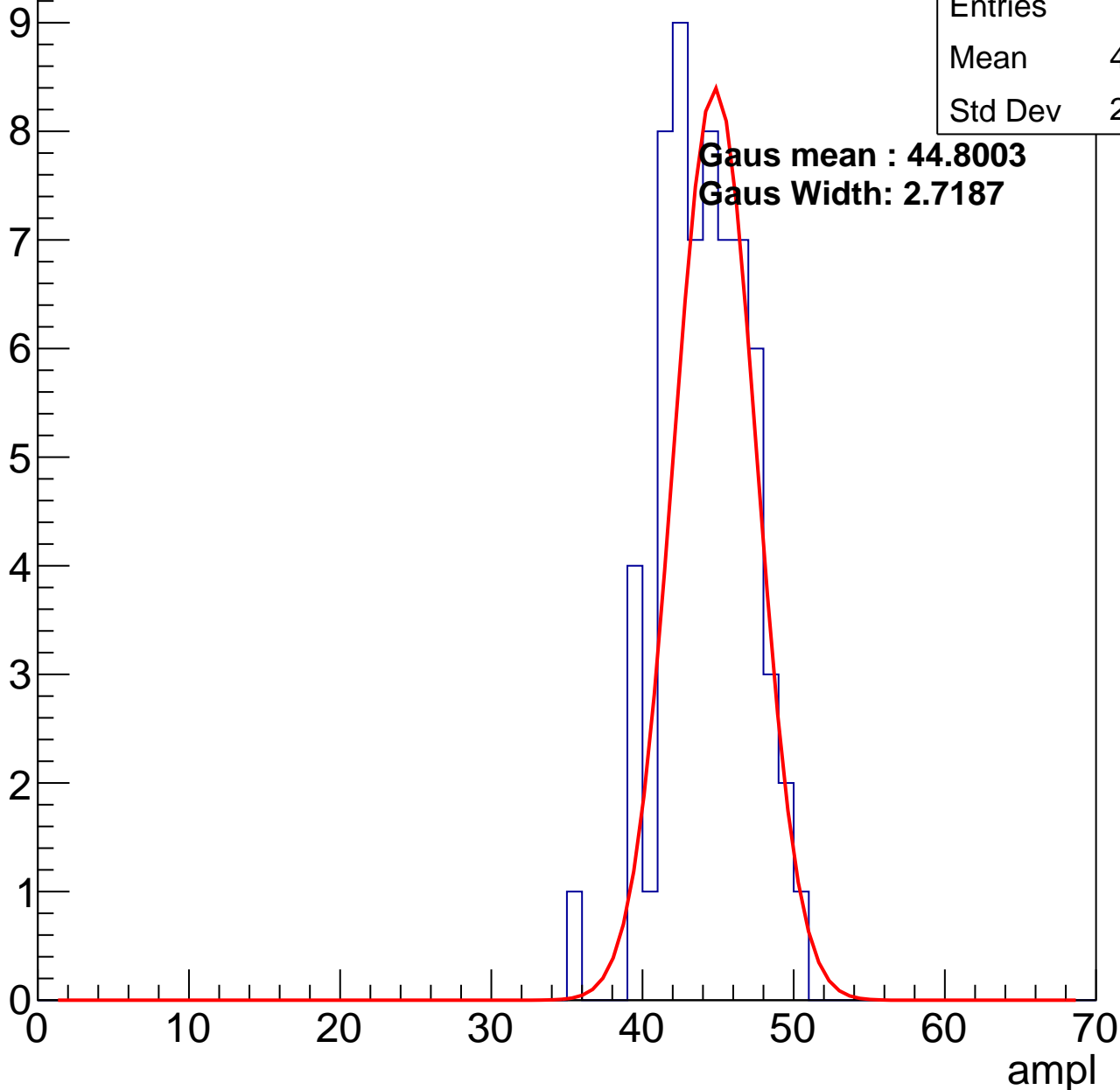
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.77
Std Dev	2.882

Gaus mean : 44.8003

Gaus Width: 2.7187

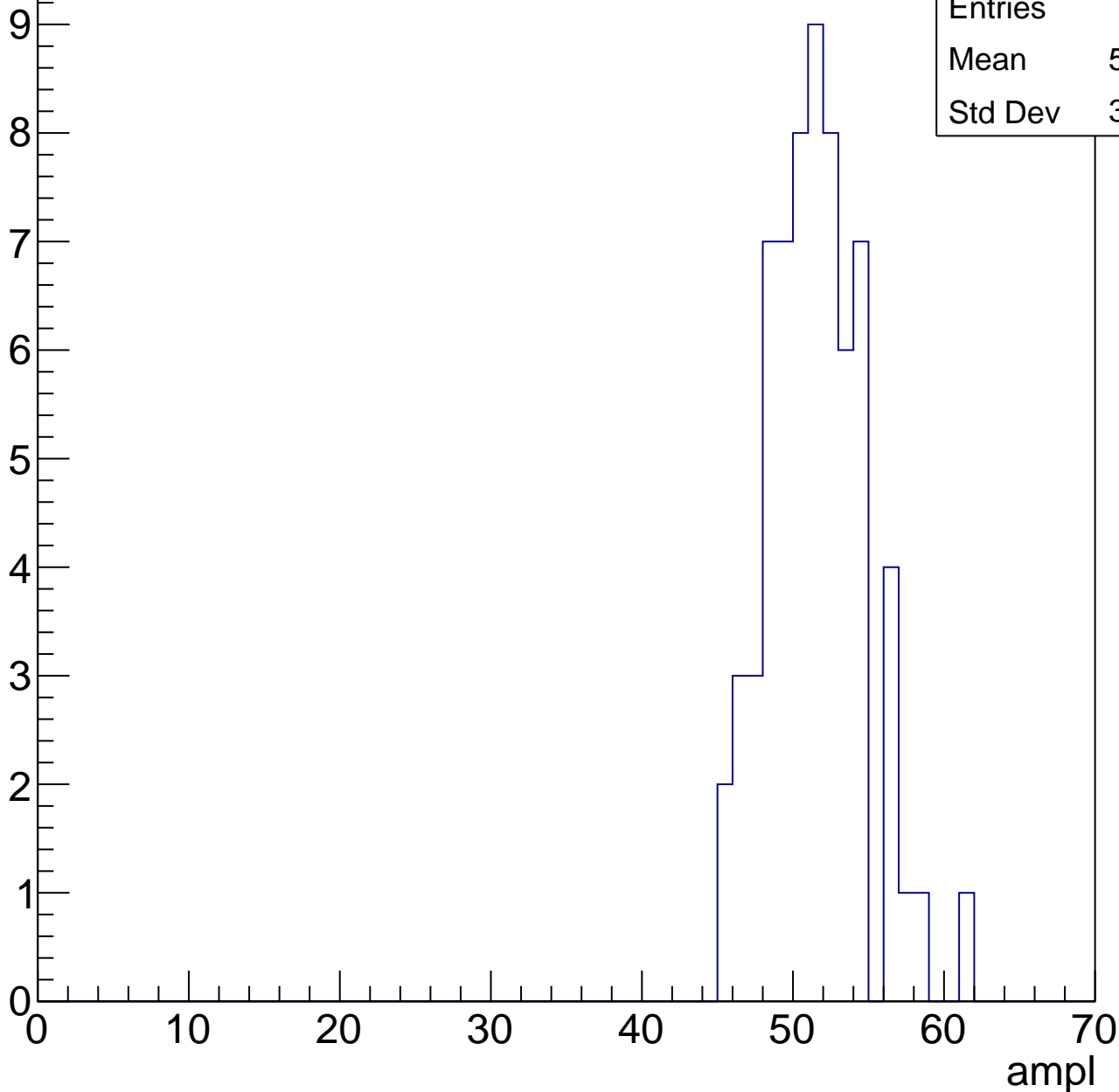


B1L103S, U24-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

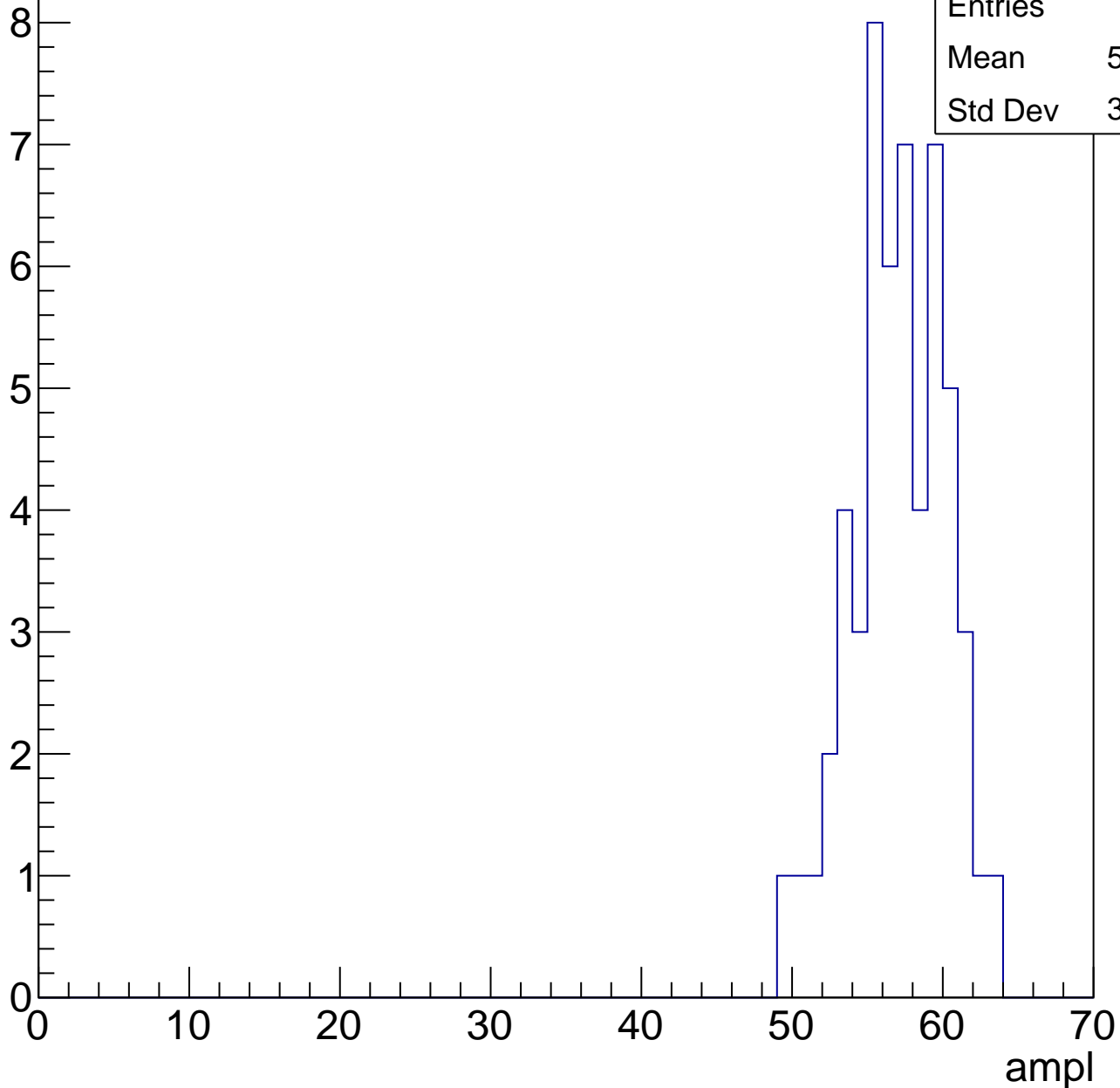
Entries	67
Mean	51.03
Std Dev	3.172



B1L103S, U24-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



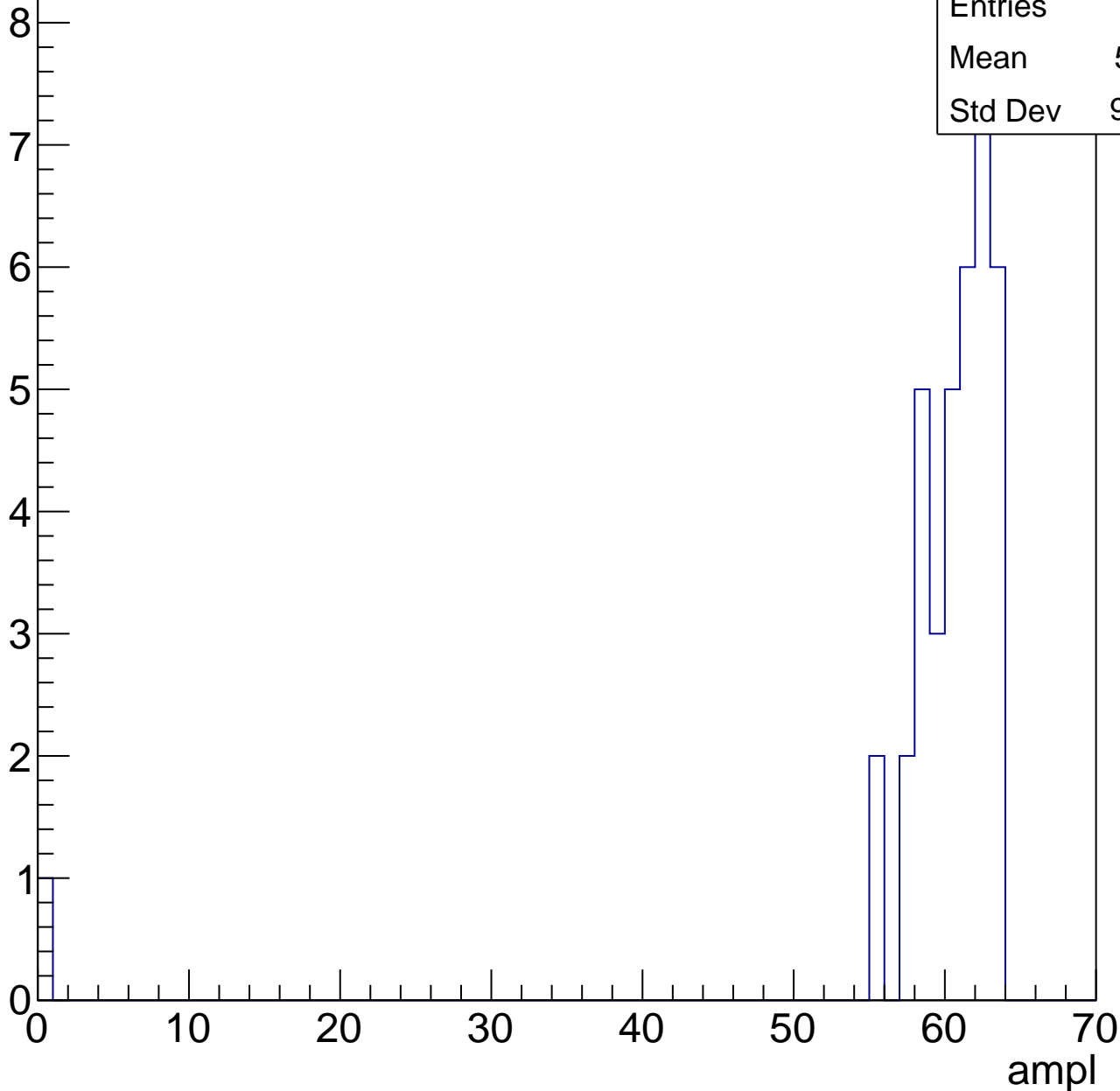
Entries	54
Mean	56.59
Std Dev	3.058

B1L103S, U24-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.71
Std Dev	9.894



B1L103S, U24-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch14, adc0

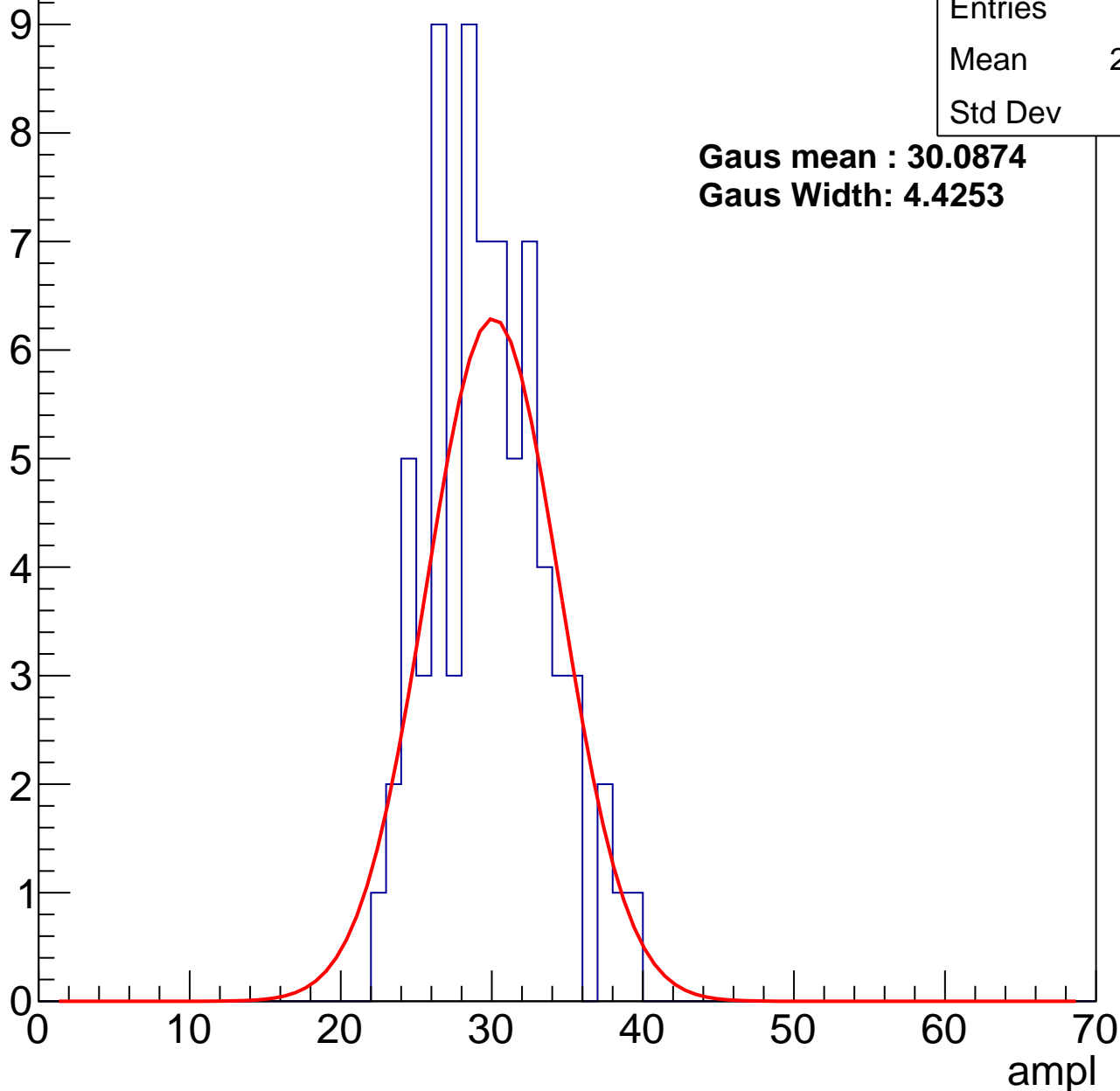
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.33
Std Dev	3.79

Gaus mean : 30.0874

Gaus Width: 4.4253



B1L103S, U24-ch14, adc1

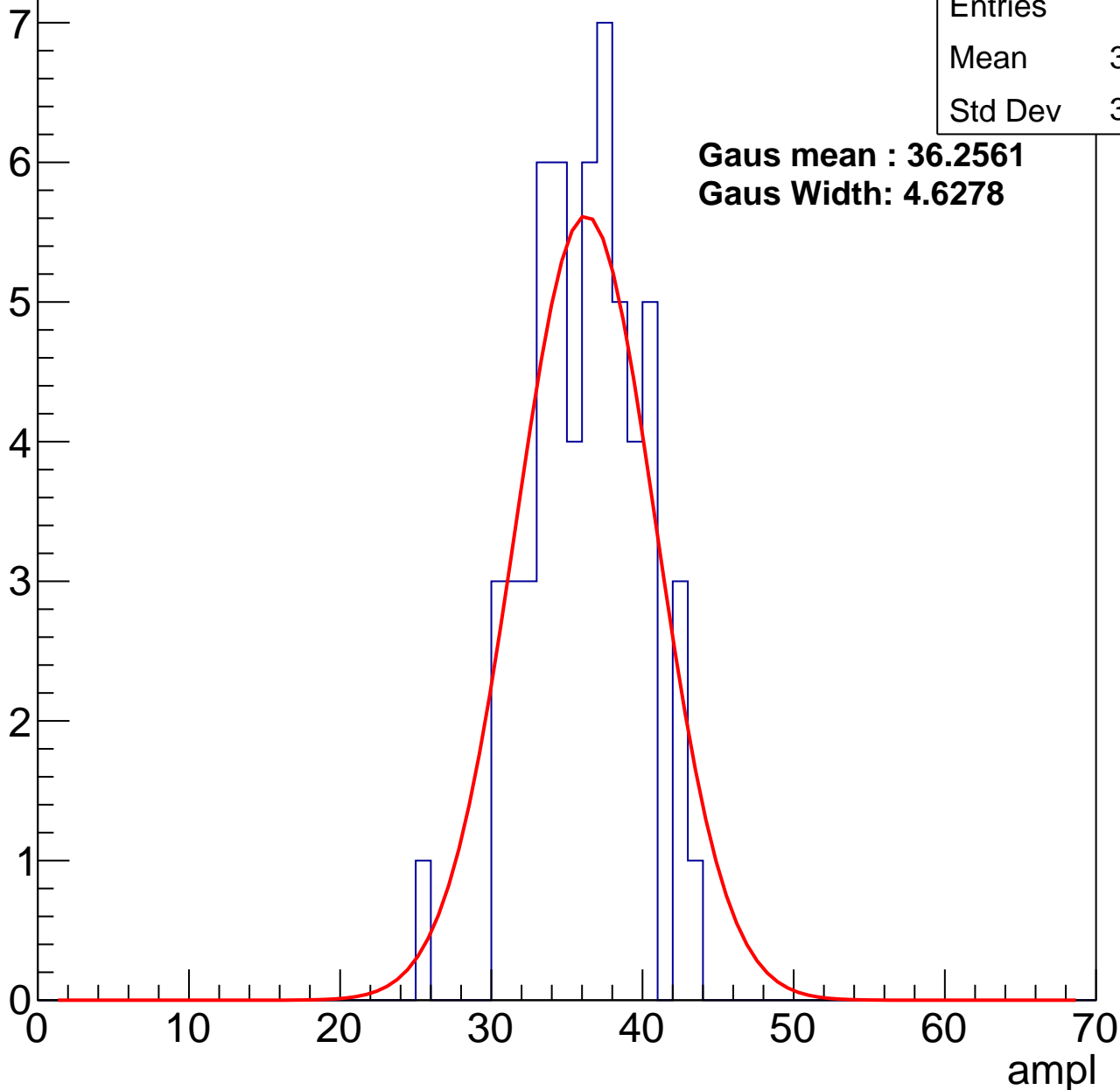
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	35.72
Std Dev	3.577

Gaus mean : 36.2561

Gaus Width: 4.6278



B1L103S, U24-ch14, adc2

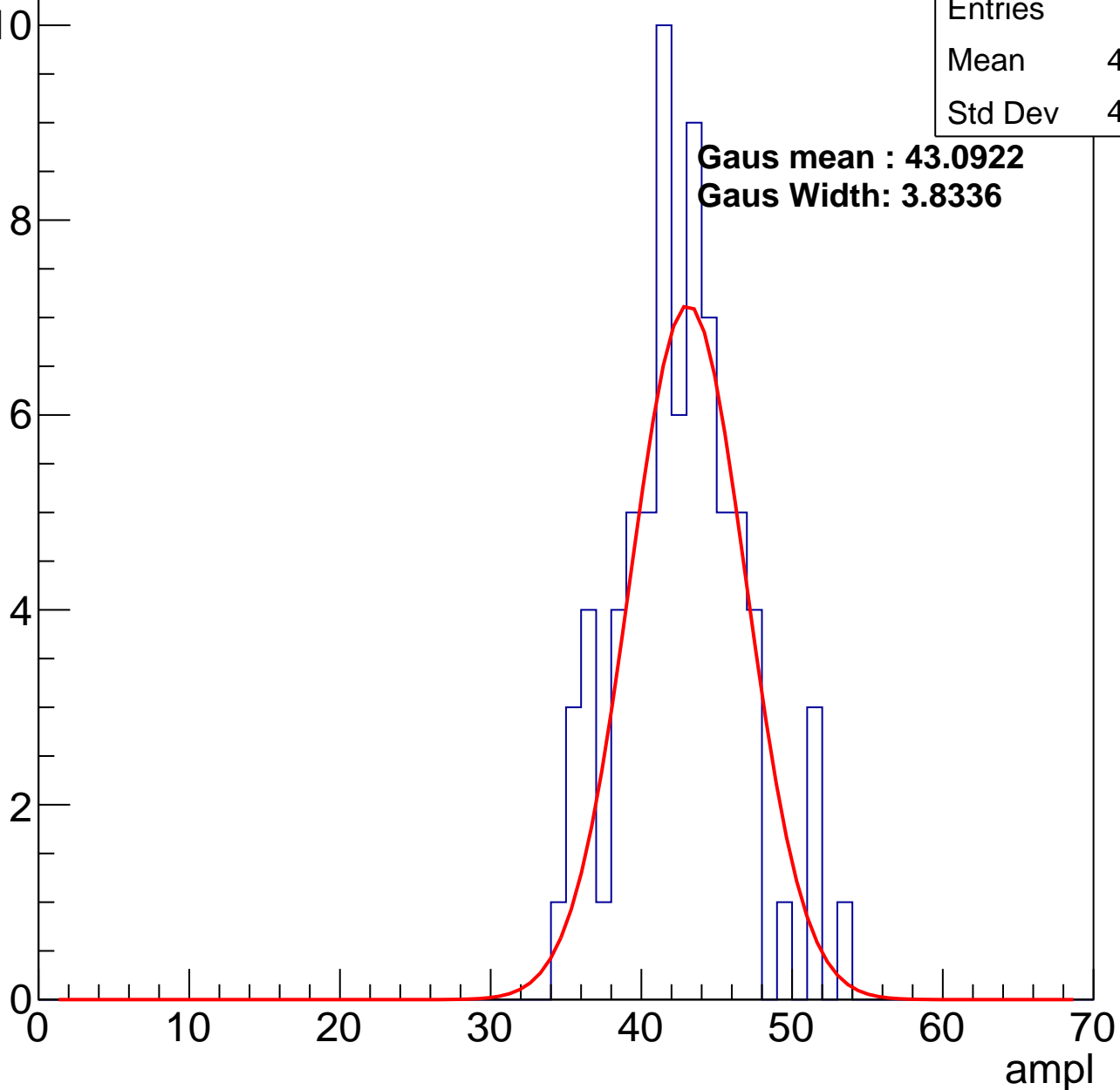
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.19
Std Dev	4.012

Gaus mean : 43.0922

Gaus Width: 3.8336

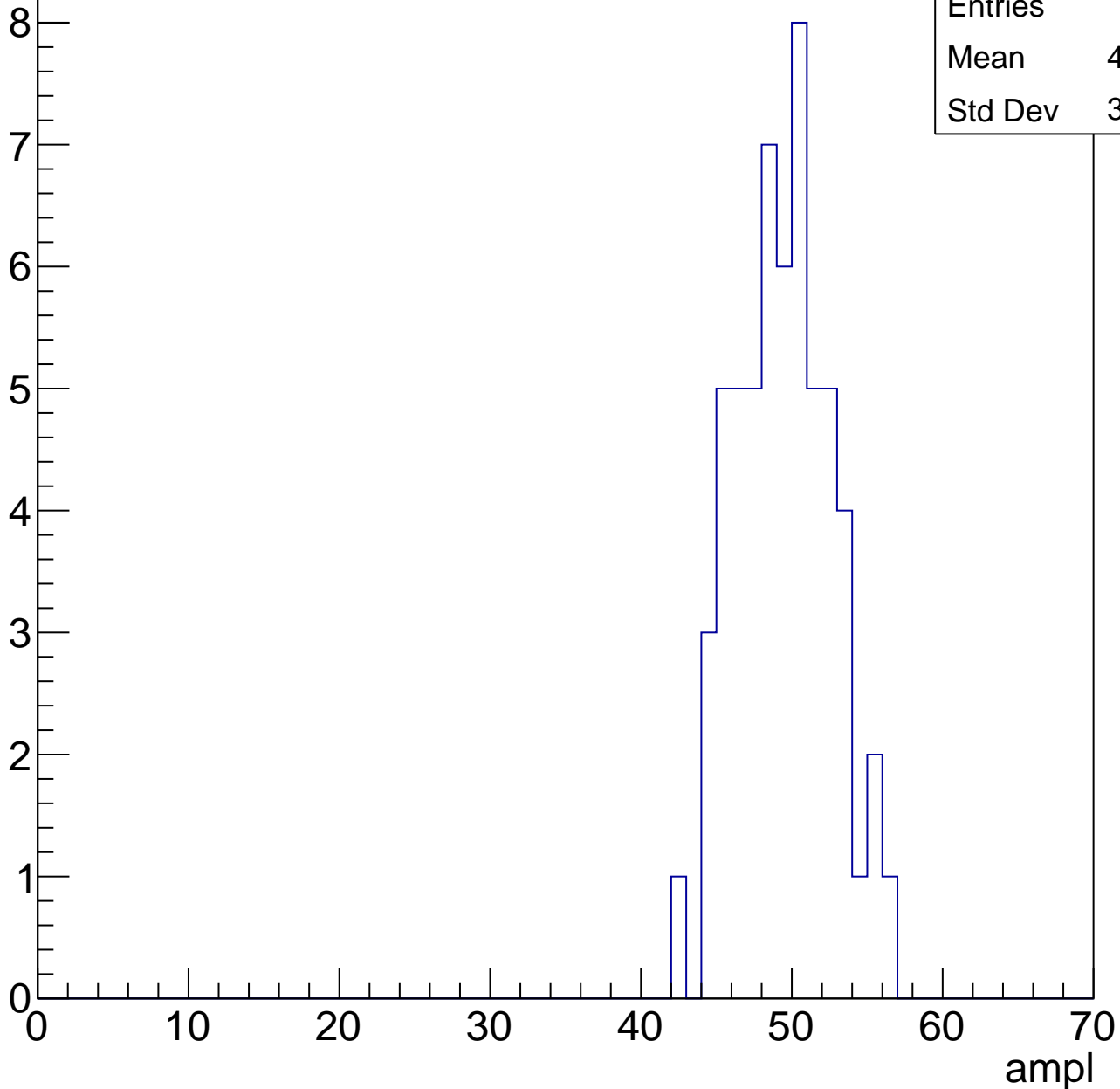


B1L103S, U24-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.98
Std Dev	3.104

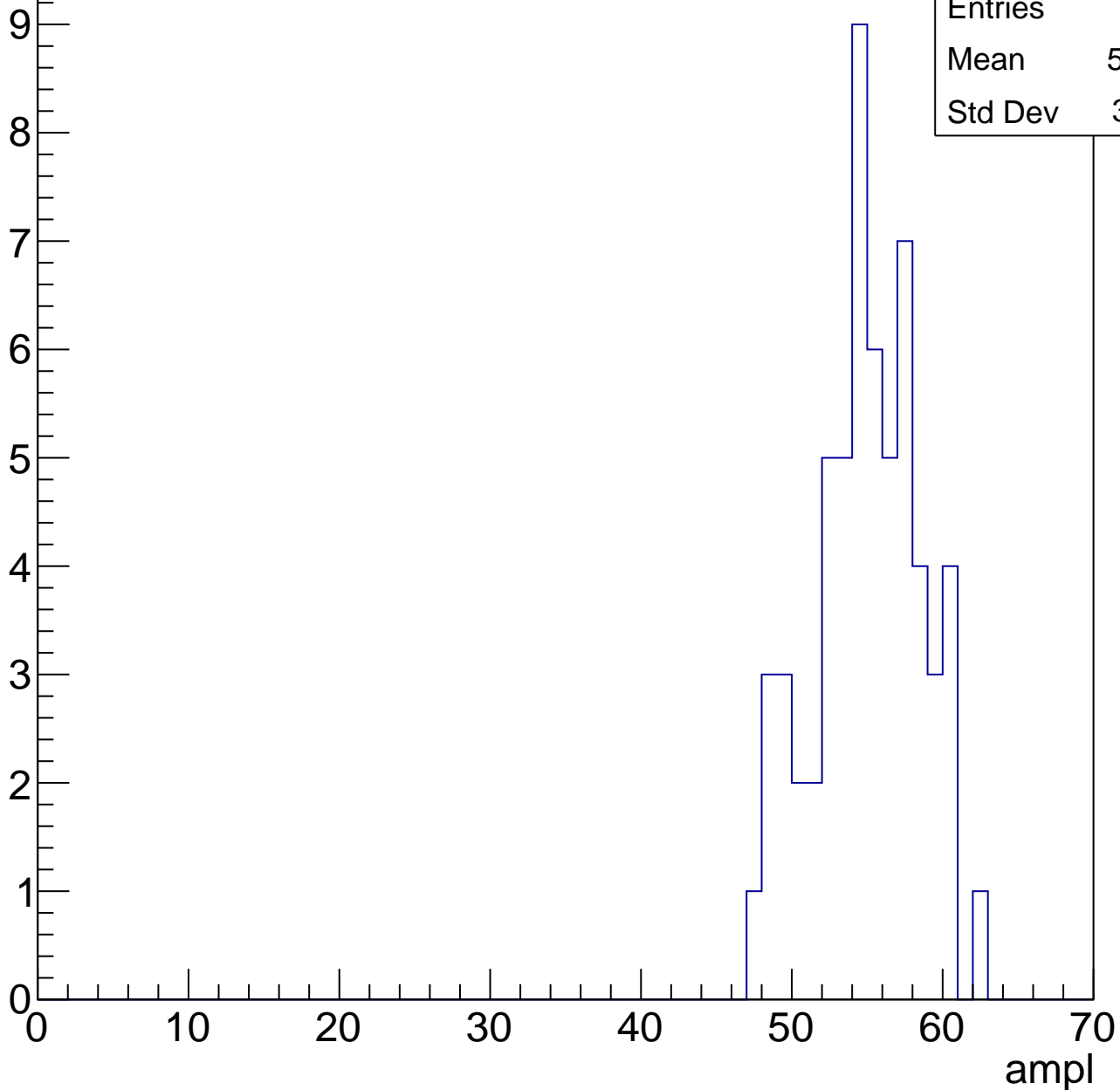


B1L103S, U24-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.52
Std Dev	3.481



B1L103S, U24-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	57
Mean	58.96
Std Dev	8.206

Entry

10

8

6

4

2

0

0

10

20

30

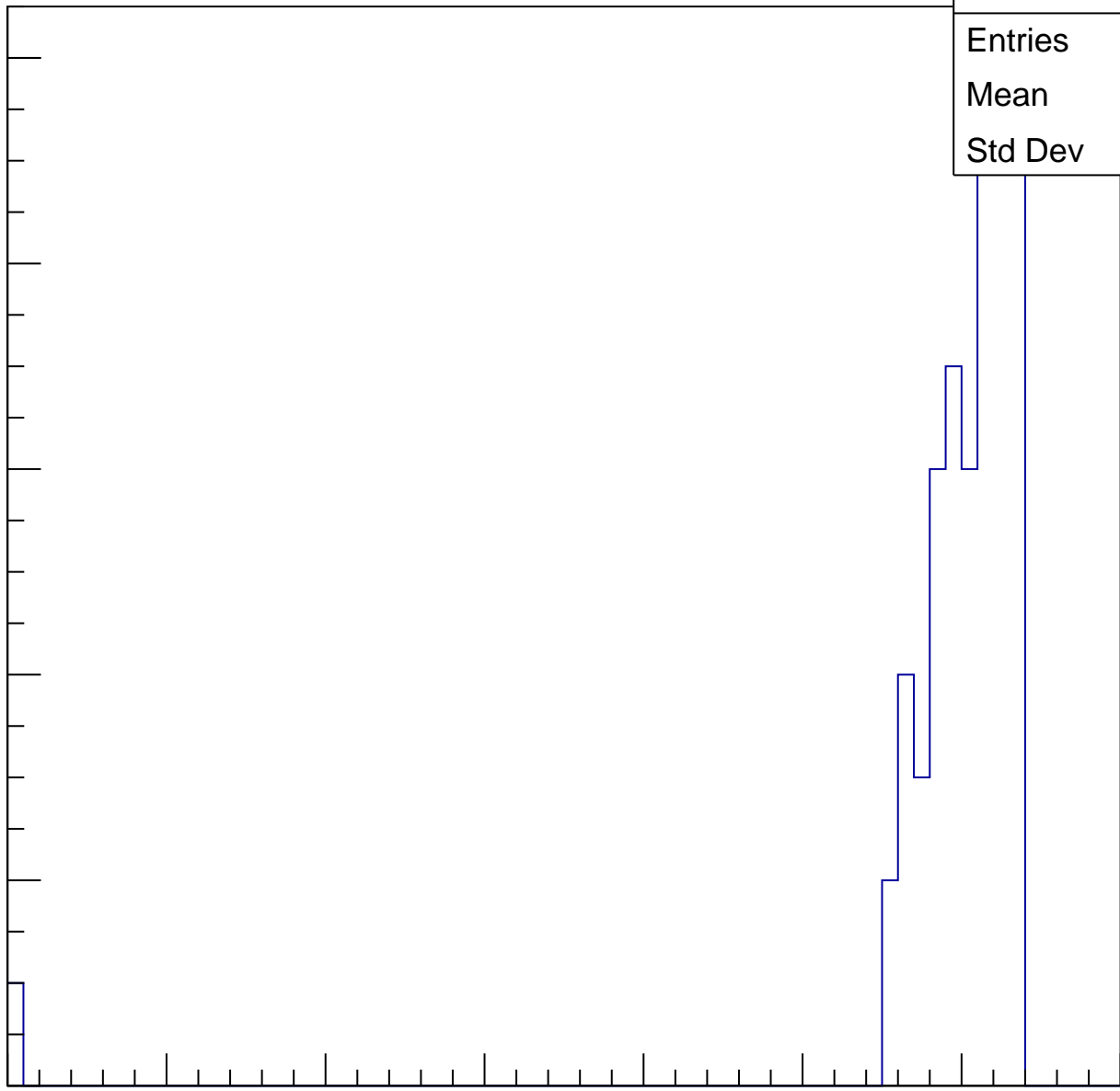
40

50

60

70

ampl



B1L103S, U24-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L103S, U24-ch15, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	30.49
Std Dev	5.121

Gaus mean : 31.6857

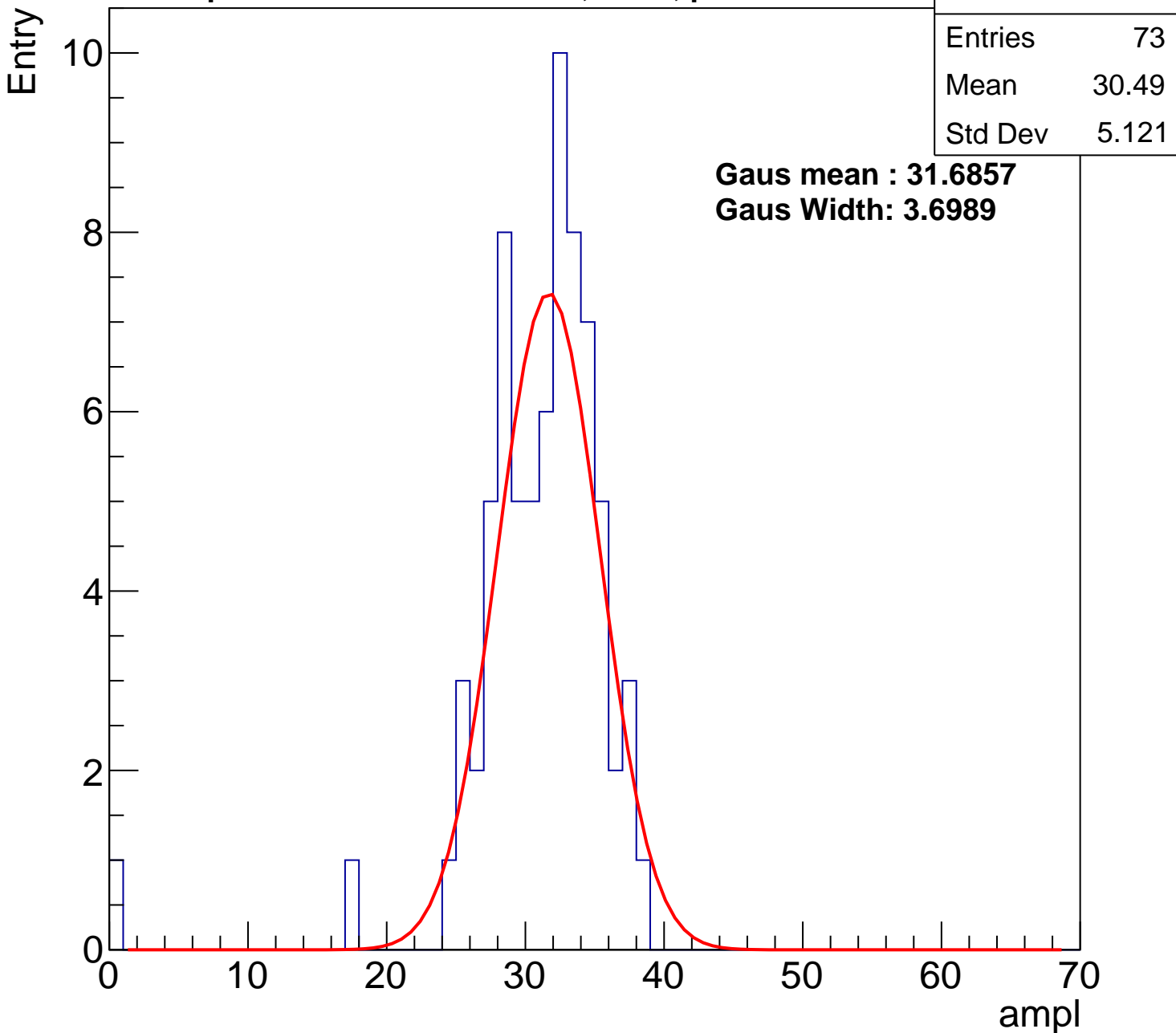
Gaus Width: 3.6989

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch15, adc1

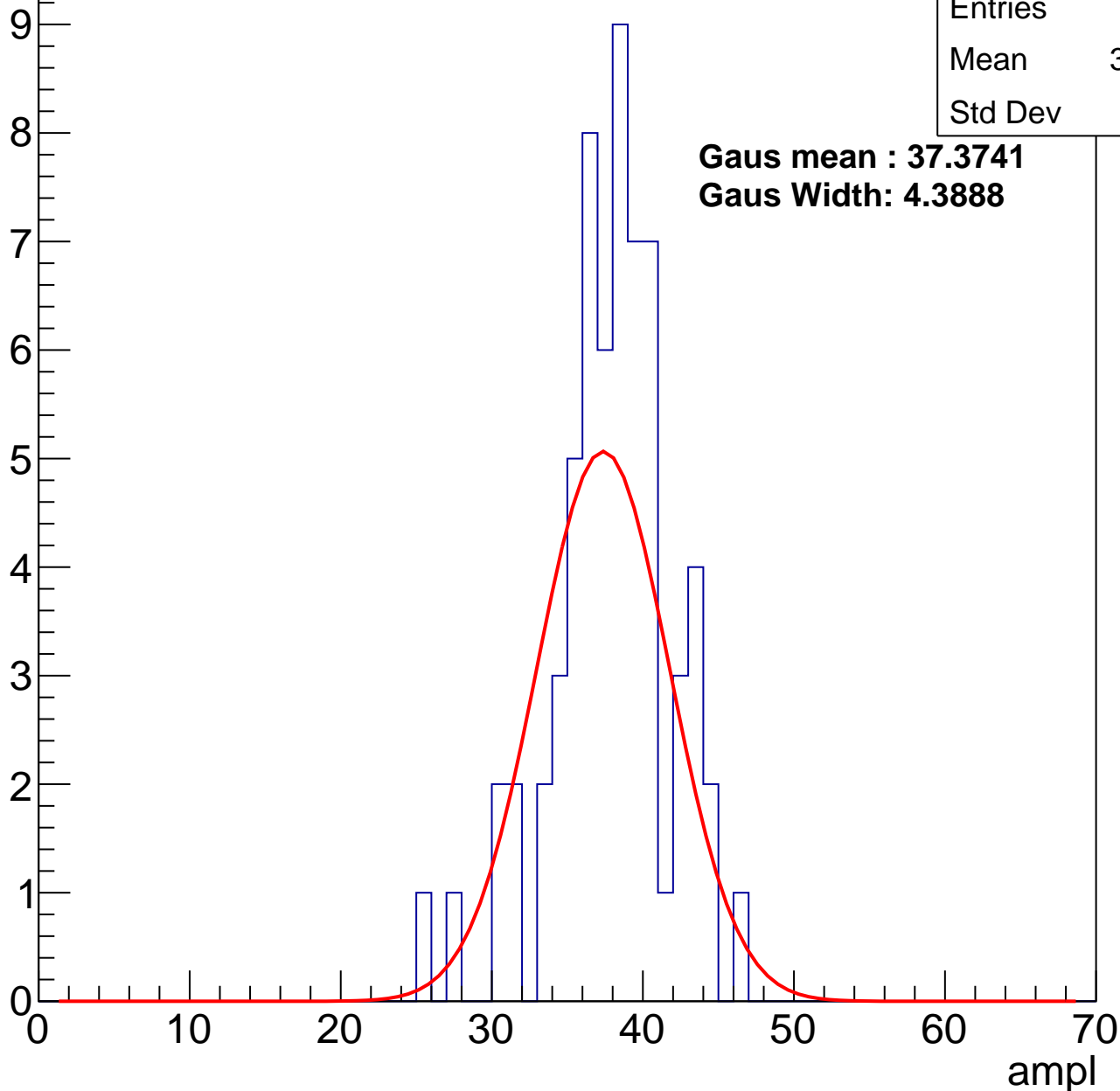
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.42
Std Dev	3.96

Gaus mean : 37.3741

Gaus Width: 4.3888



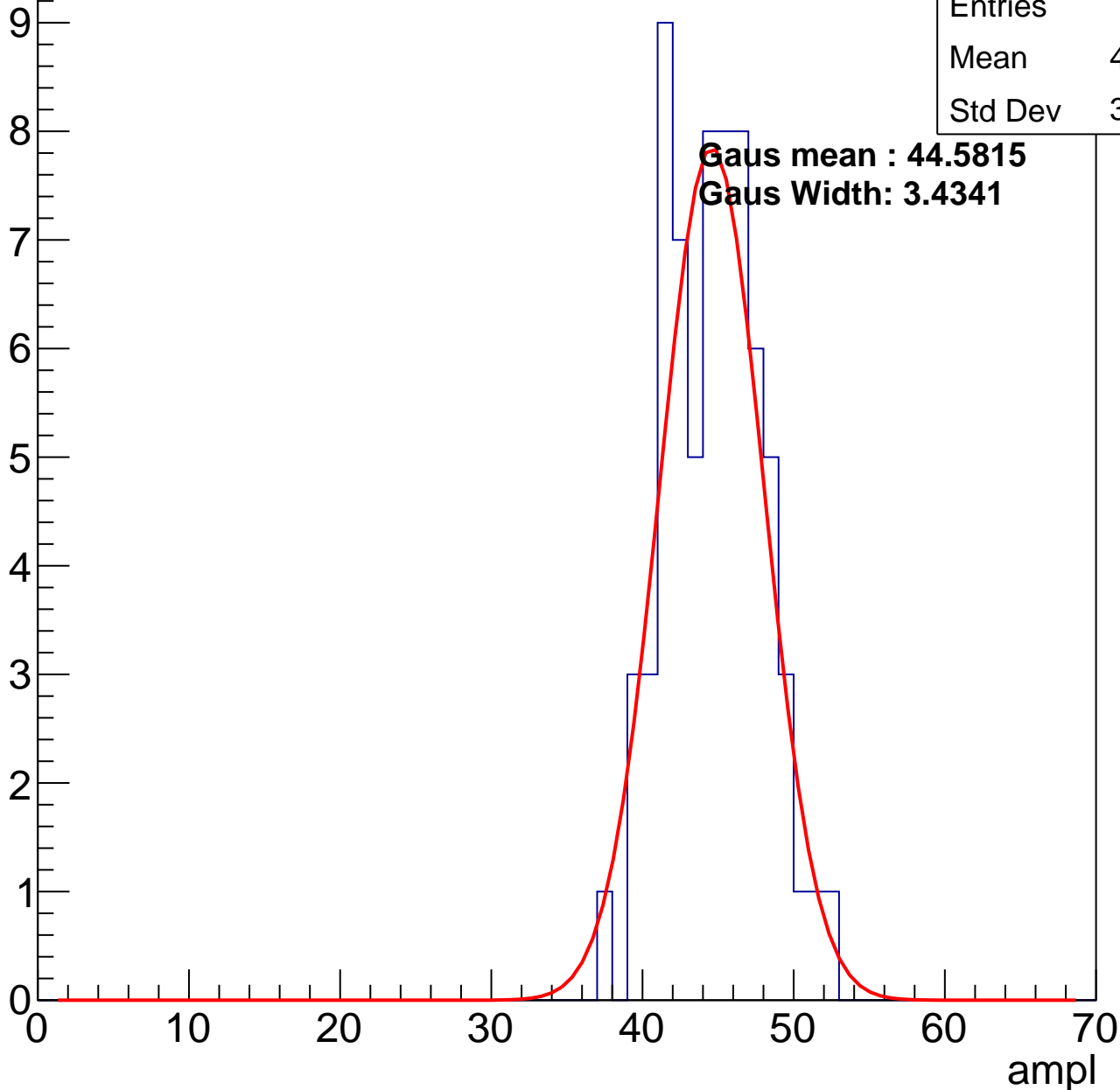
B1L103S, U24-ch15, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	44.26
Std Dev	3.138

Gaus mean : 44.5815
Gaus Width: 3.4341

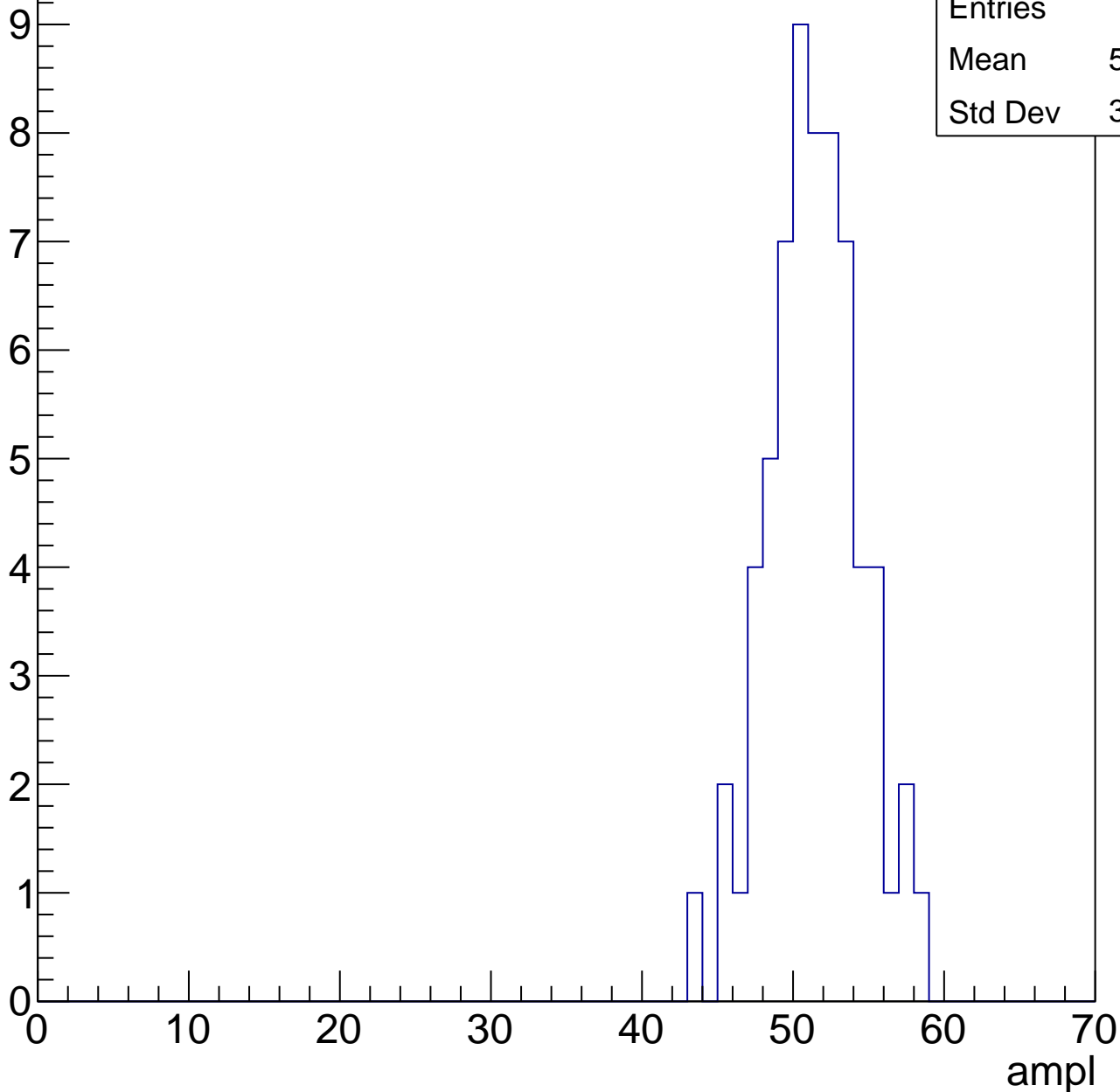


B1L103S, U24-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.92
Std Dev	3.033

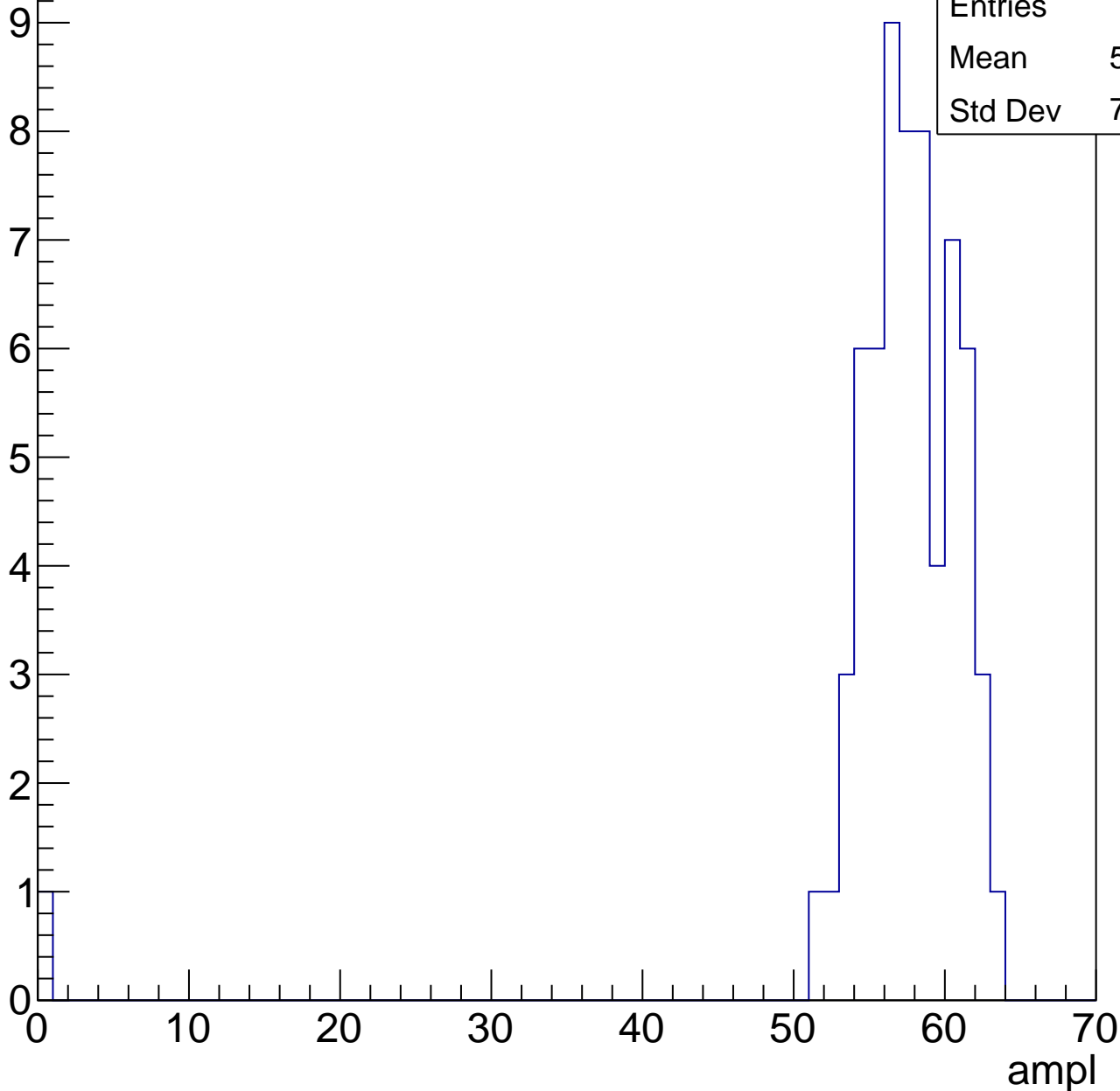


B1L103S, U24-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	56.42
Std Dev	7.622

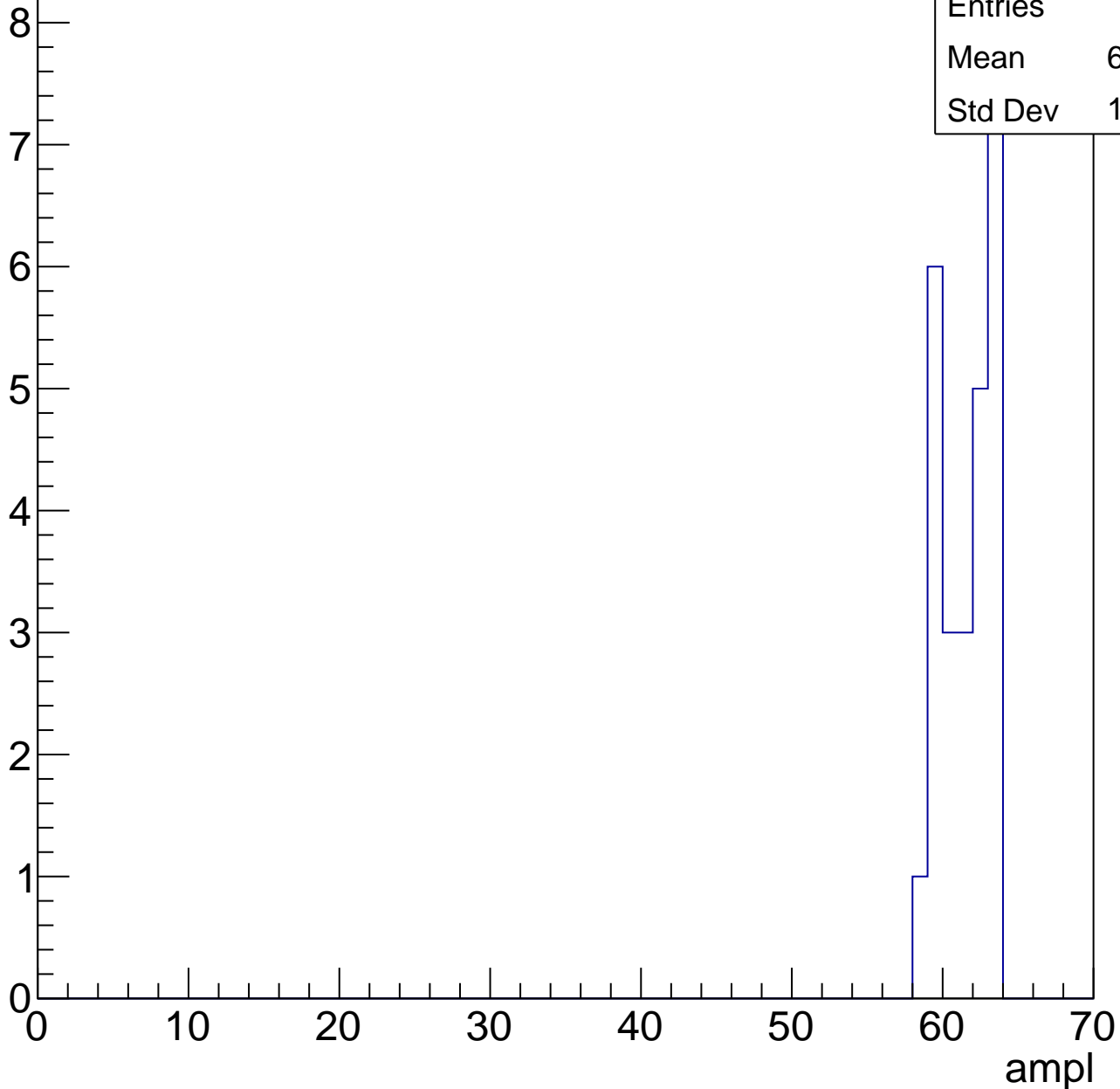


B1L103S, U24-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	61.12
Std Dev	1.672



B1L103S, U24-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch16, adc0

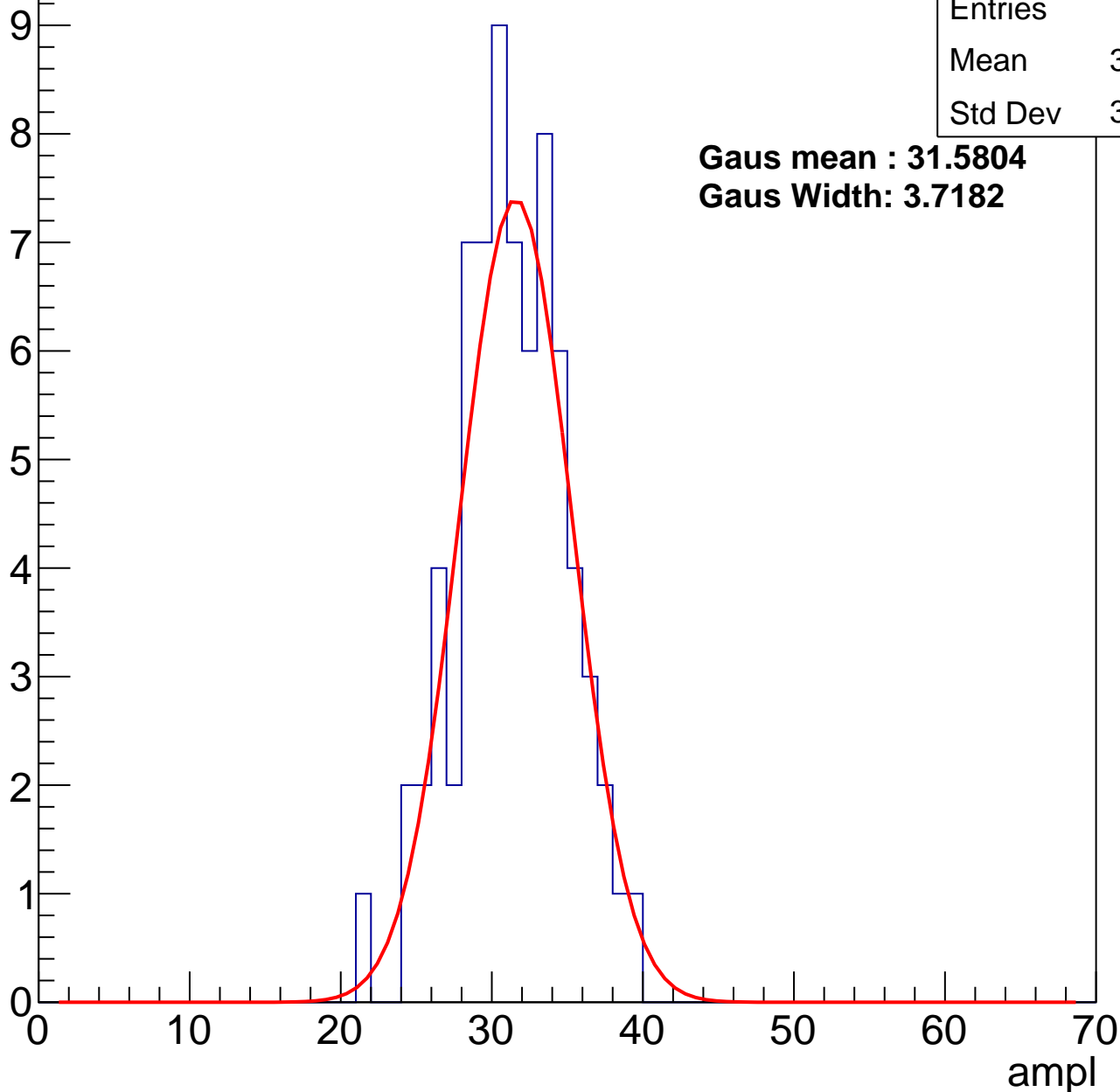
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.86
Std Dev	3.568

Gaus mean : 31.5804

Gaus Width: 3.7182



B1L103S, U24-ch16, adc1

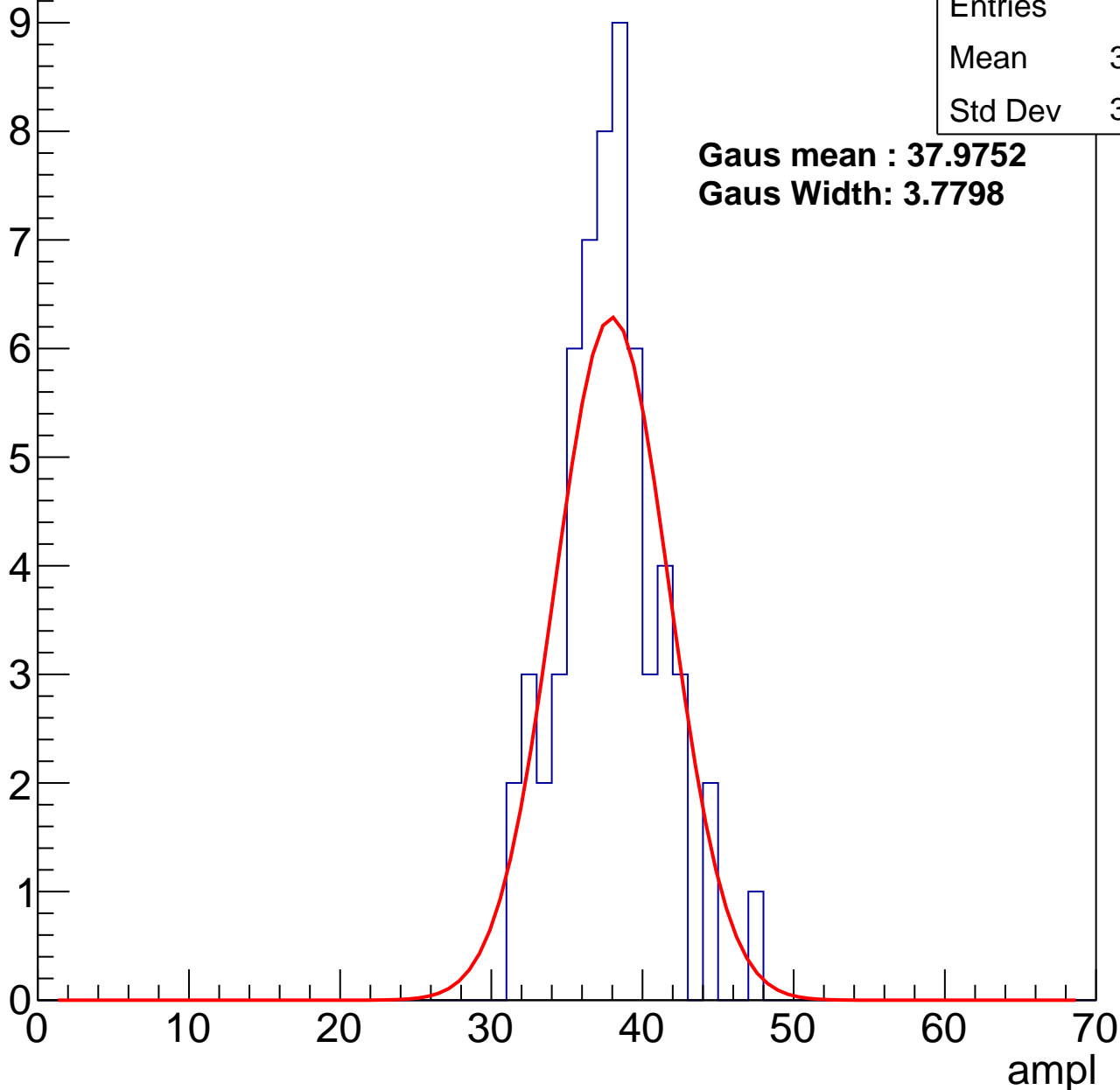
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	37.37
Std Dev	3.257

Gaus mean : 37.9752

Gaus Width: 3.7798



B1L103S, U24-ch16, adc2

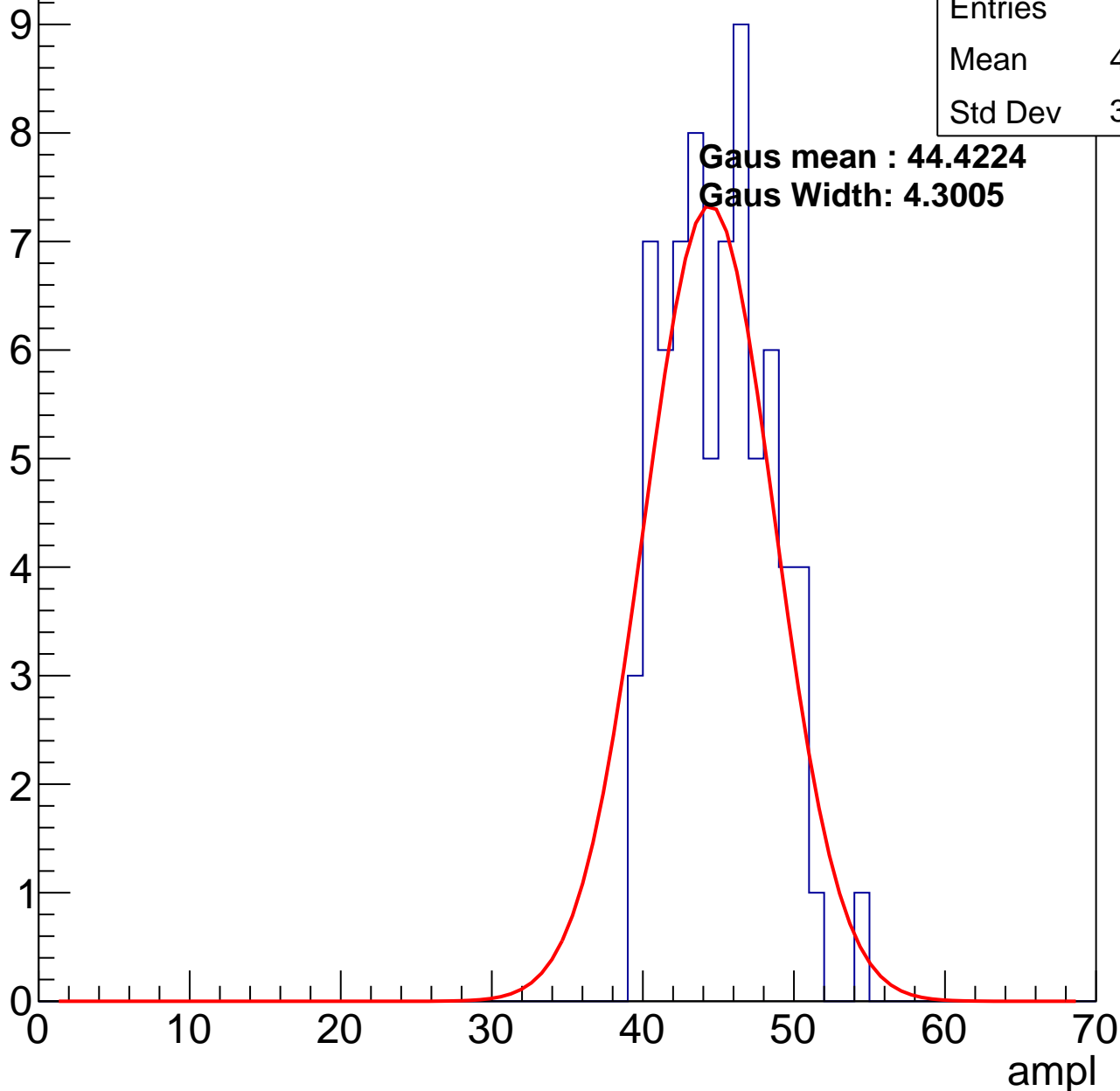
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	44.58
Std Dev	3.372

Gaus mean : 44.4224

Gaus Width: 4.3005

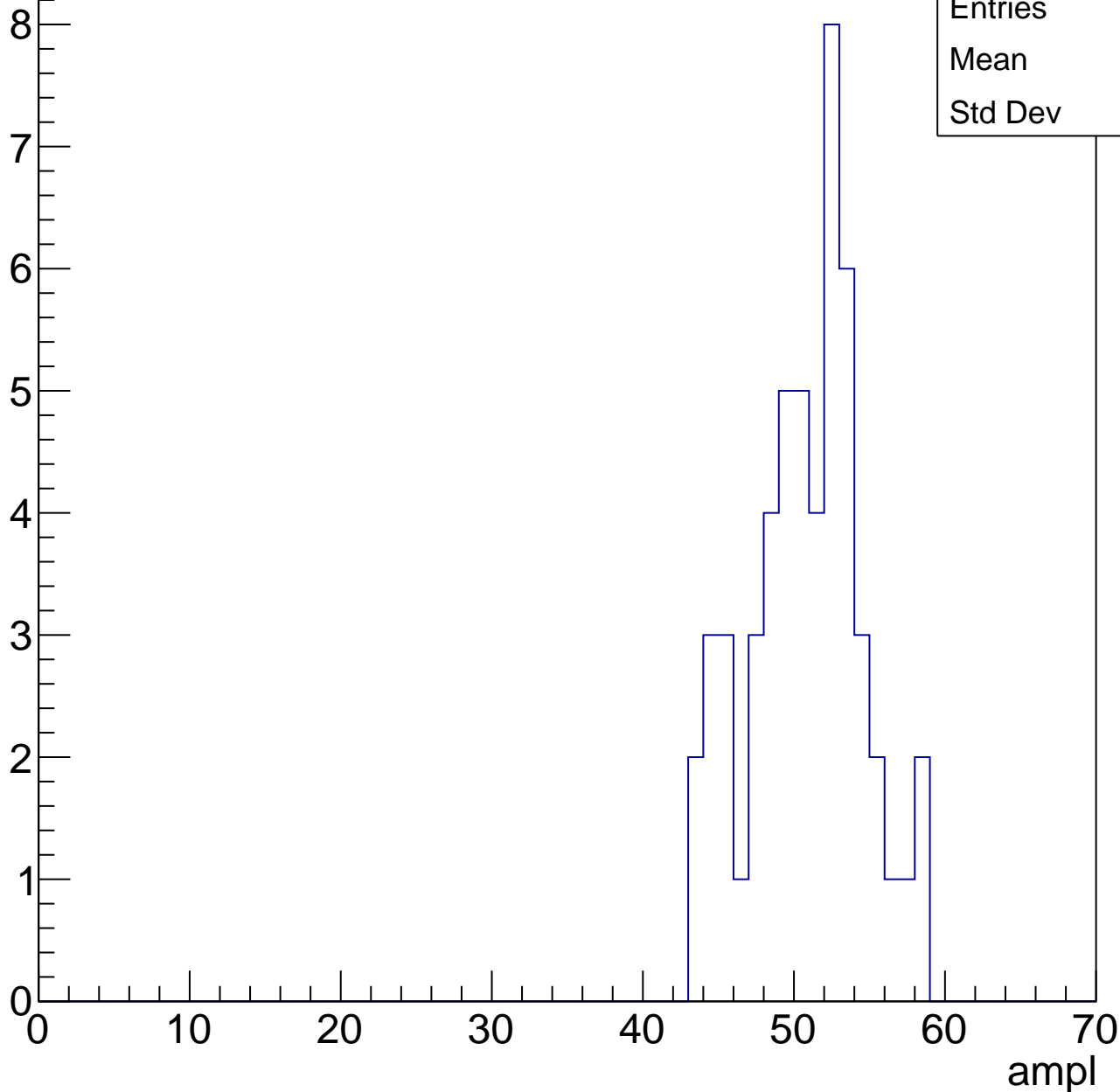


B1L103S, U24-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

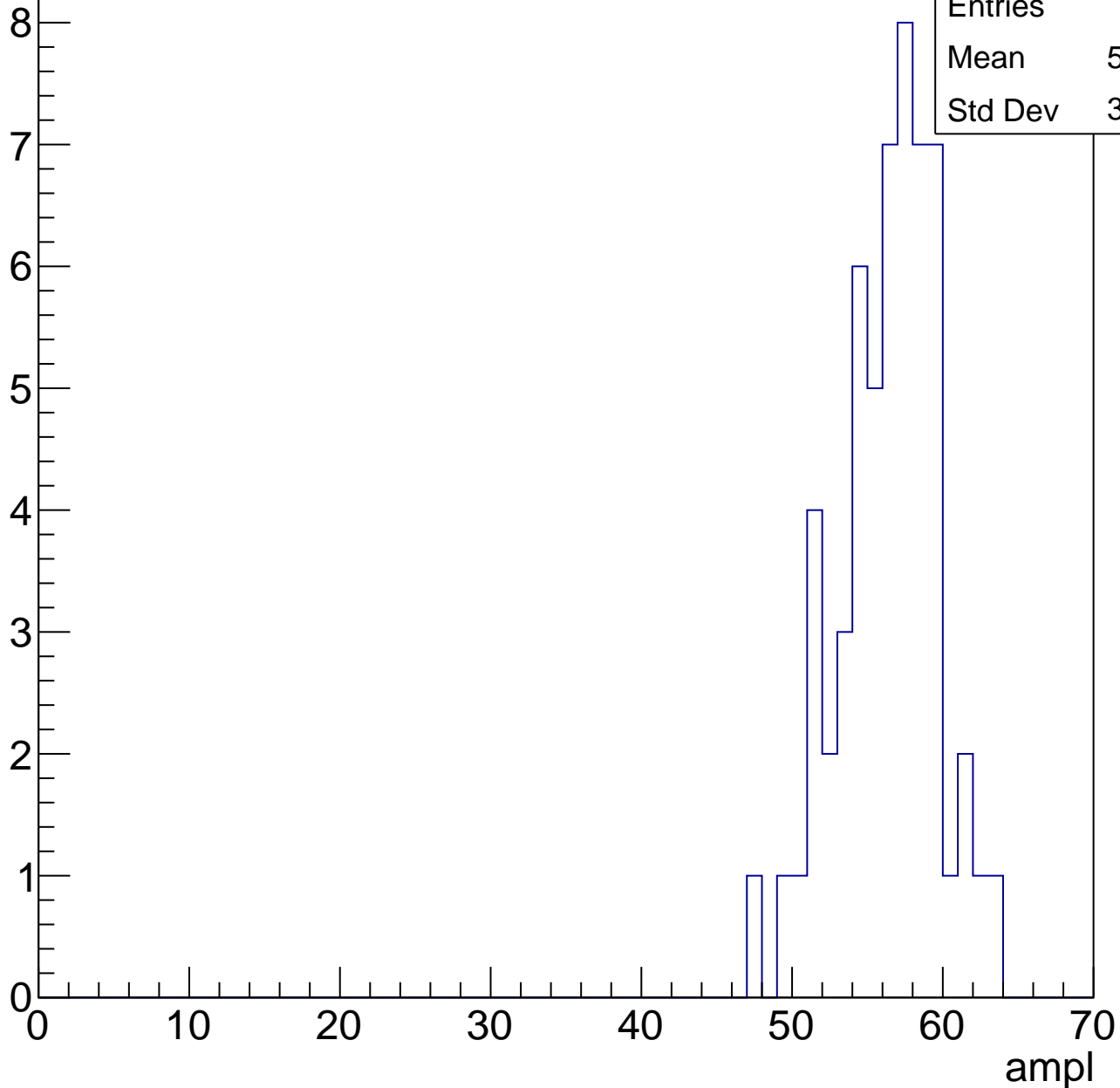
Entries	53
Mean	50.3
Std Dev	3.74



B1L103S, U24-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

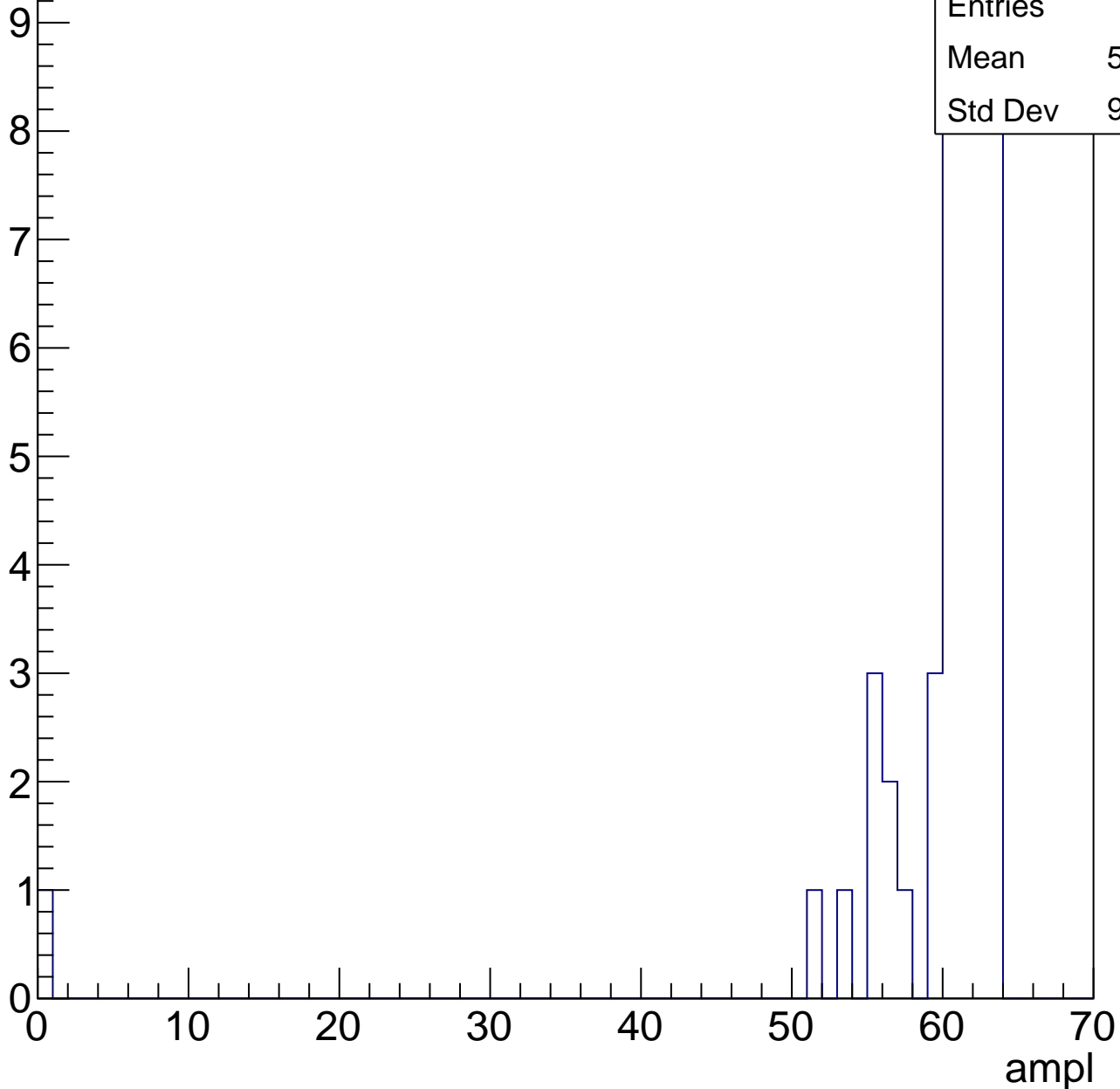


B1L103S, U24-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

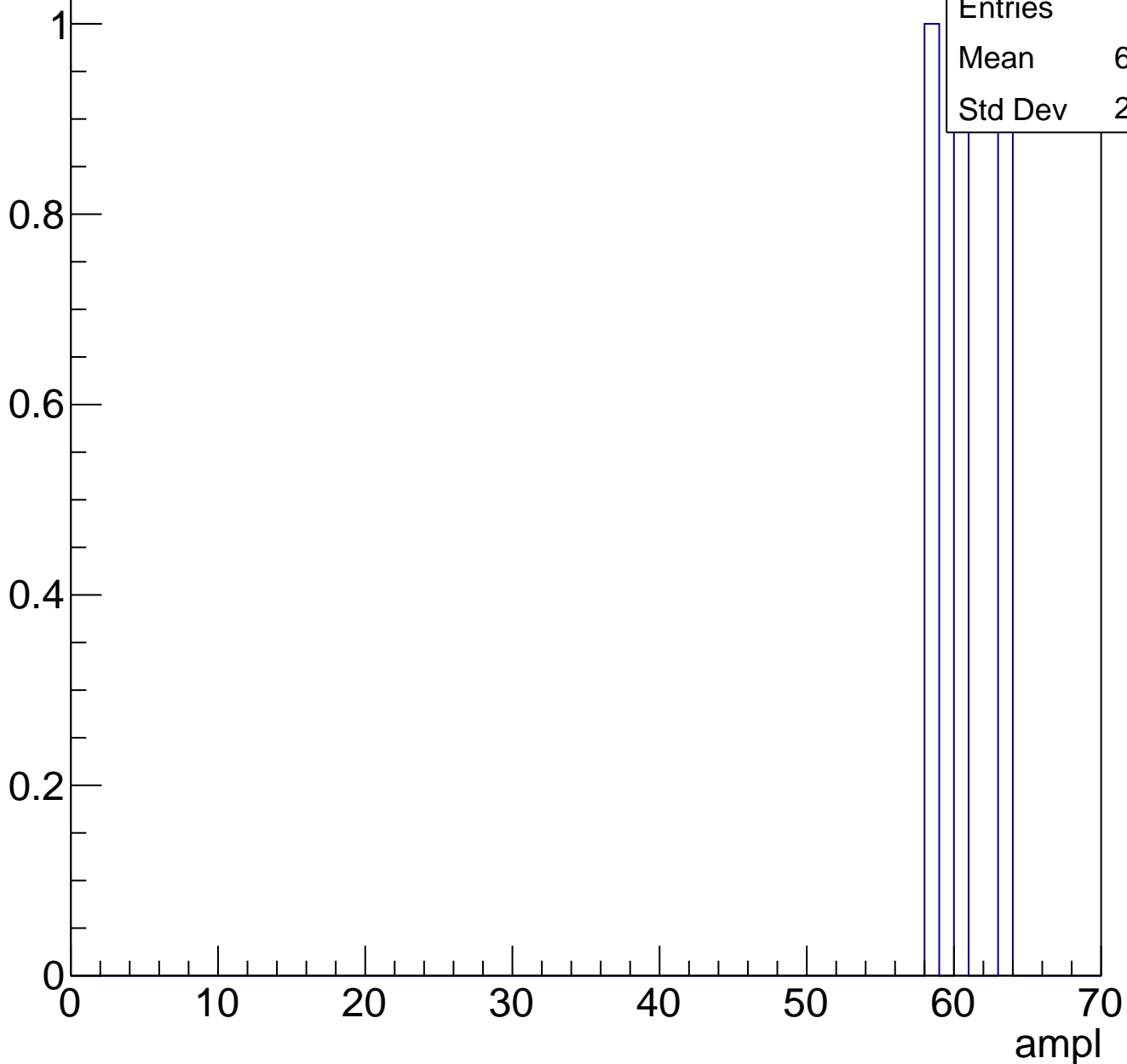
Entries	45
Mean	58.78
Std Dev	9.307



B1L103S, U24-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch17, adc0

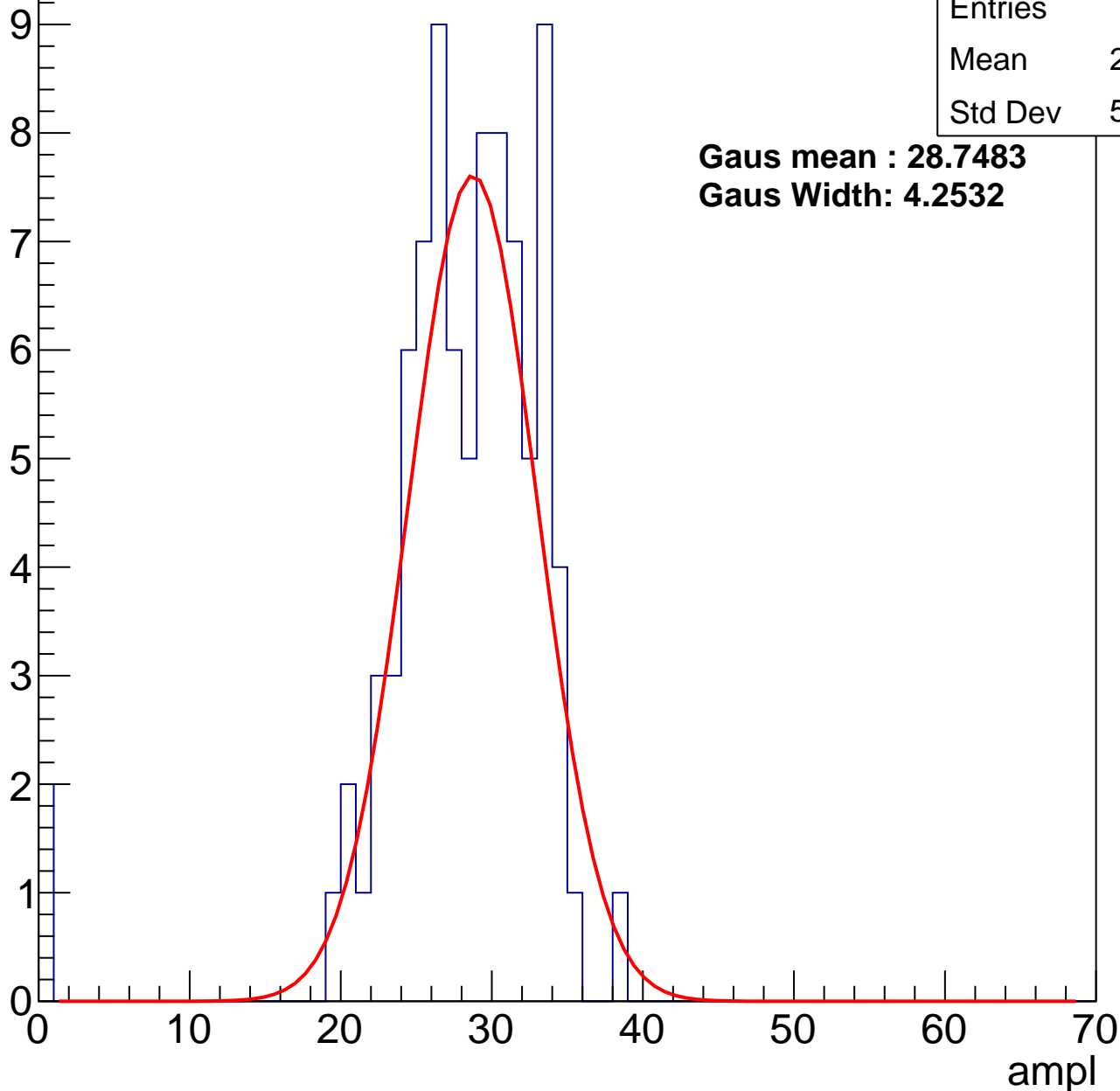
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	88
Mean	27.56
Std Dev	5.734

Gaus mean : 28.7483

Gaus Width: 4.2532



B1L103S, U24-ch17, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	36.93
Std Dev	3.531

Gaus mean : 37.5967

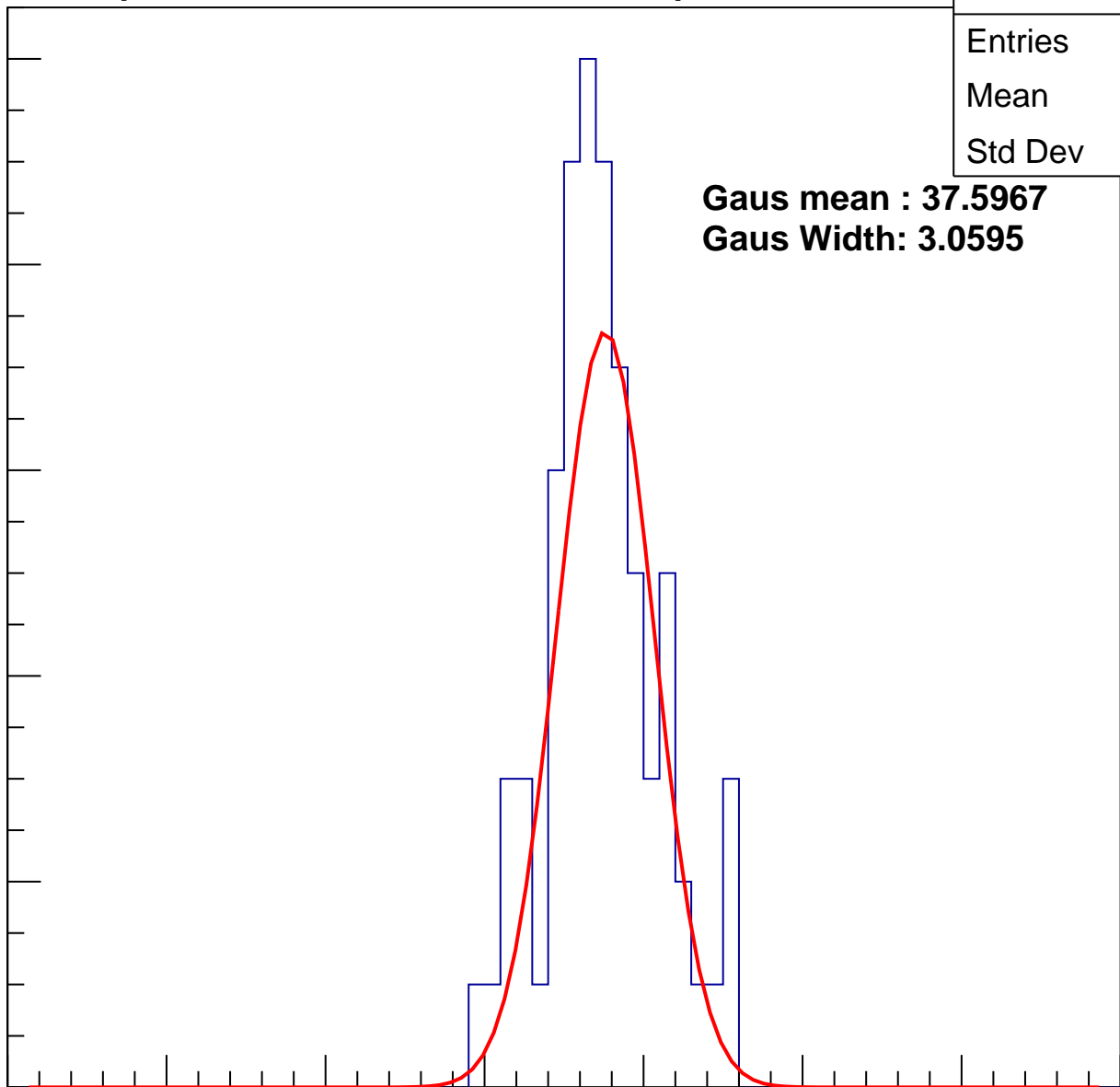
Gaus Width: 3.0595

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch17, adc2

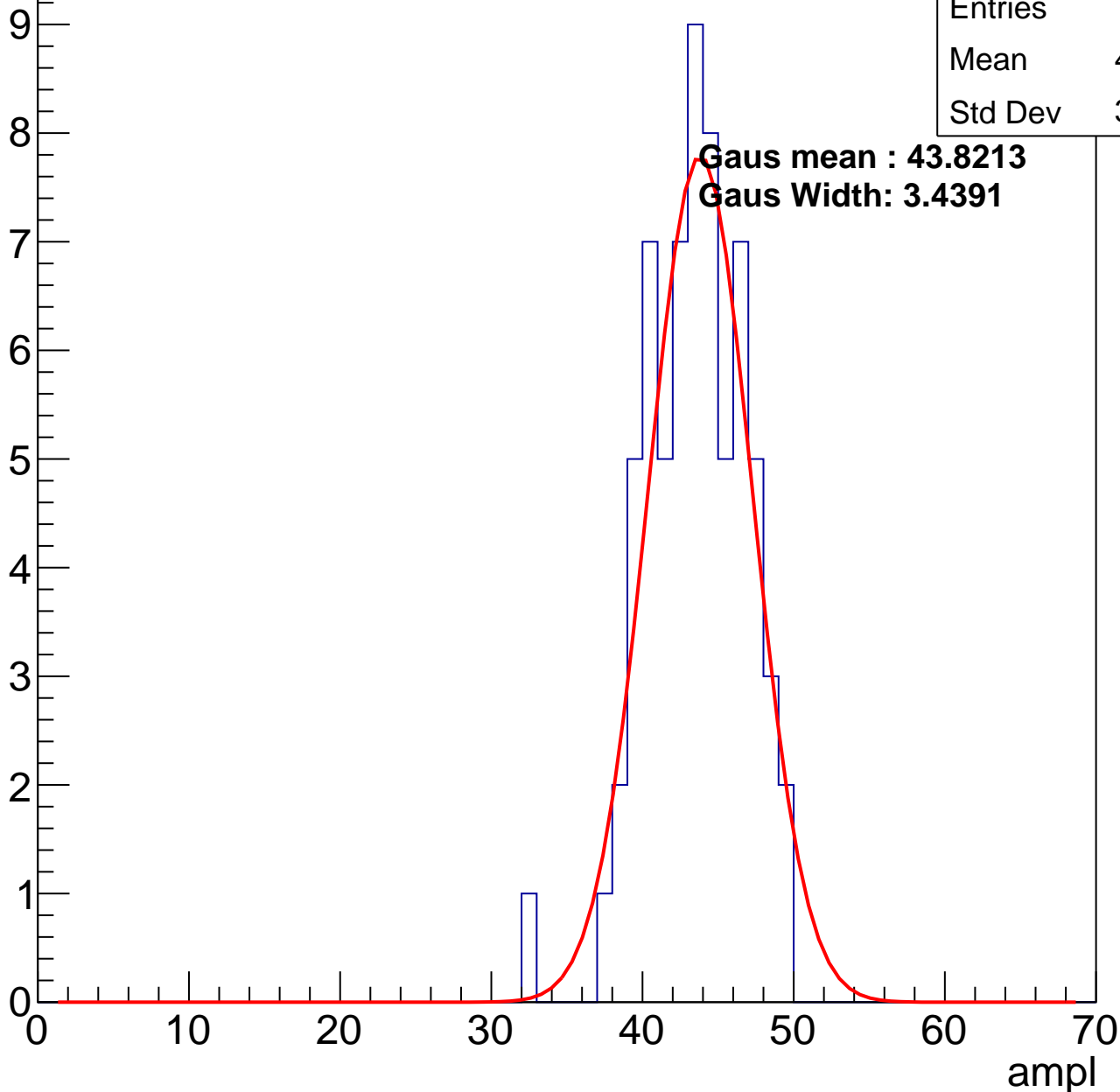
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.01
Std Dev	3.221

Gaus mean : 43.8213

Gaus Width: 3.4391

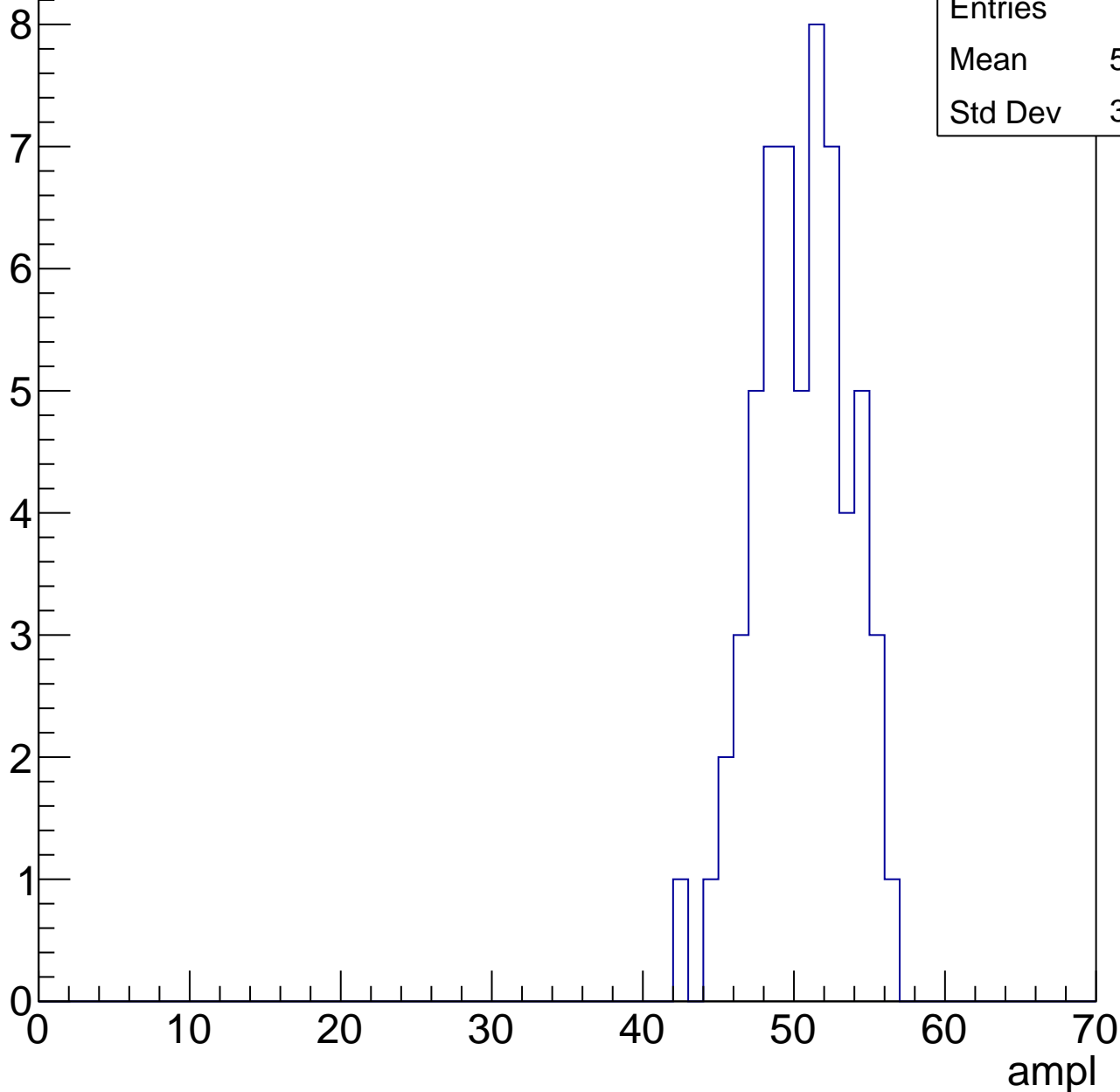


B1L103S, U24-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.05
Std Dev	3.028

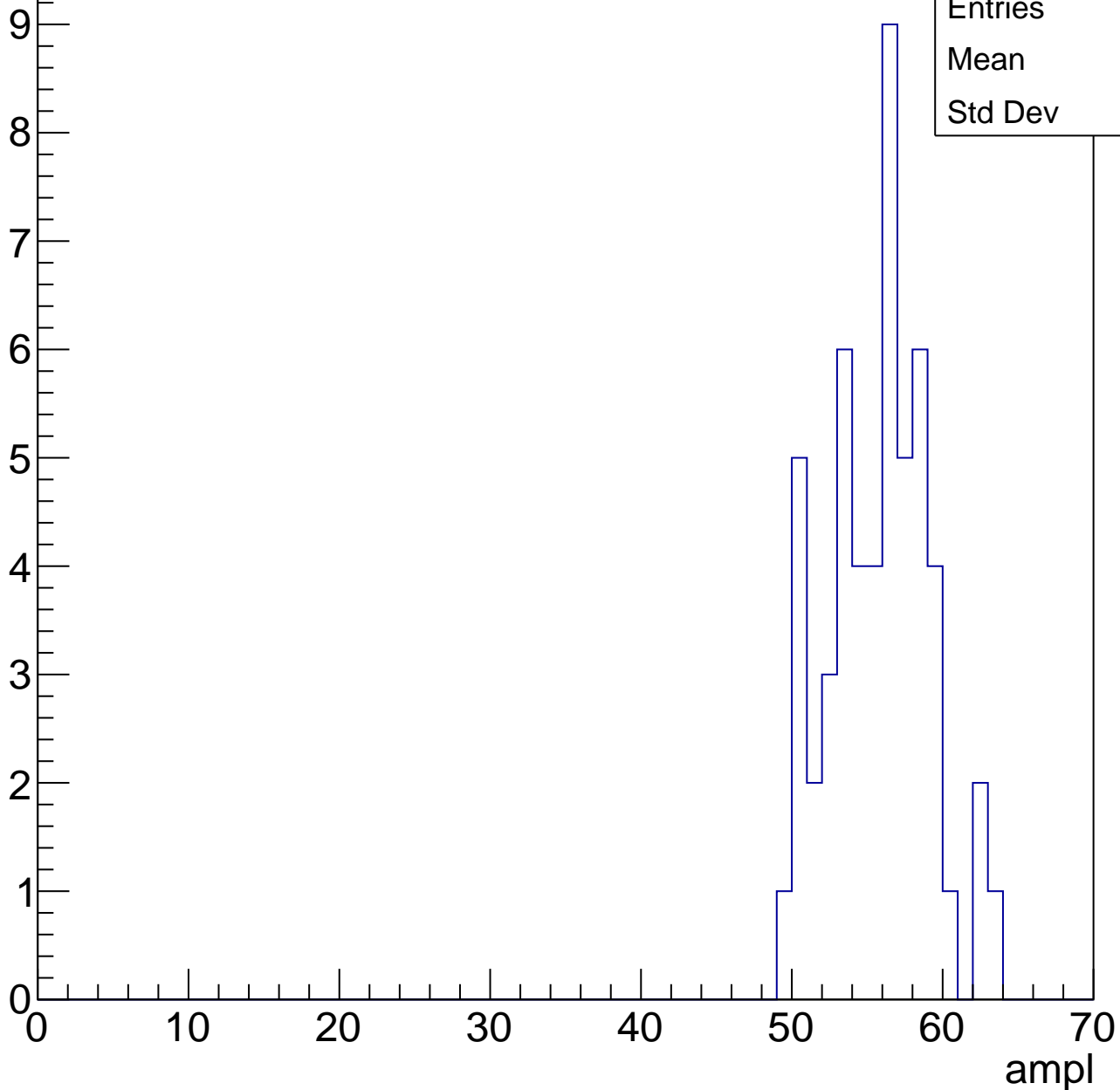


B1L103S, U24-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.3
Std Dev	3.3



B1L103S, U24-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	54
Mean	60.02
Std Dev	2.345

B1L103S, U24-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	31
Std Dev	31

B1L103S, U24-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch18, adc0

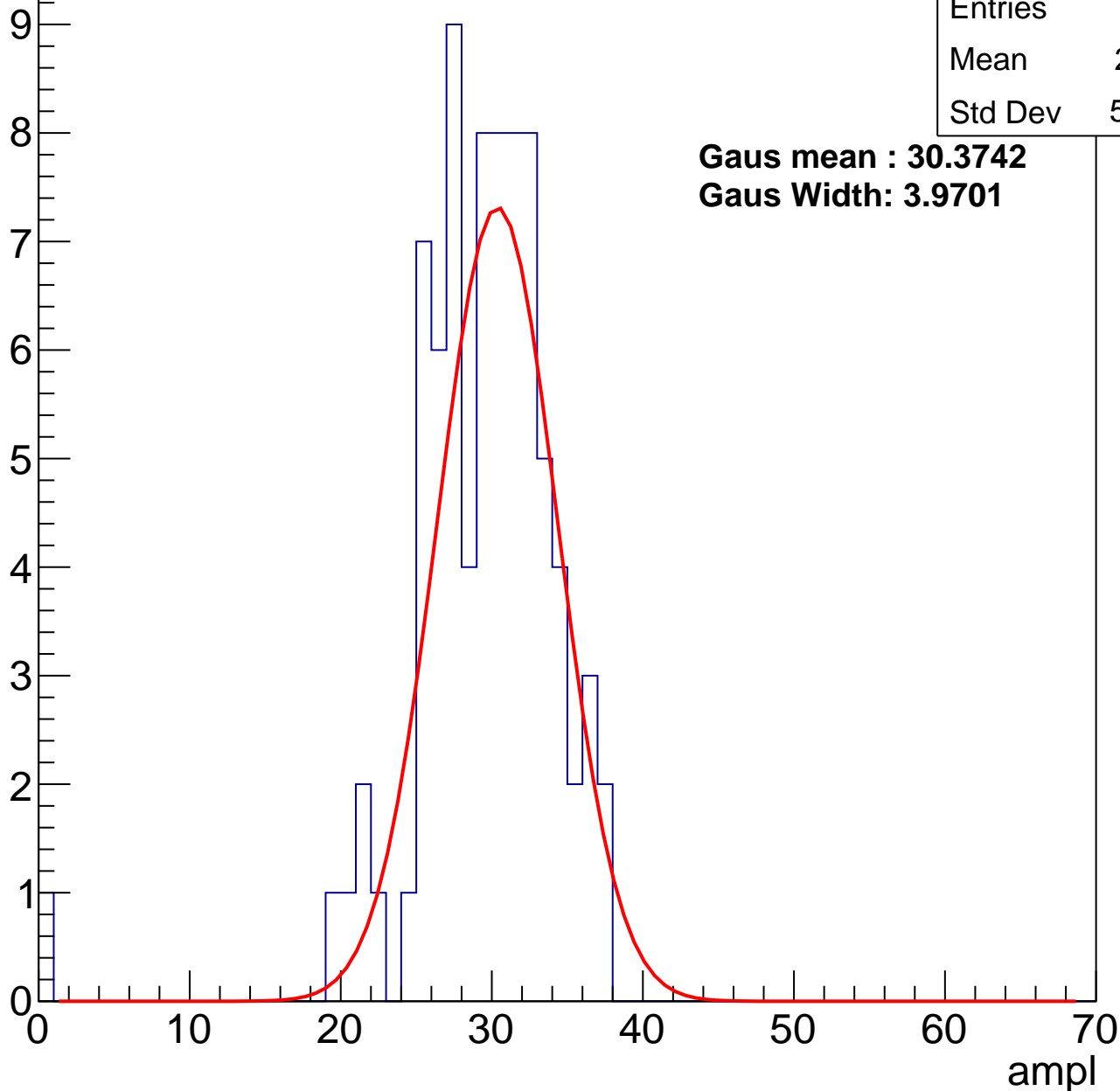
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	28.91
Std Dev	5.053

Gaus mean : 30.3742

Gaus Width: 3.9701



B1L103S, U24-ch18, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	37.17
Std Dev	3.281

Gaus mean : 37.5125

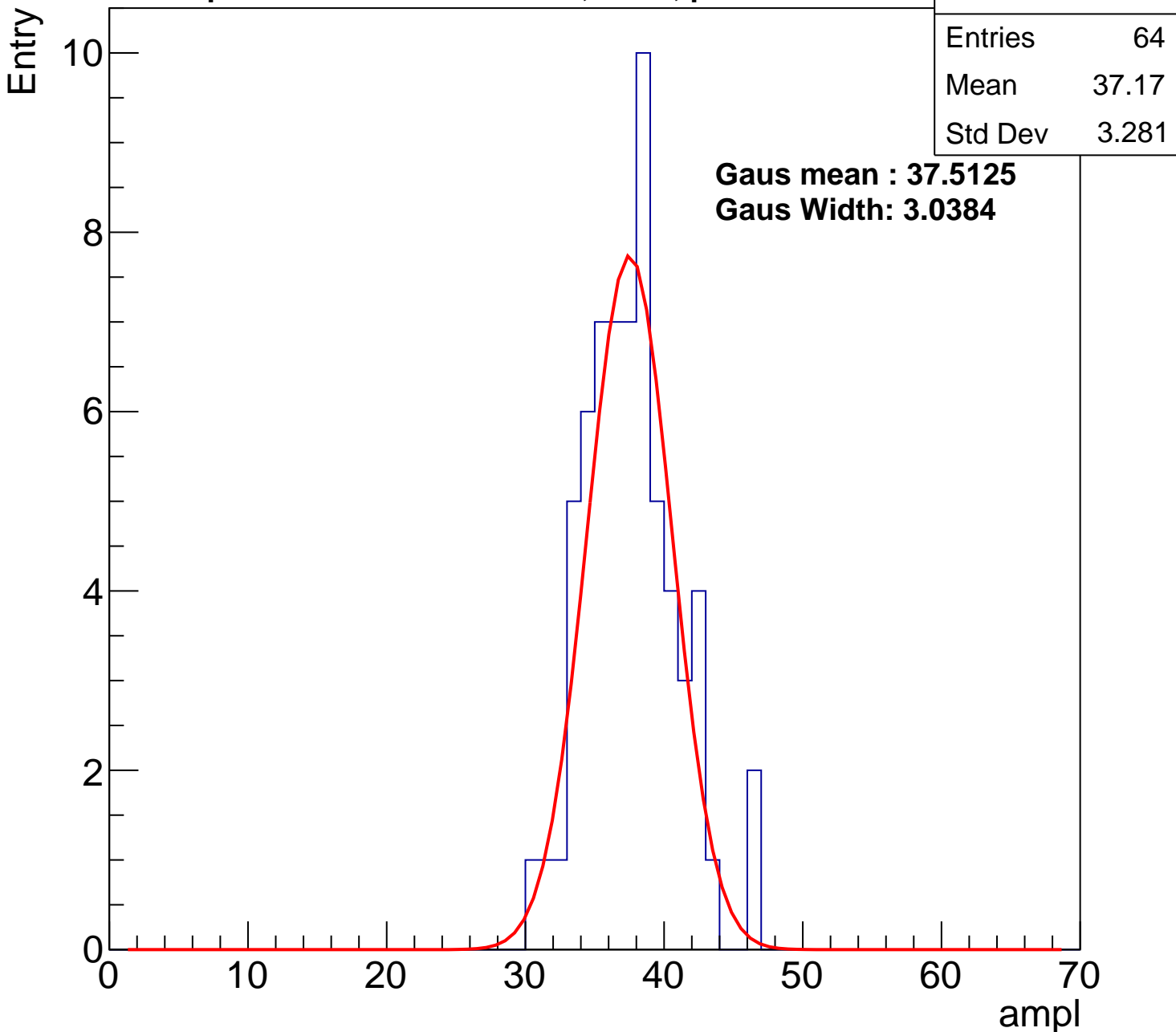
Gaus Width: 3.0384

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch18, adc2

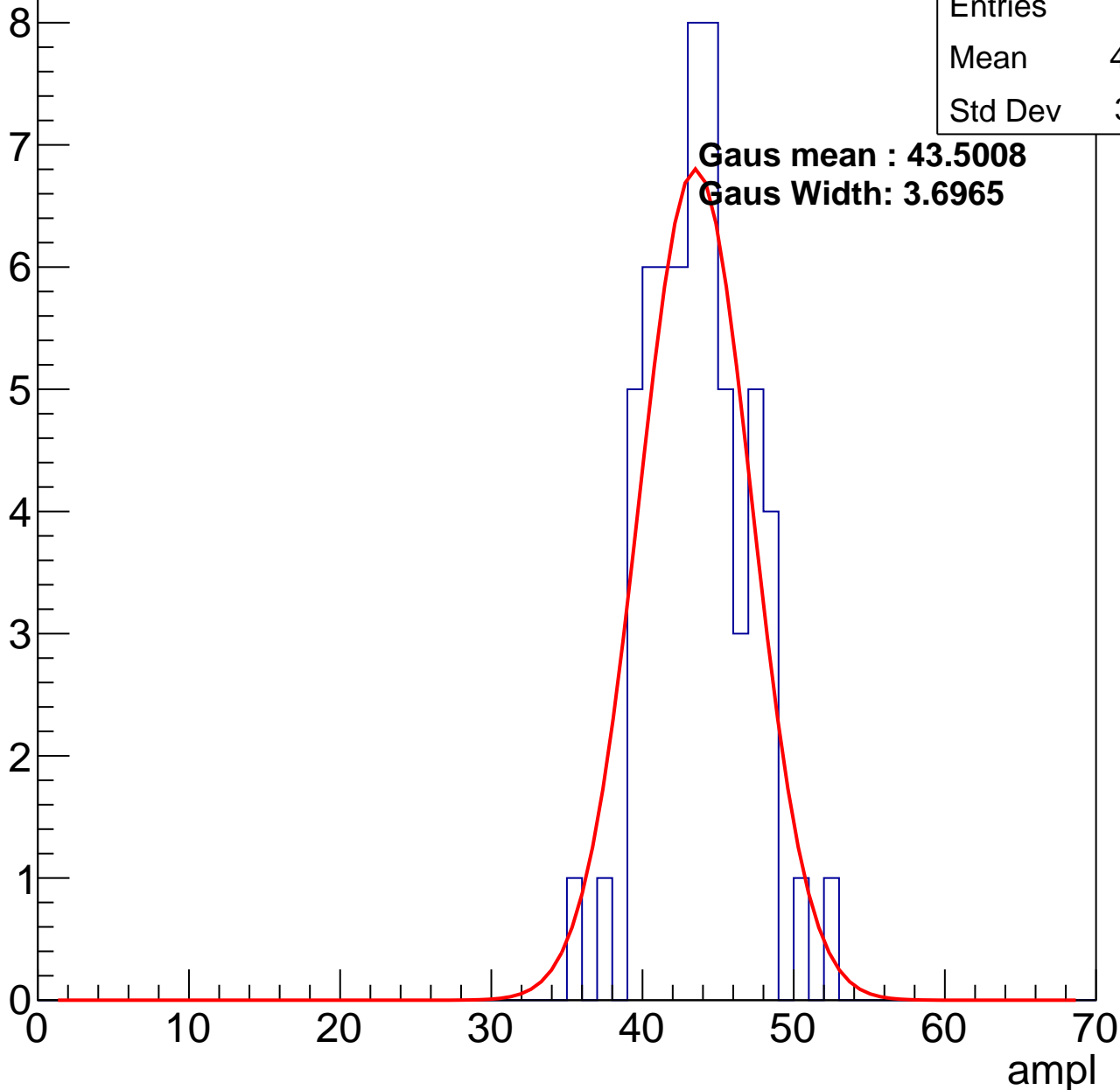
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.22
Std Dev	3.231

Gaus mean : 43.5008

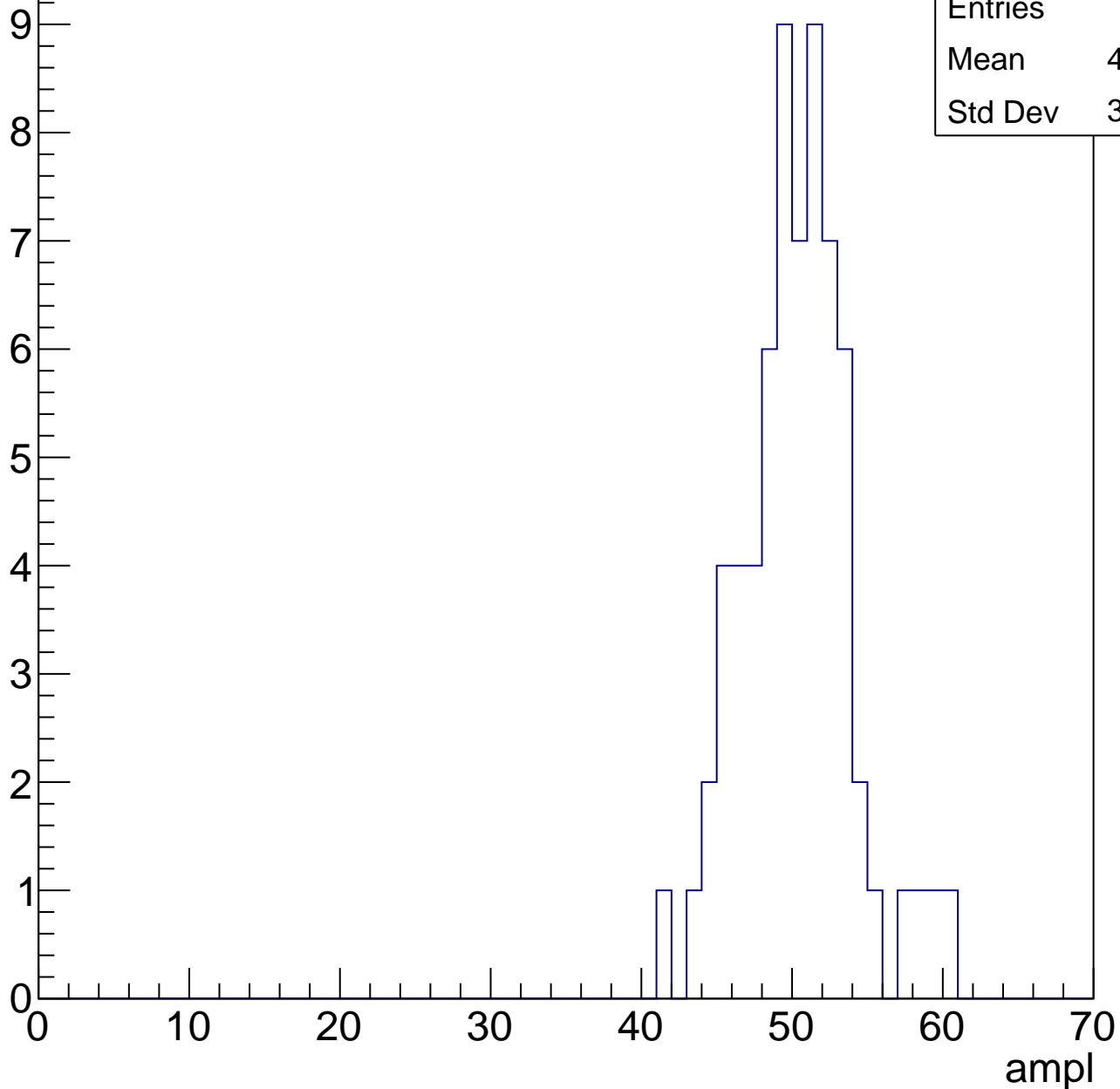
Gaus Width: 3.6965



B1L103S, U24-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	67
Mean	49.87
Std Dev	3.615

B1L103S, U24-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

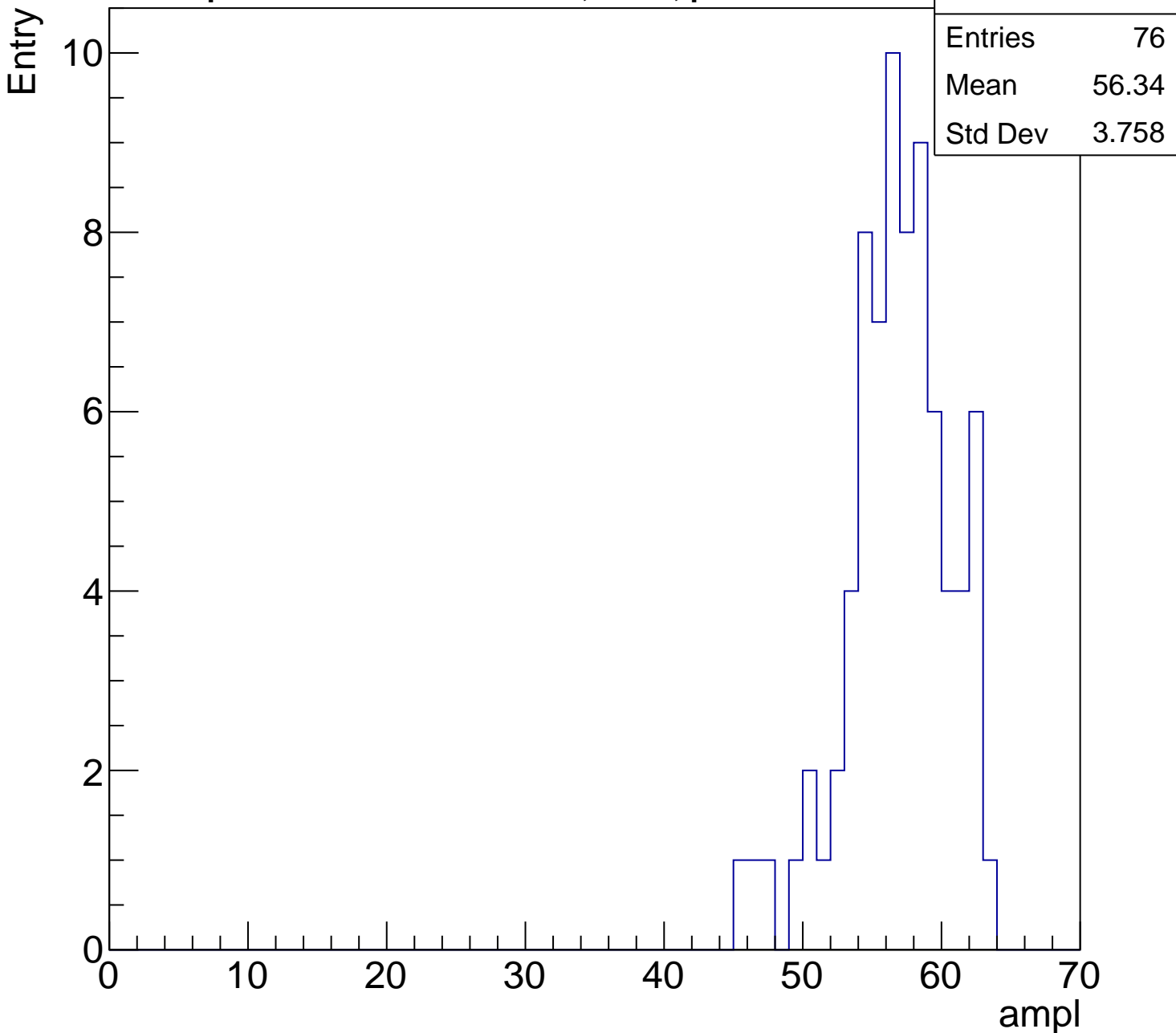
Entries	76
Mean	56.34
Std Dev	3.758

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

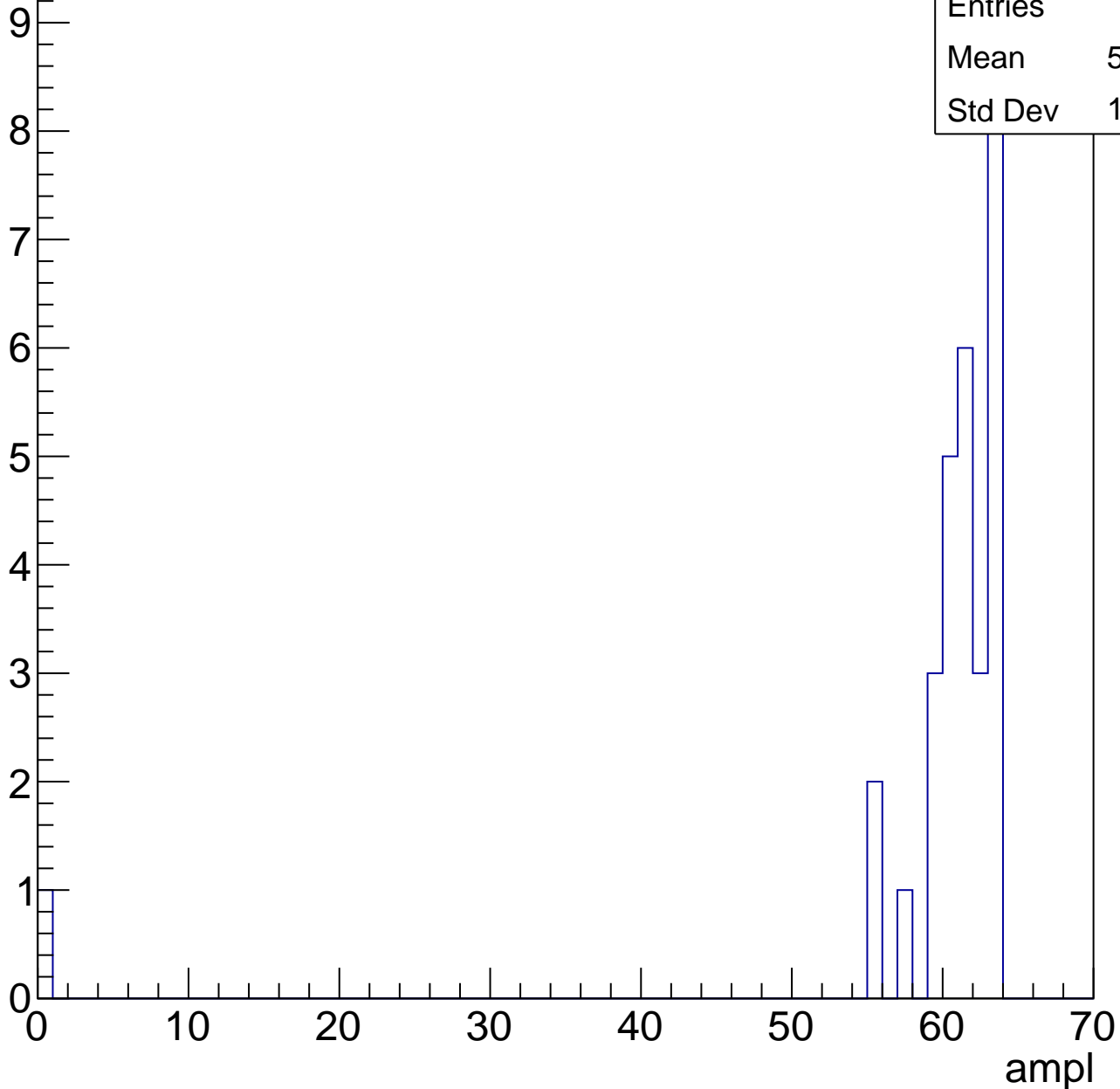


B1L103S, U24-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	58.77
Std Dev	11.13



B1L103S, U24-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch19, adc0

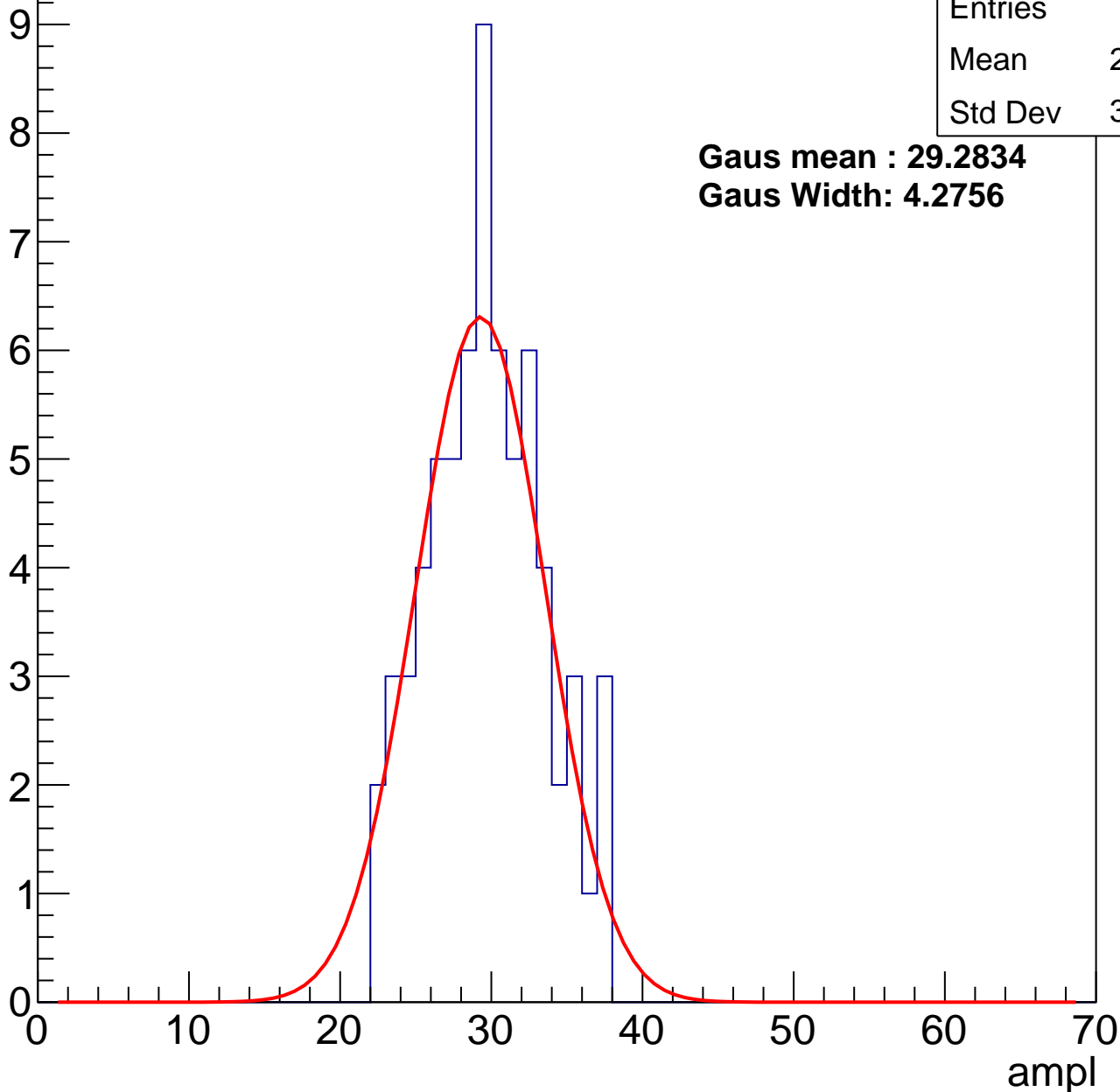
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.22
Std Dev	3.777

Gaus mean : 29.2834

Gaus Width: 4.2756



B1L103S, U24-ch19, adc1

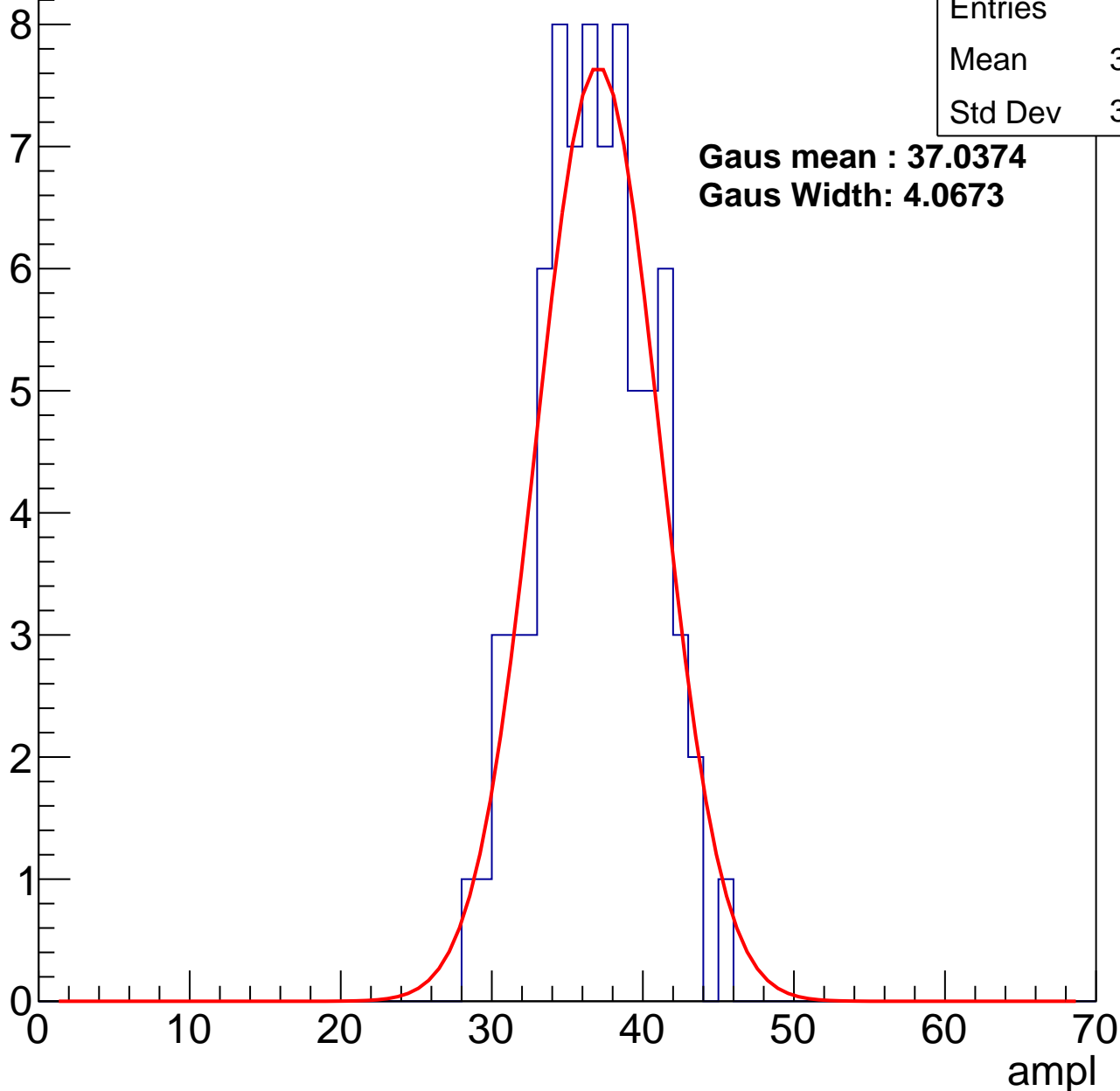
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.36
Std Dev	3.654

Gaus mean : 37.0374

Gaus Width: 4.0673



B1L103S, U24-ch19, adc2

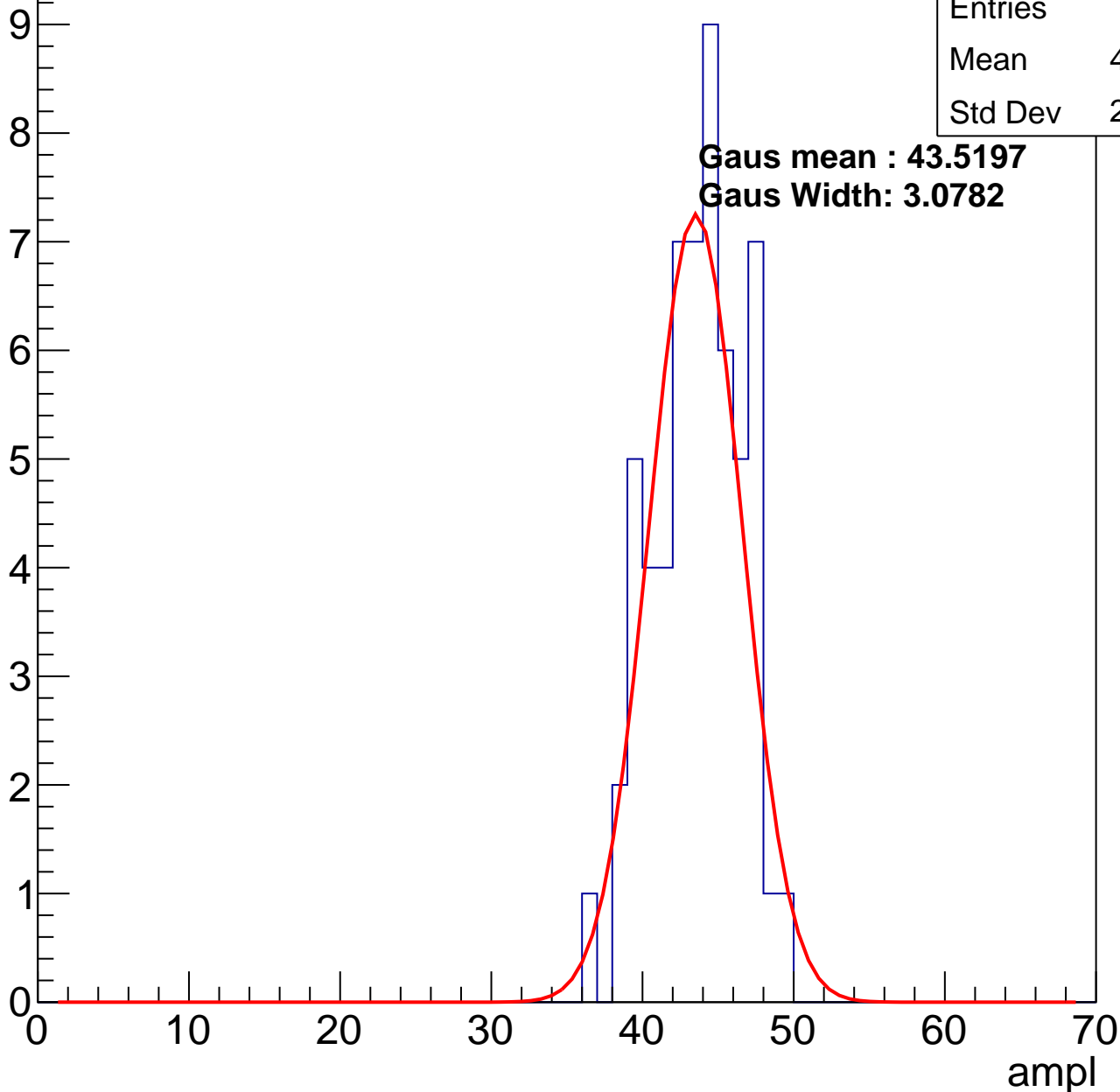
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.19
Std Dev	2.873

Gaus mean : 43.5197

Gaus Width: 3.0782

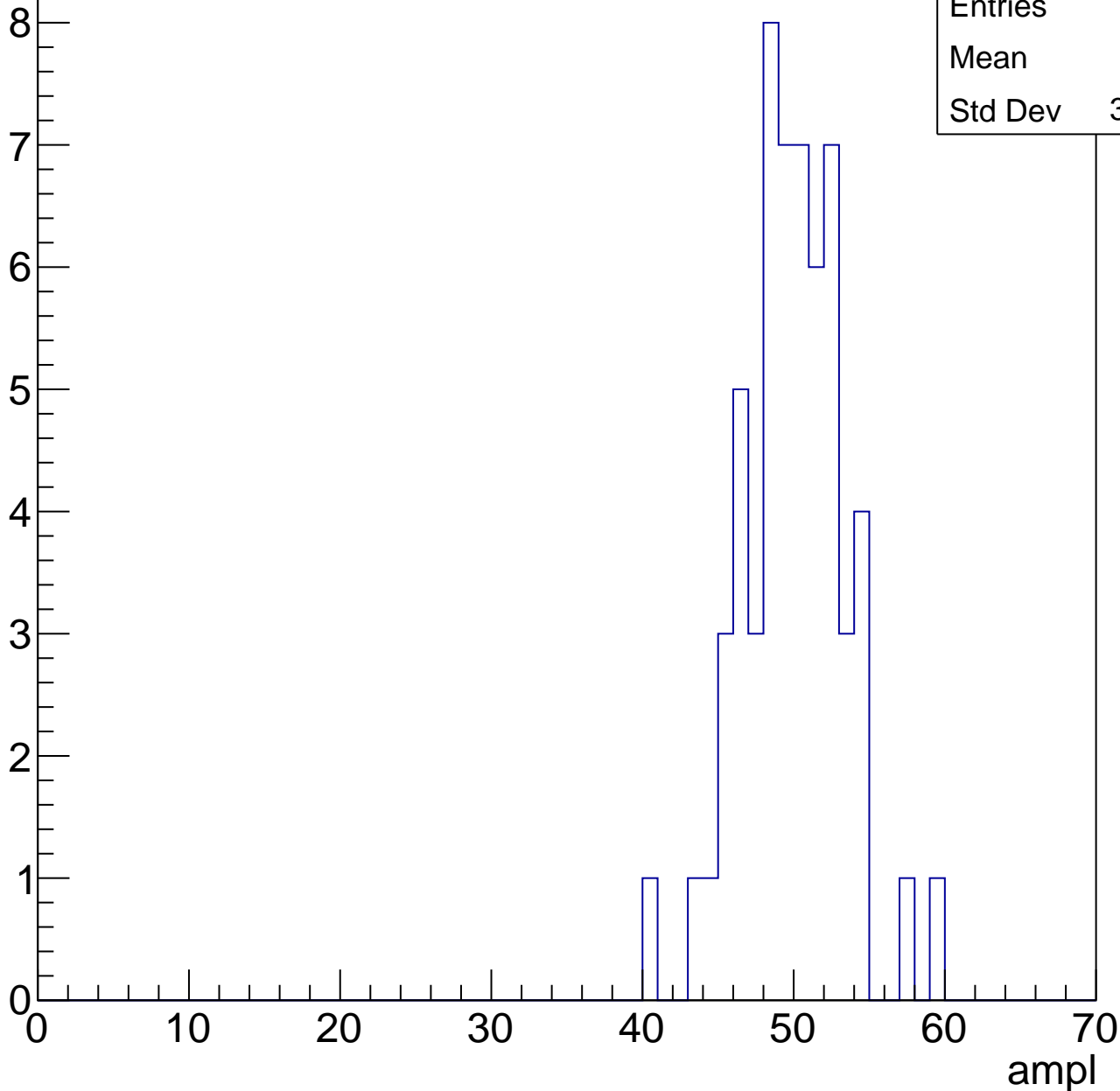


B1L103S, U24-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.5
Std Dev	3.339

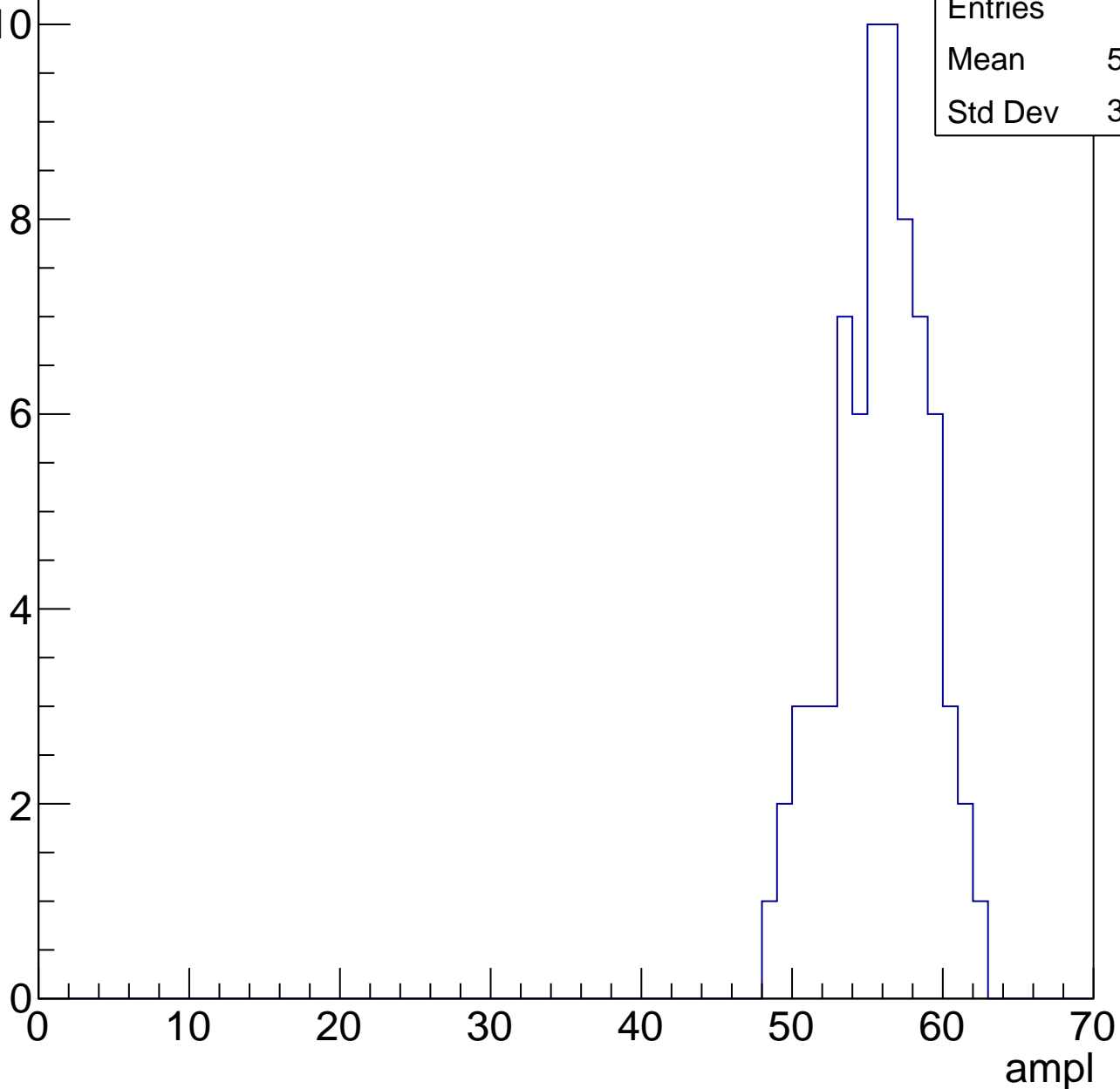


B1L103S, U24-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	55.42
Std Dev	3.095

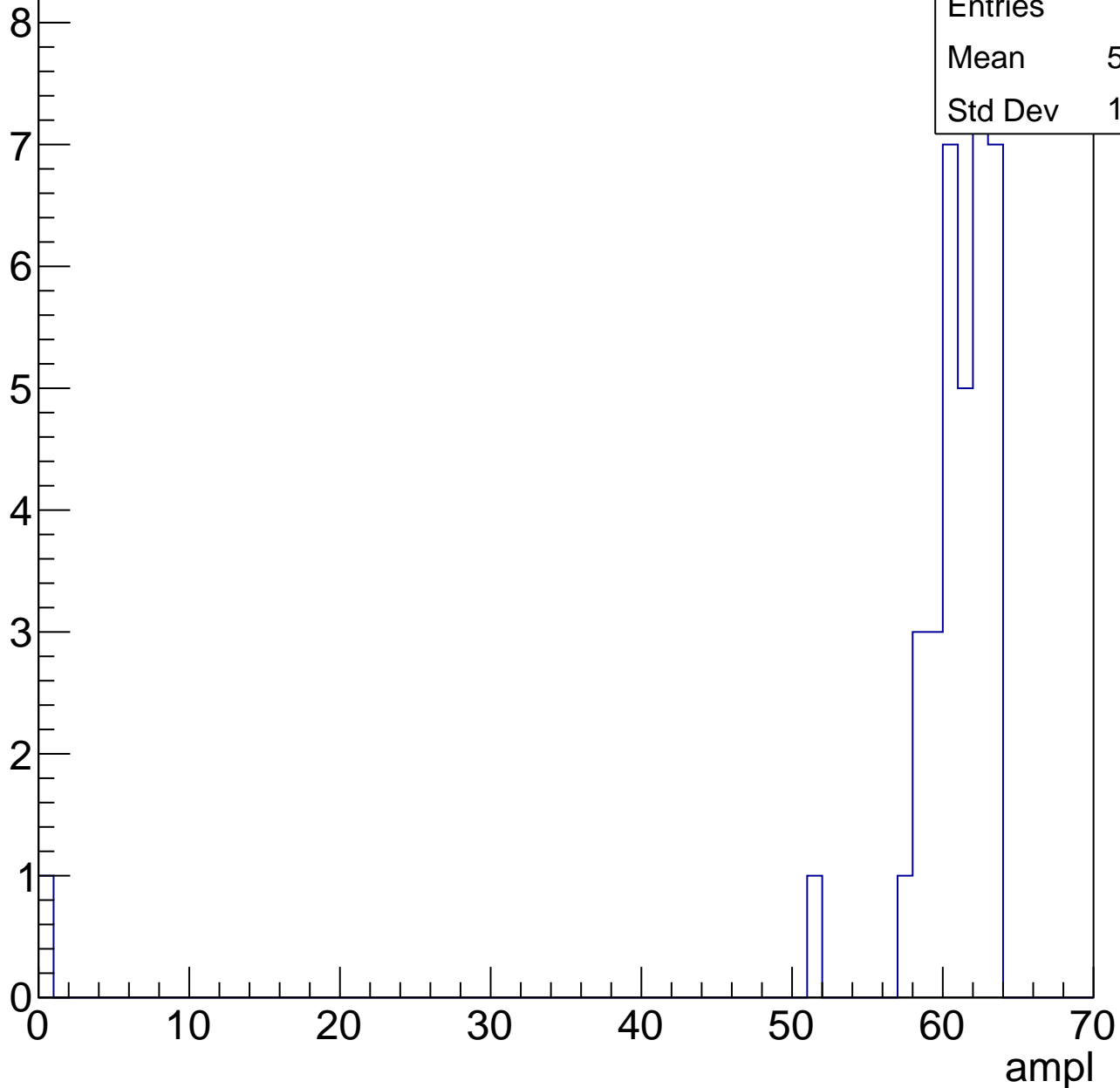


B1L103S, U24-ch19, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

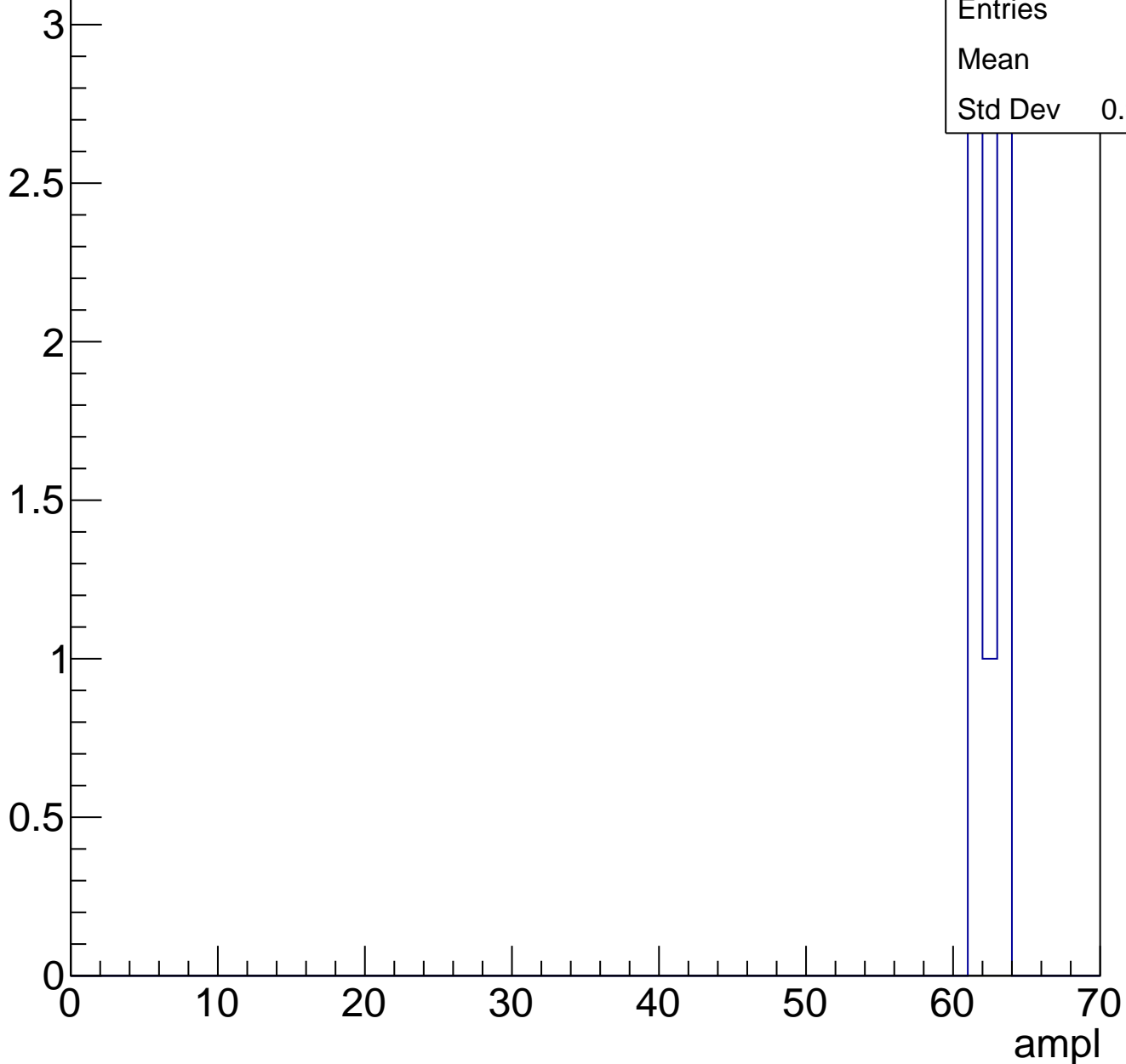
Entries	36
Mean	58.92
Std Dev	10.22



B1L103S, U24-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U24-ch20, adc0

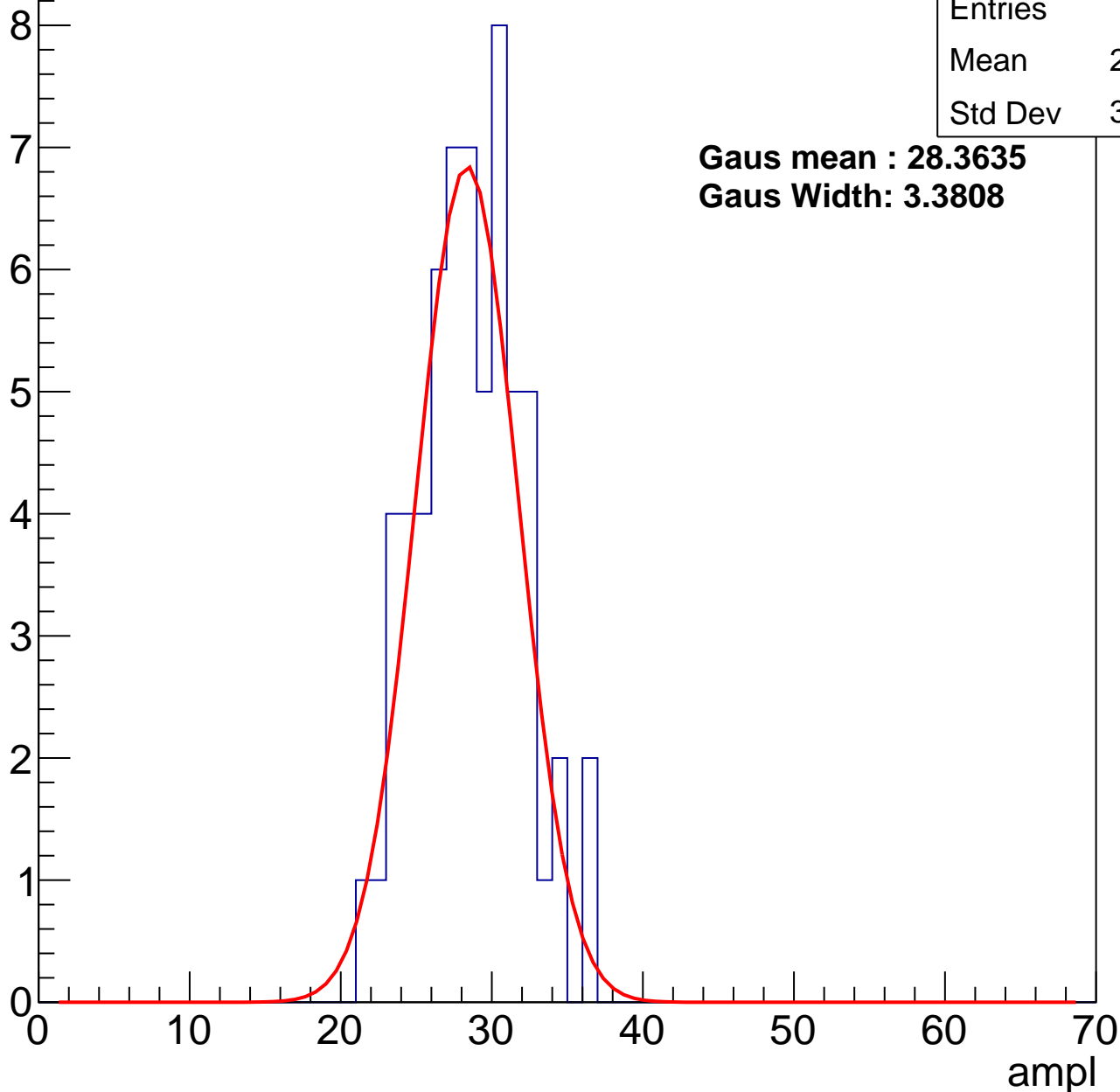
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.15
Std Dev	3.364

Gaus mean : 28.3635

Gaus Width: 3.3808



B1L103S, U24-ch20, adc1

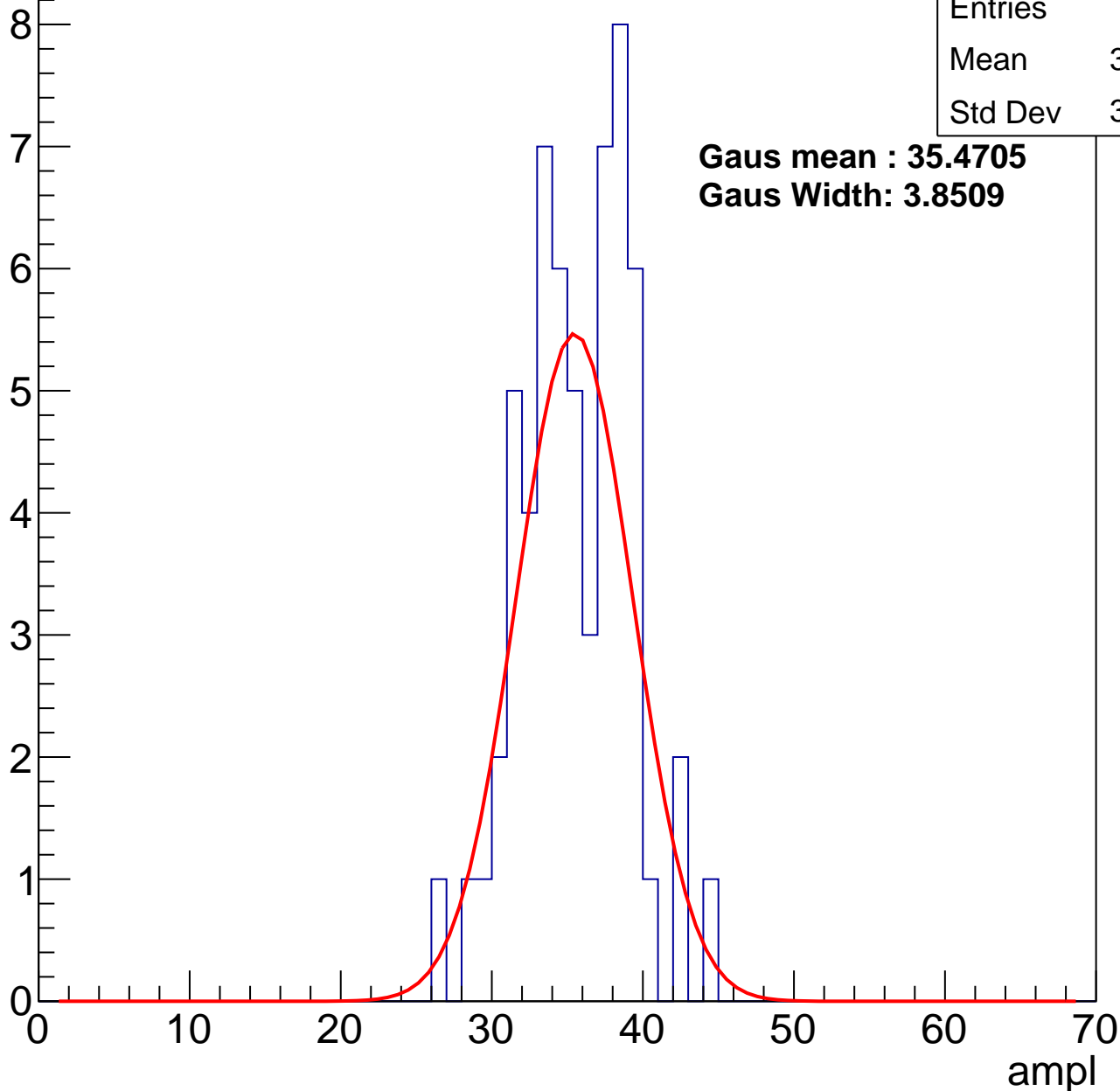
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.15
Std Dev	3.577

Gaus mean : 35.4705

Gaus Width: 3.8509



B1L103S, U24-ch20, adc2

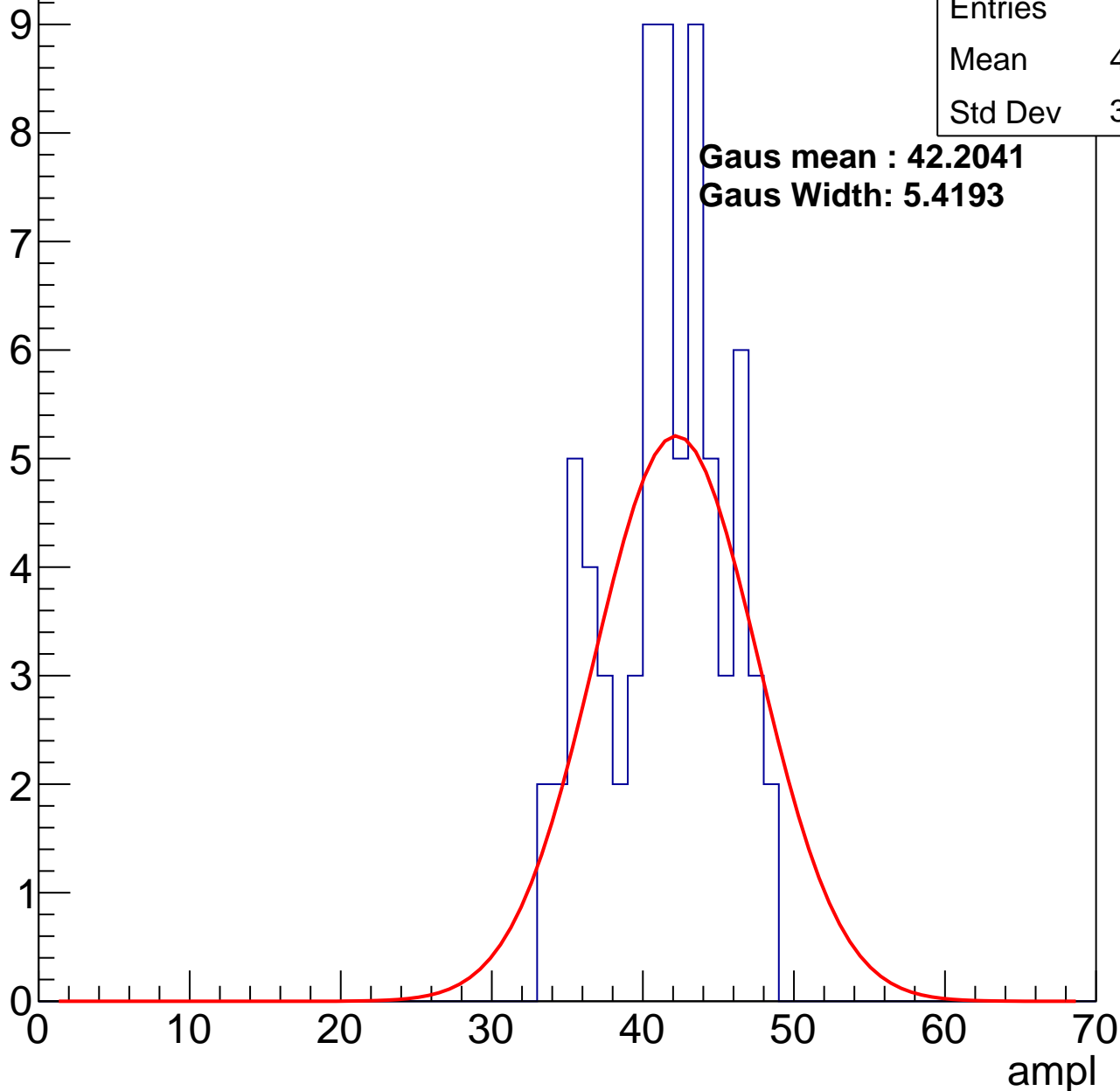
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	40.99
Std Dev	3.878

Gaus mean : 42.2041

Gaus Width: 5.4193

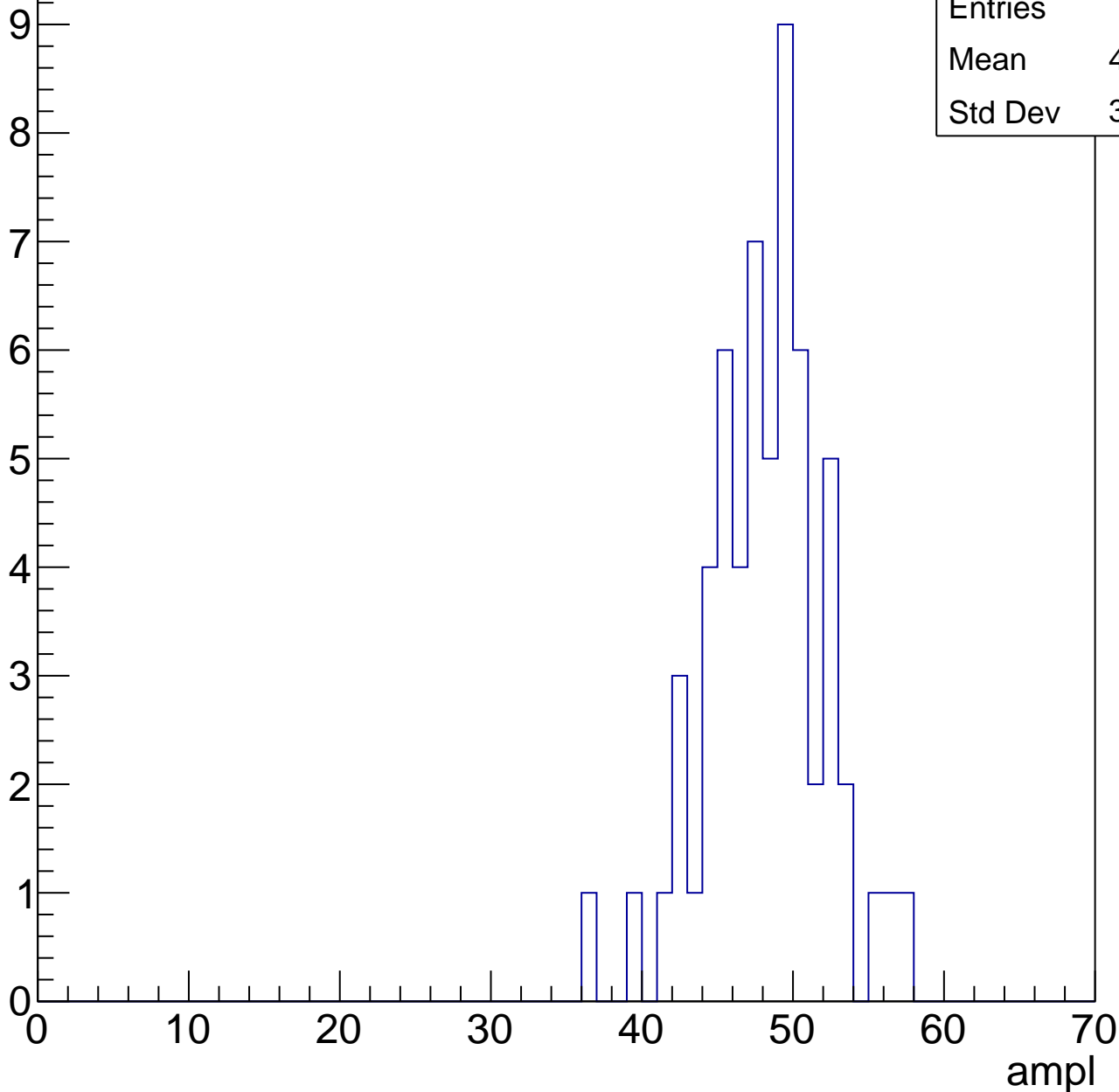


B1L103S, U24-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	47.68
Std Dev	3.909

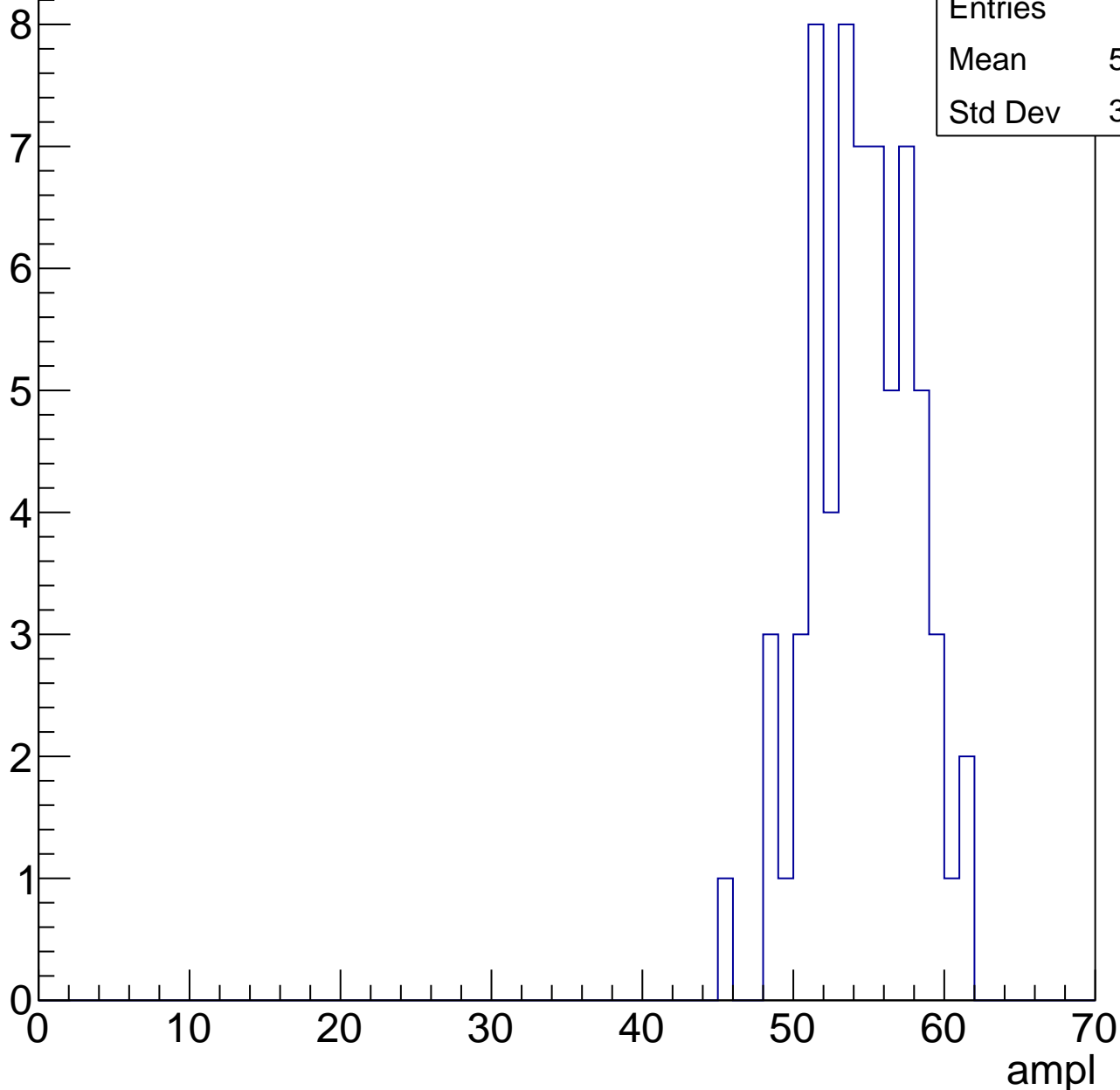


B1L103S, U24-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	54.14
Std Dev	3.364

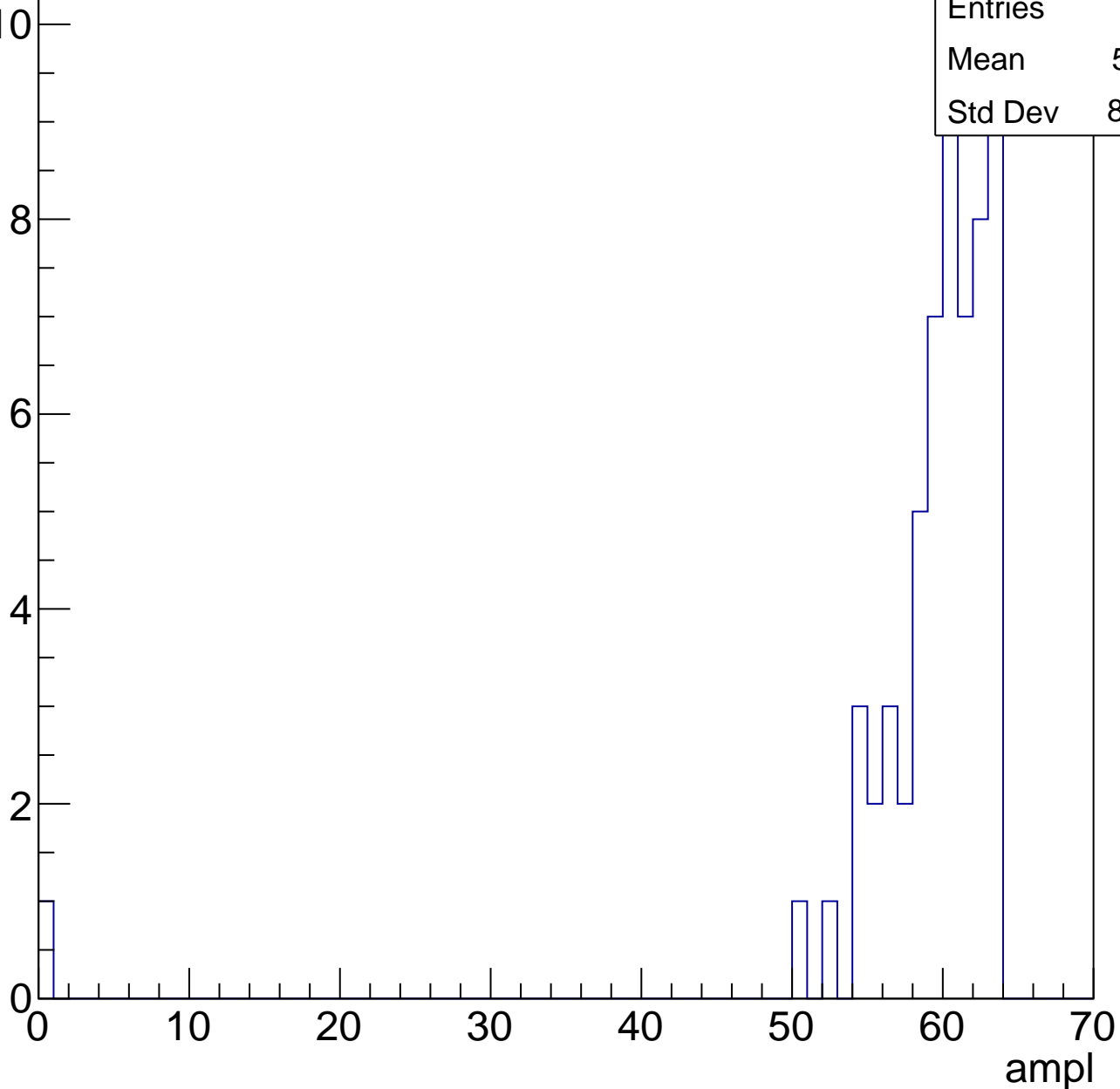


B1L103S, U24-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

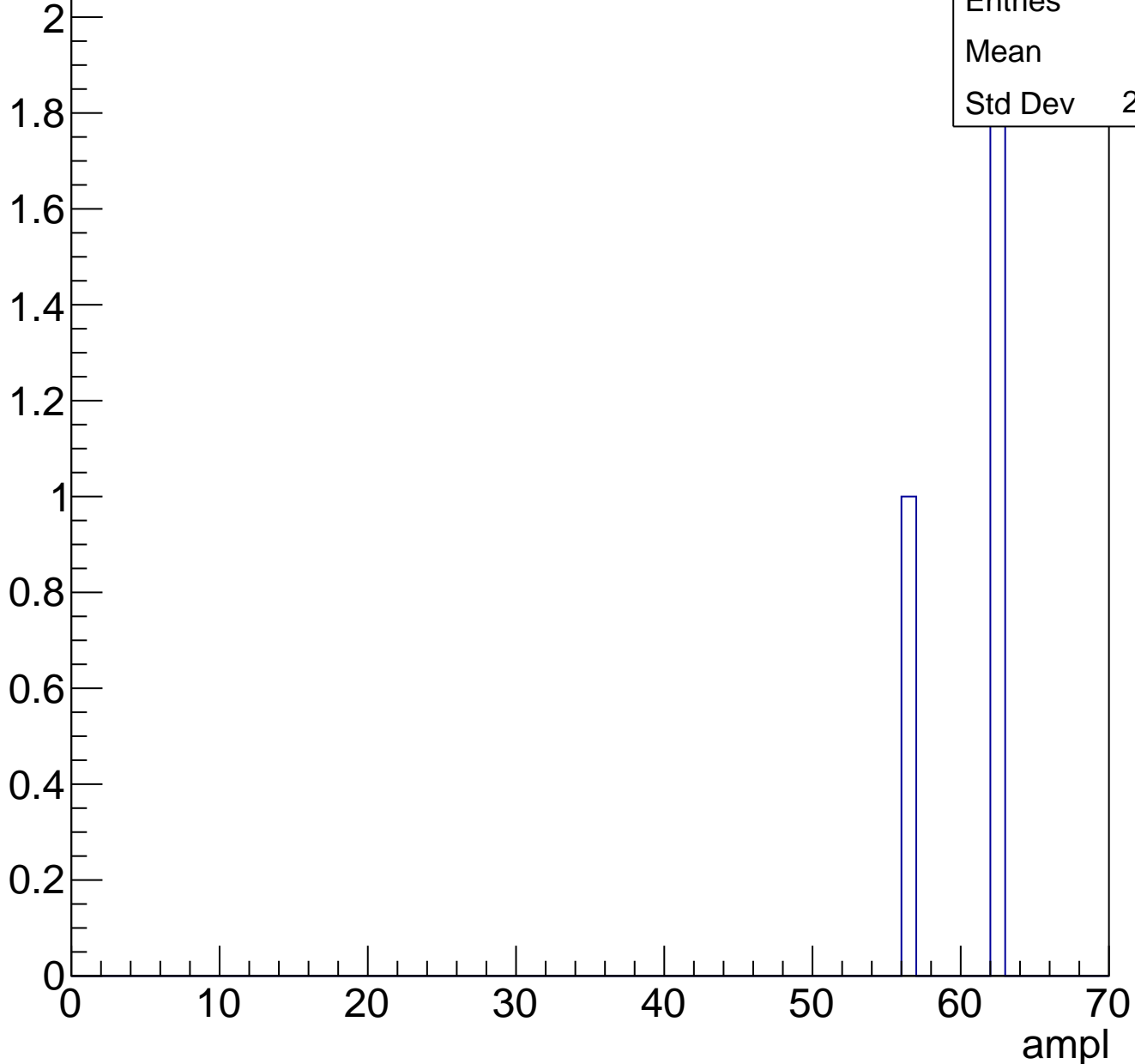
Entries	59
Mean	58.51
Std Dev	8.247



B1L103S, U24-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

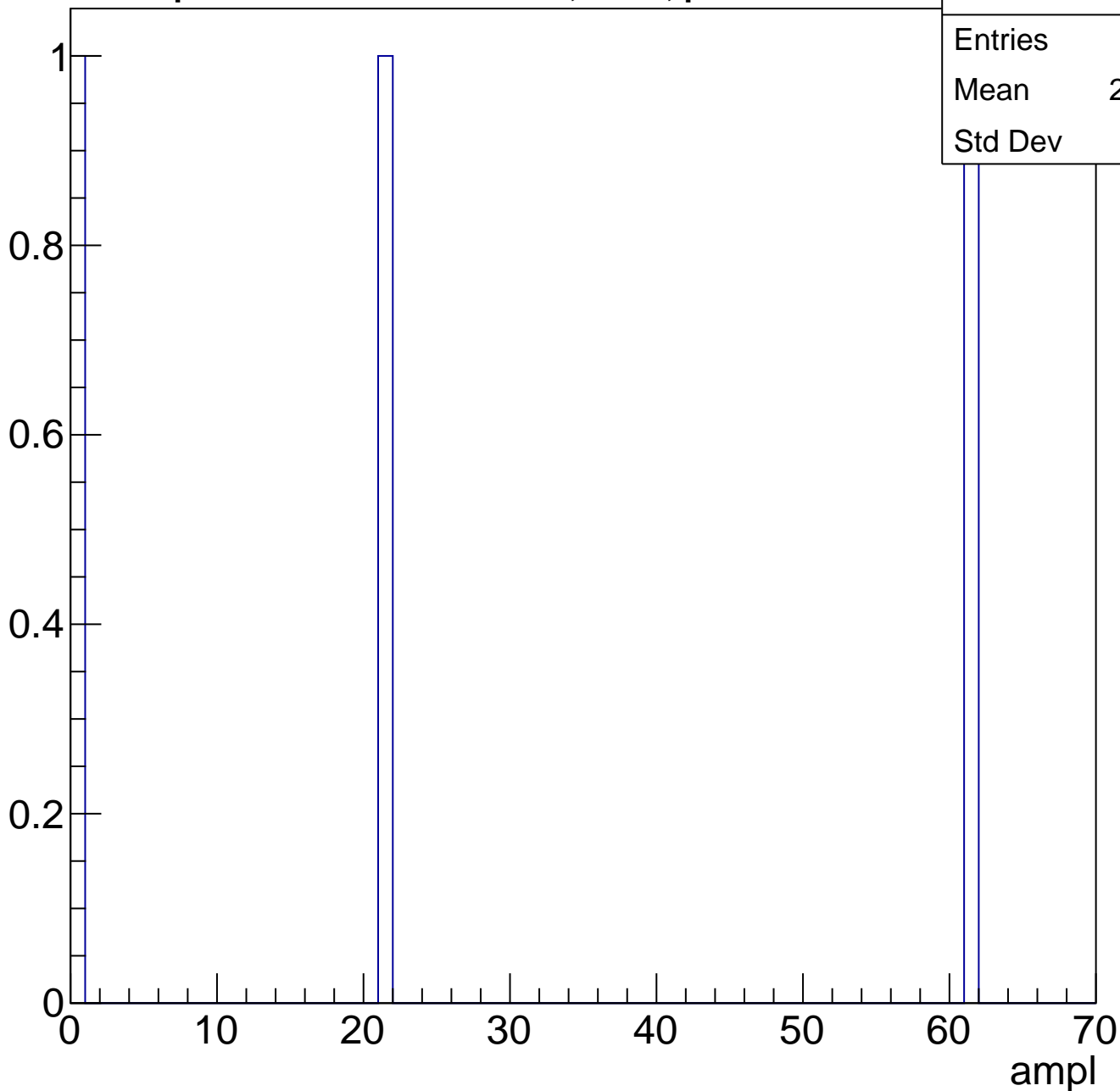
Entry



B1L103S, U24-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	27.33
Std Dev	25.3

B1L103S, U24-ch21, adc0

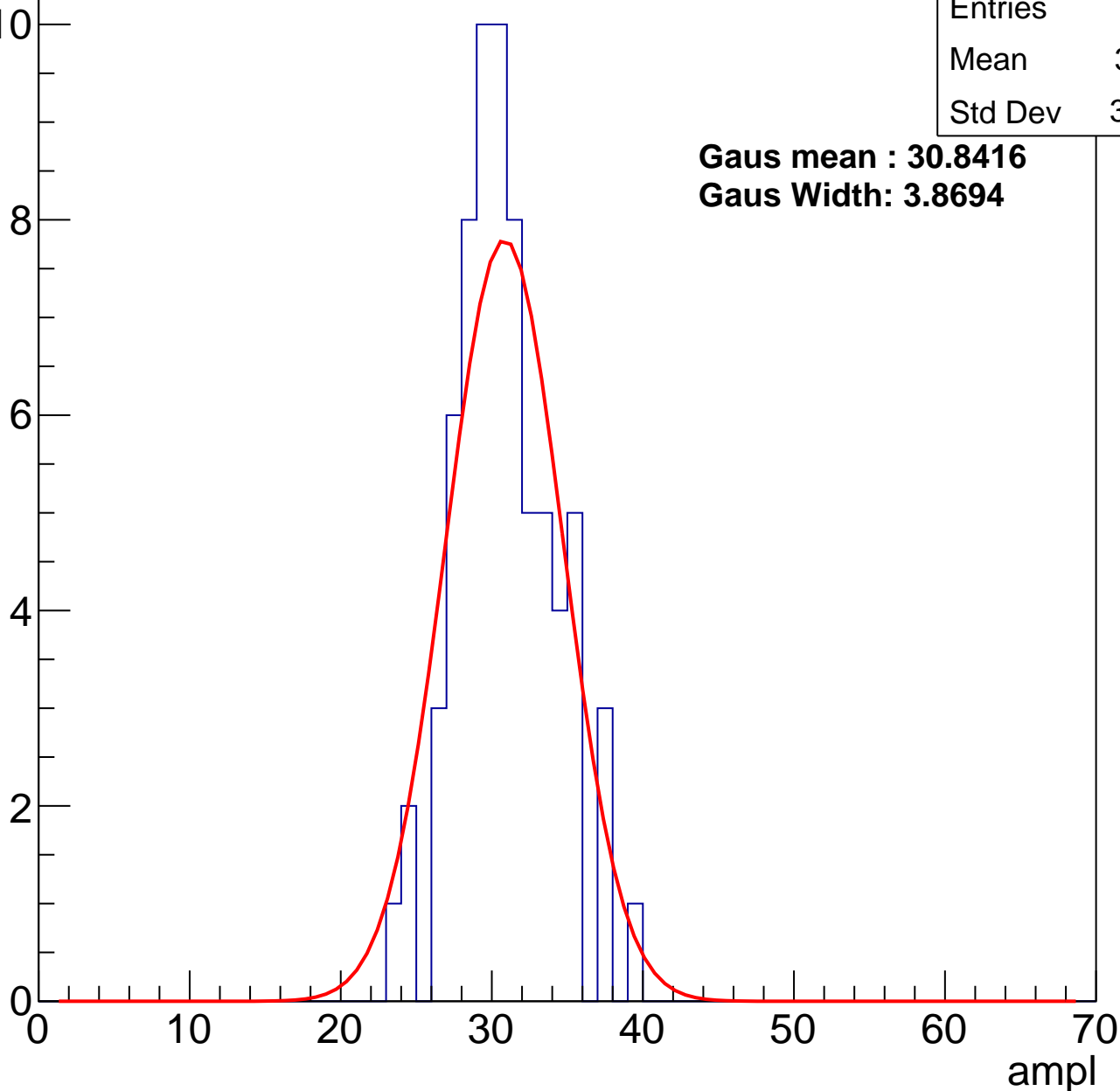
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.41
Std Dev	3.235

Gaus mean : 30.8416

Gaus Width: 3.8694



B1L103S, U24-ch21, adc1

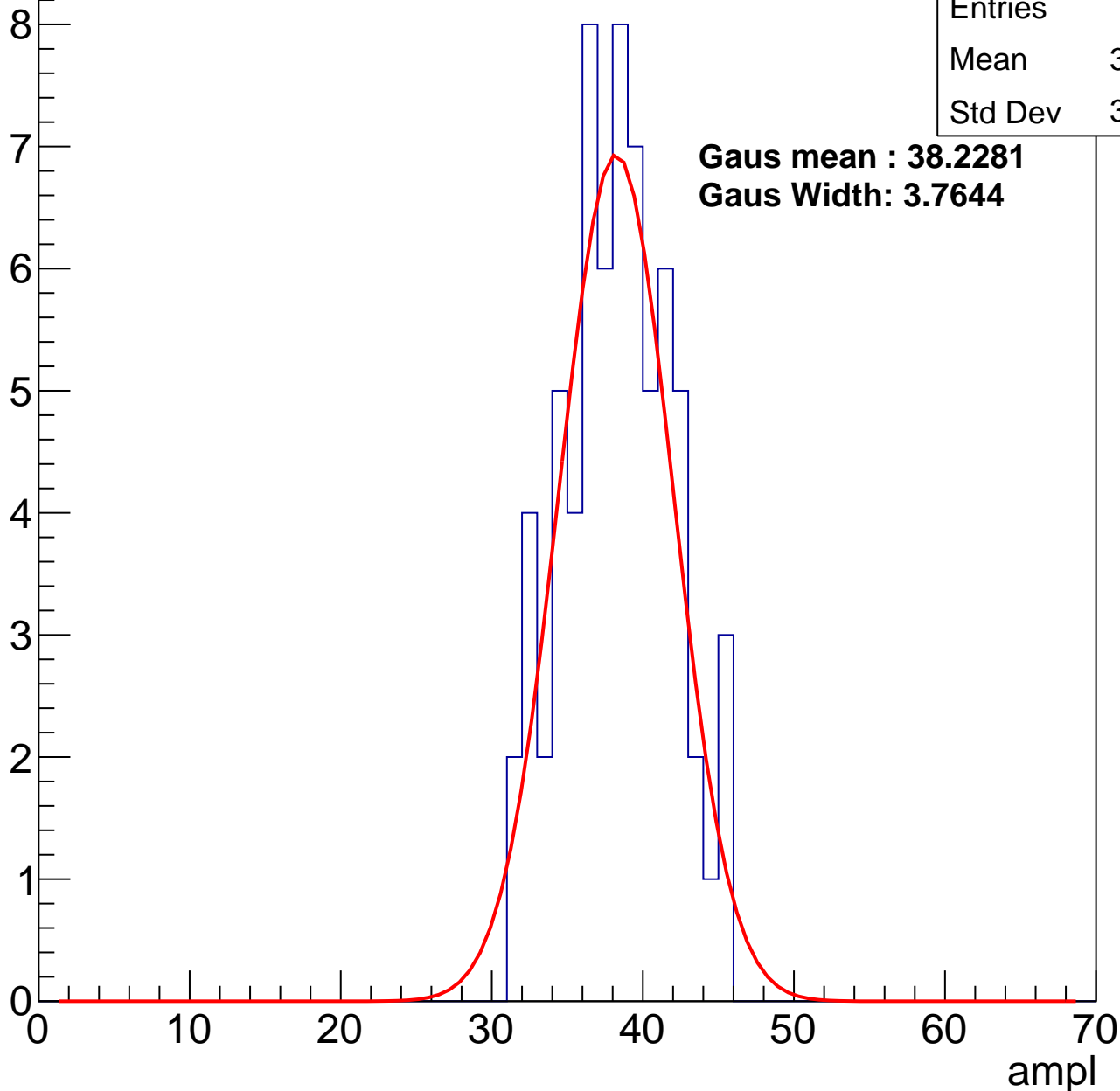
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.85
Std Dev	3.512

Gaus mean : 38.2281

Gaus Width: 3.7644



B1L103S, U24-ch21, adc2

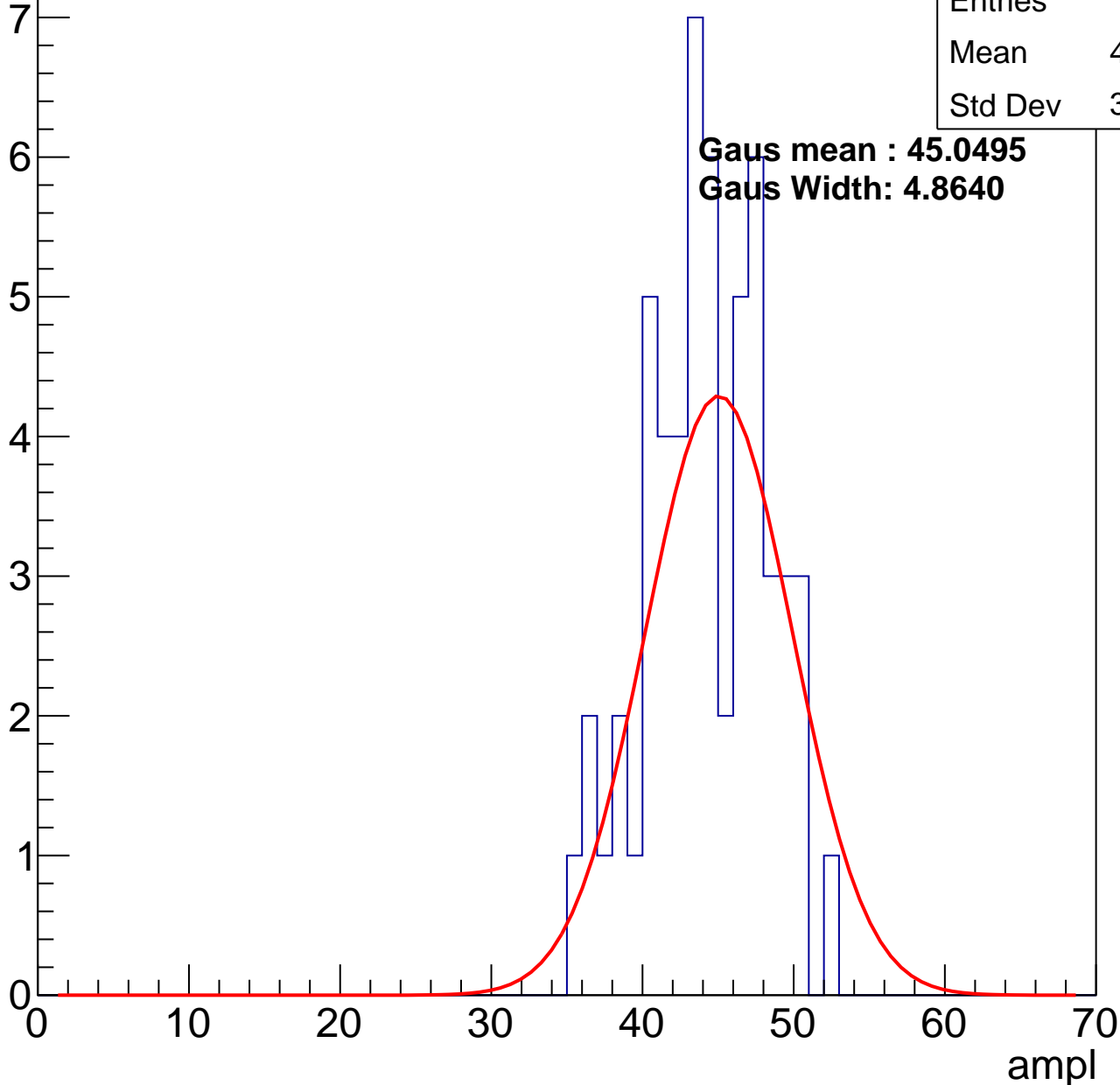
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.77
Std Dev	3.923

Gaus mean : 45.0495

Gaus Width: 4.8640

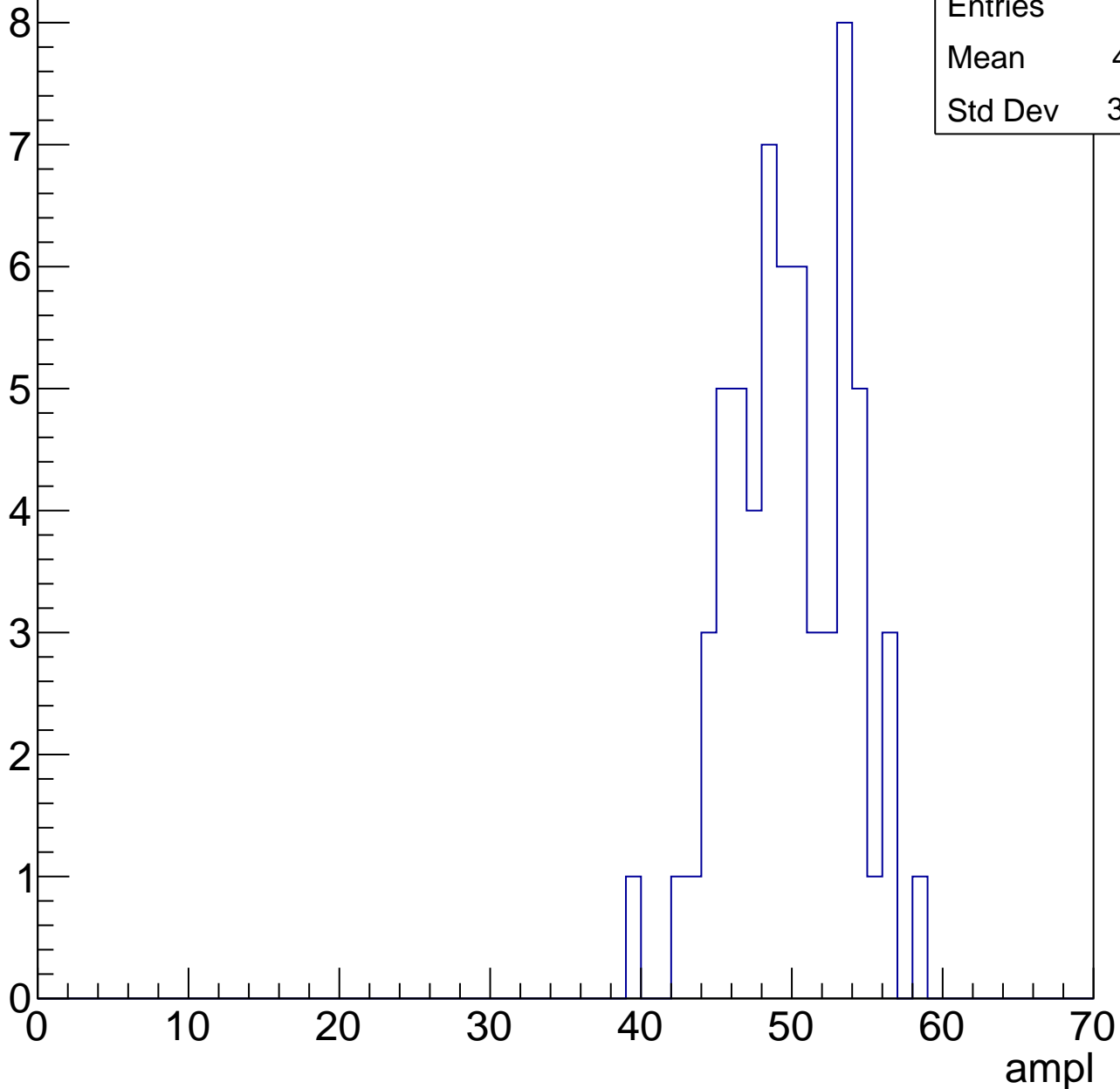


B1L103S, U24-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

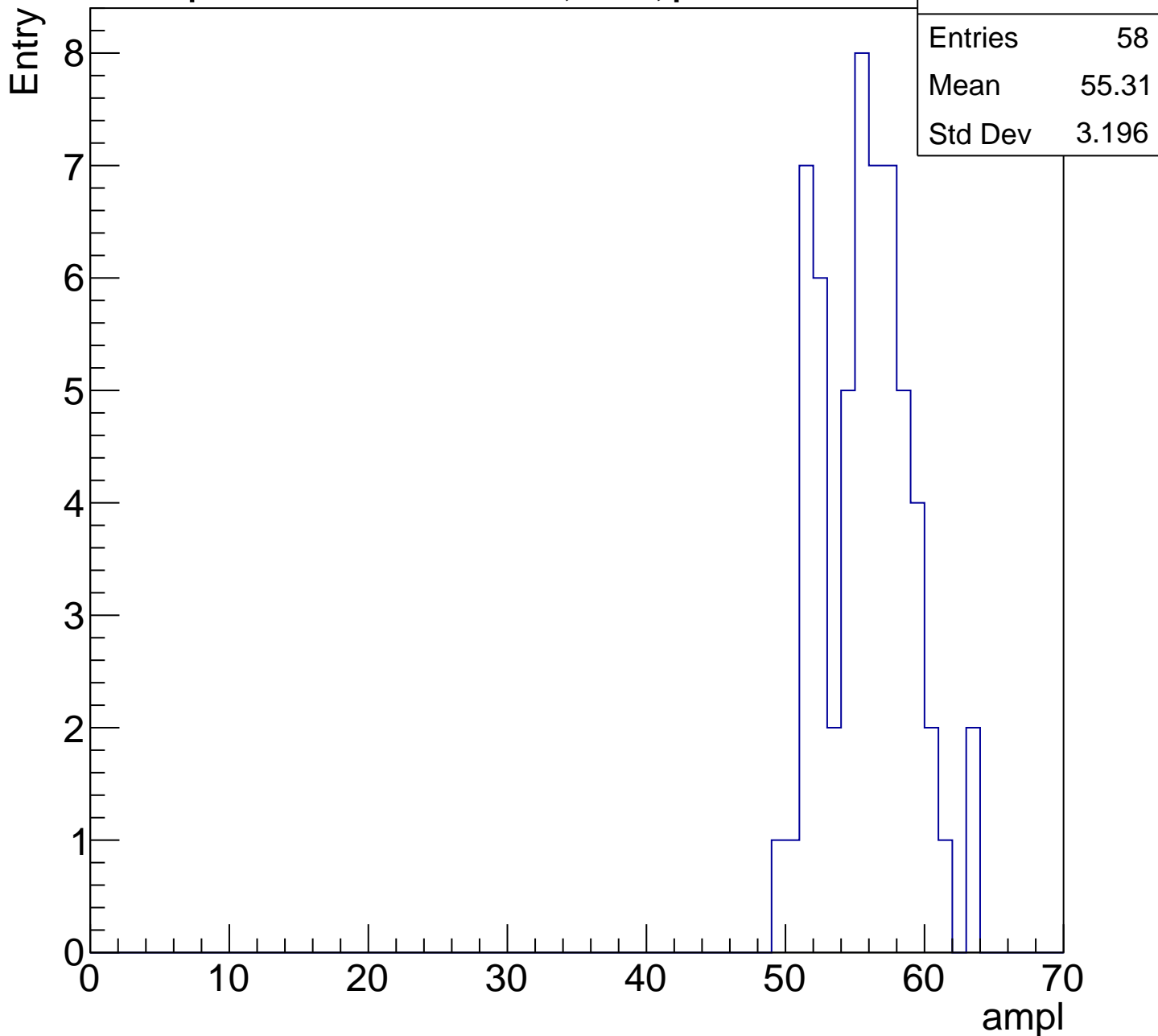
Entry

Entries	63
Mean	49.41
Std Dev	3.902



B1L103S, U24-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

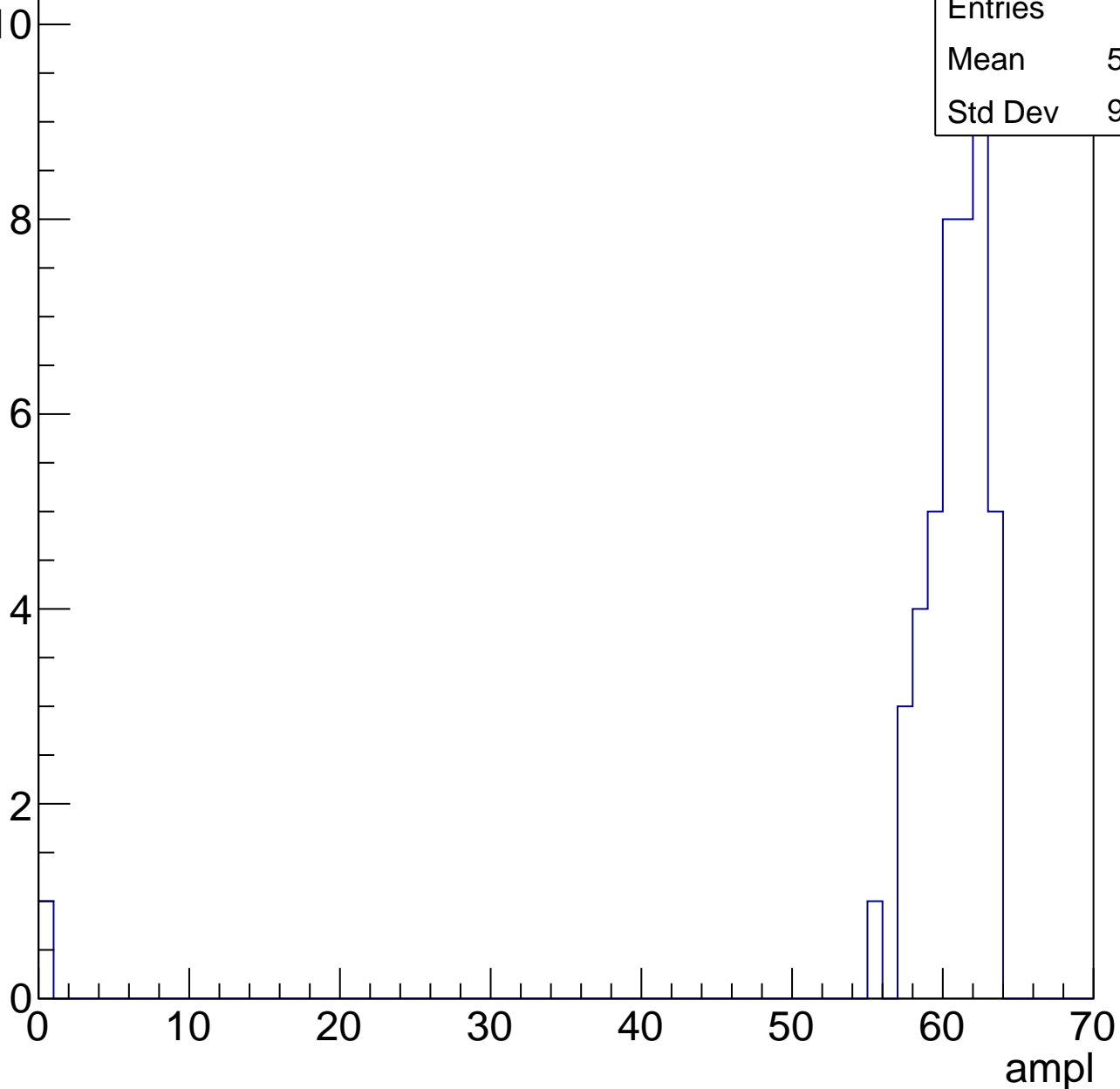


B1L103S, U24-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

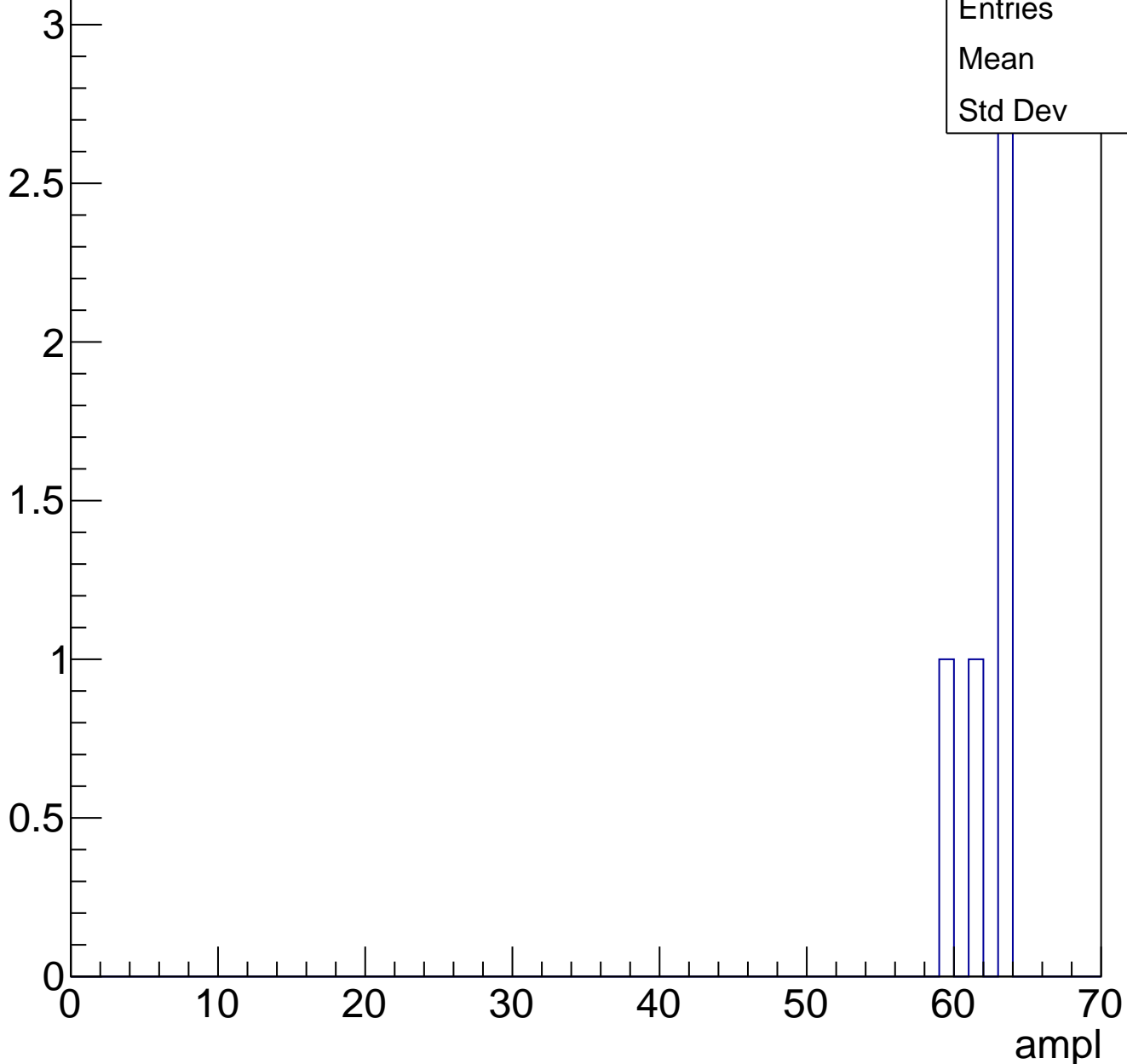
Entries	45
Mean	59.02
Std Dev	9.096



B1L103S, U24-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	61.8
Std Dev	1.6

B1L103S, U24-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch22, adc0

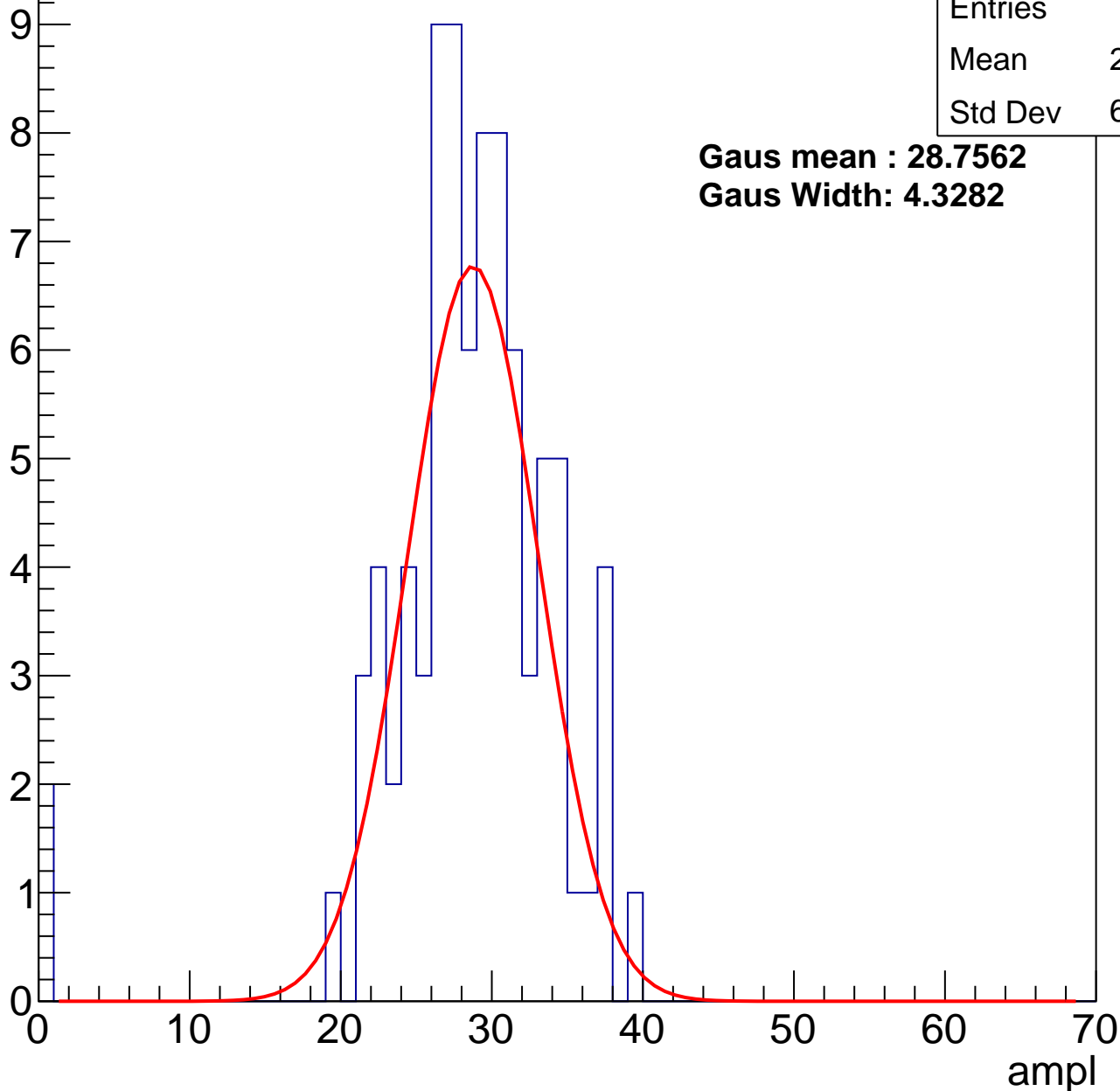
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	27.99
Std Dev	6.069

Gaus mean : 28.7562

Gaus Width: 4.3282



B1L103S, U24-ch22, adc1

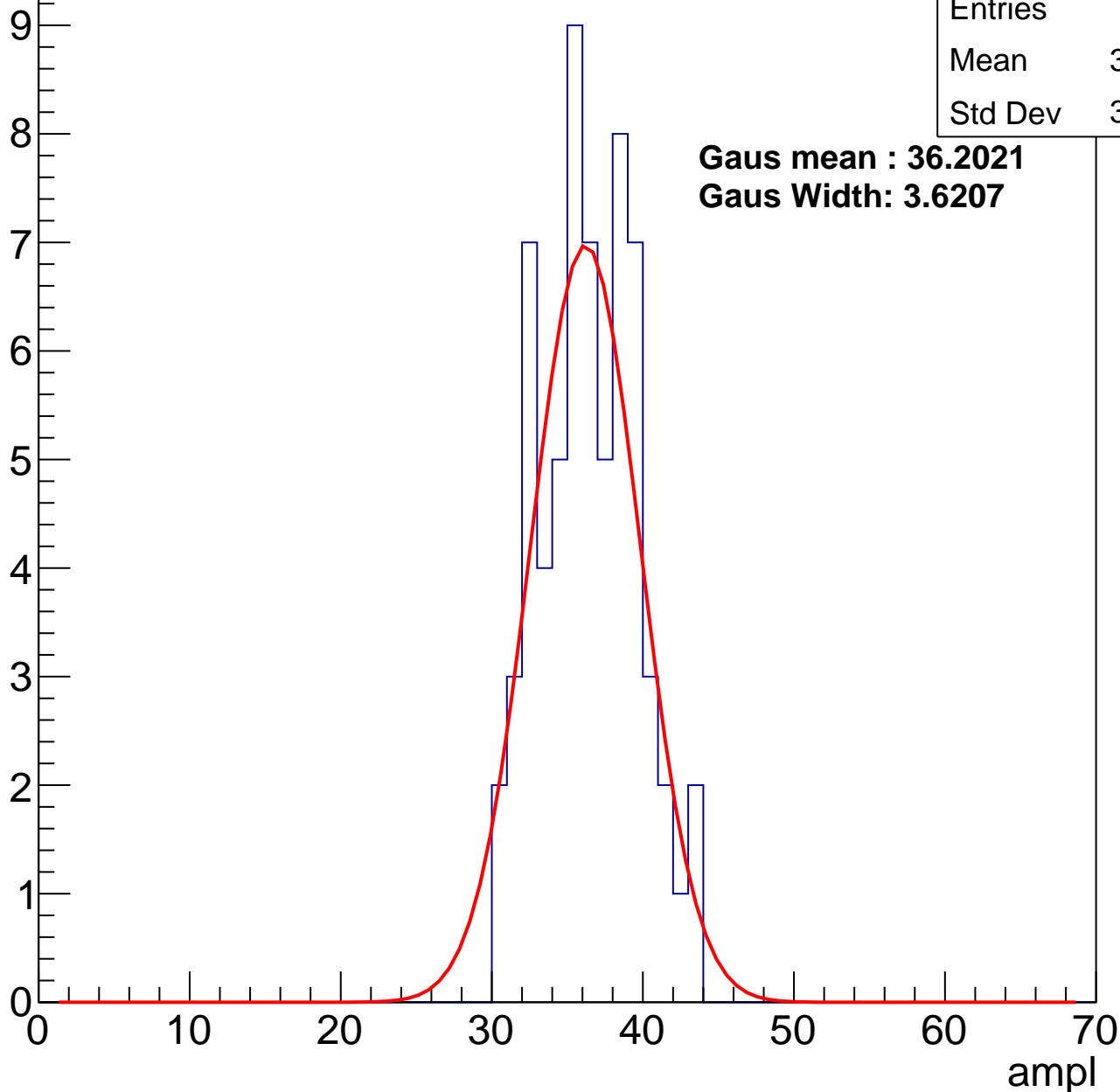
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.97
Std Dev	3.177

Gaus mean : 36.2021

Gaus Width: 3.6207



B1L103S, U24-ch22, adc2

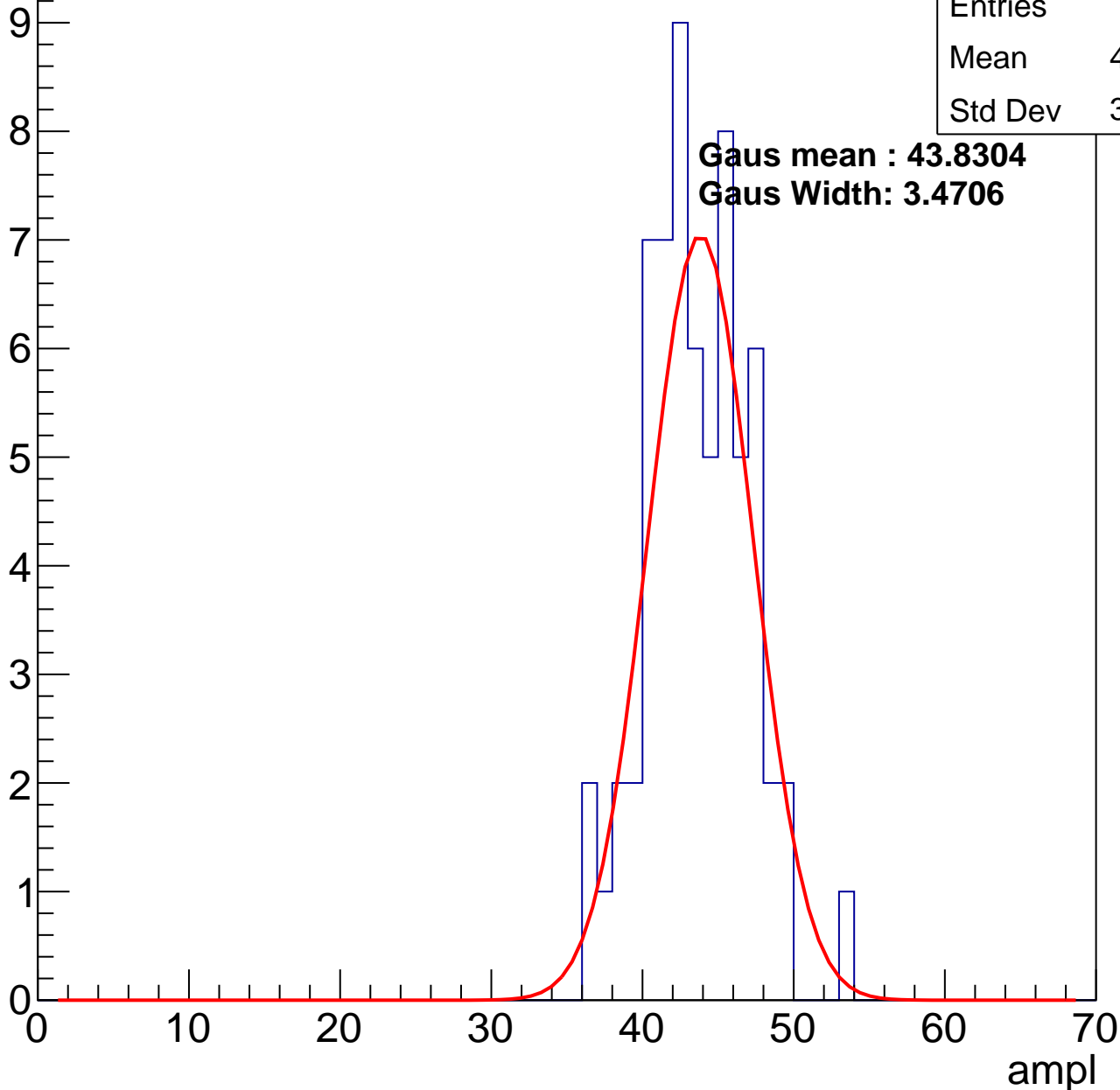
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.15
Std Dev	3.315

Gaus mean : 43.8304

Gaus Width: 3.4706

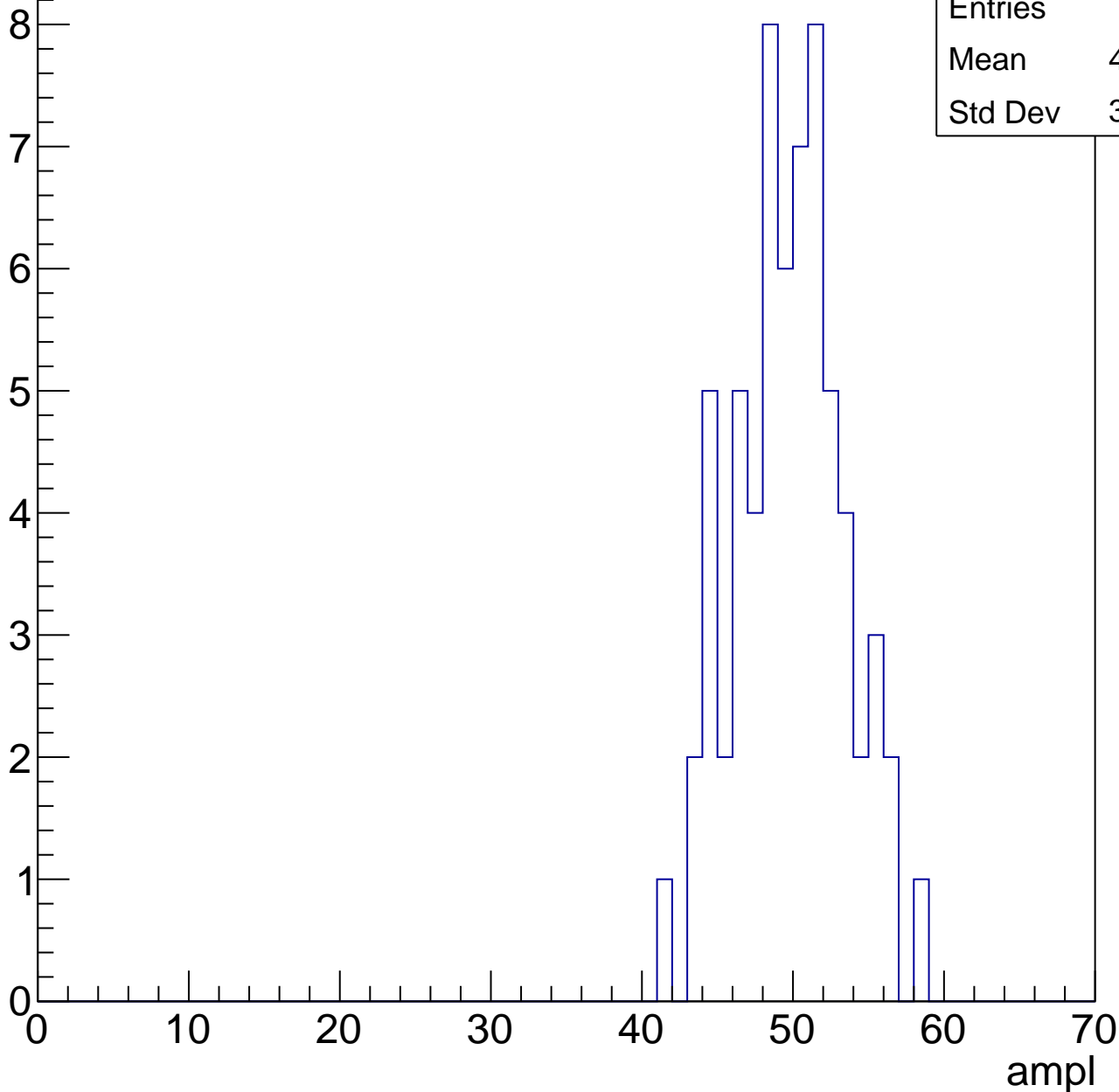


B1L103S, U24-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

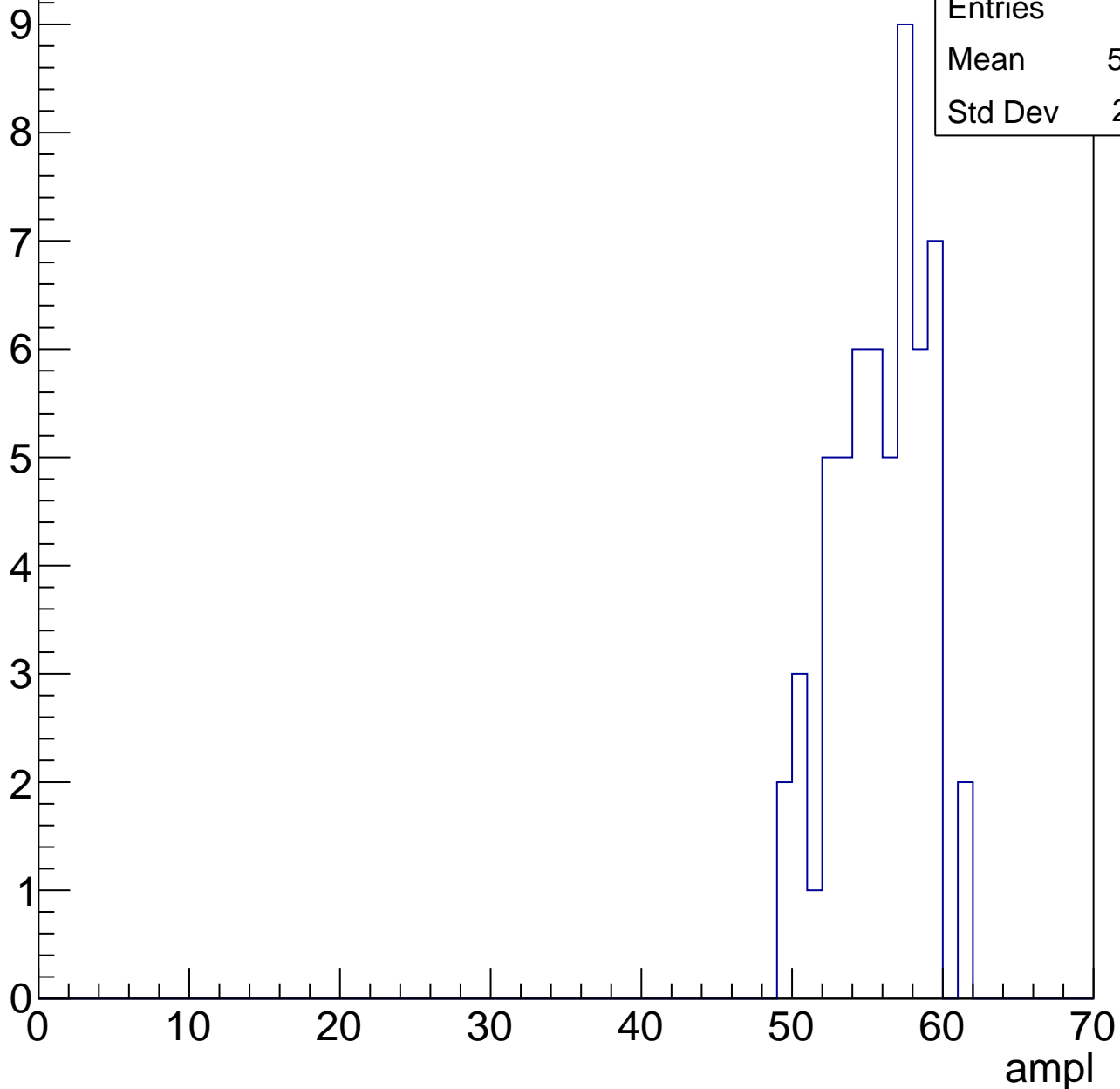
Entries	65
Mean	49.32
Std Dev	3.595



B1L103S, U24-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

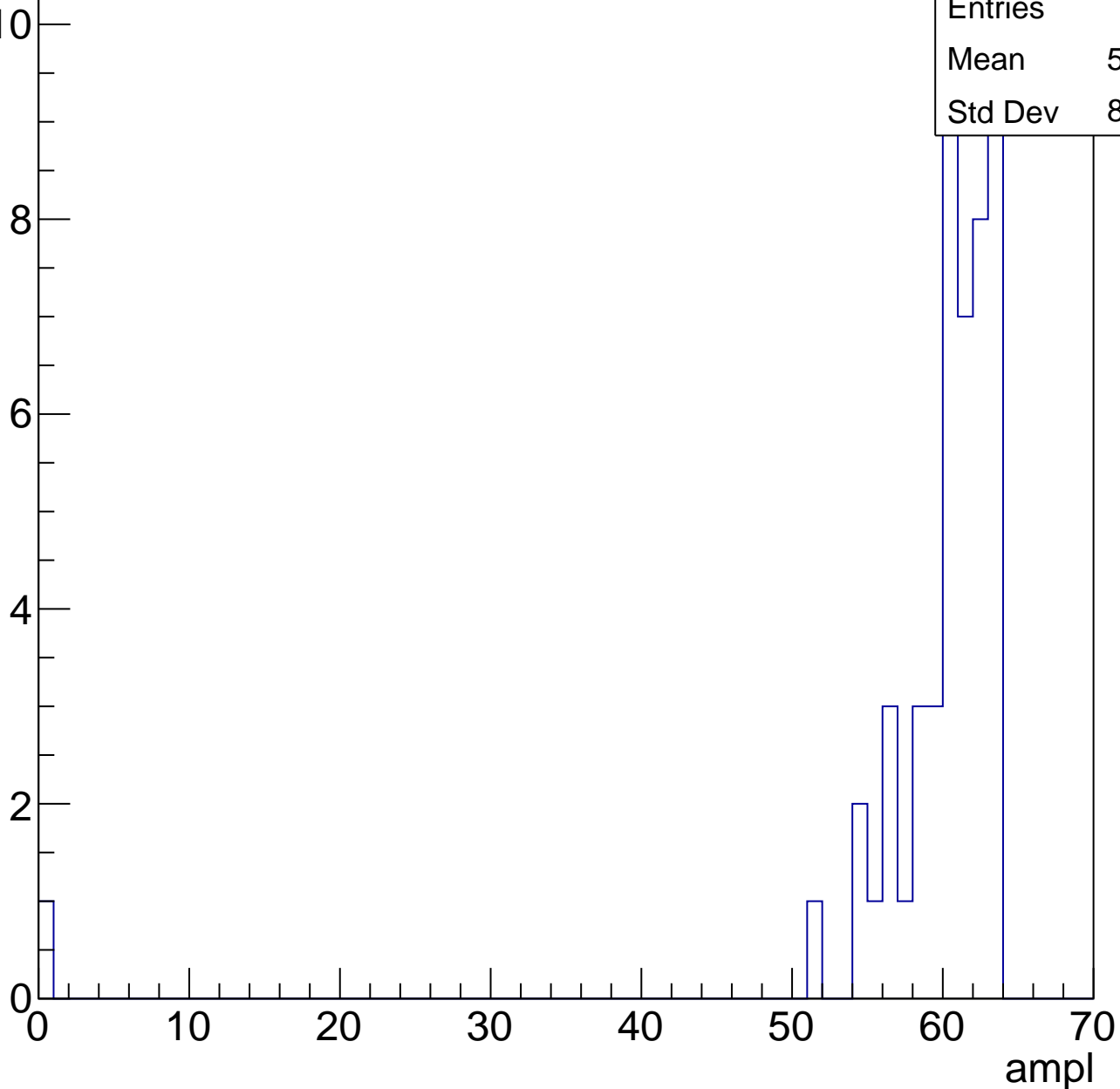


B1L103S, U24-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.78
Std Dev	8.917



B1L103S, U24-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch23, adc0

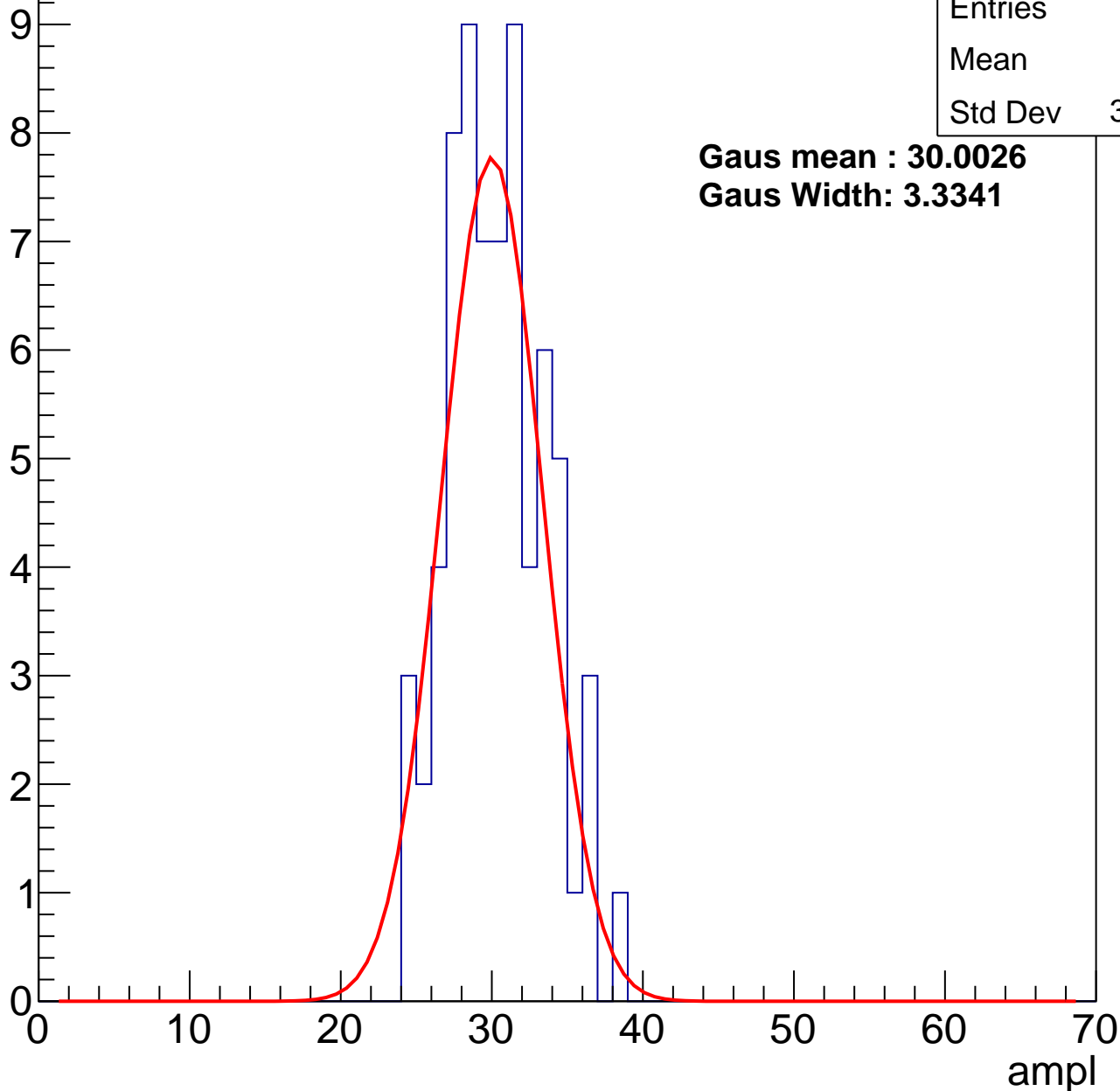
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.9
Std Dev	3.168

Gaus mean : 30.0026

Gaus Width: 3.3341



B1L103S, U24-ch23, adc1

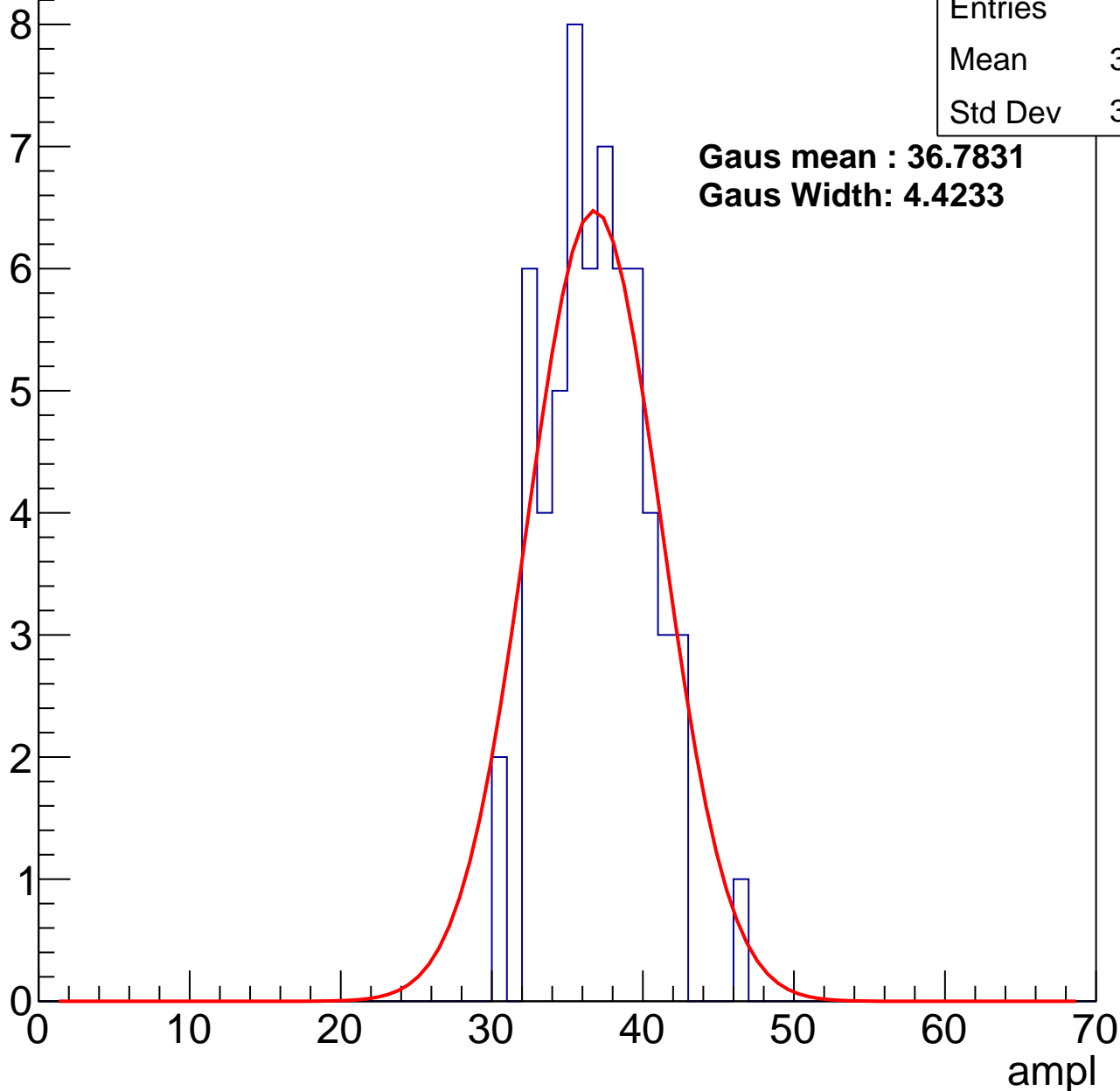
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.49
Std Dev	3.262

Gaus mean : 36.7831

Gaus Width: 4.4233



B1L103S, U24-ch23, adc2

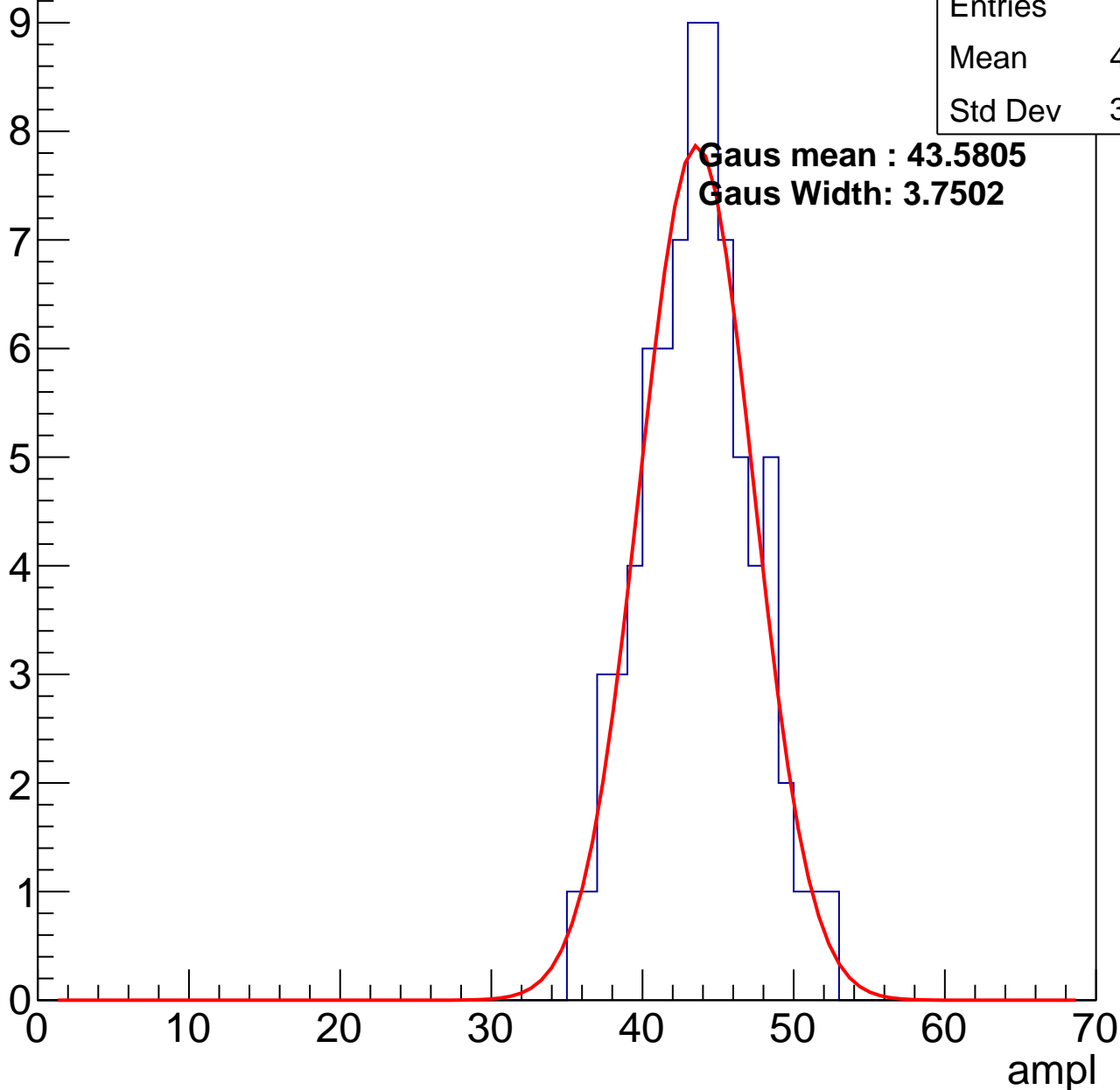
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	43.19
Std Dev	3.614

Gaus mean : 43.5805

Gaus Width: 3.7502

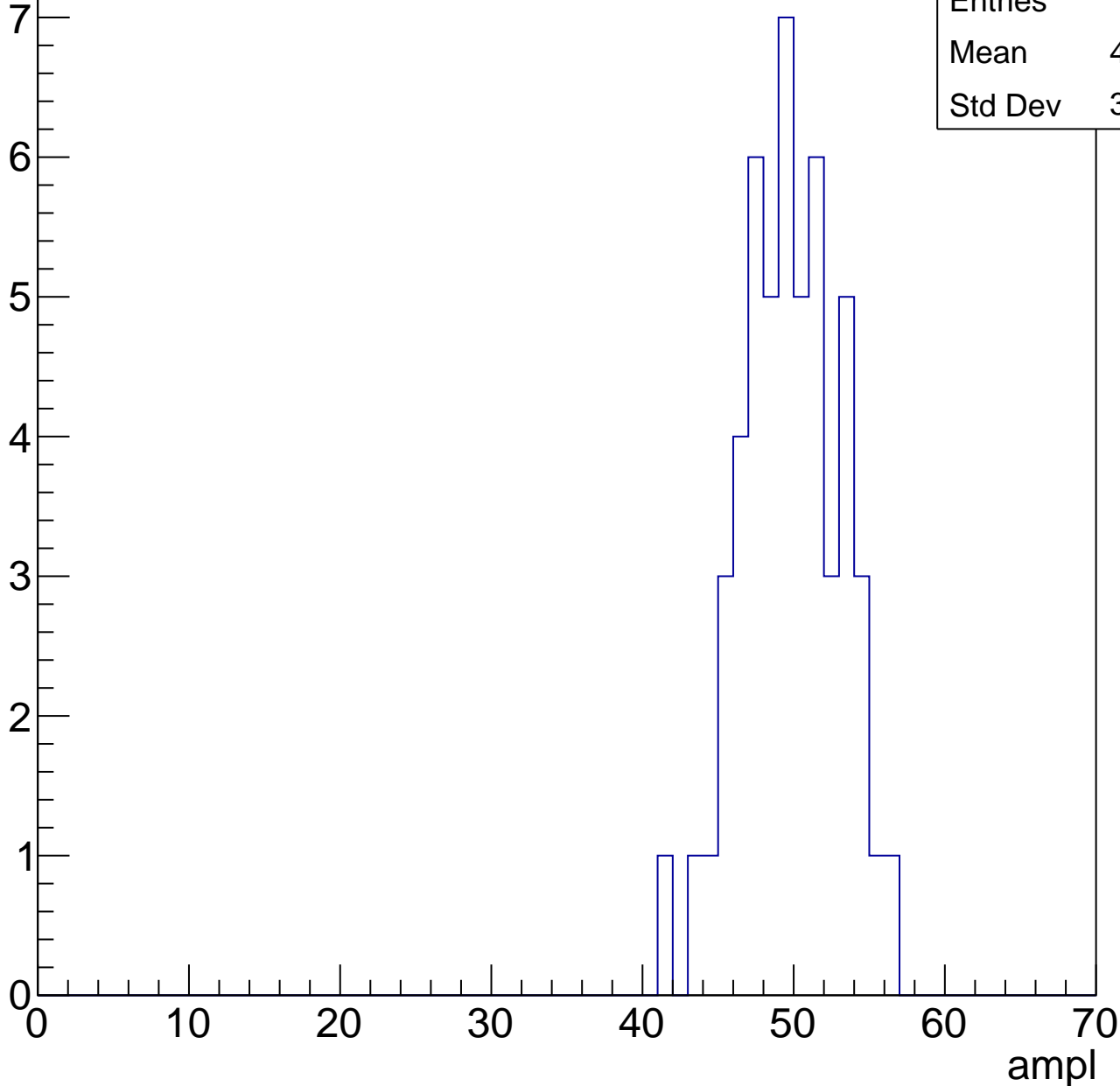


B1L103S, U24-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	49.27
Std Dev	3.193



B1L103S, U24-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

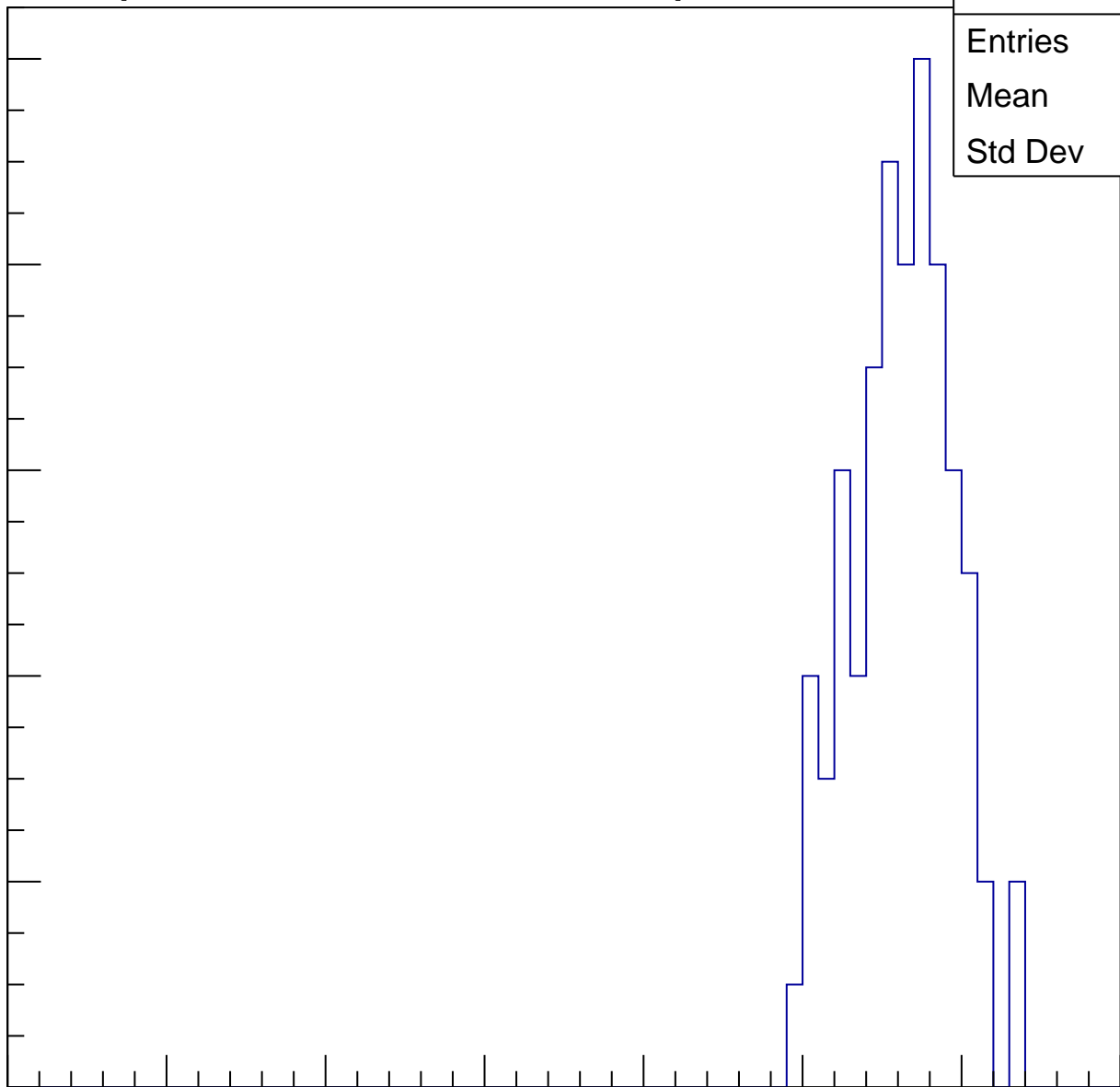
Entries	75
Mean	55.77
Std Dev	3.177

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

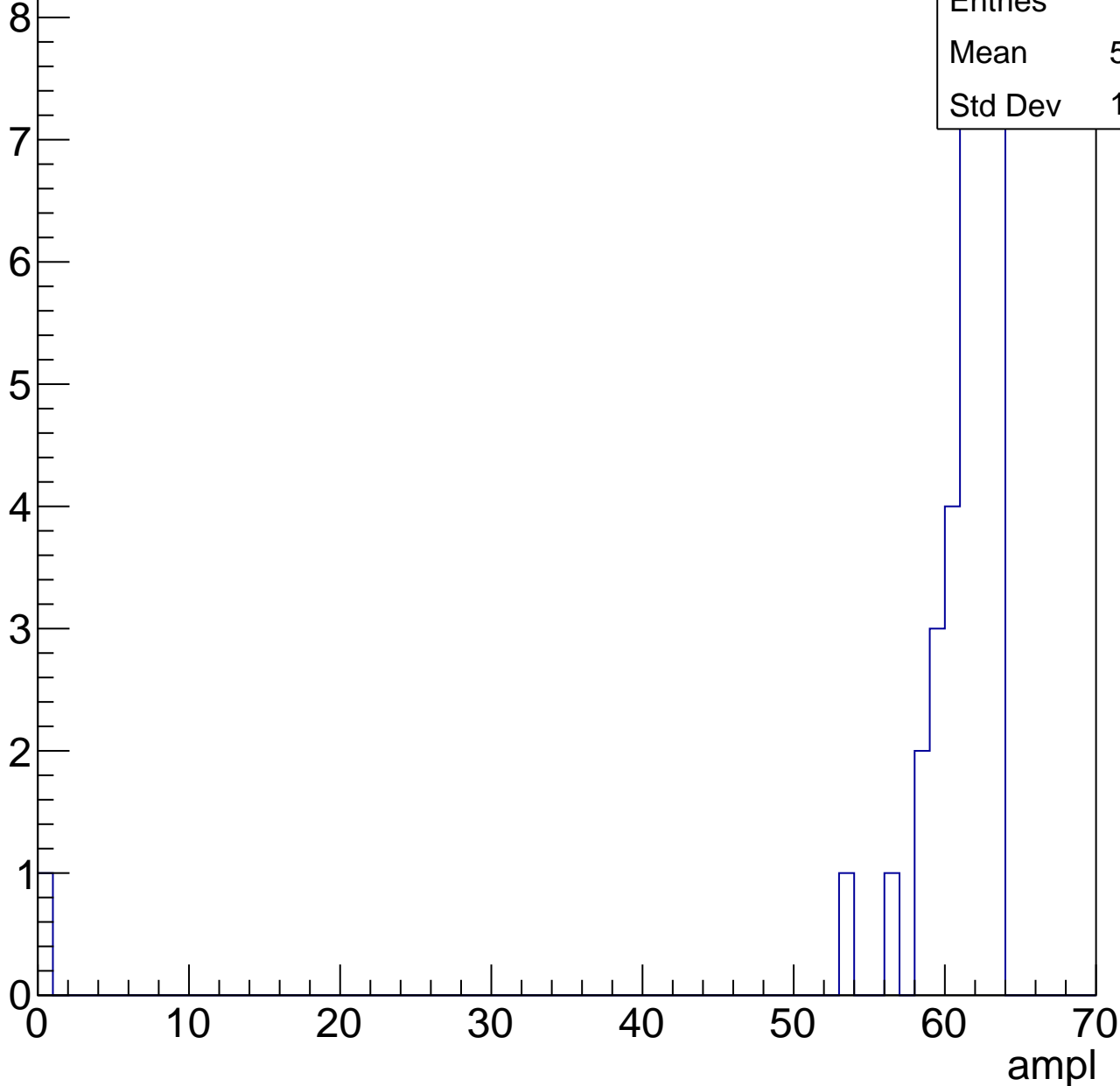


B1L103S, U24-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

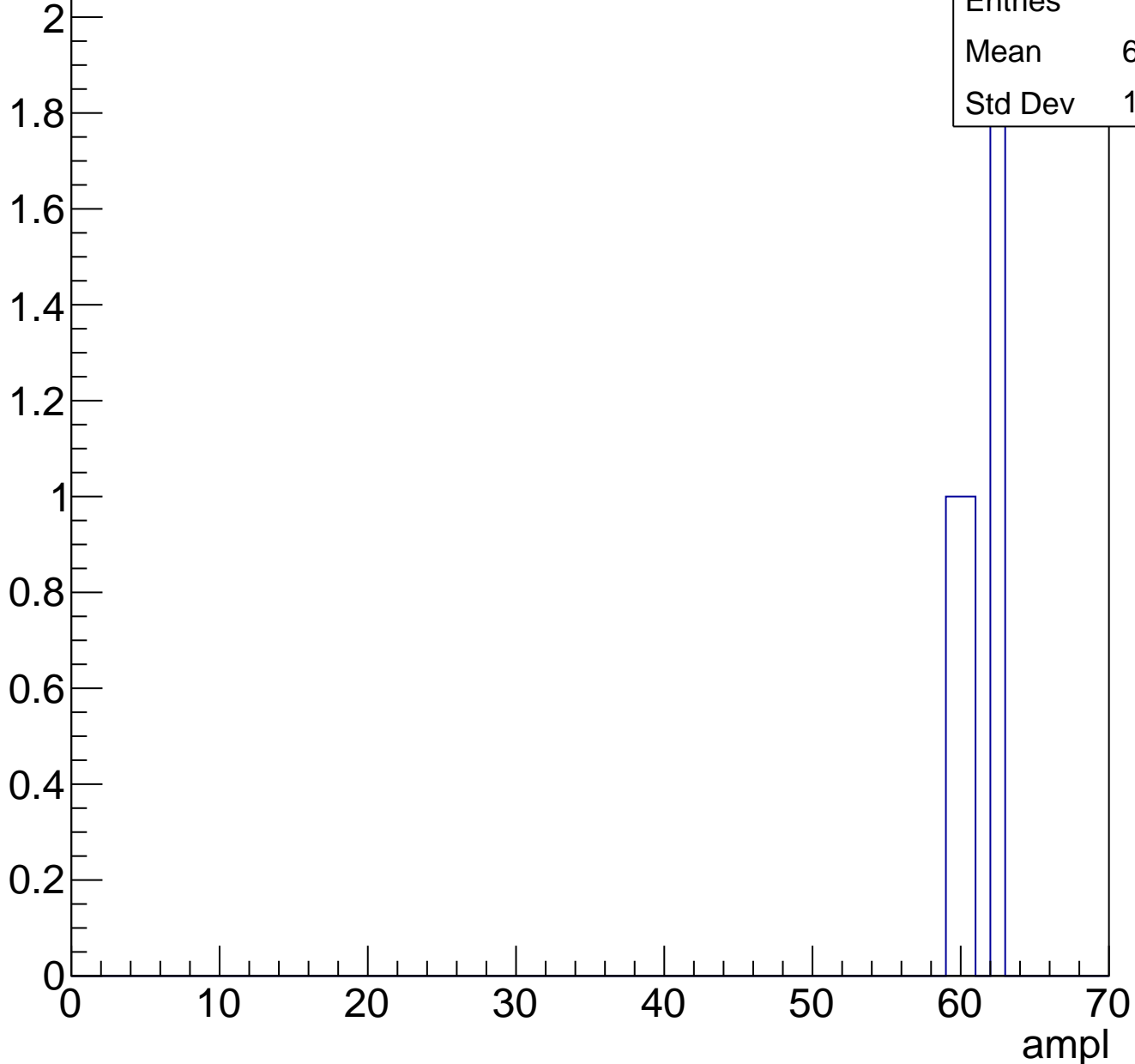
Entries	36
Mean	59.17
Std Dev	10.22



B1L103S, U24-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

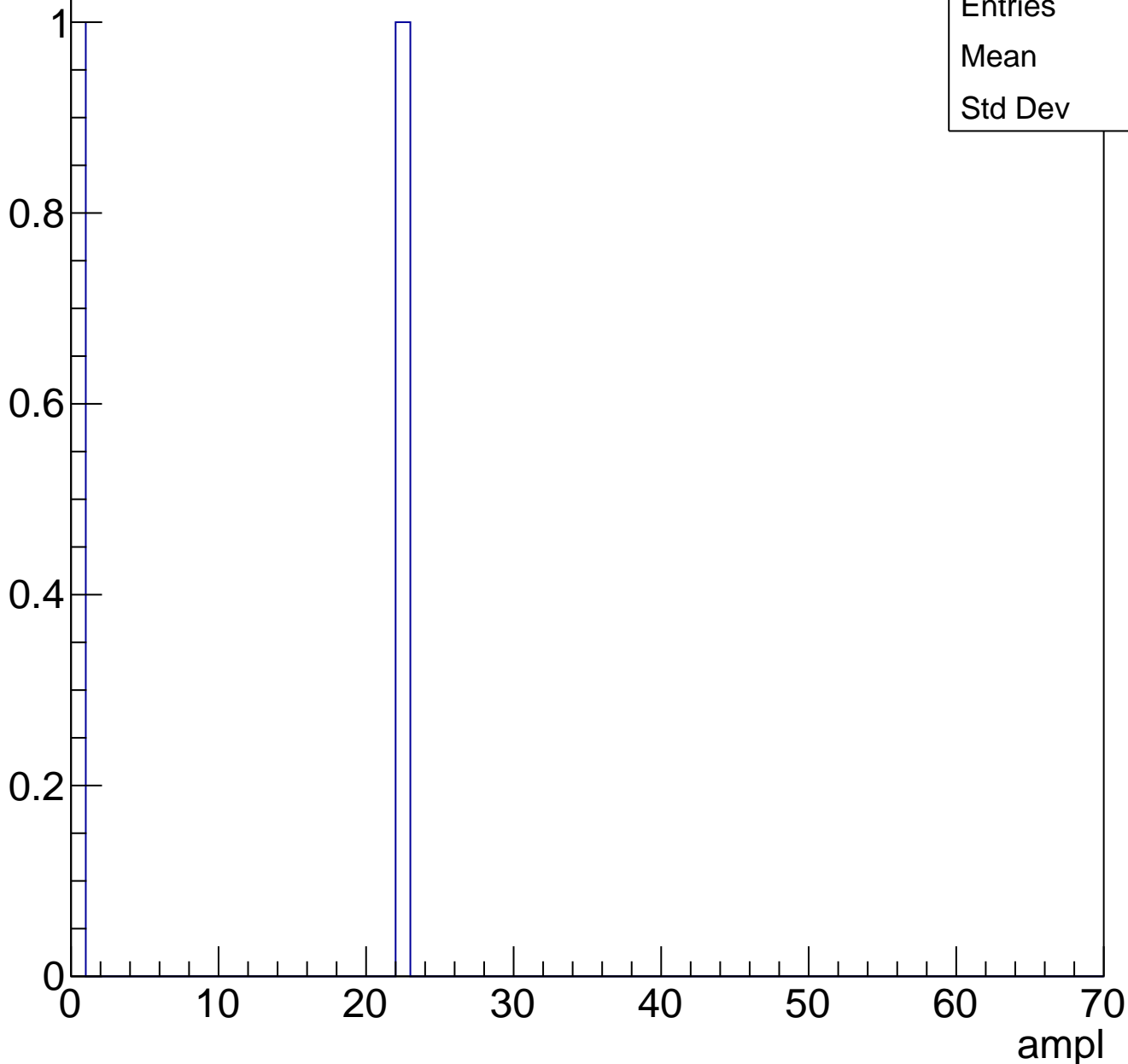
Entry



B1L103S, U24-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch24, adc0

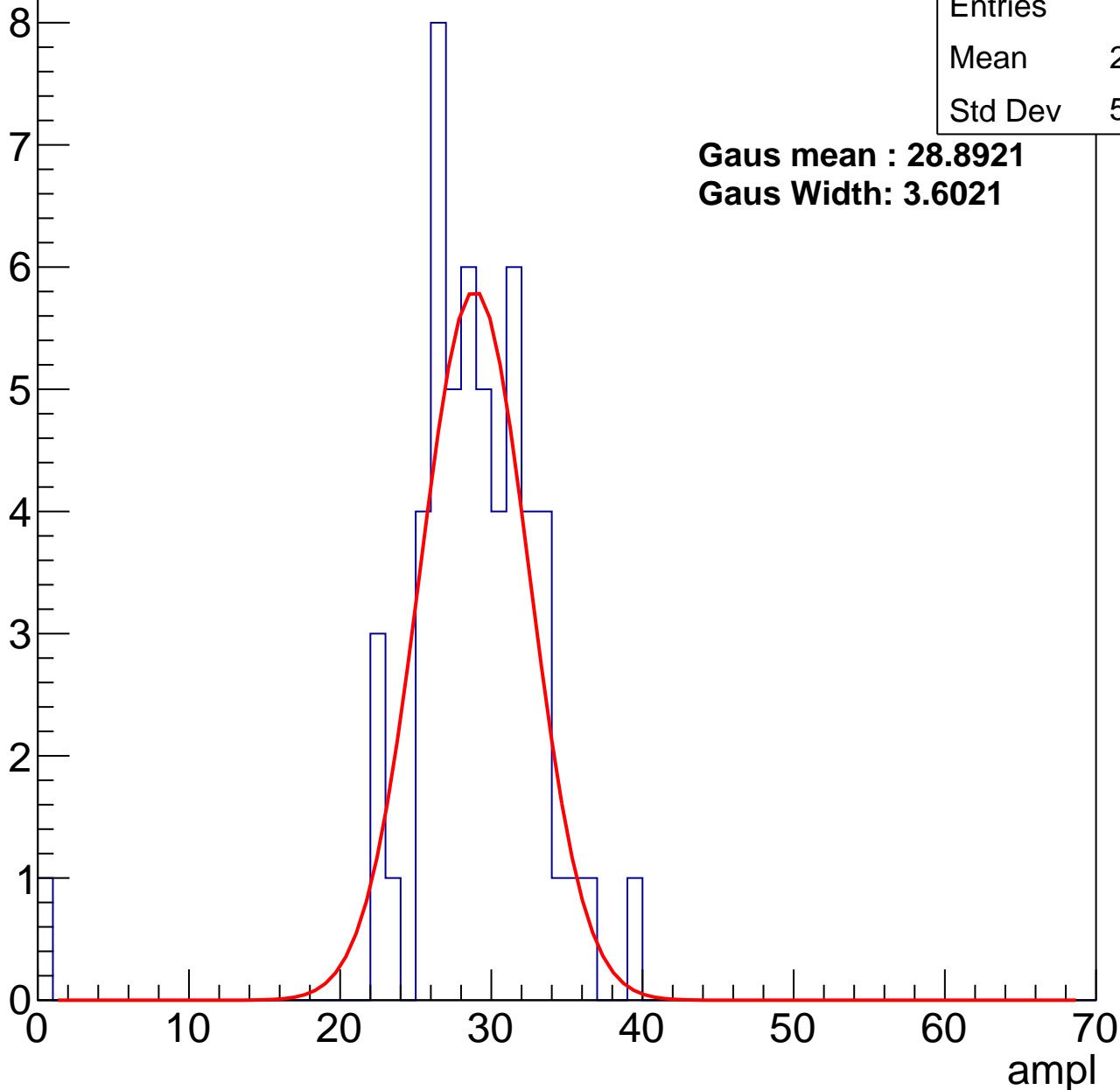
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	28.27
Std Dev	5.213

Gaus mean : 28.8921

Gaus Width: 3.6021



B1L103S, U24-ch24, adc1

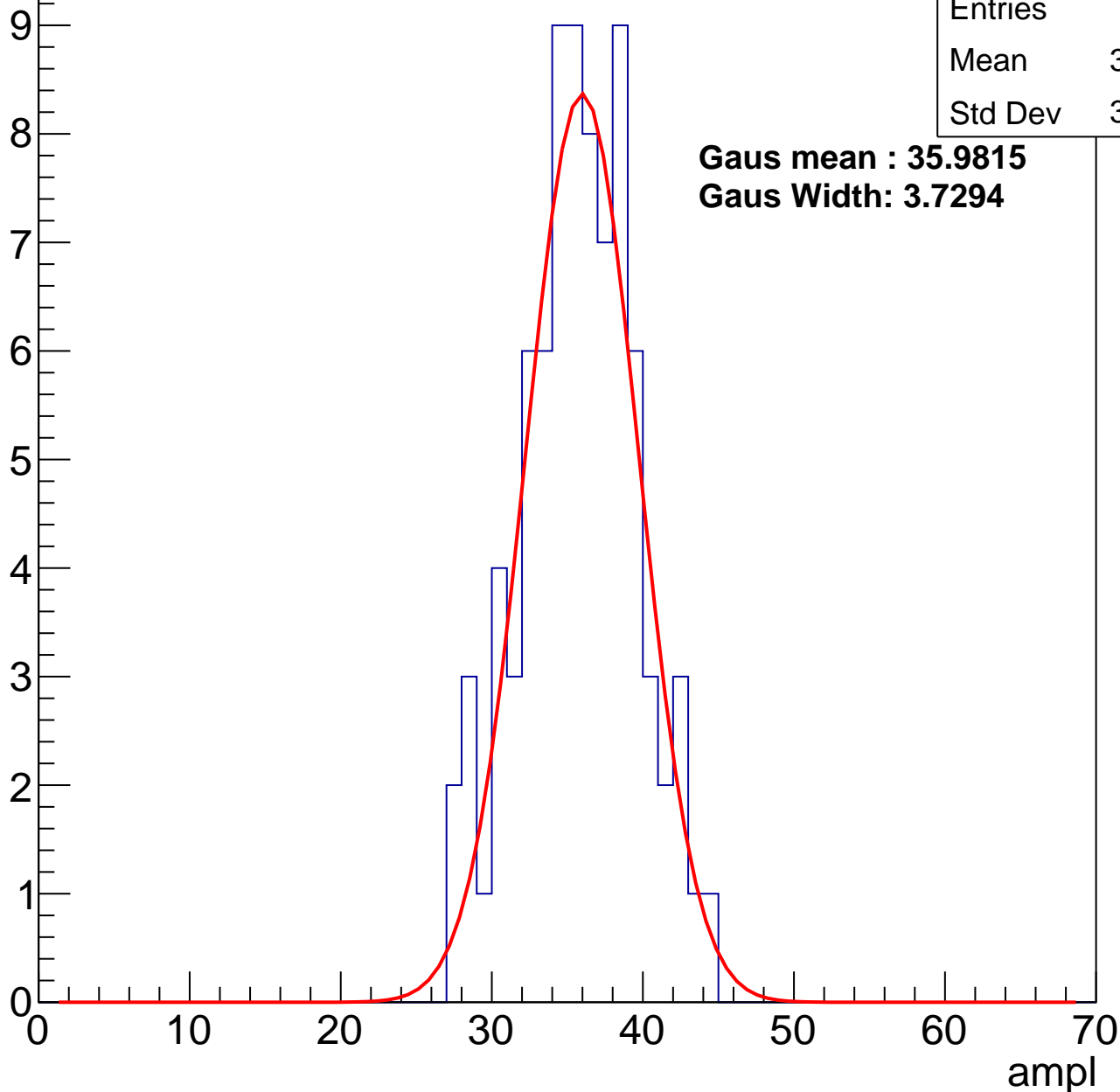
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	35.29
Std Dev	3.788

Gaus mean : 35.9815

Gaus Width: 3.7294



B1L103S, U24-ch24, adc2

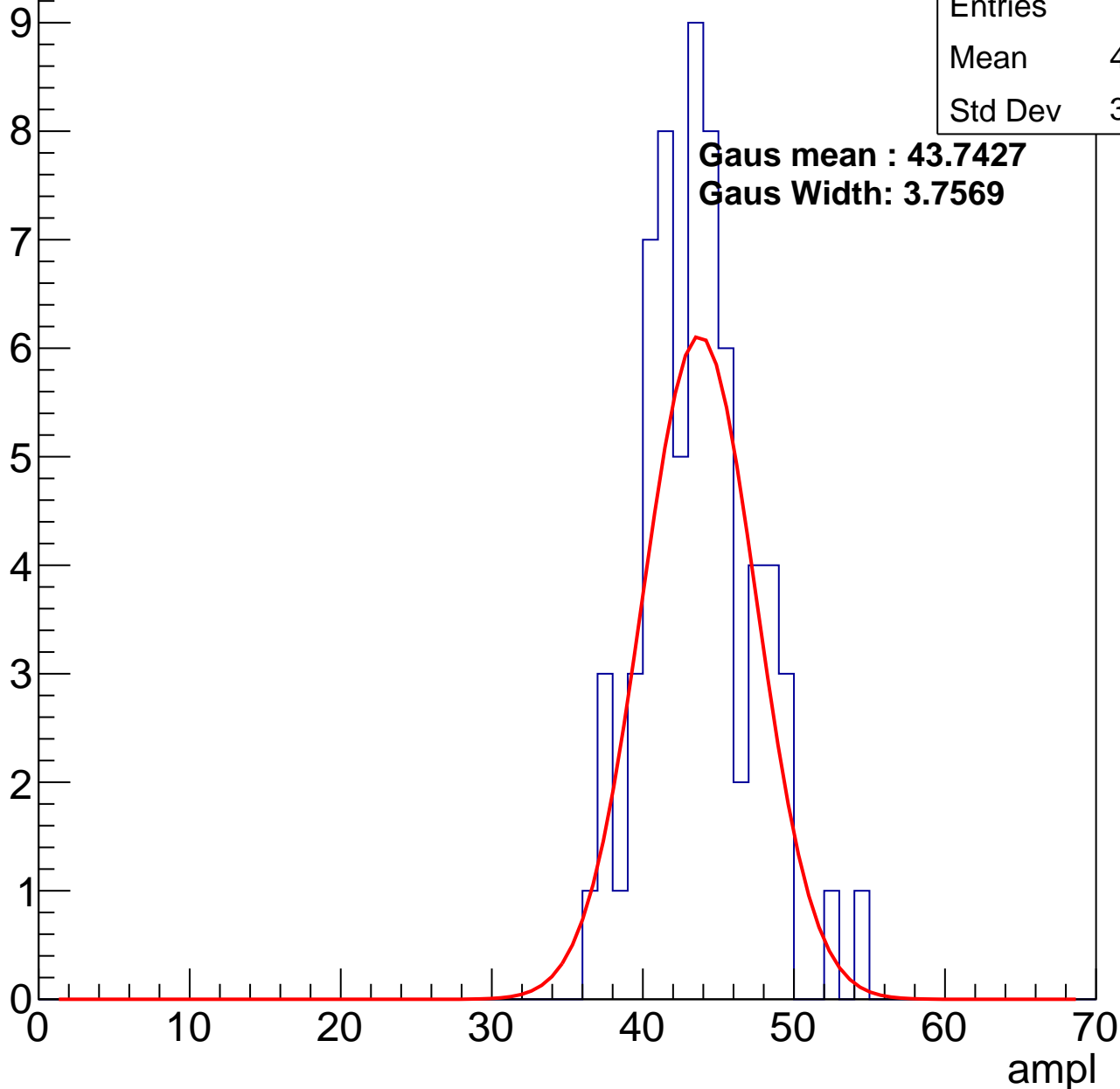
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	43.24
Std Dev	3.589

Gaus mean : 43.7427

Gaus Width: 3.7569

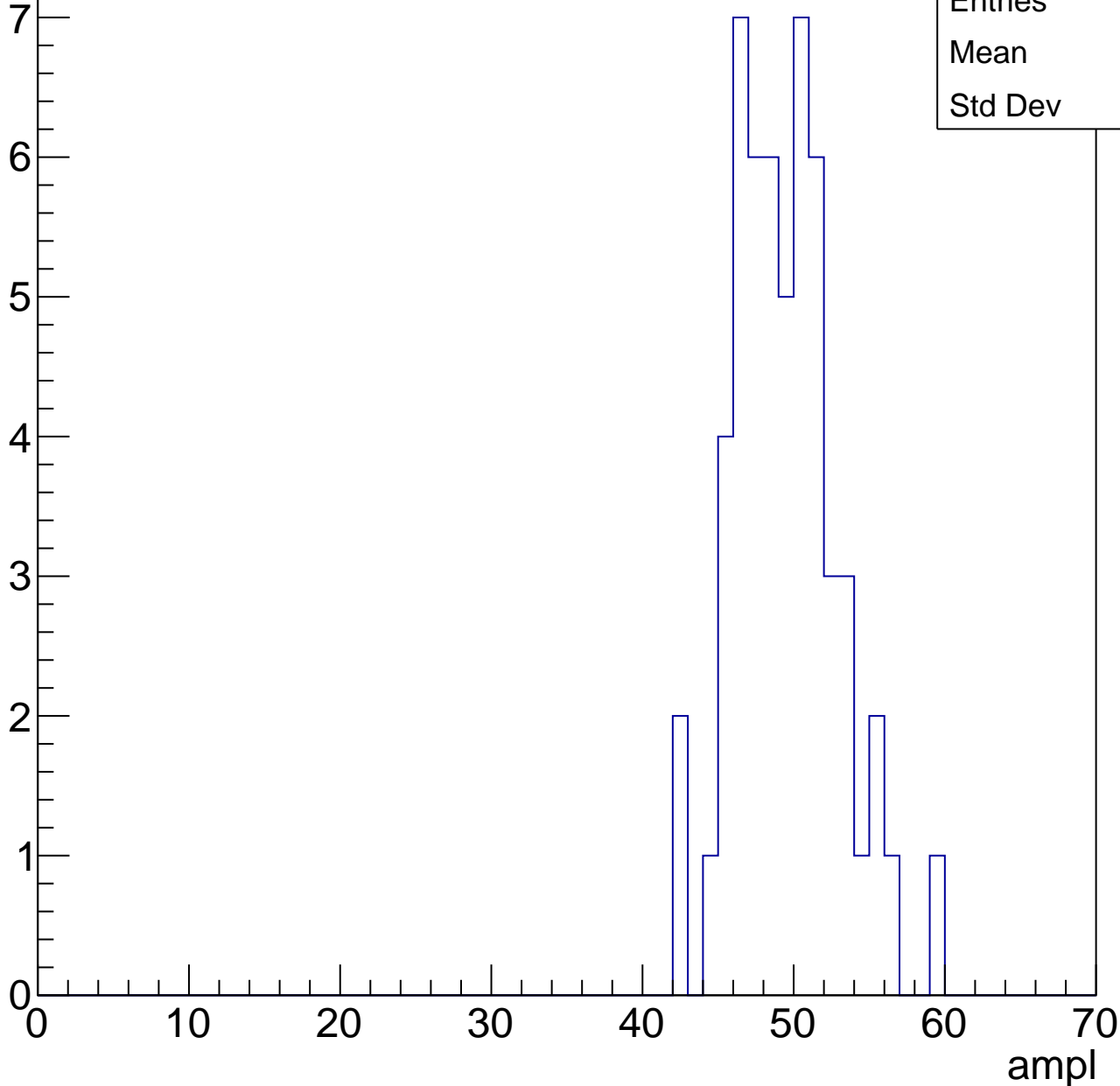


B1L103S, U24-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49
Std Dev	3.39

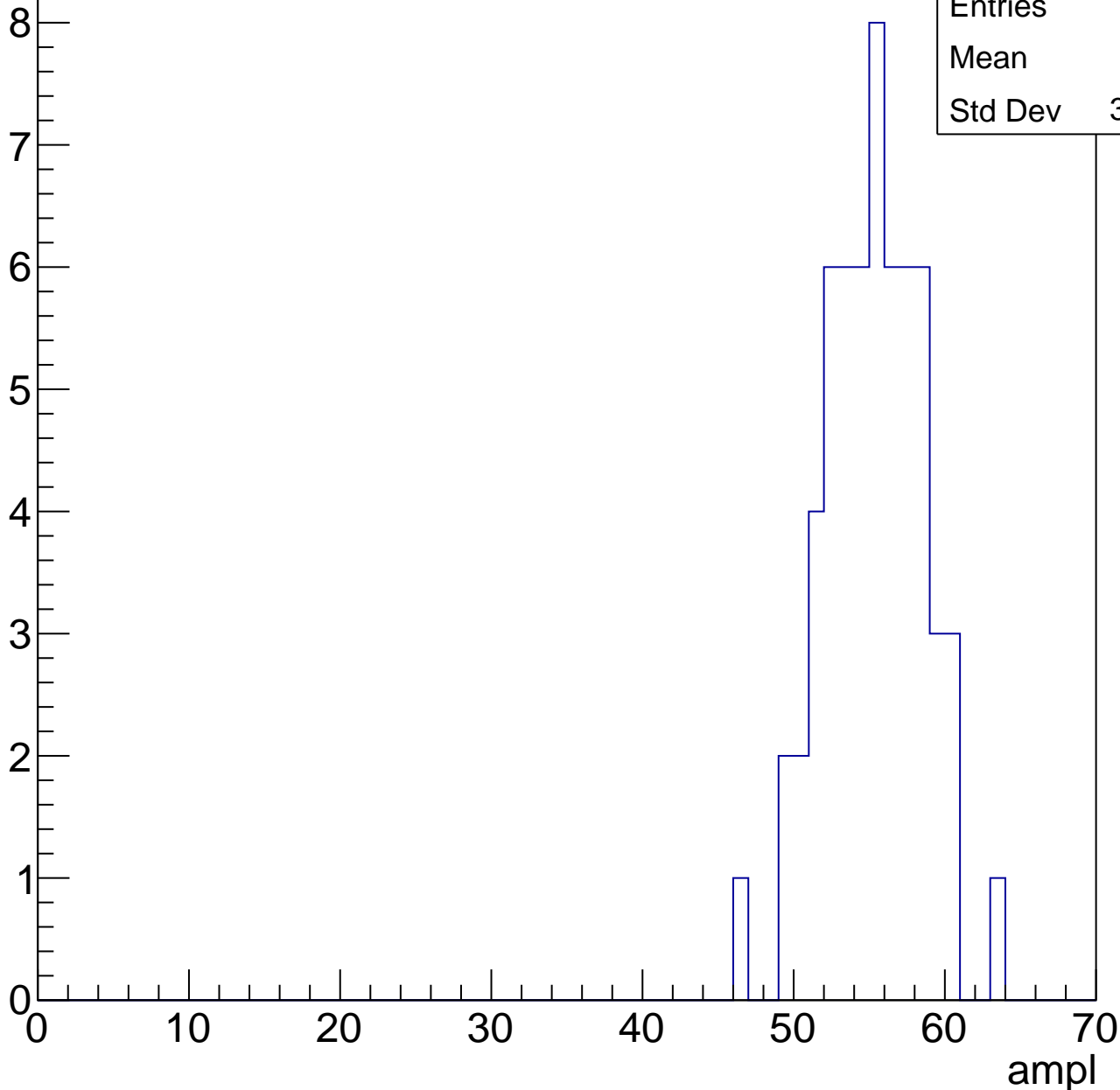


B1L103S, U24-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.8
Std Dev	3.214

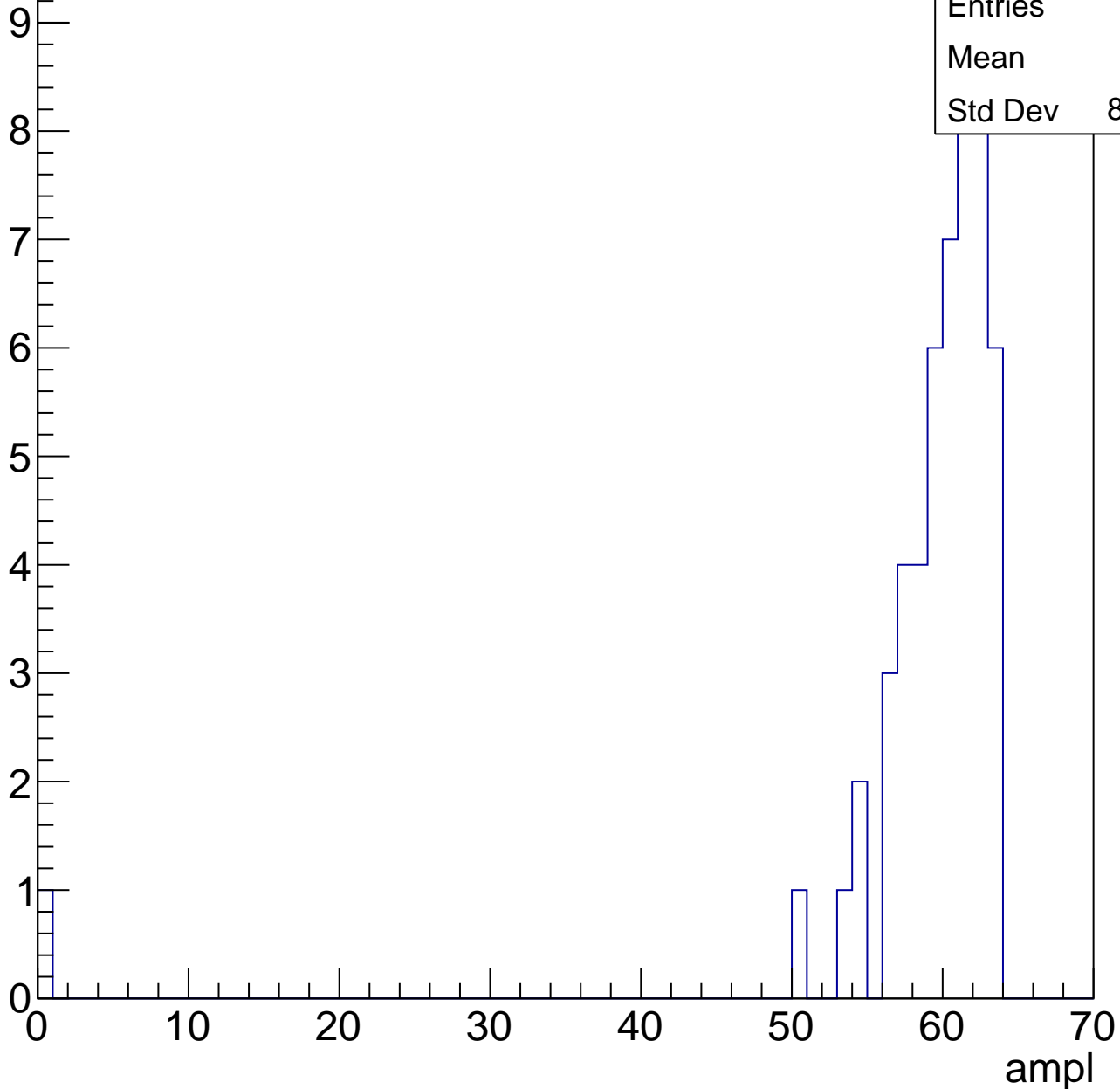


B1L103S, U24-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.4
Std Dev	8.652



B1L103S, U24-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch25, adc0

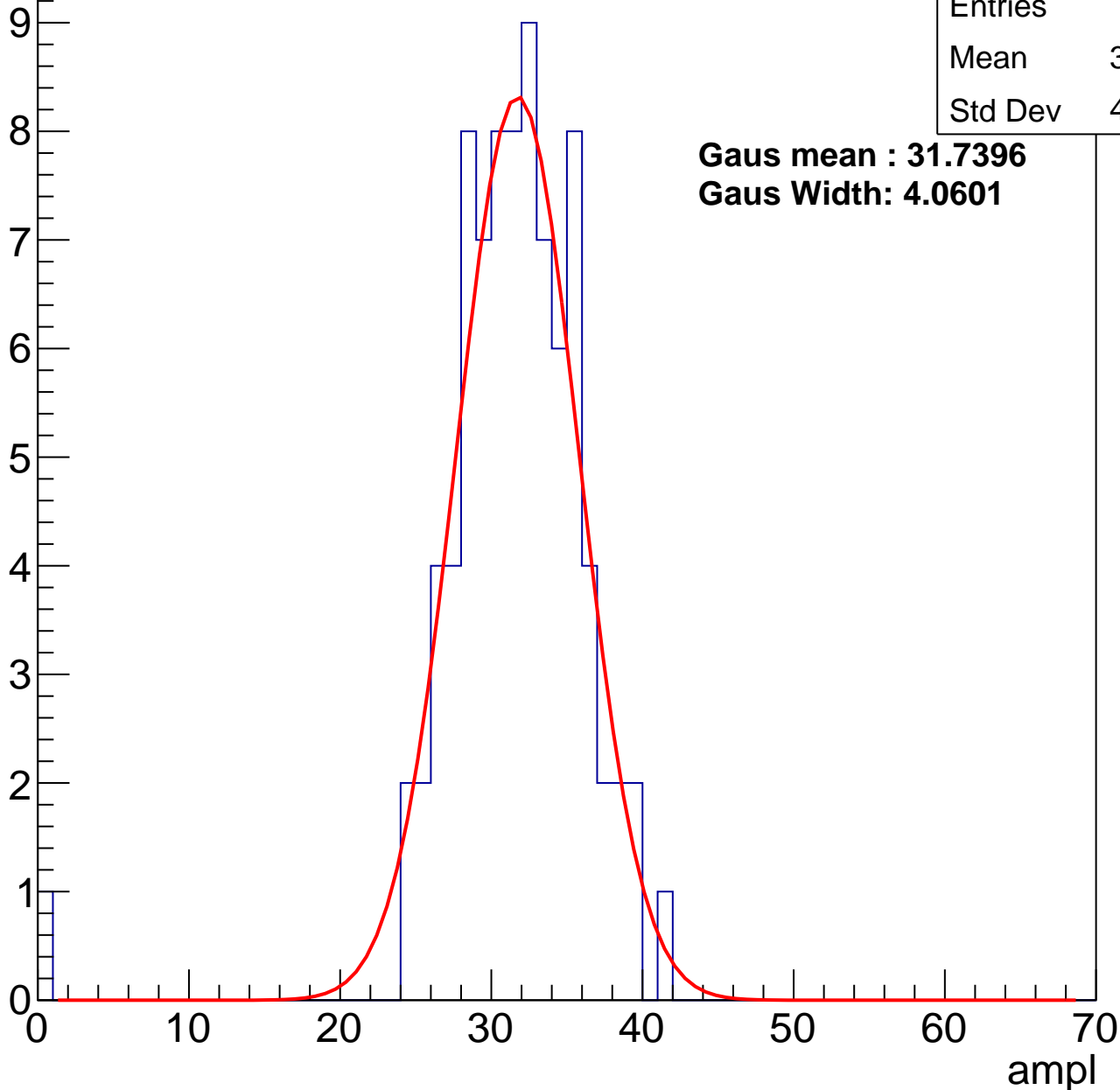
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	31.07
Std Dev	4.994

Gaus mean : 31.7396

Gaus Width: 4.0601



B1L103S, U24-ch25, adc1

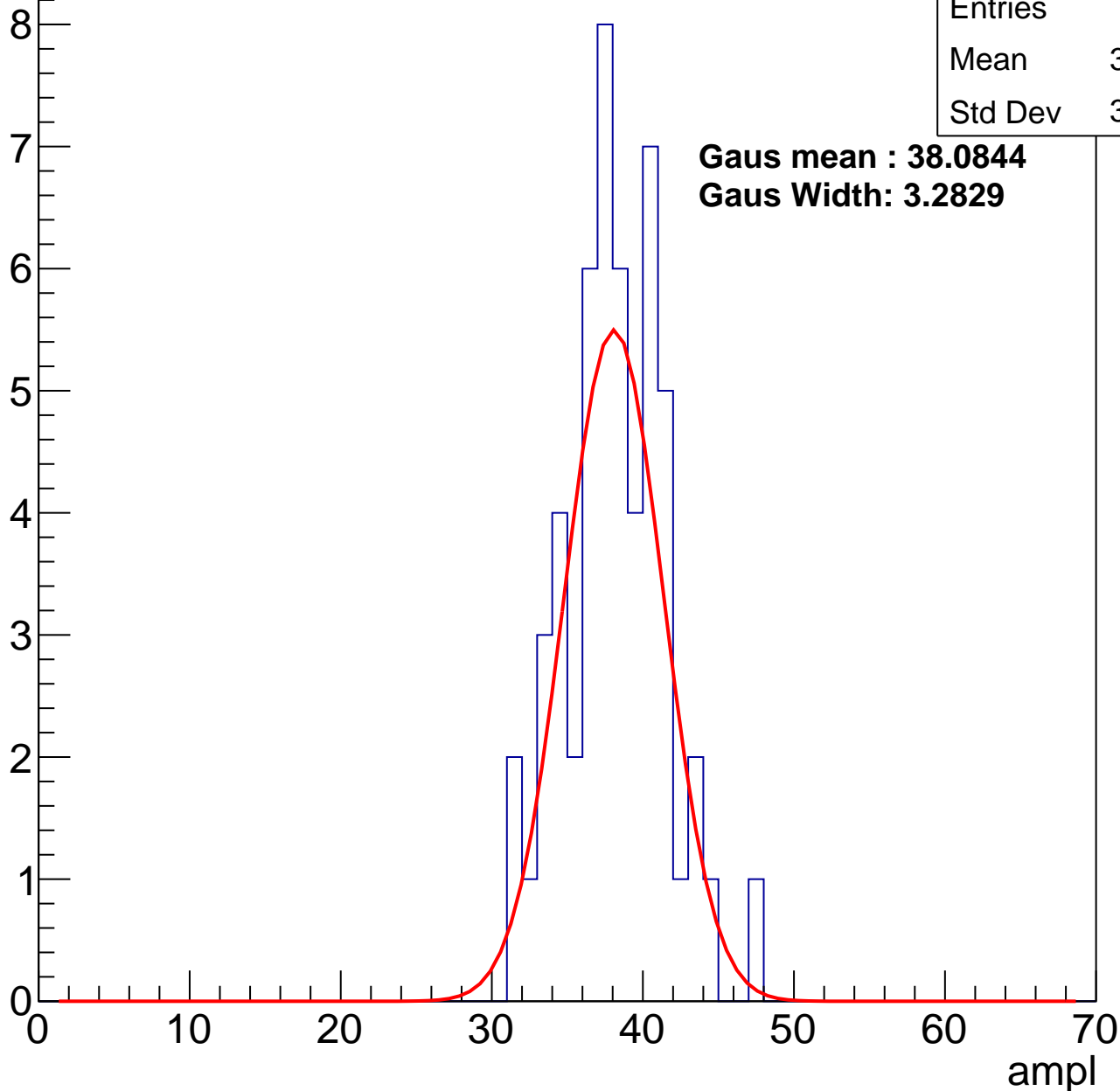
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	37.72
Std Dev	3.305

Gaus mean : 38.0844

Gaus Width: 3.2829



B1L103S, U24-ch25, adc2

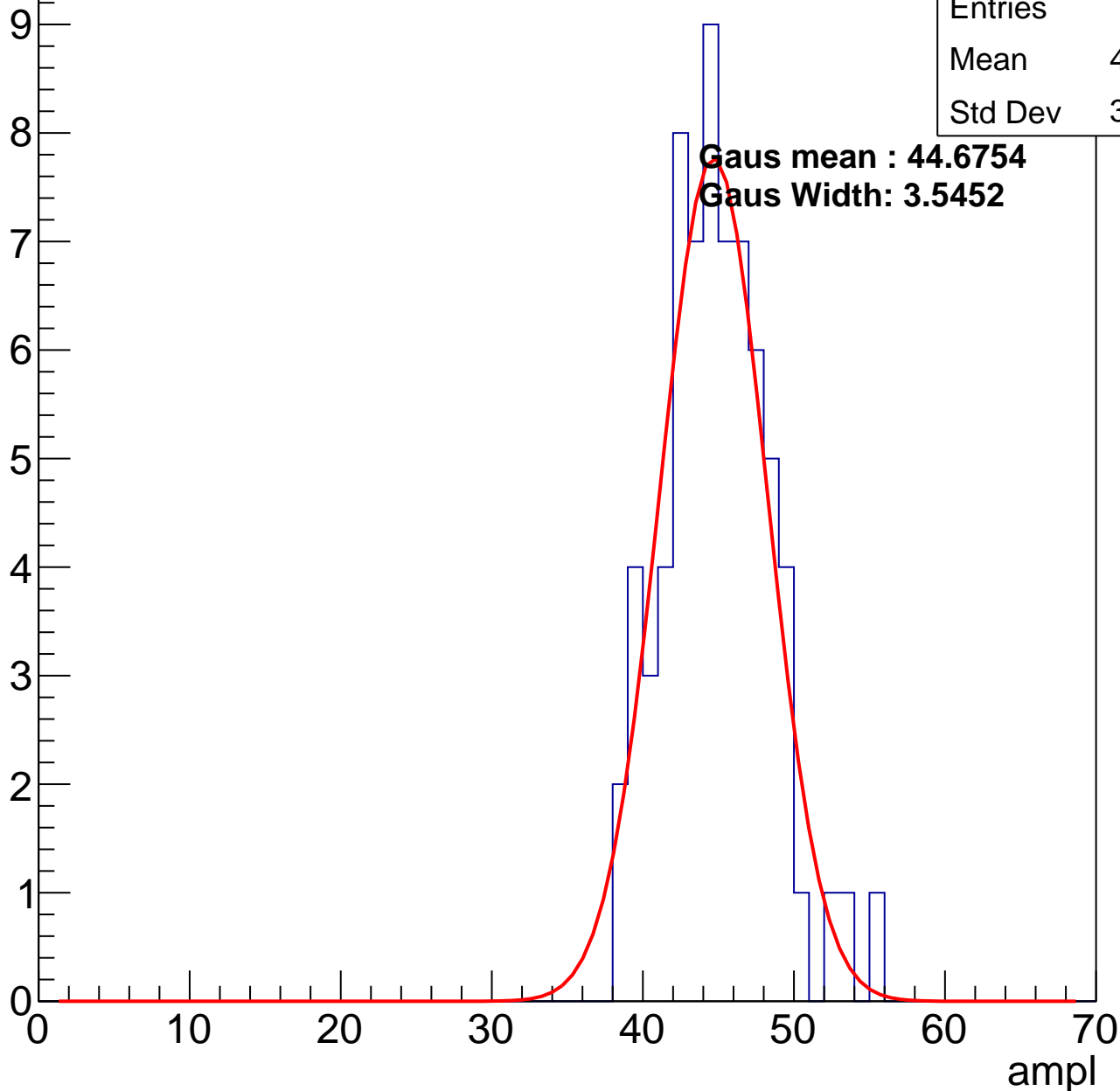
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.49
Std Dev	3.484

Gaus mean : 44.6754

Gaus Width: 3.5452

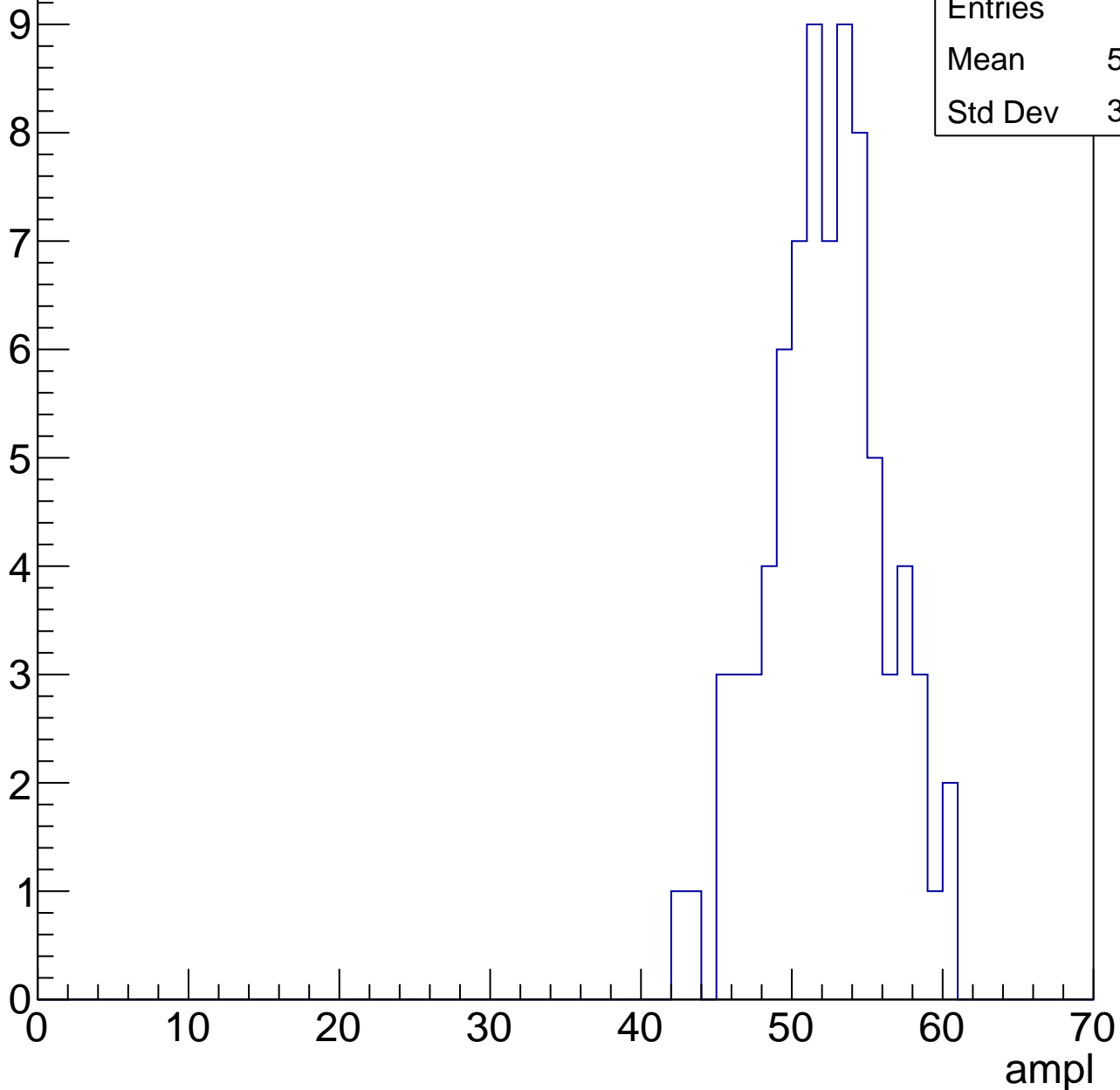


B1L103S, U24-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	51.78
Std Dev	3.867

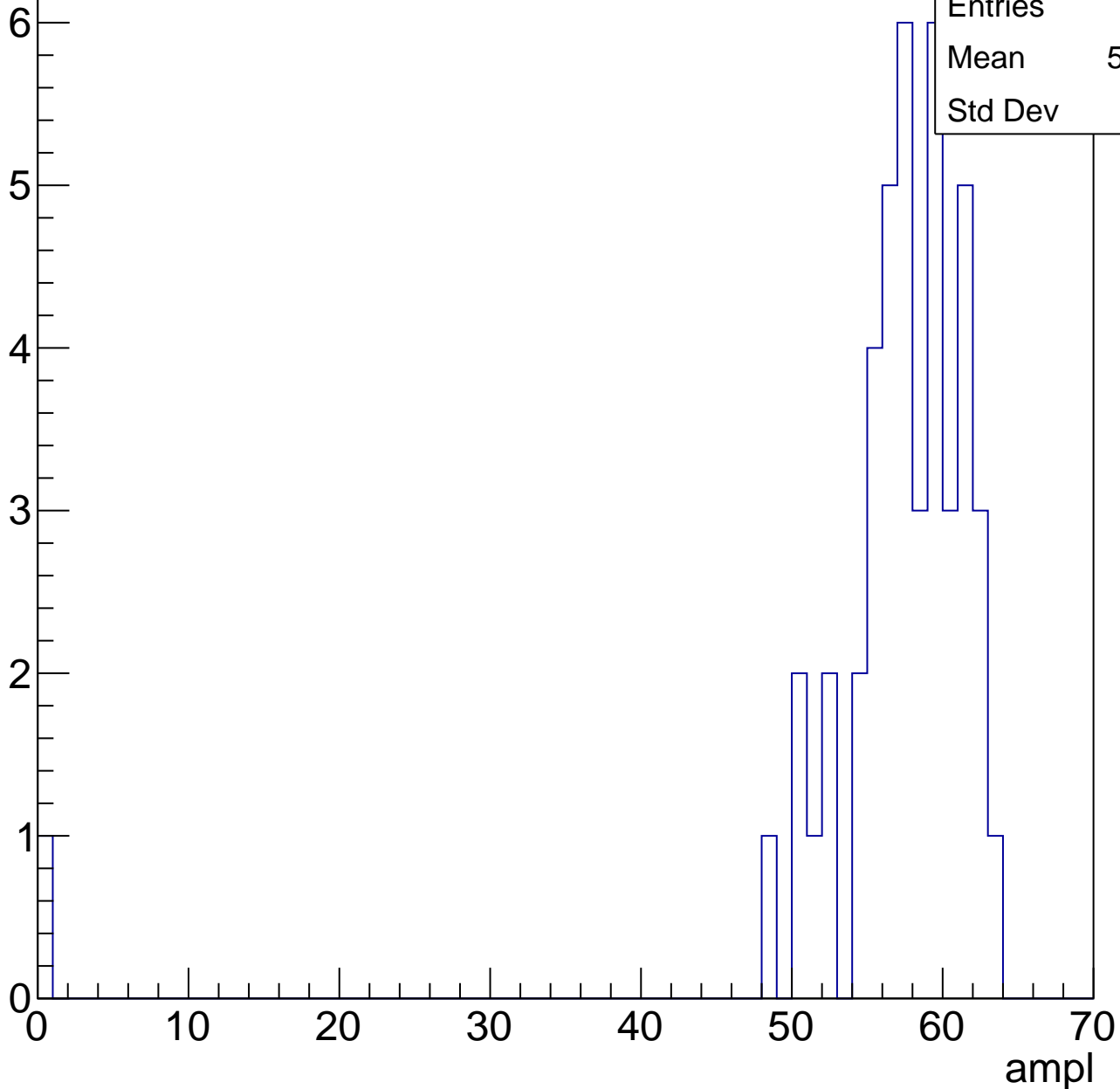


B1L103S, U24-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.89
Std Dev	9.12

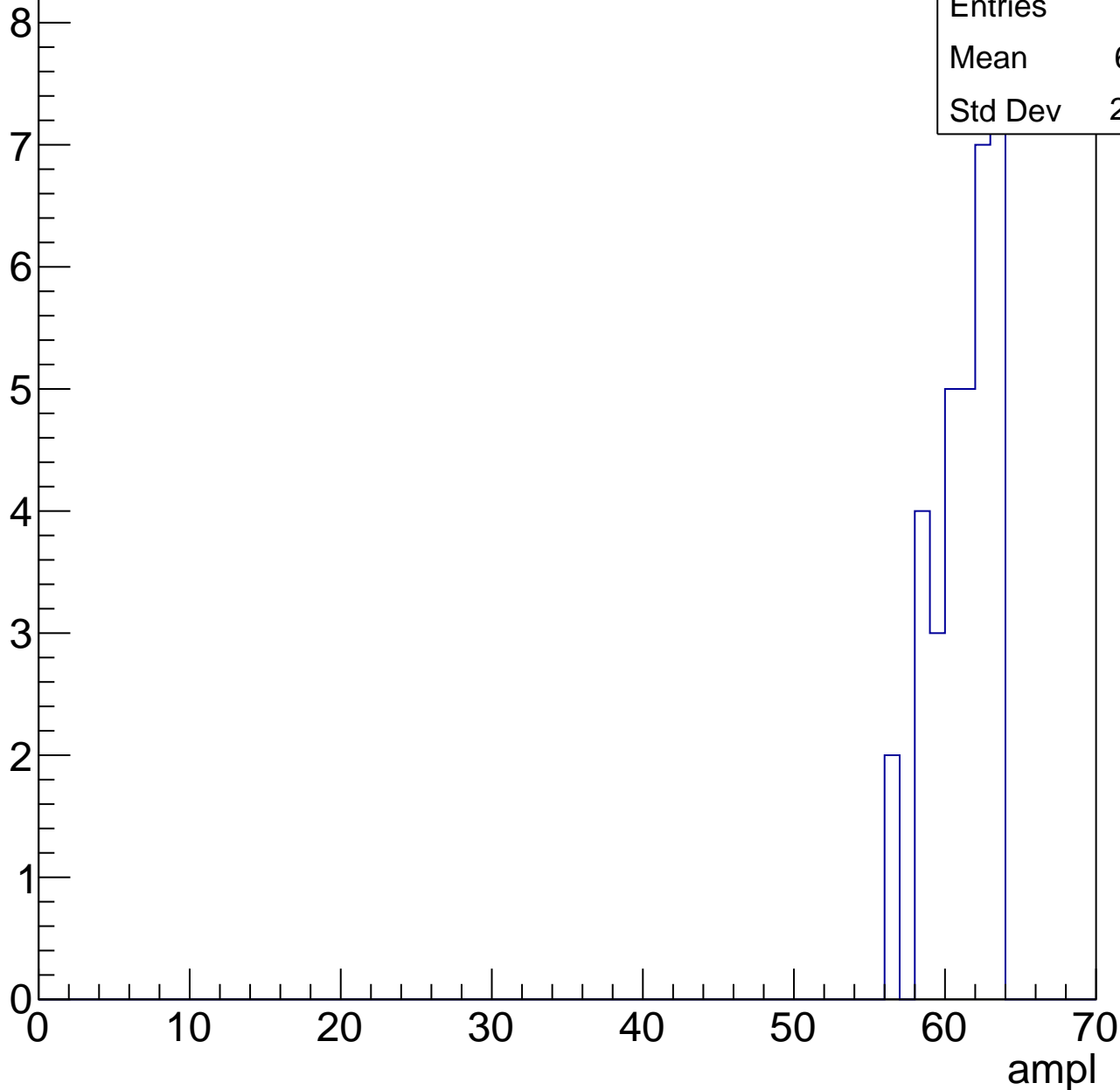


B1L103S, U24-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	60.71
Std Dev	2.022



B1L103S, U24-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U24-ch26, adc0

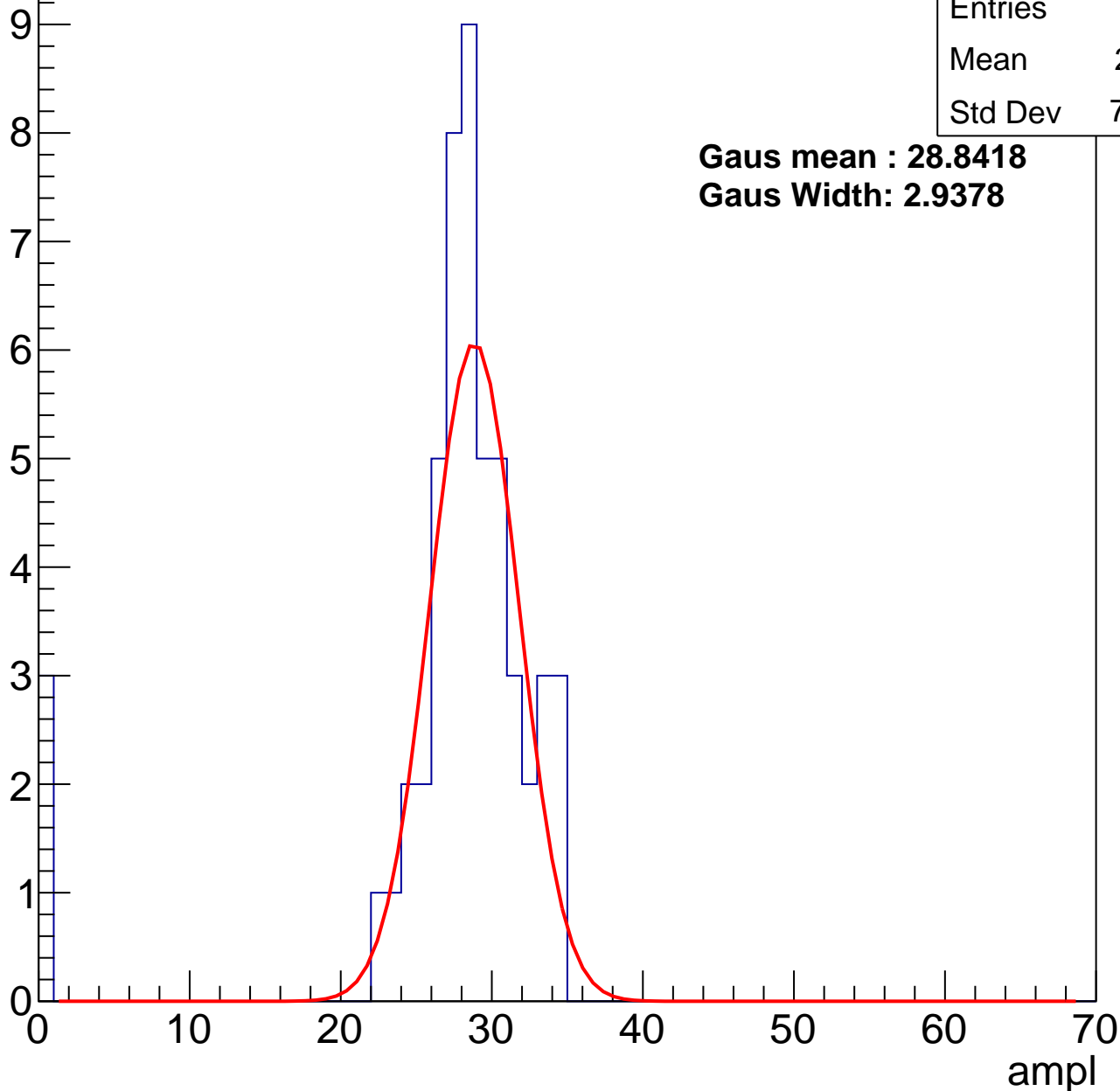
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	26.81
Std Dev	7.184

Gaus mean : 28.8418

Gaus Width: 2.9378



B1L103S, U24-ch26, adc1

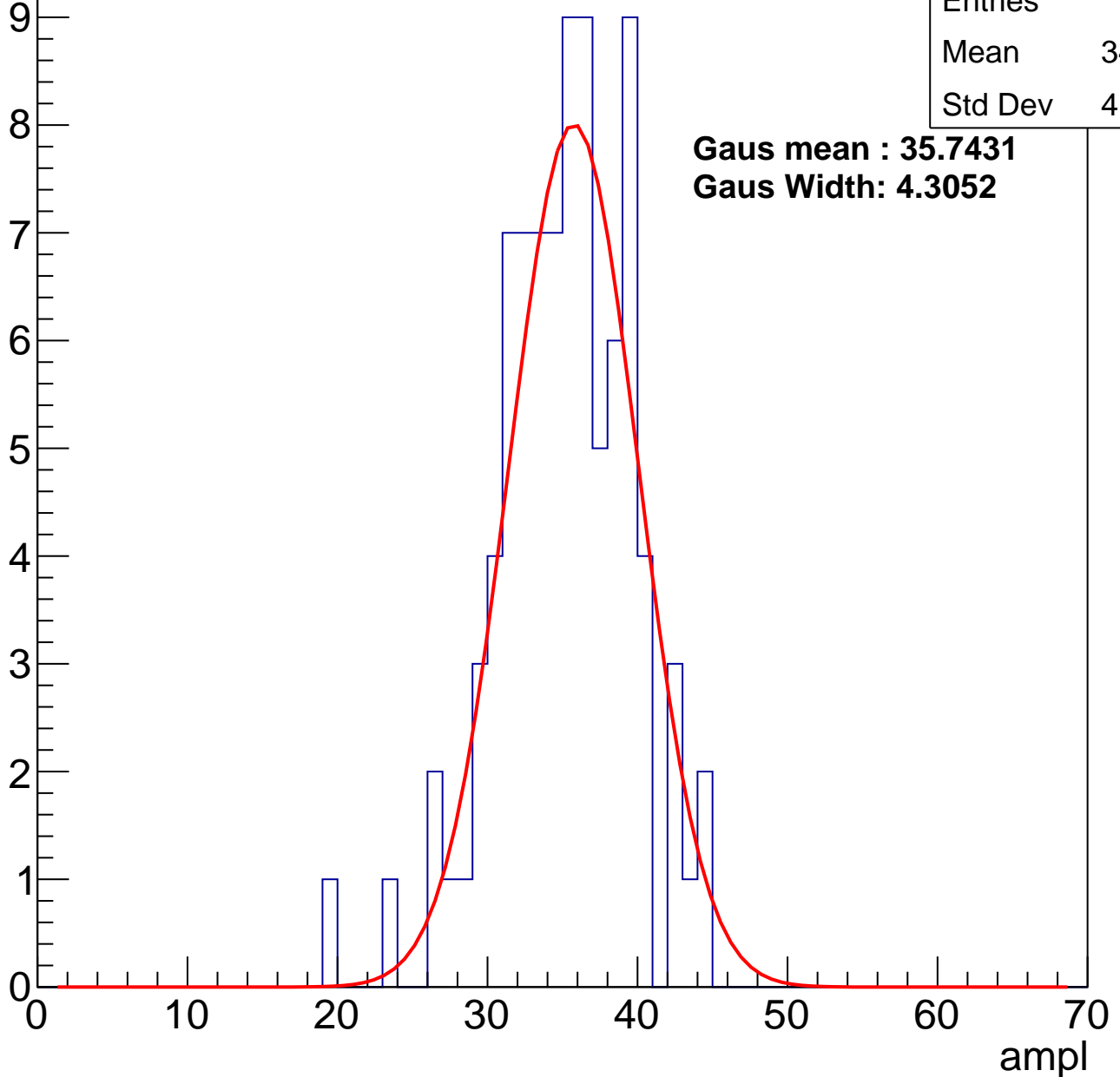
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	34.67
Std Dev	4.477

Gaus mean : 35.7431

Gaus Width: 4.3052



B1L103S, U24-ch26, adc2

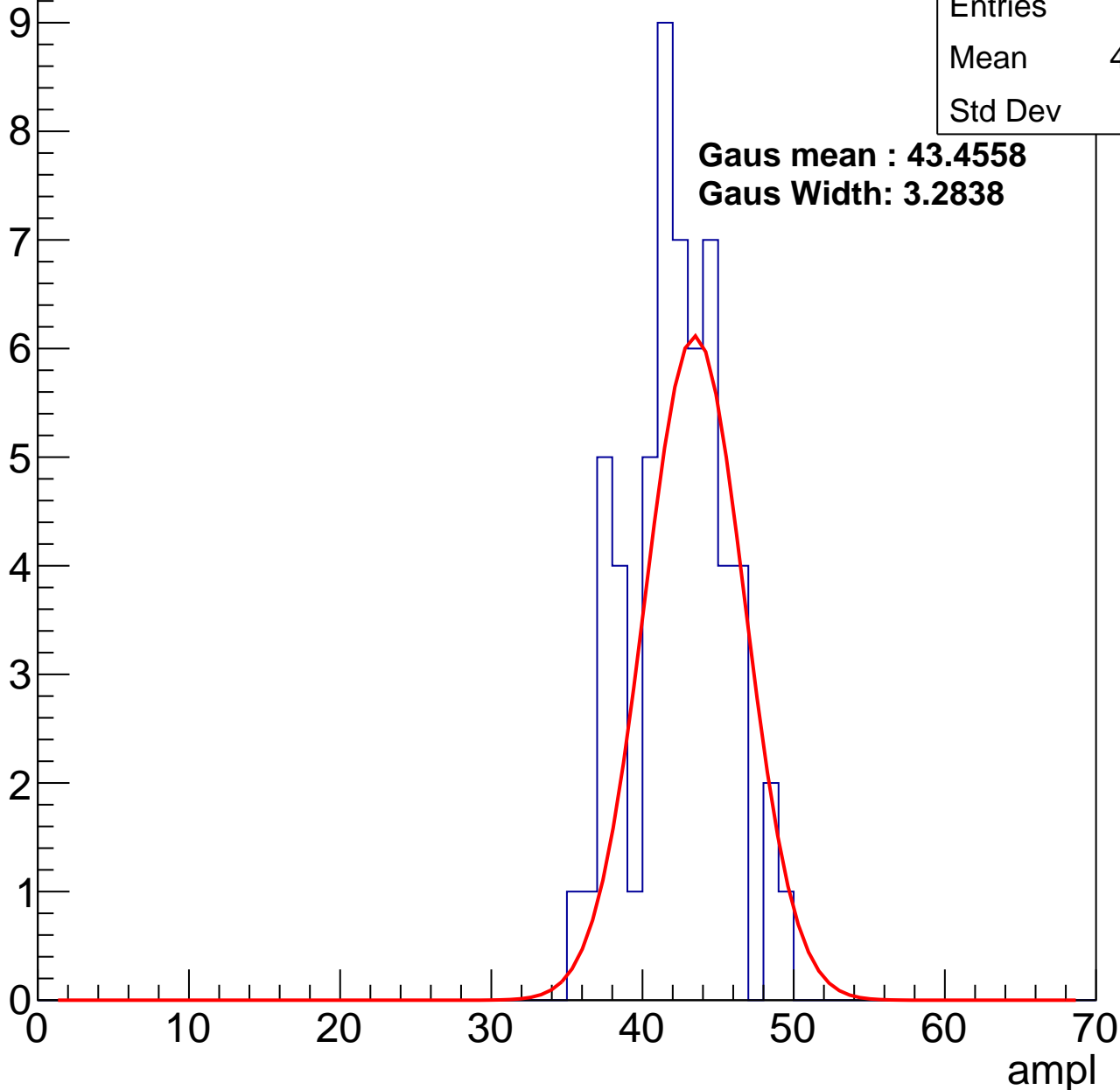
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.84
Std Dev	3.15

Gaus mean : 43.4558

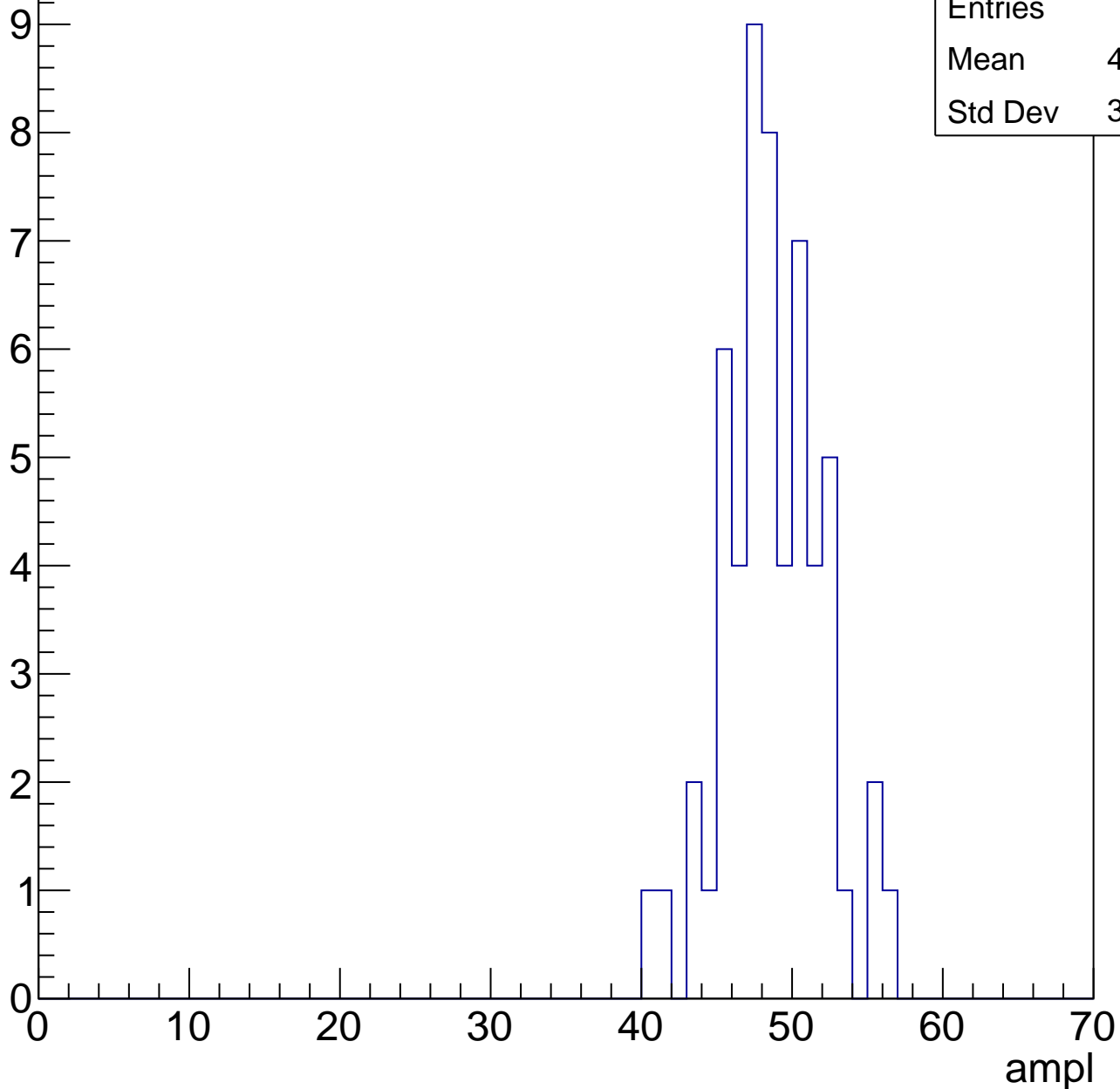
Gaus Width: 3.2838



B1L103S, U24-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



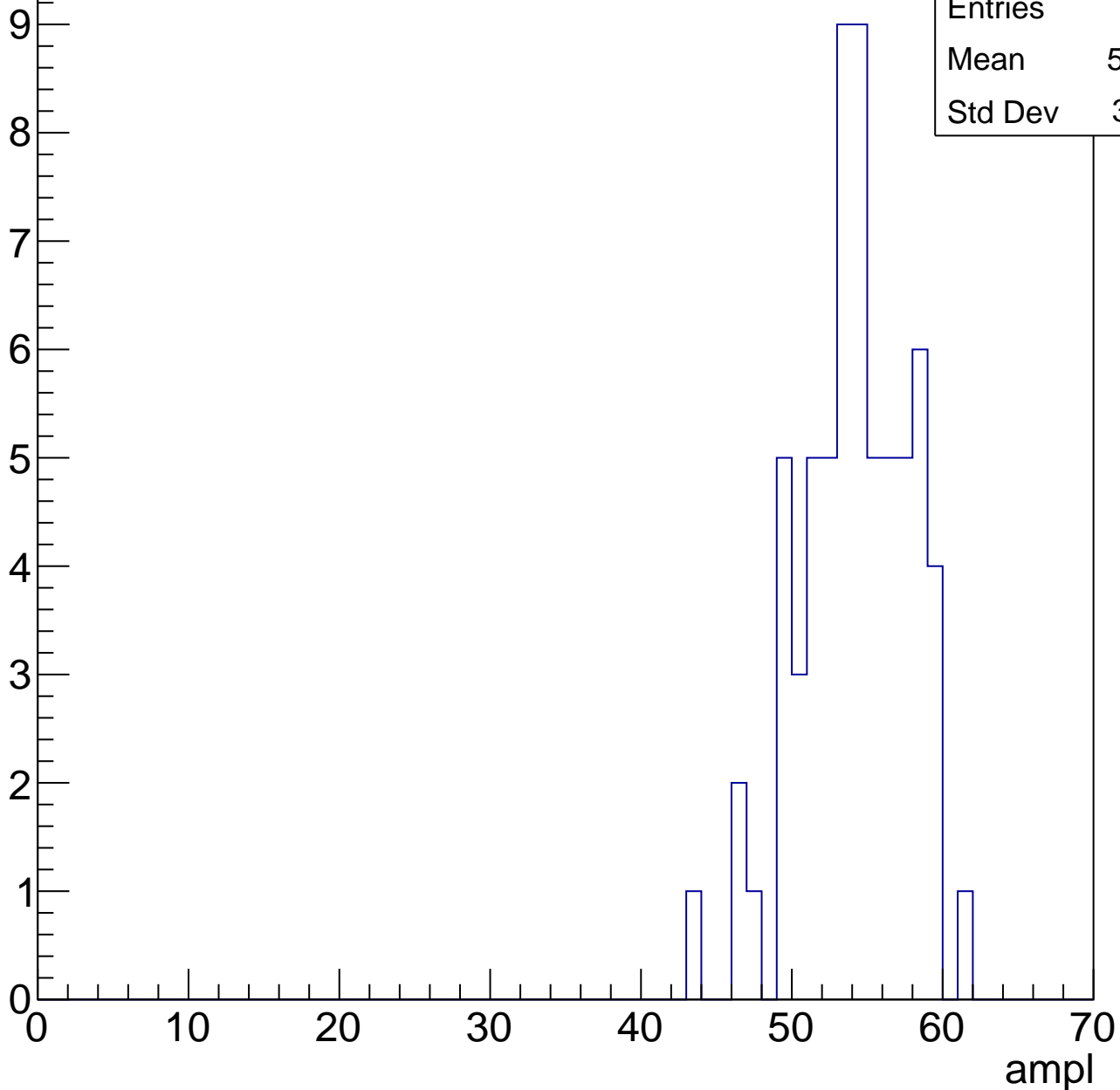
Entries	56
Mean	48.23
Std Dev	3.246

B1L103S, U24-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	53.64
Std Dev	3.591

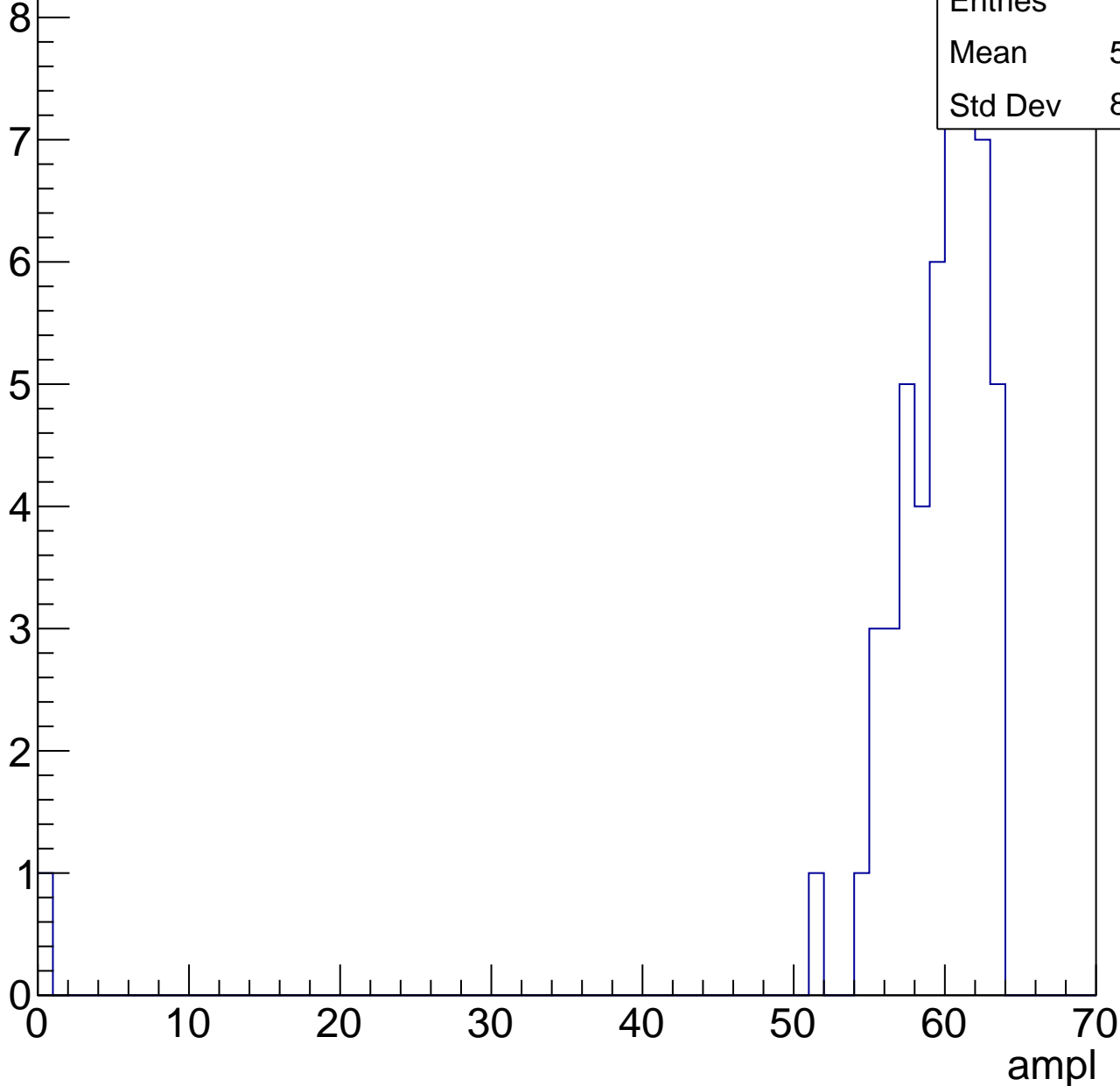


B1L103S, U24-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.19
Std Dev	8.569

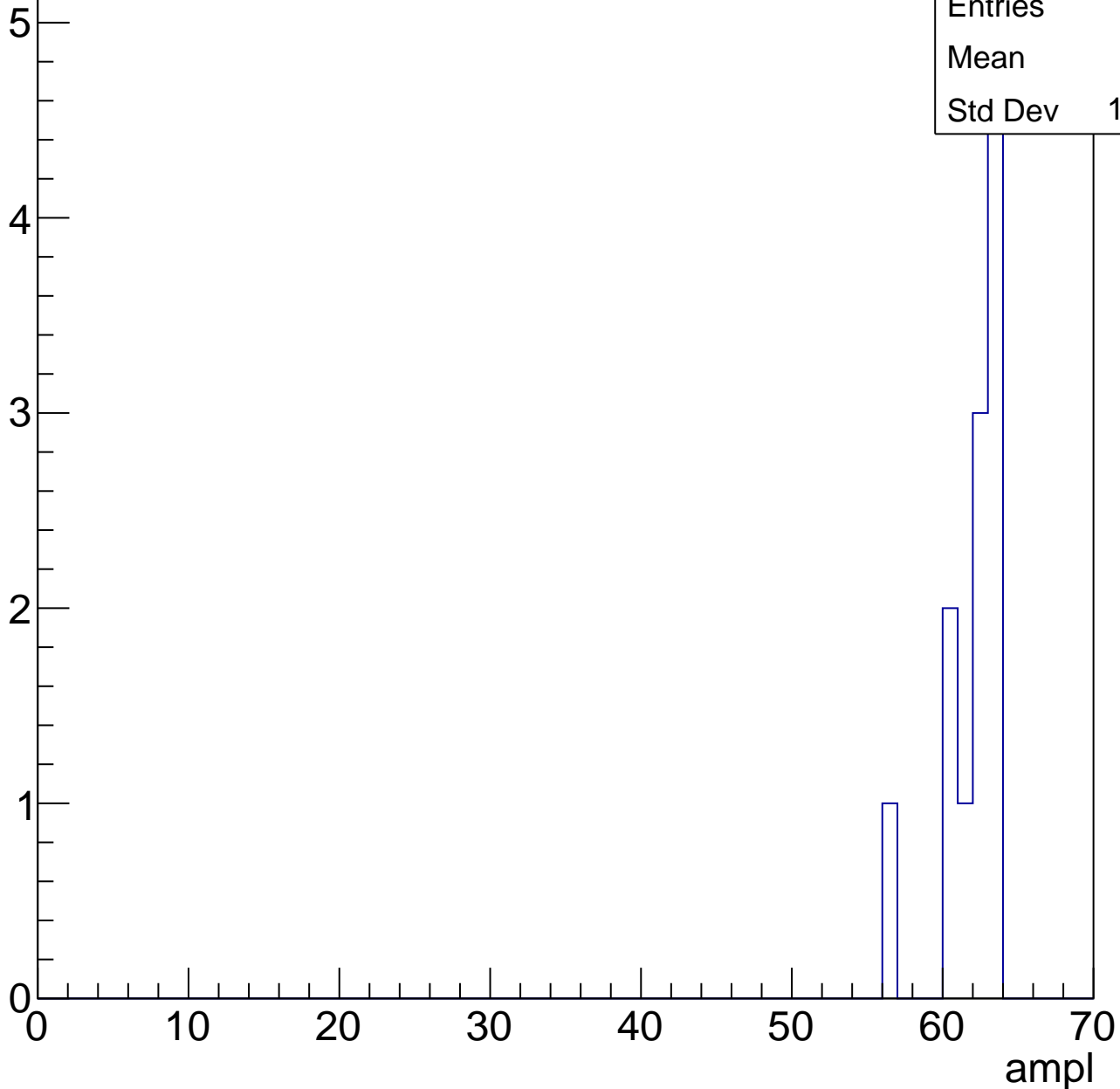


B1L103S, U24-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.5
Std Dev	1.979



B1L103S, U24-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch27, adc0

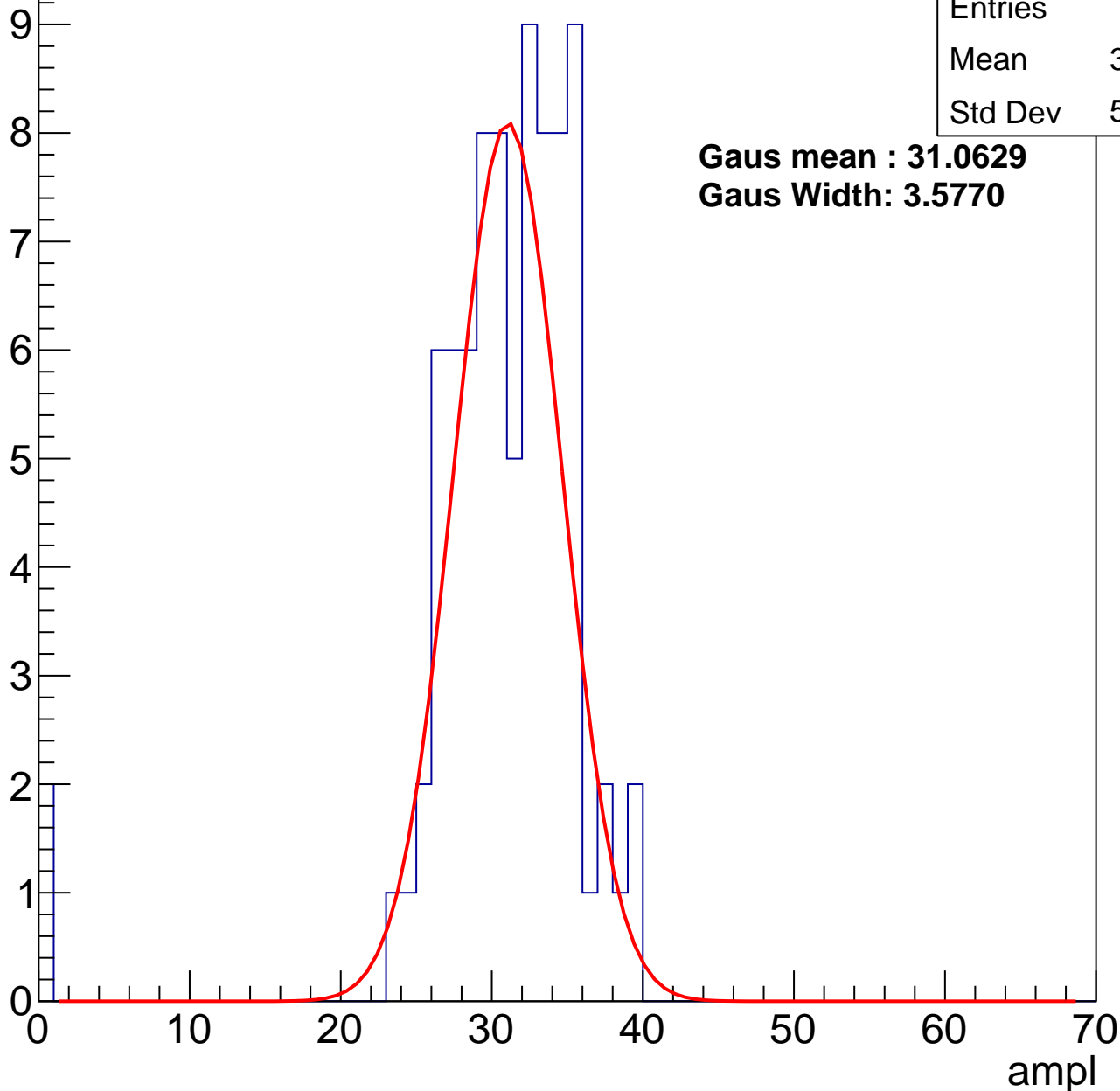
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	30.29
Std Dev	5.877

Gaus mean : 31.0629

Gaus Width: 3.5770



B1L103S, U24-ch27, adc1

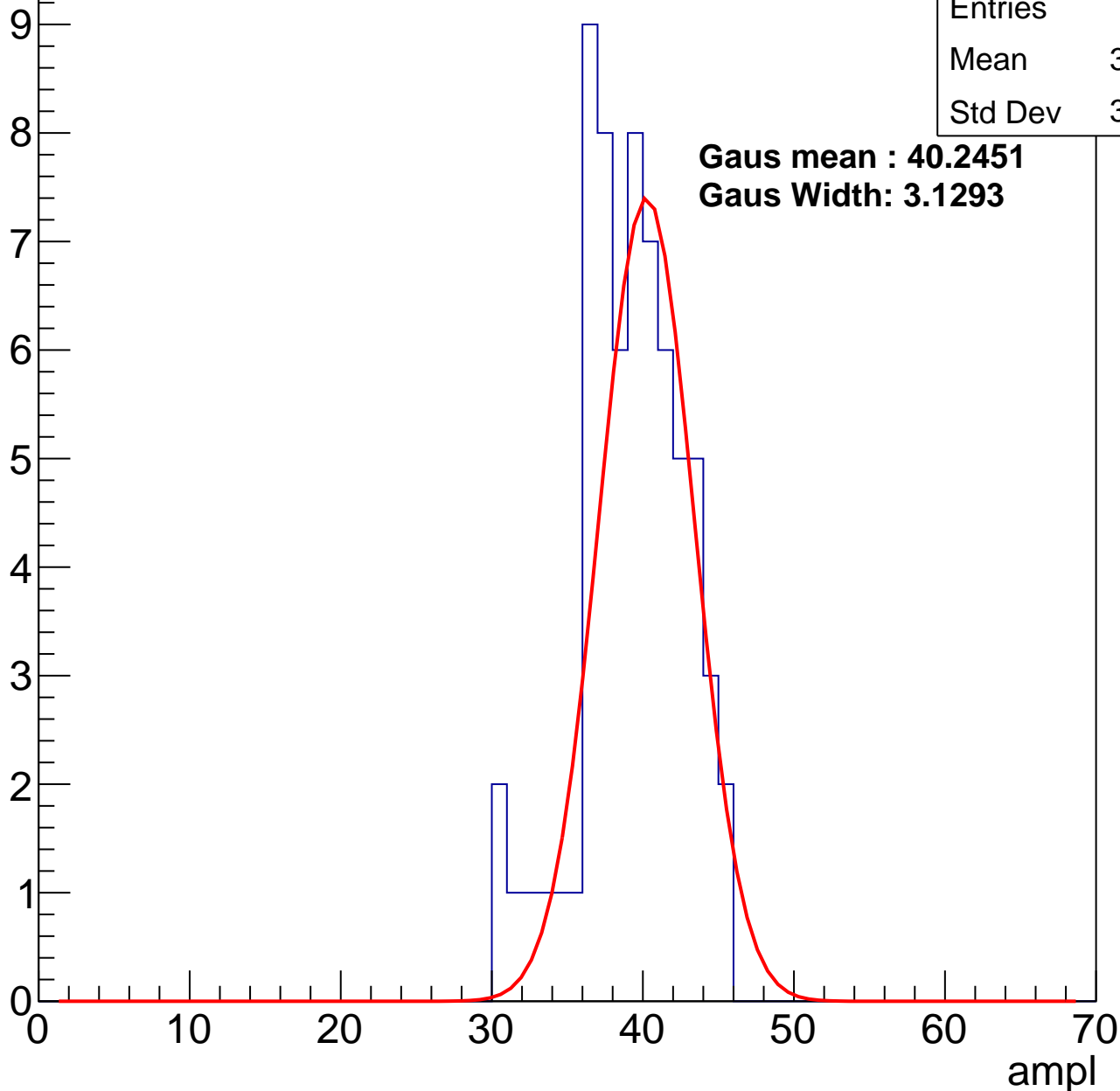
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	38.76
Std Dev	3.416

Gaus mean : 40.2451

Gaus Width: 3.1293



B1L103S, U24-ch27, adc2

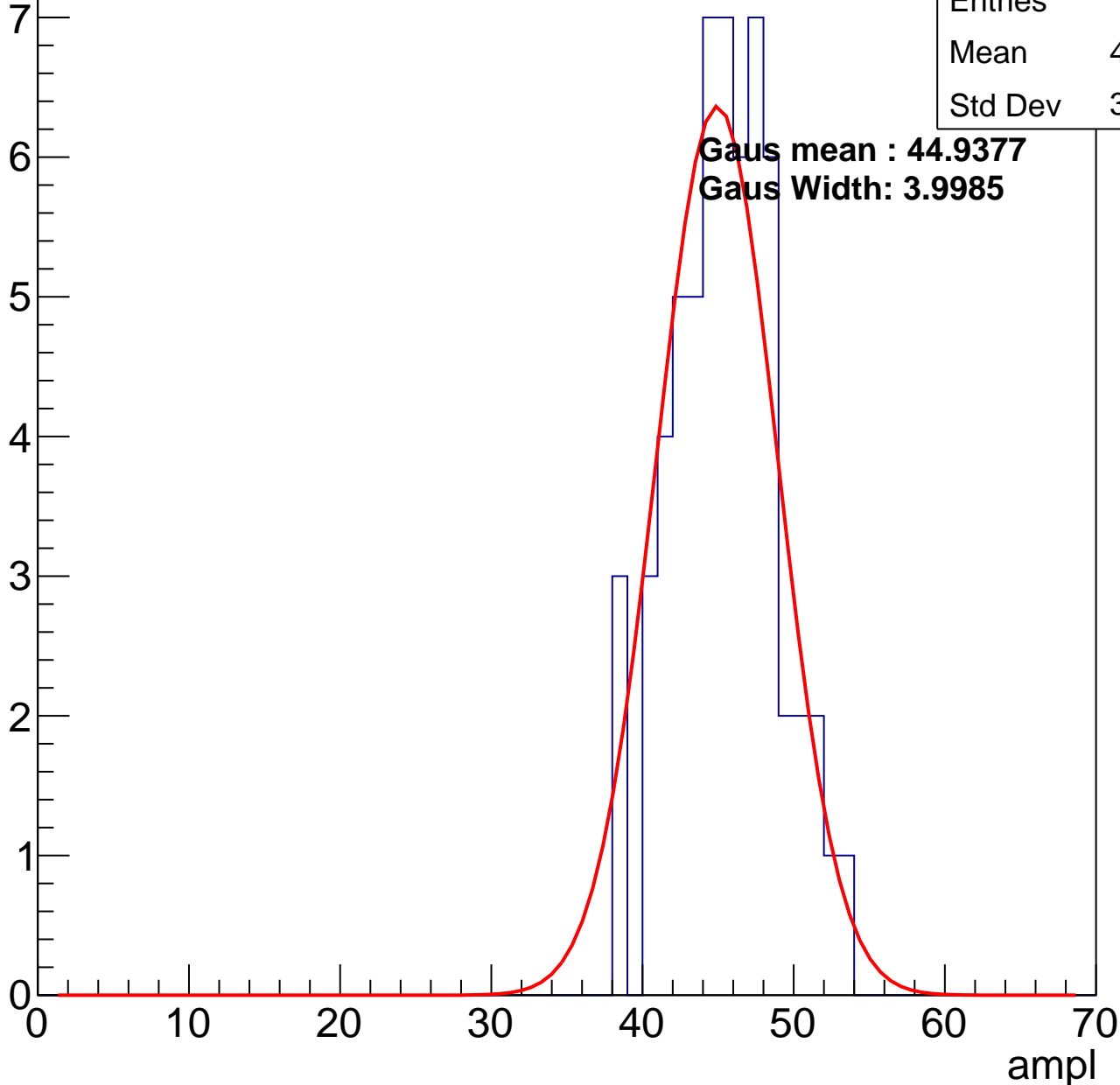
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	44.98
Std Dev	3.419

Gaus mean : 44.9377

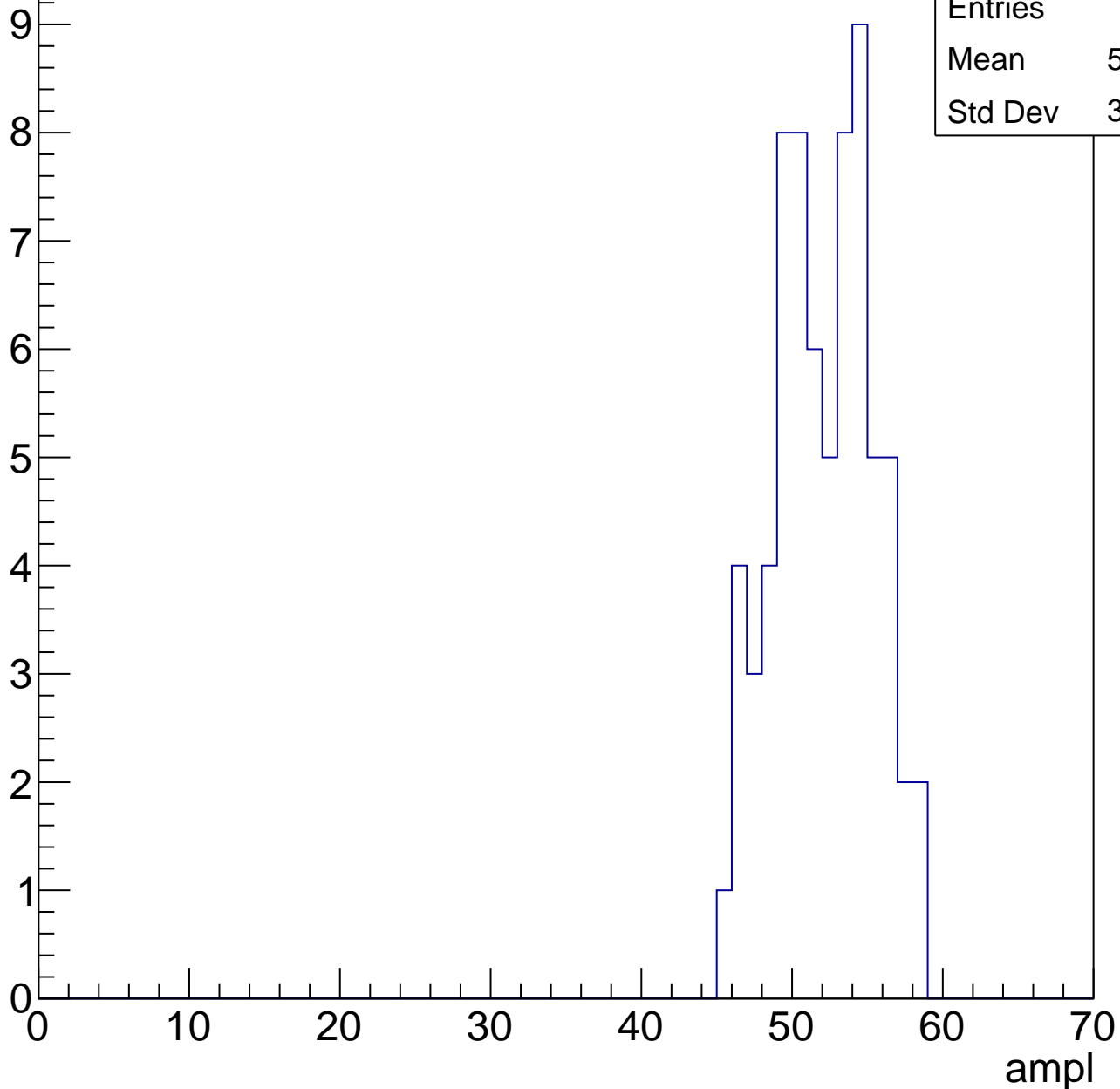
Gaus Width: 3.9985



B1L103S, U24-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

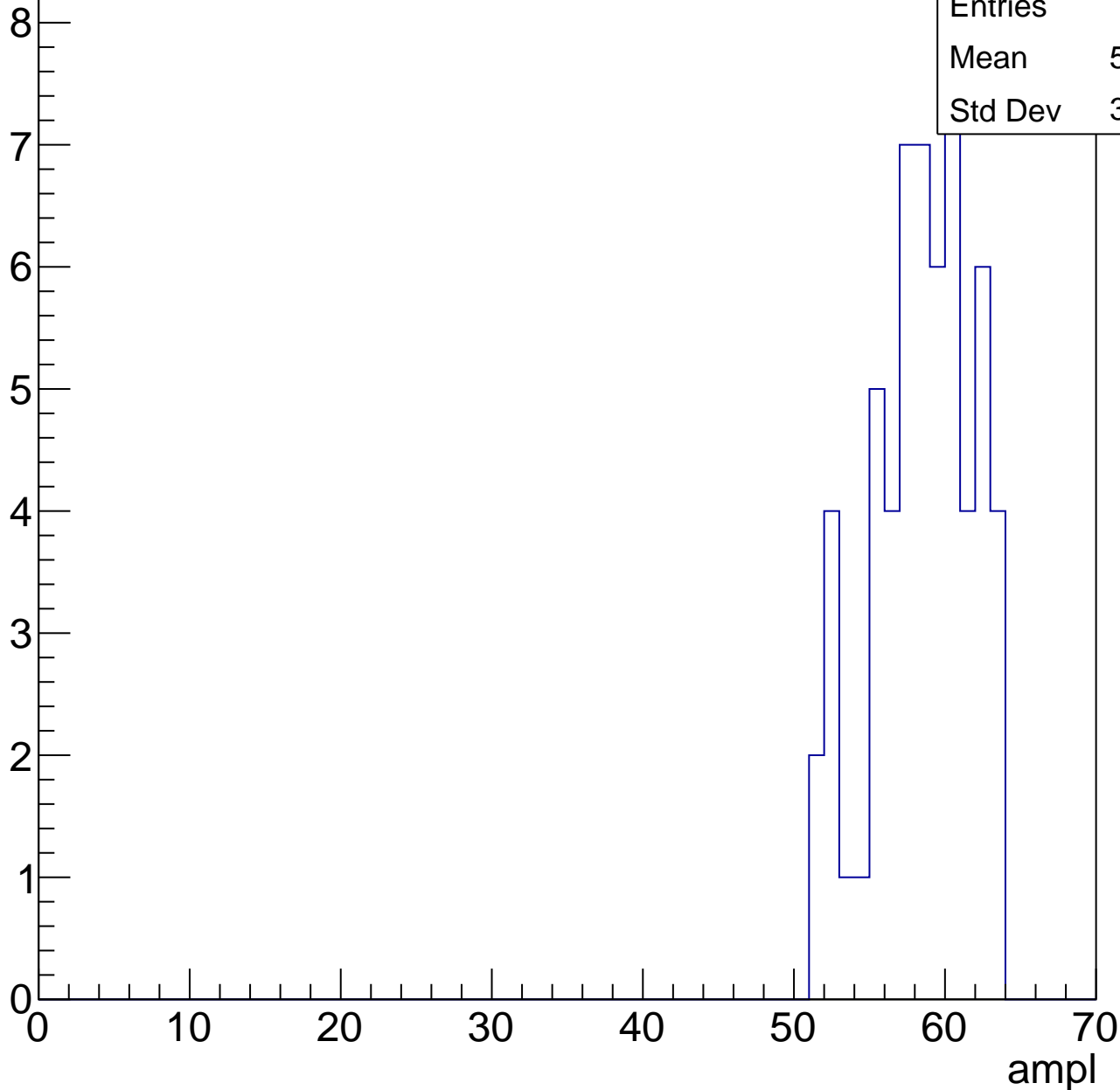


B1L103S, U24-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

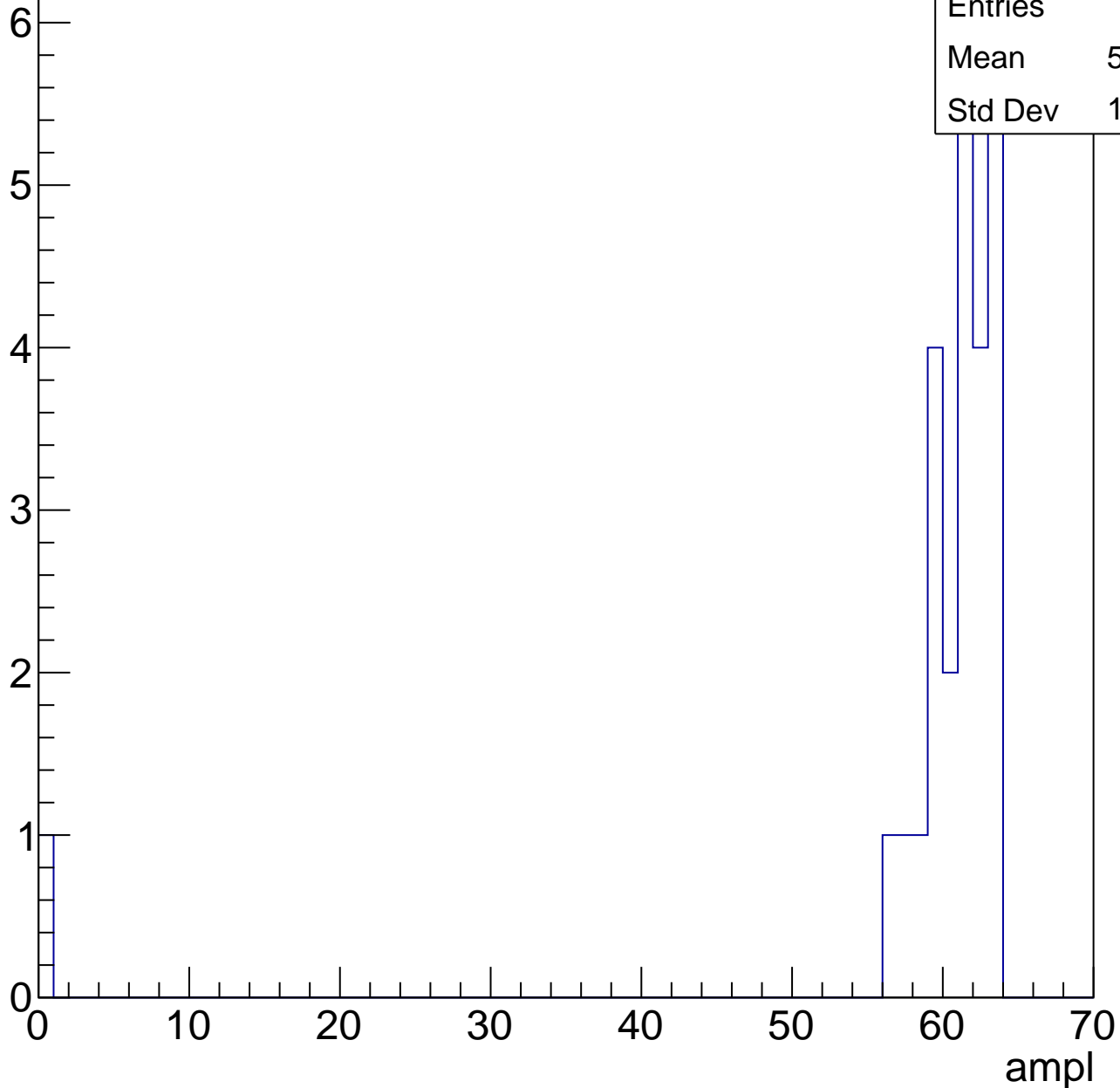
Entries	59
Mean	58.02
Std Dev	3.244



B1L103S, U24-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch28, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	28.29
Std Dev	4.939

Gaus mean : 29.9157

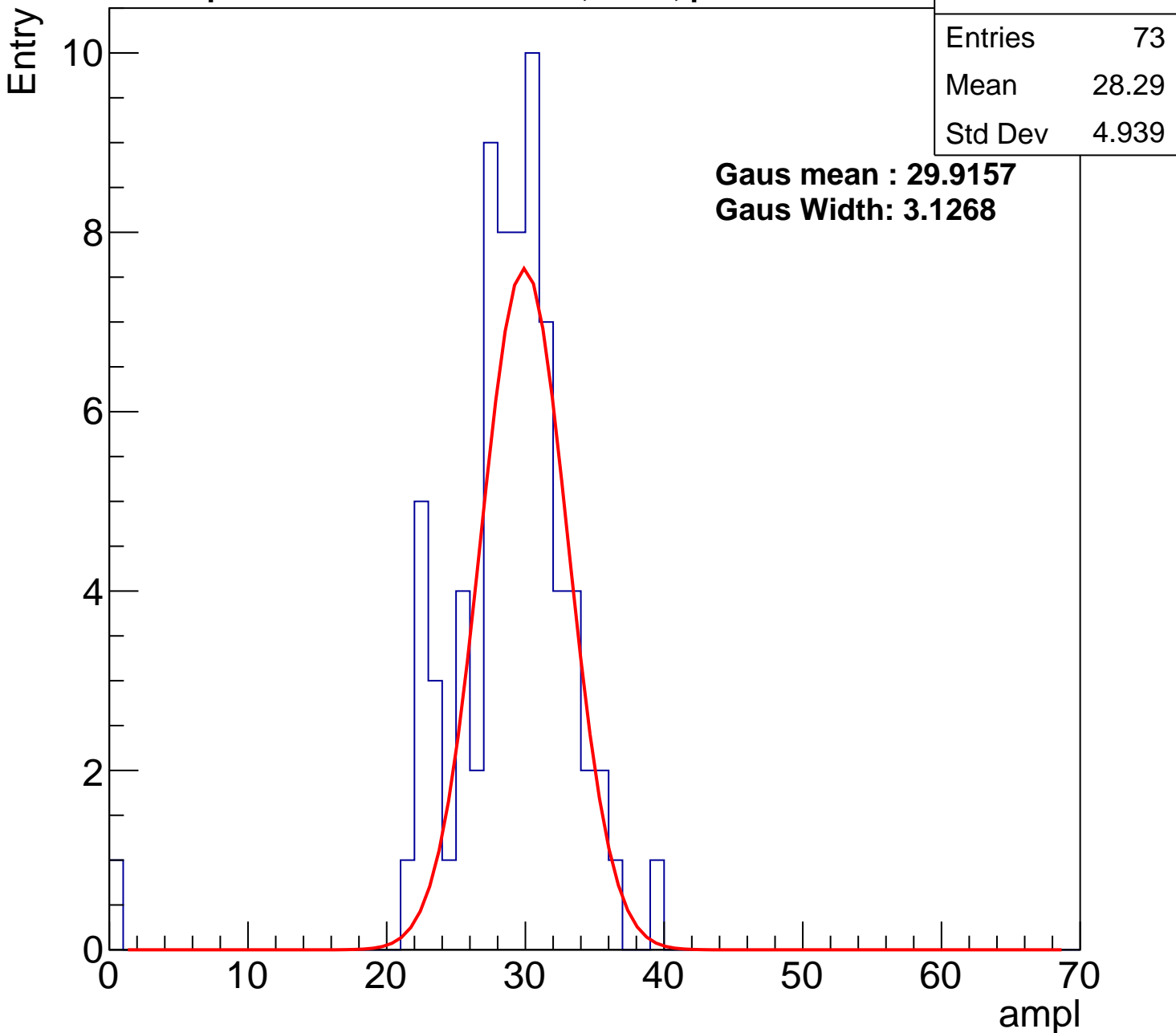
Gaus Width: 3.1268

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch28, adc1

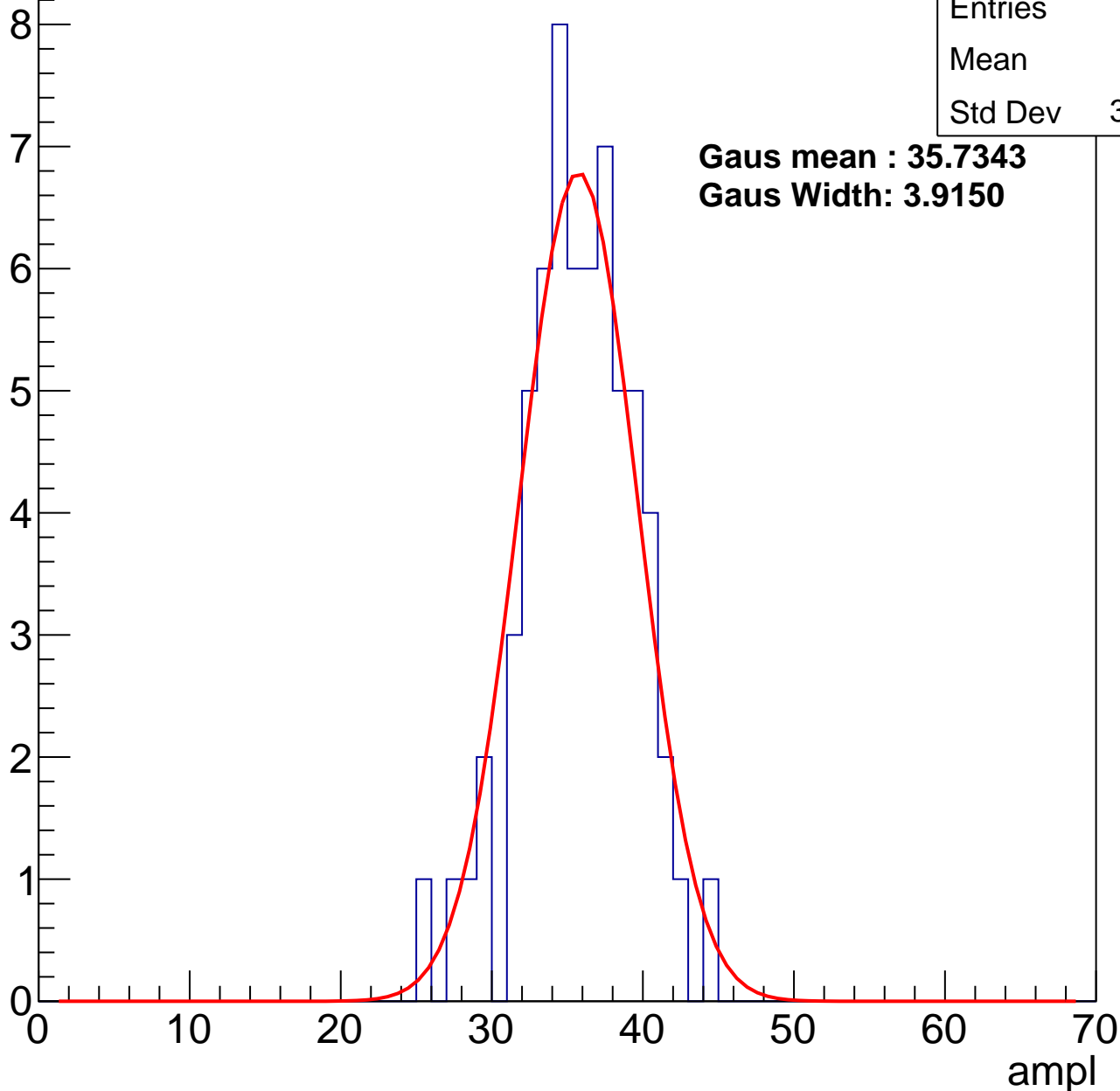
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.3
Std Dev	3.677

Gaus mean : 35.7343

Gaus Width: 3.9150



B1L103S, U24-ch28, adc2

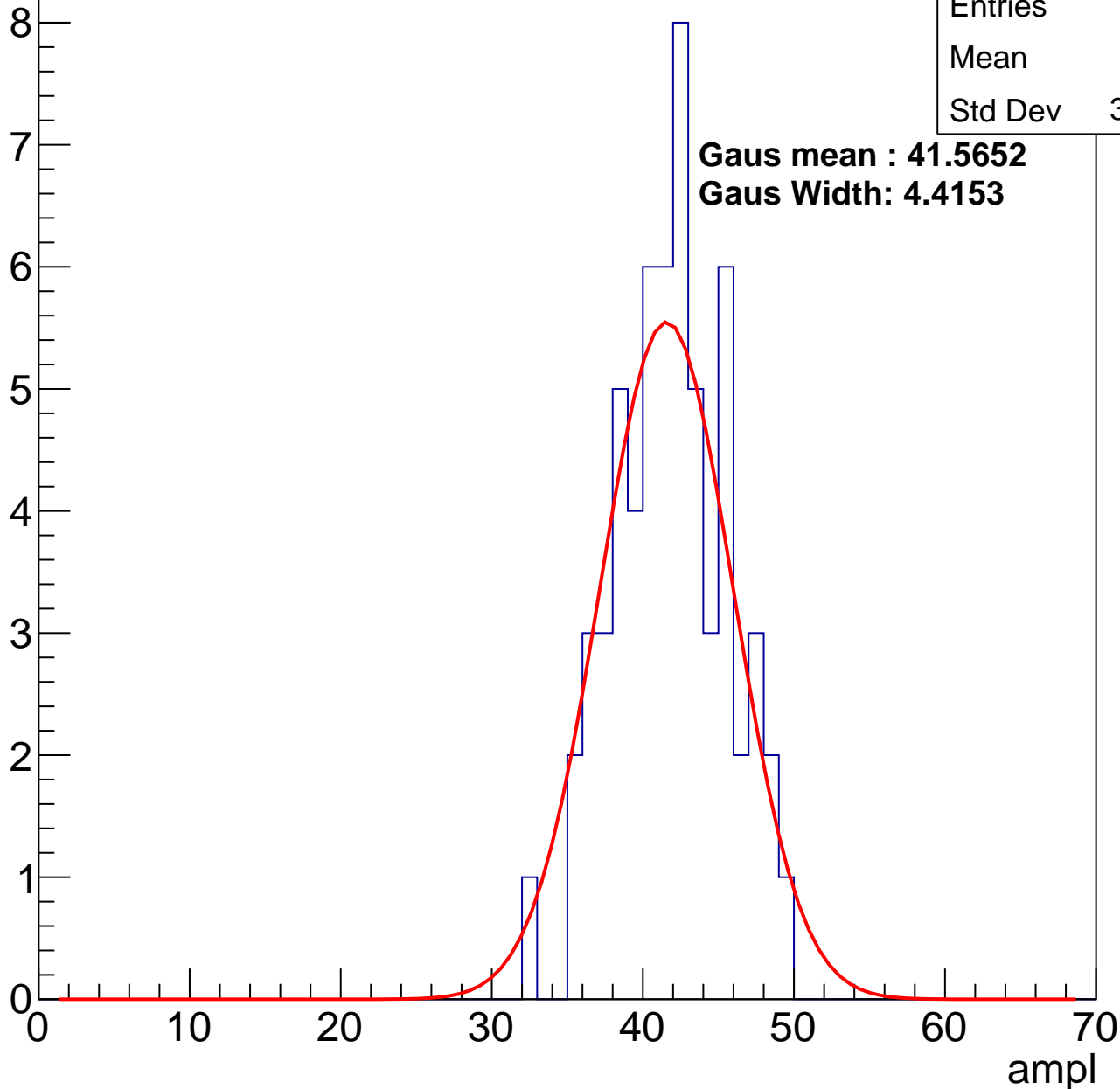
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	41.4
Std Dev	3.662

Gaus mean : 41.5652

Gaus Width: 4.4153

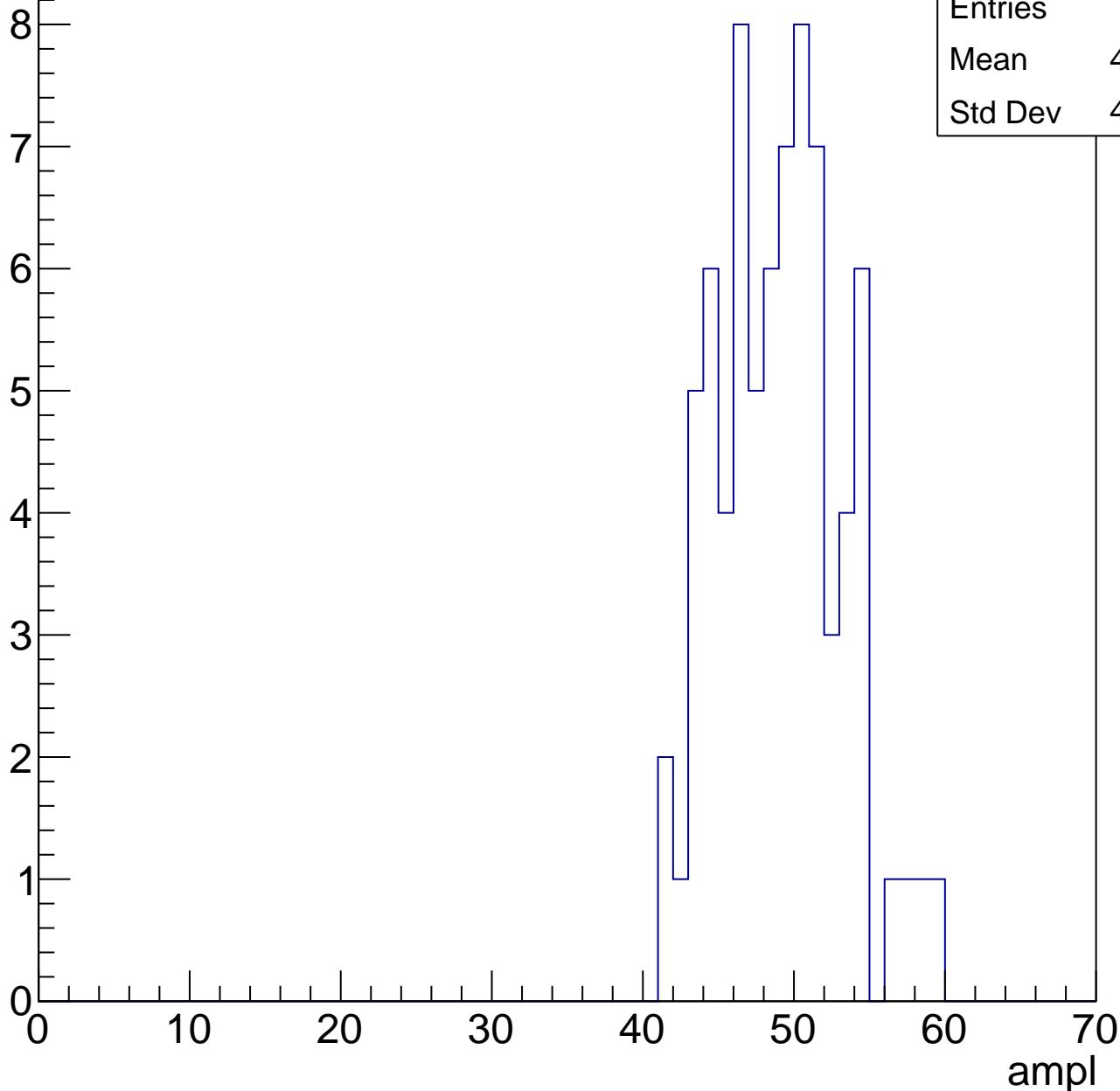


B1L103S, U24-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	48.63
Std Dev	4.022

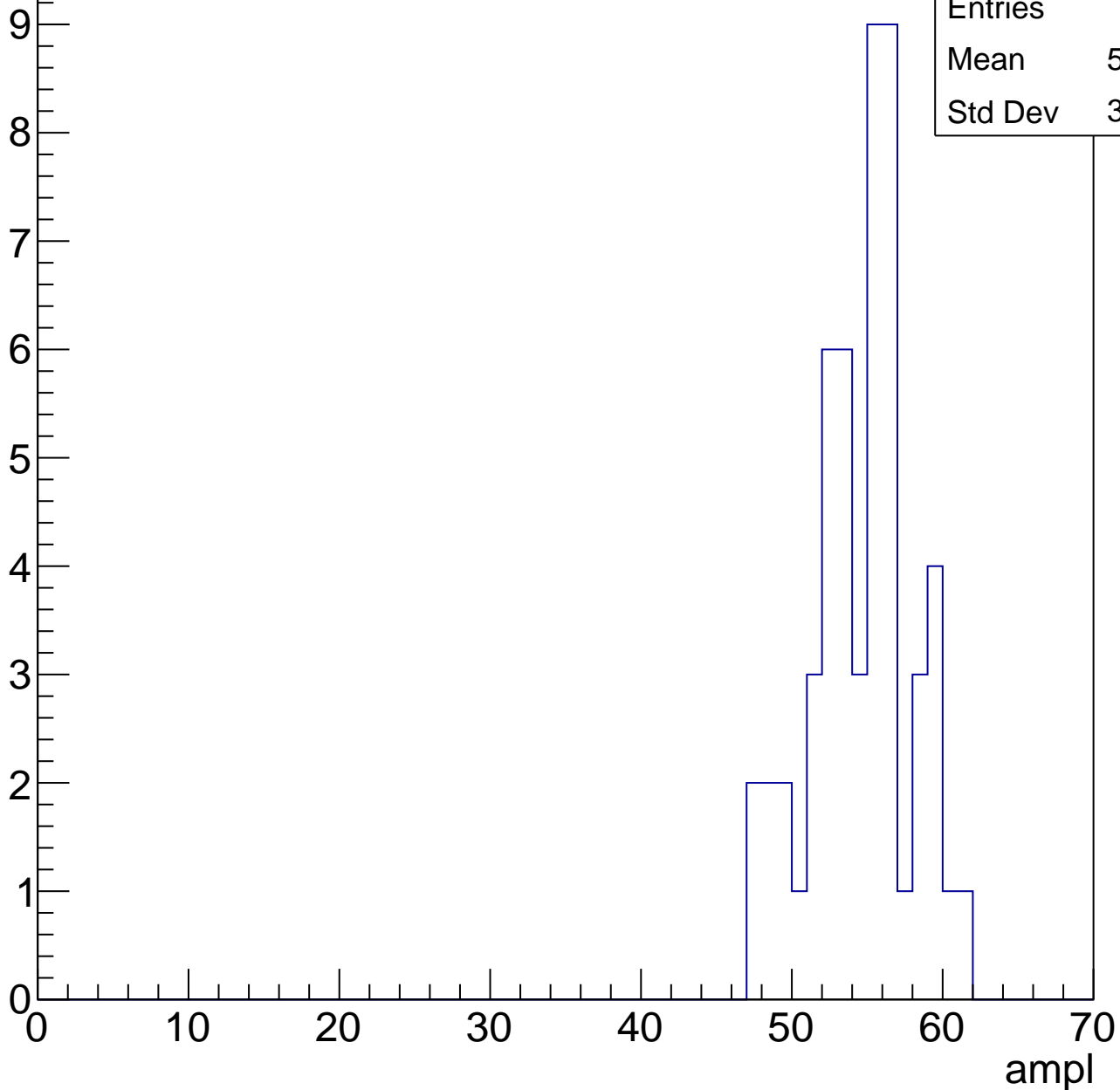


B1L103S, U24-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

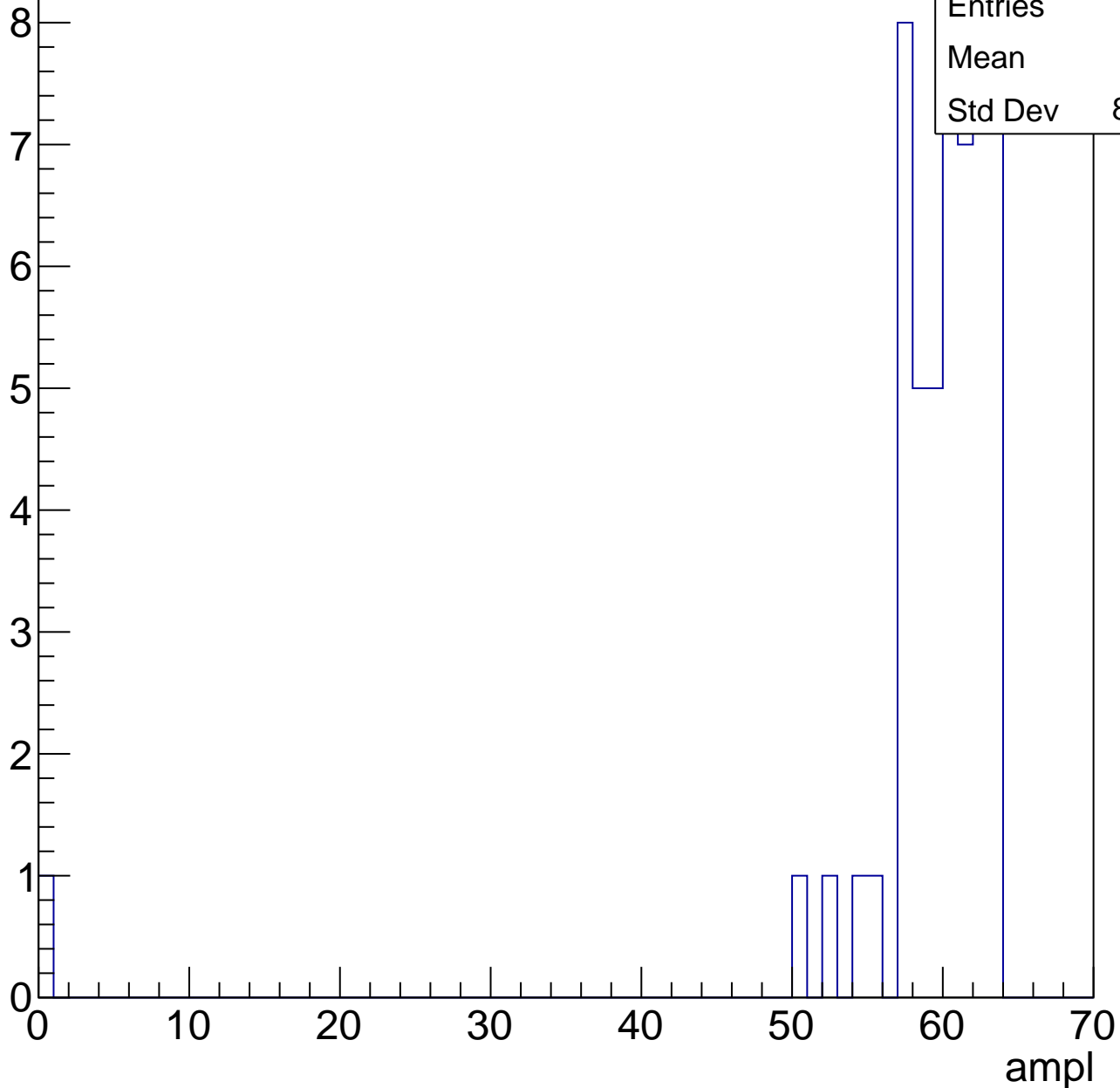
Entries	53
Mean	54.15
Std Dev	3.305



B1L103S, U24-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

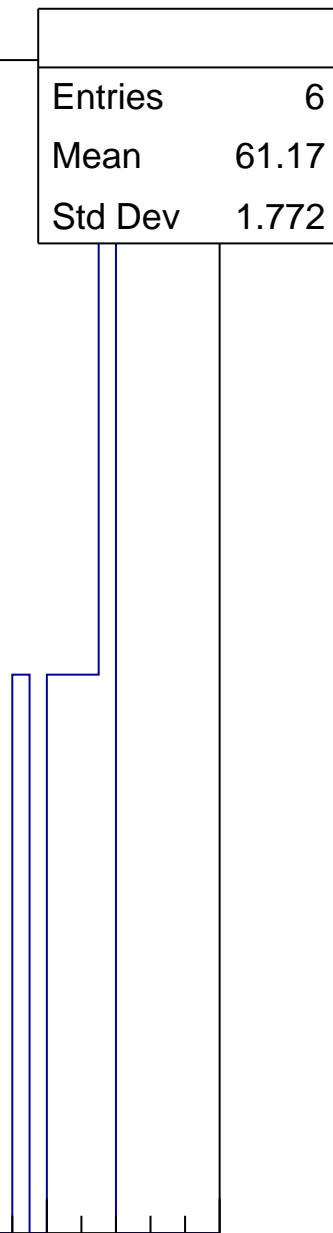
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.772

ampl

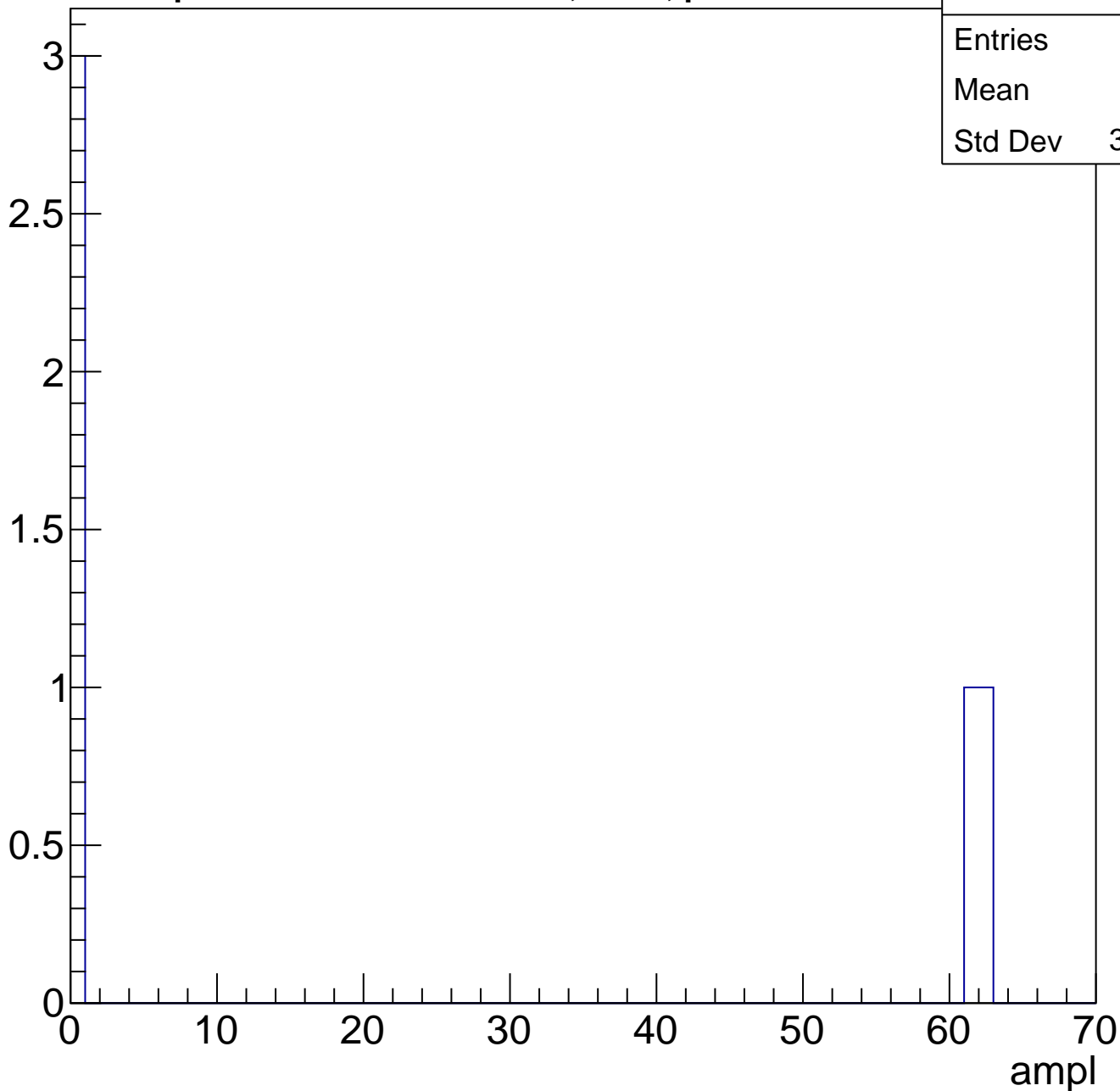
0 10 20 30 40 50 60 70



B1L103S, U24-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch29, adc0

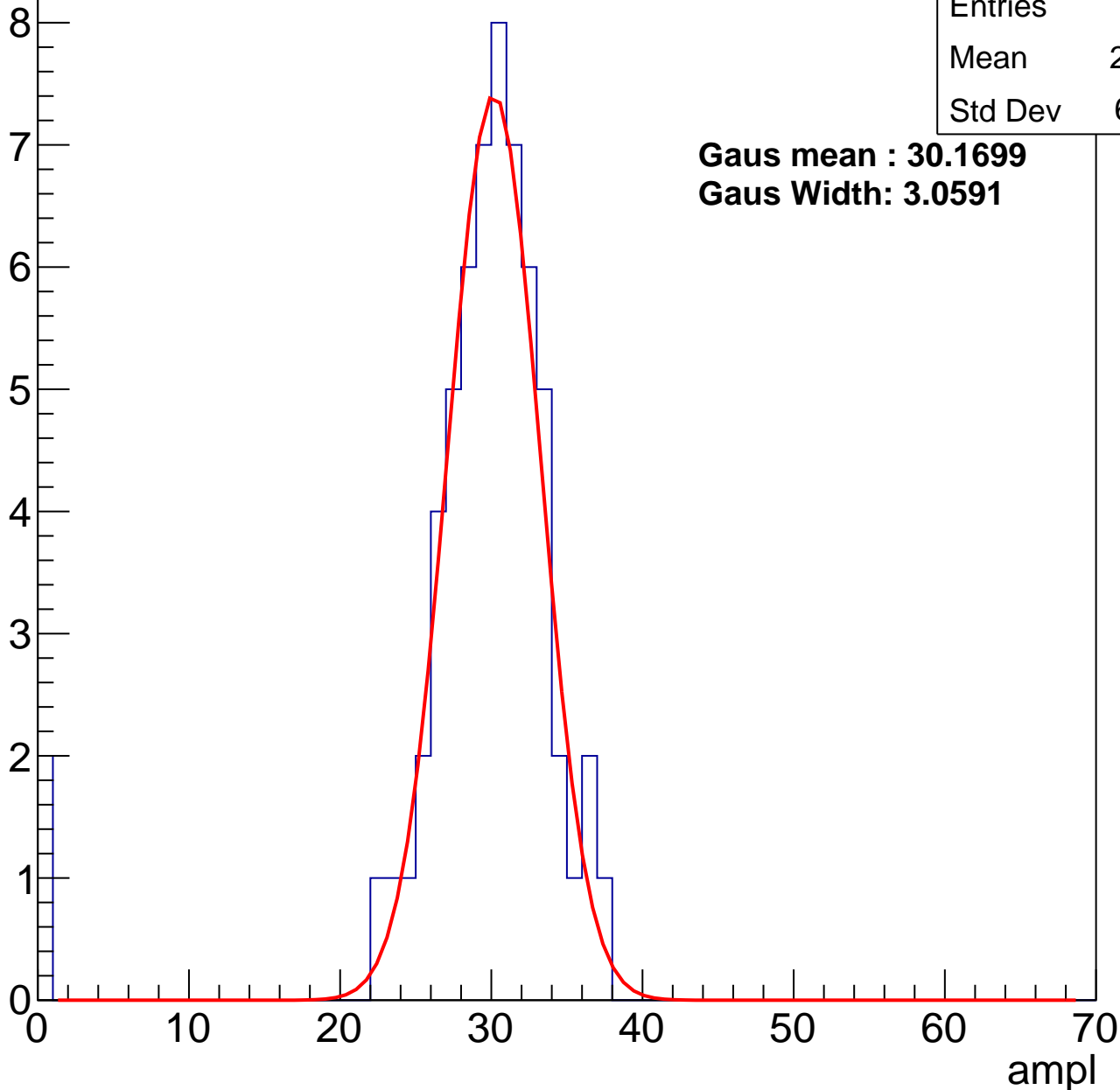
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.77
Std Dev	6.141

Gaus mean : 30.1699

Gaus Width: 3.0591



B1L103S, U24-ch29, adc1

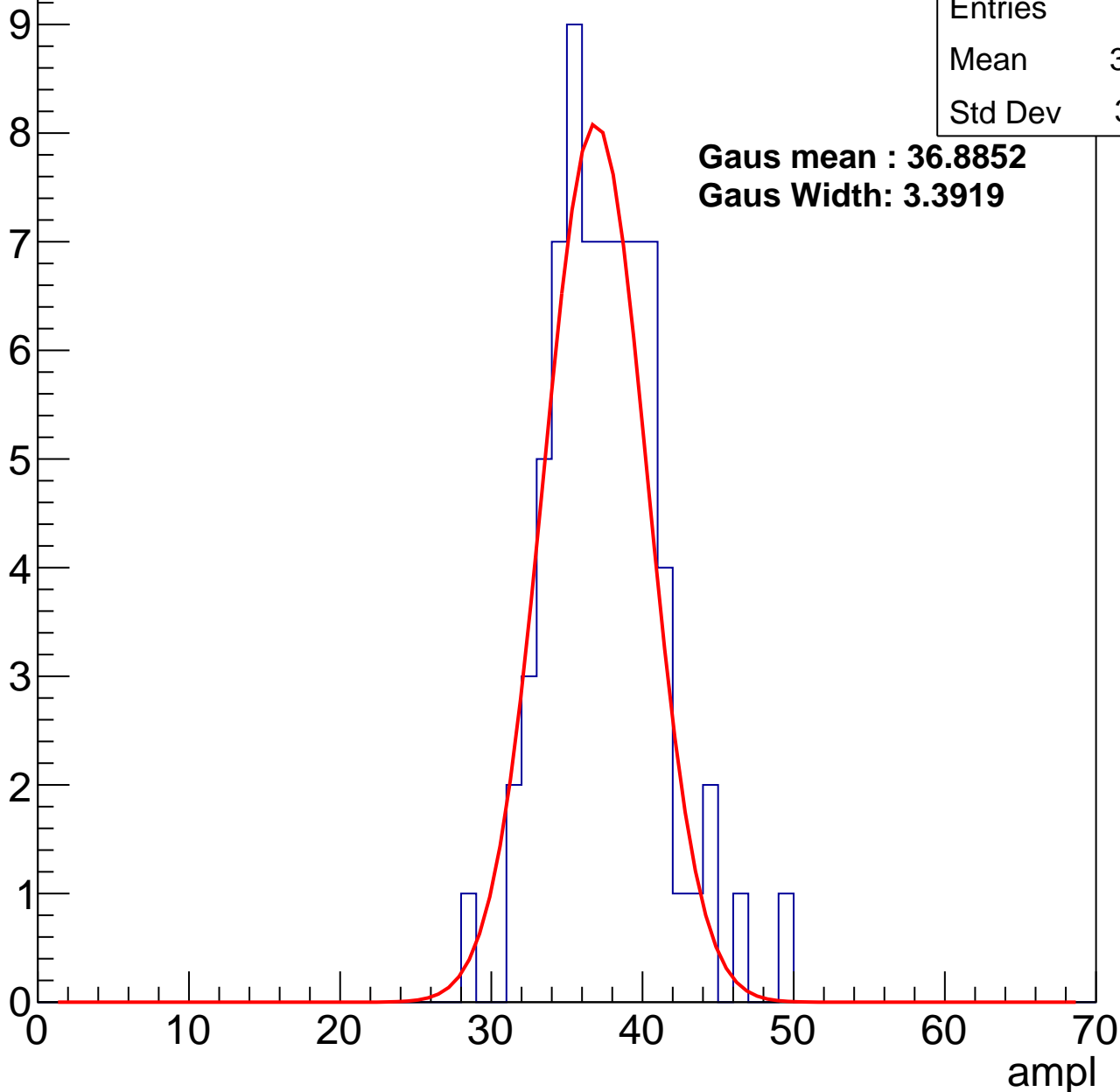
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	37.03
Std Dev	3.651

Gaus mean : 36.8852

Gaus Width: 3.3919



B1L103S, U24-ch29, adc2

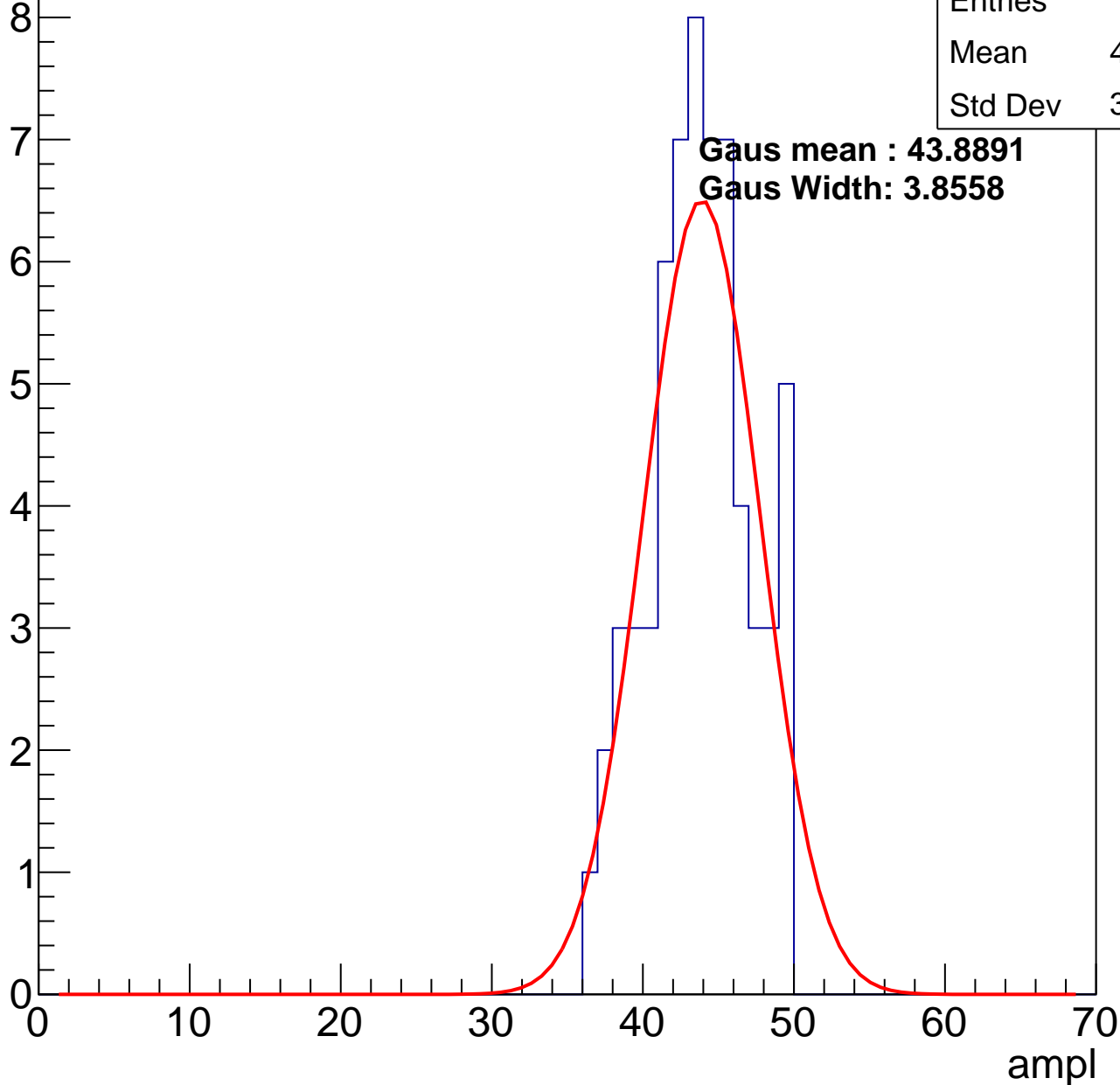
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.26
Std Dev	3.292

Gaus mean : 43.8891

Gaus Width: 3.8558

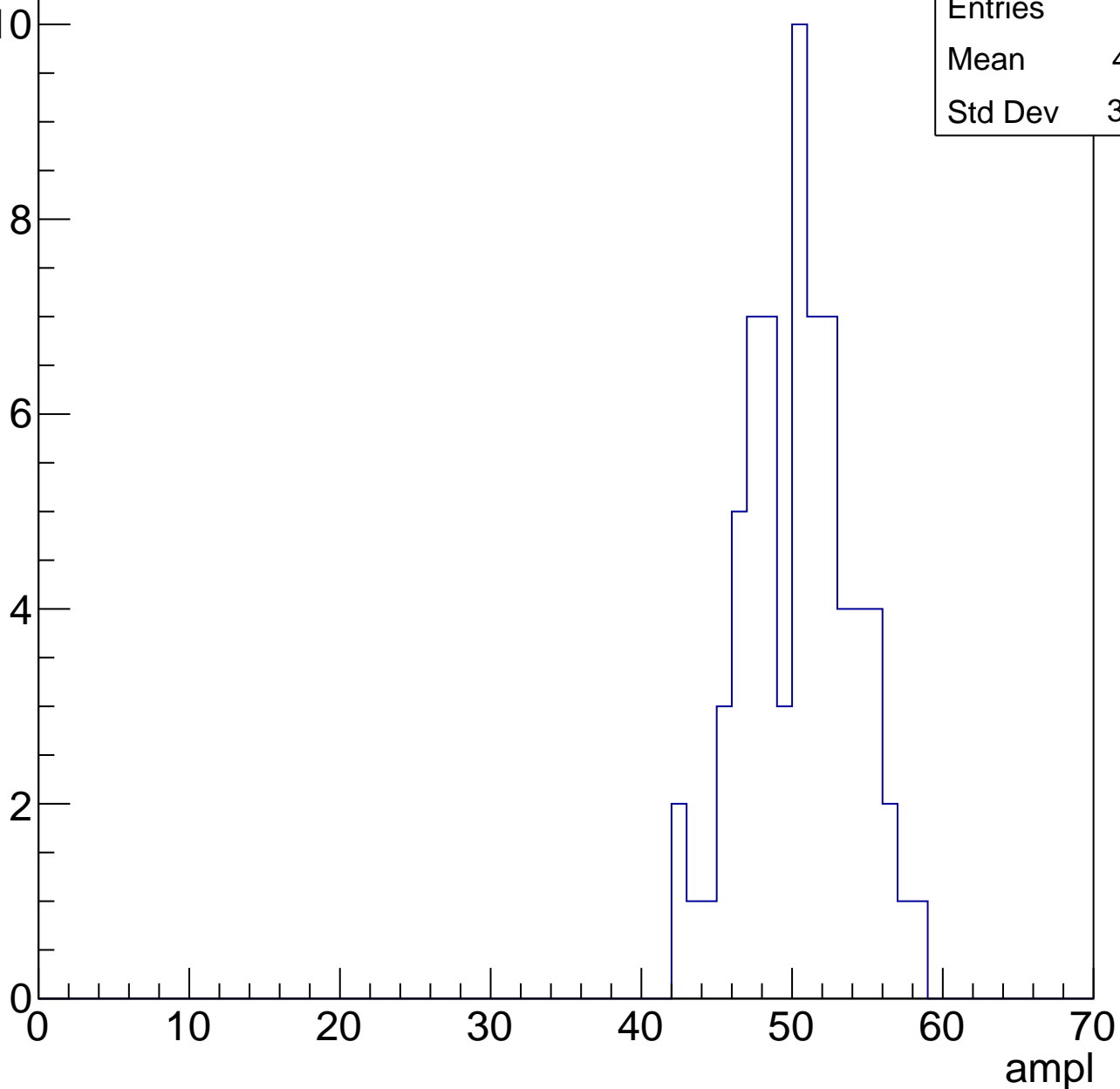


B1L103S, U24-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

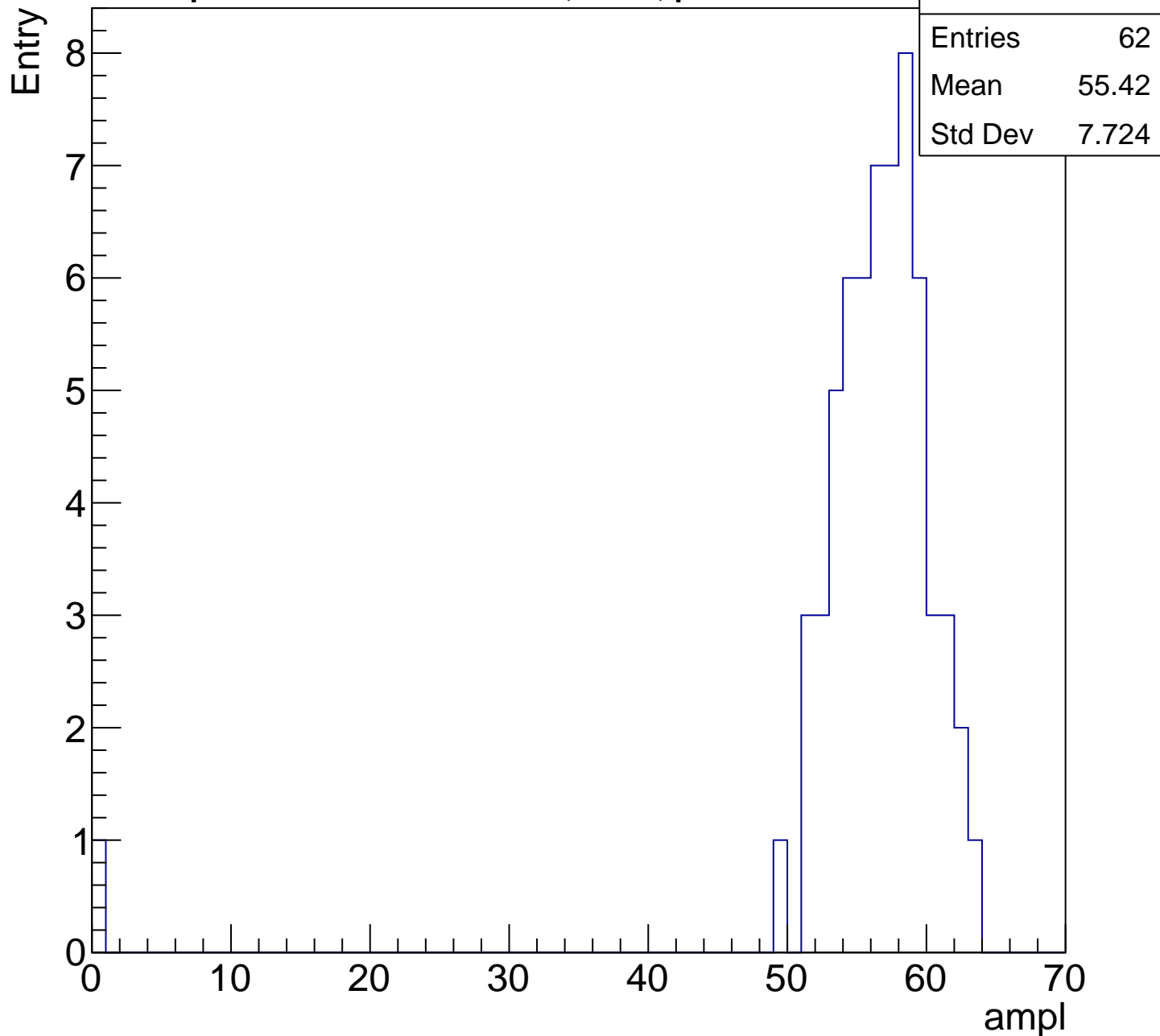
Entry

Entries	69
Mean	49.91
Std Dev	3.574



B1L103S, U24-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

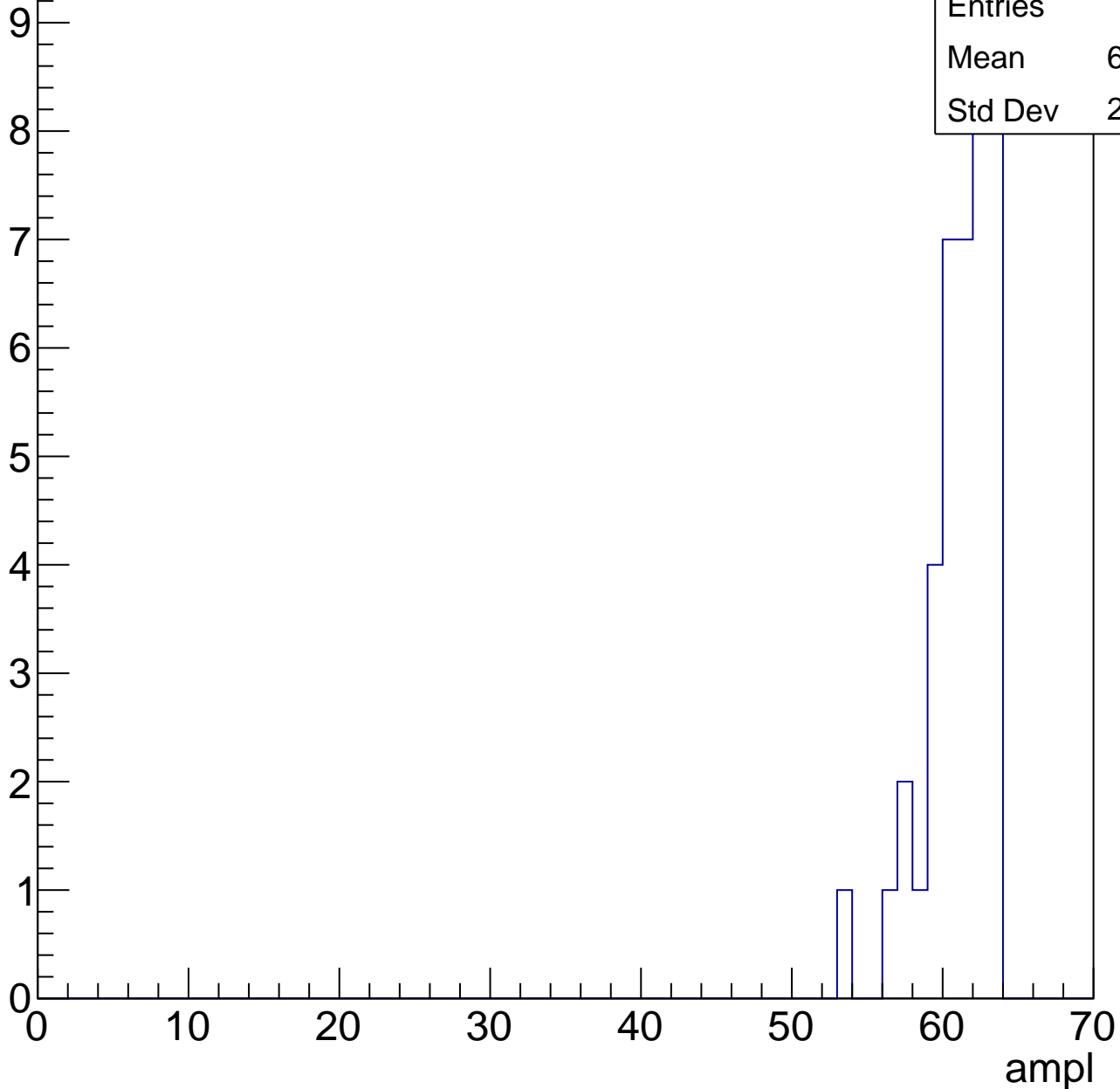


B1L103S, U24-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	60.67
Std Dev	2.195



B1L103S, U24-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U24-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U24-ch30, adc0

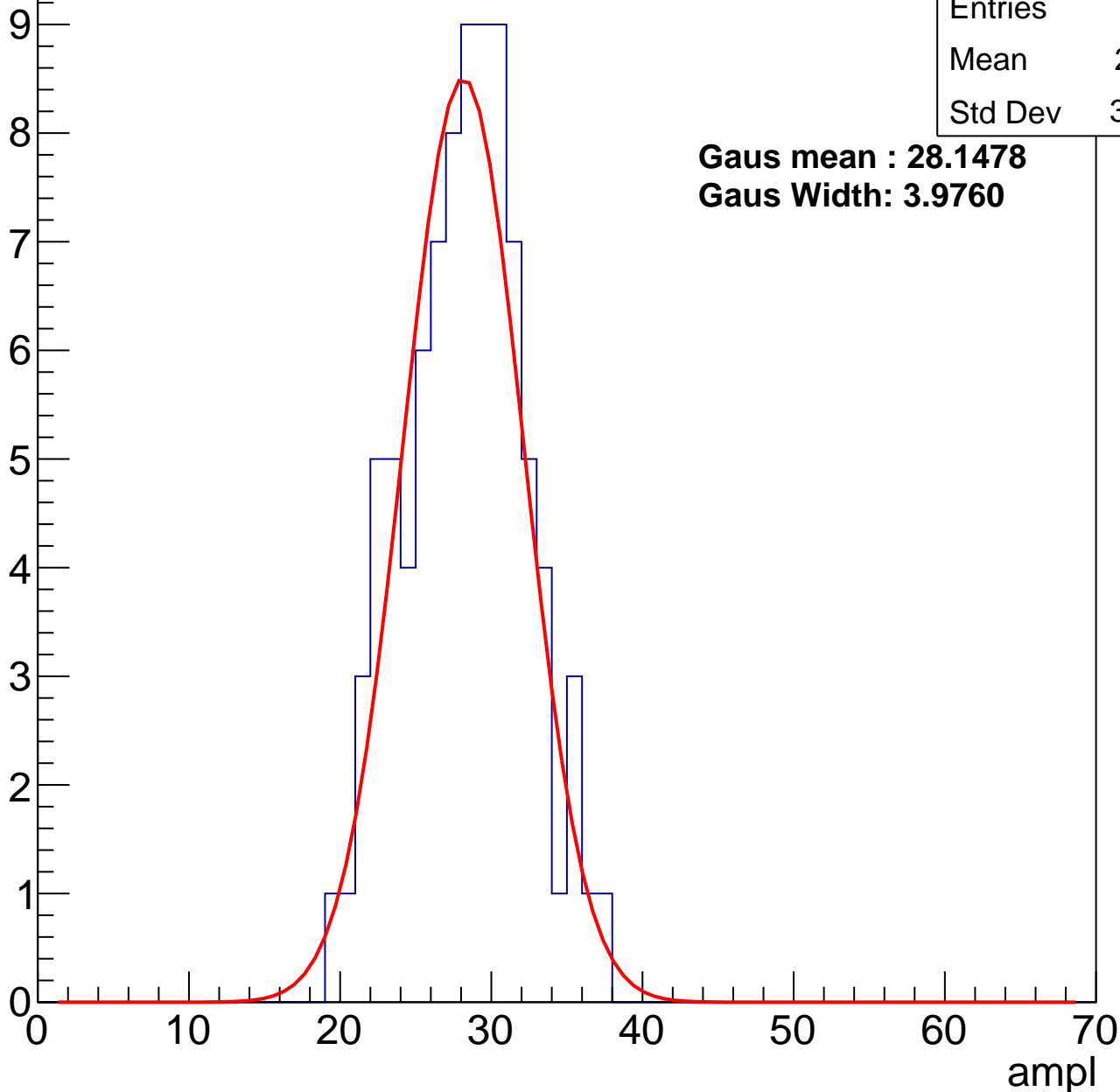
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	27.81
Std Dev	3.914

Gaus mean : 28.1478

Gaus Width: 3.9760



B1L103S, U24-ch30, adc1

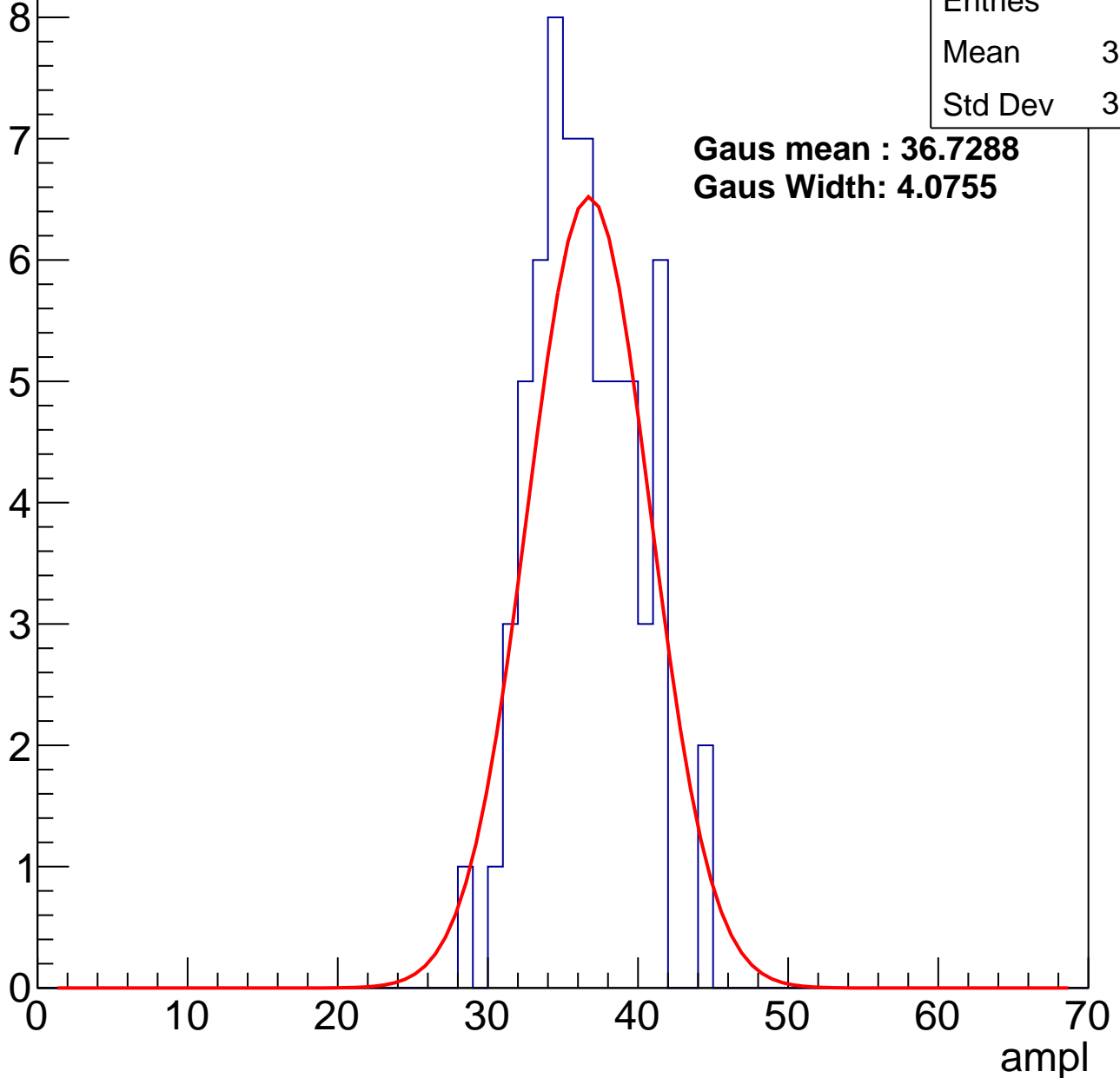
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.97
Std Dev	3.409

Gaus mean : 36.7288

Gaus Width: 4.0755



B1L103S, U24-ch30, adc2

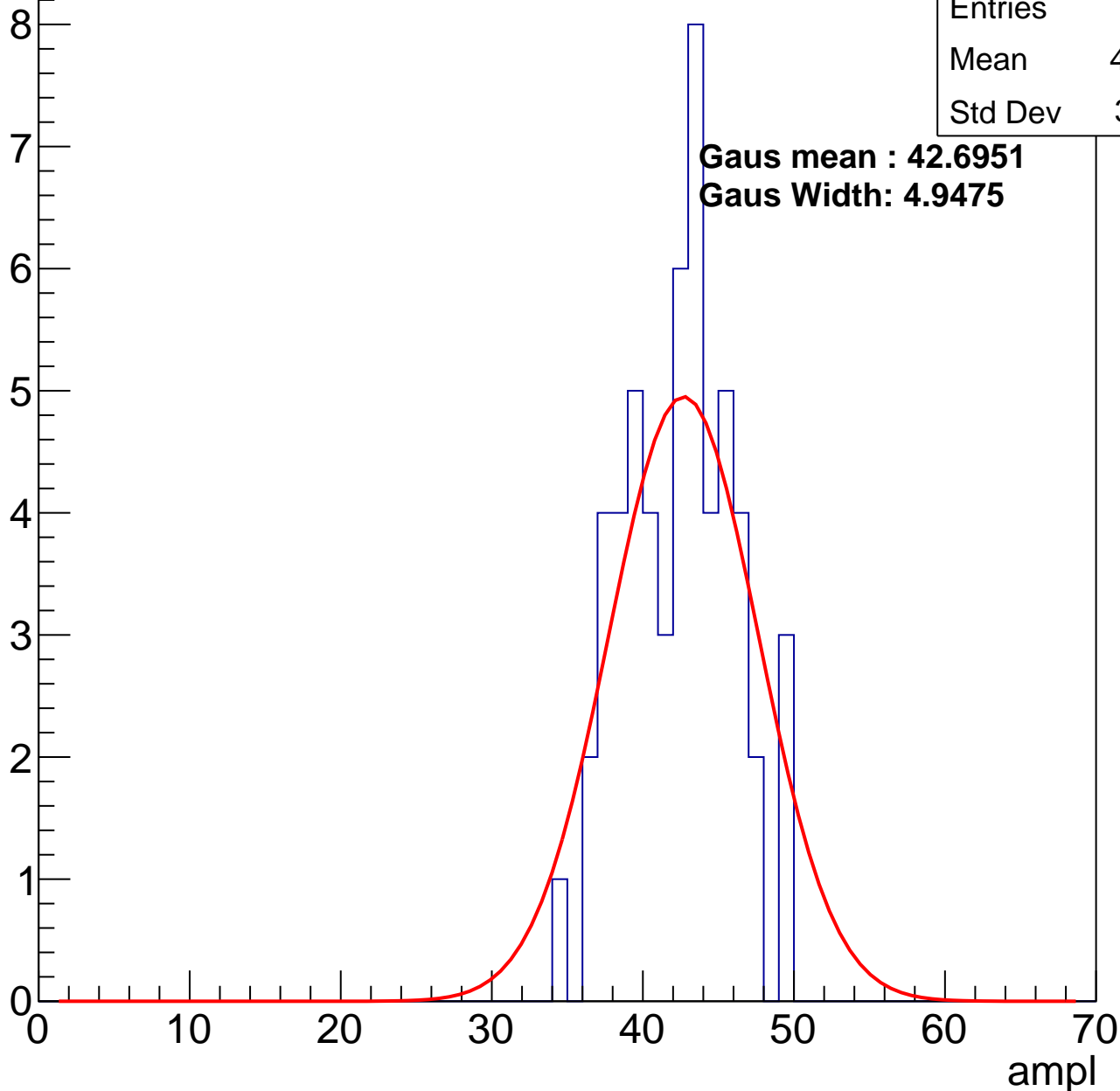
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.93
Std Dev	3.551

Gaus mean : 42.6951

Gaus Width: 4.9475

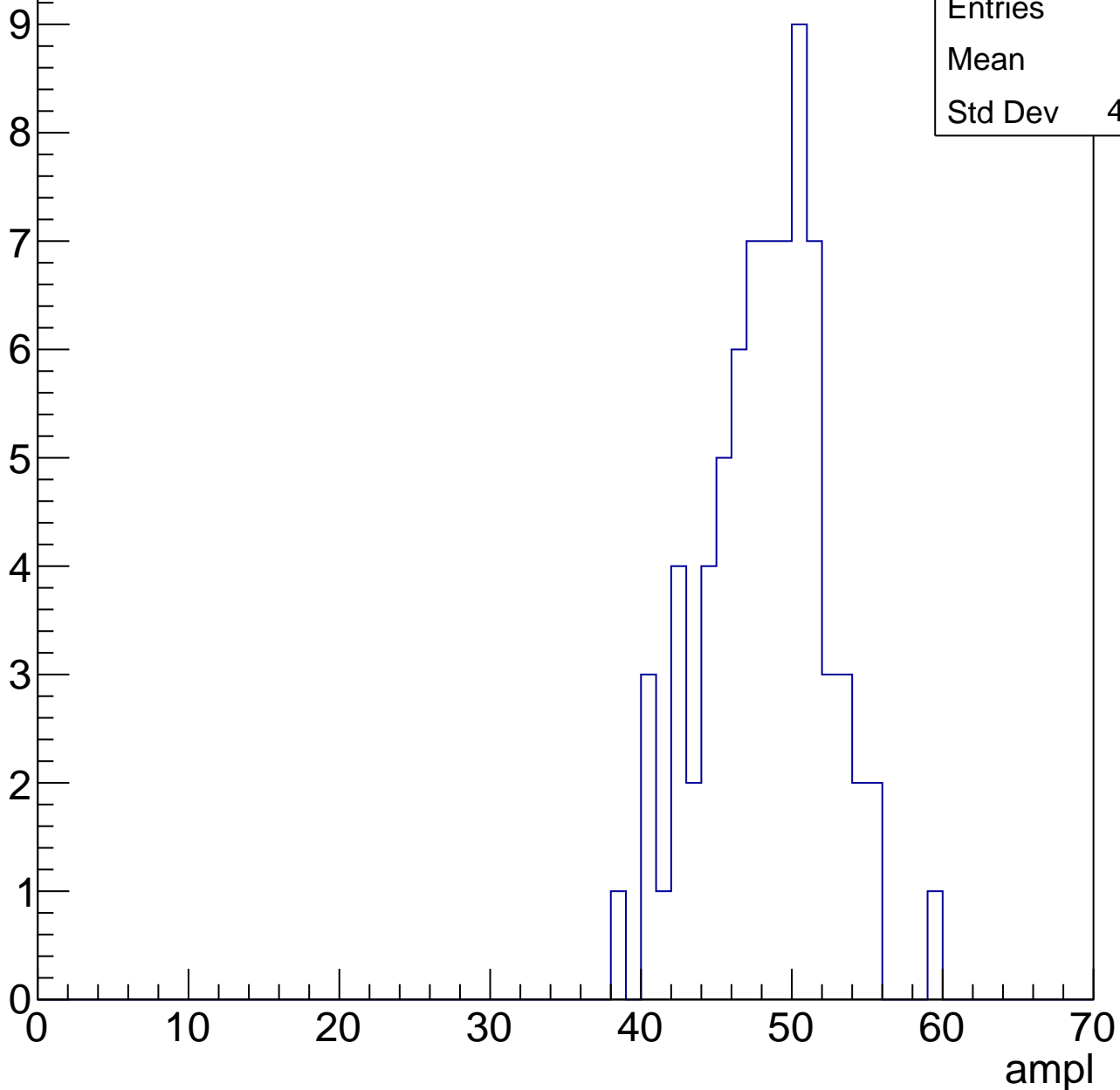


B1L103S, U24-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	47.8
Std Dev	4.013

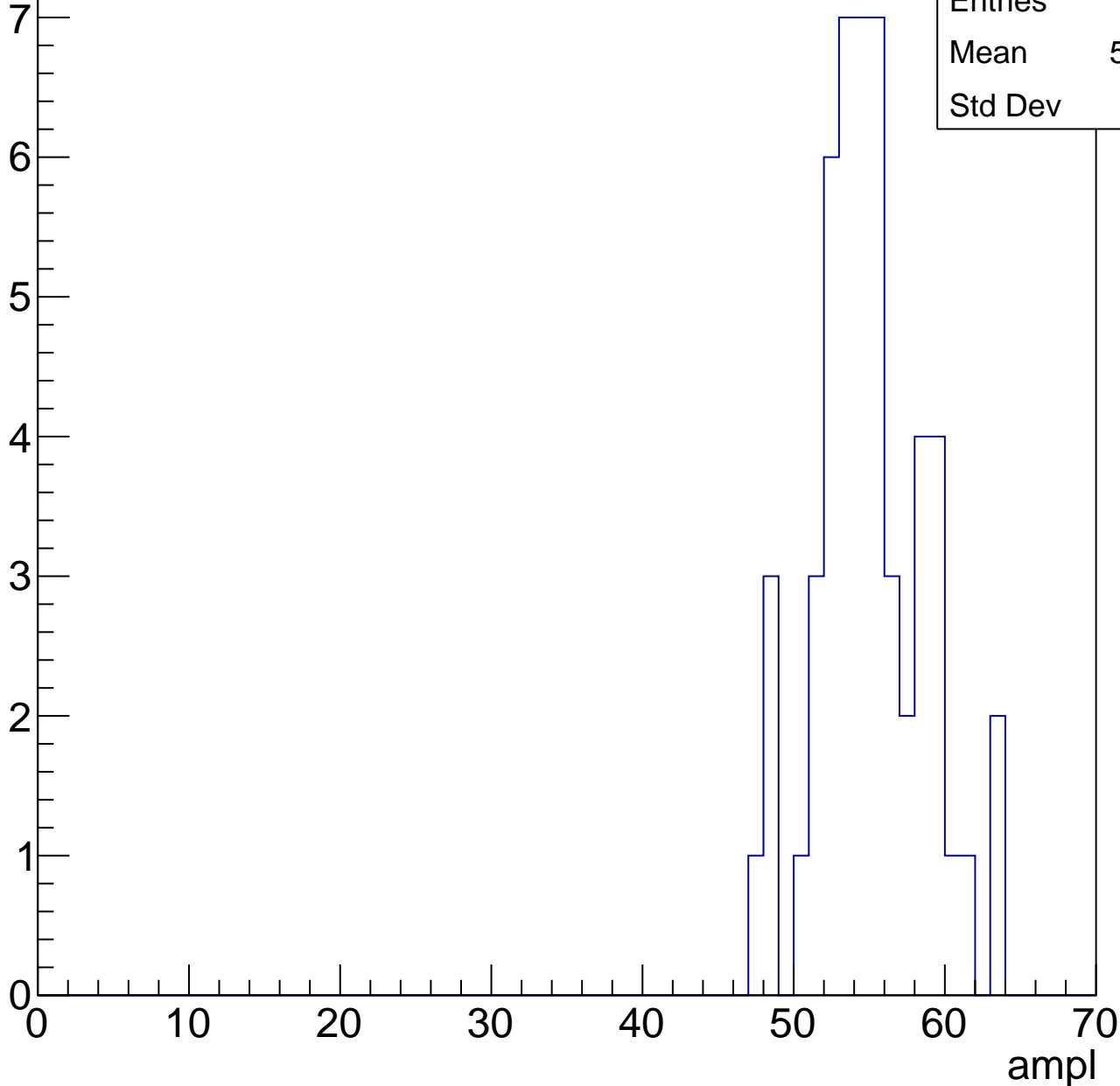


B1L103S, U24-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.56
Std Dev	3.57

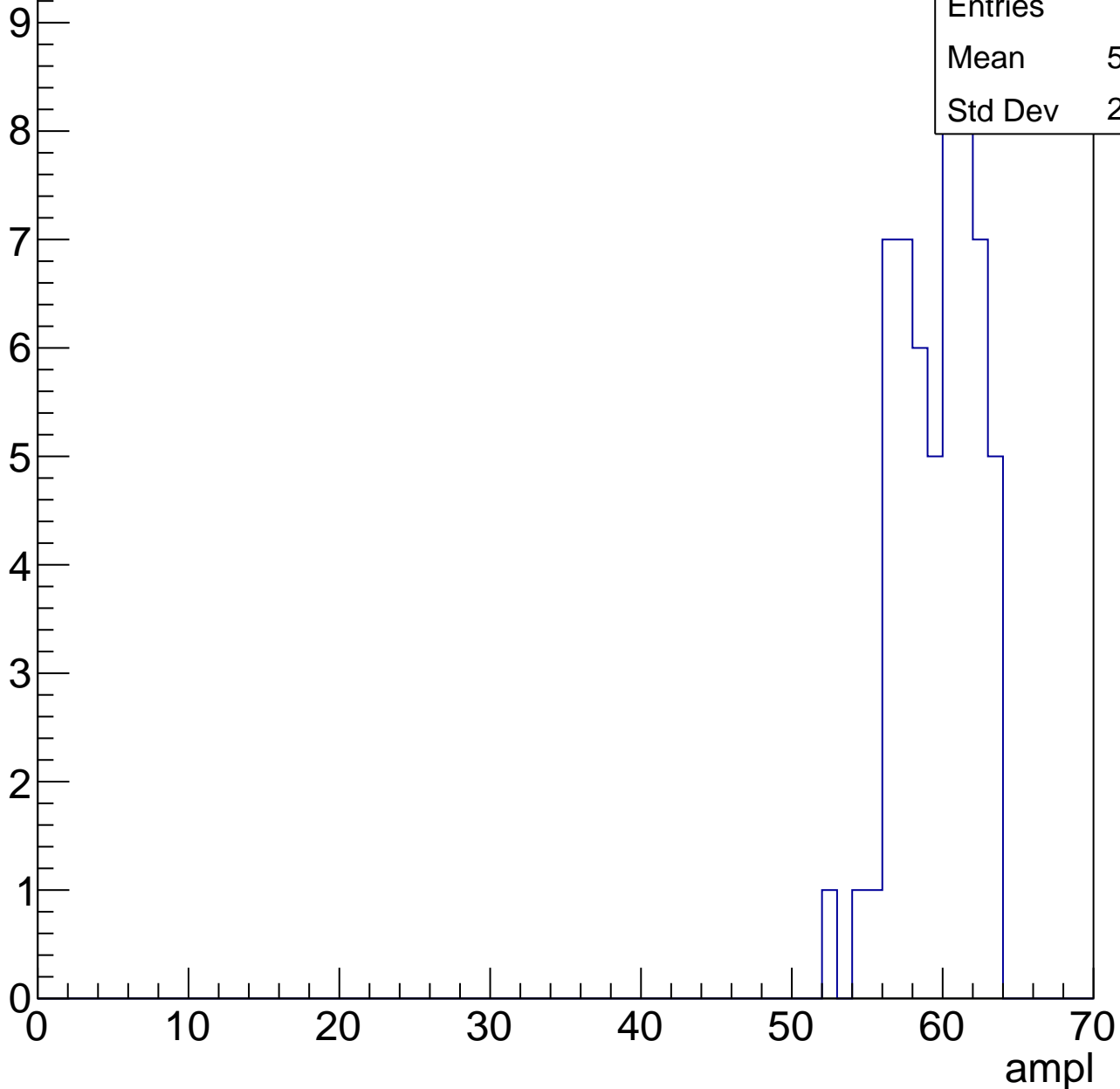


B1L103S, U24-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

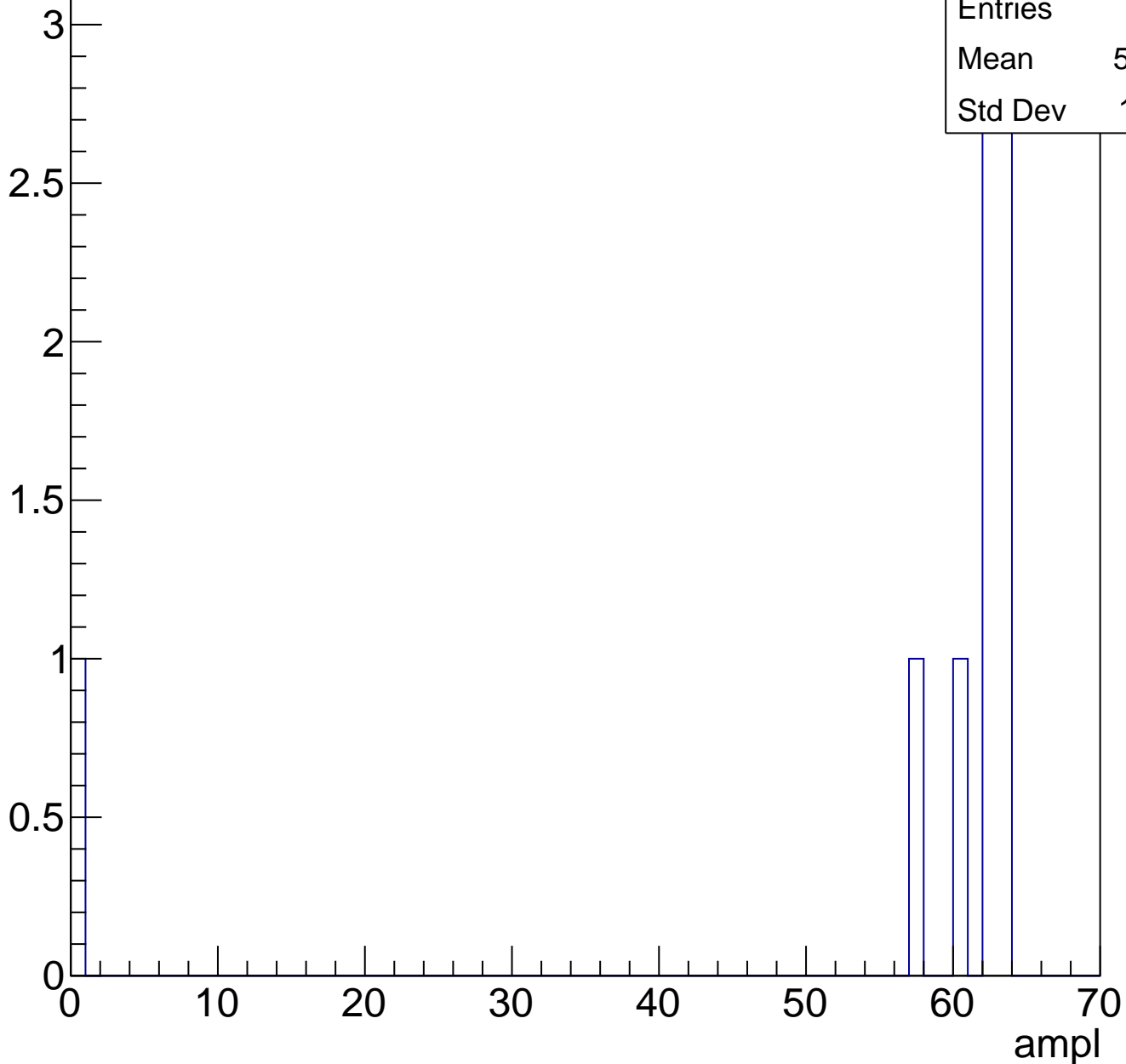
Entries	57
Mean	59.18
Std Dev	2.556



B1L103S, U24-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	17
Std Dev	0

ampl

B1L103S, U24-ch31, adc0

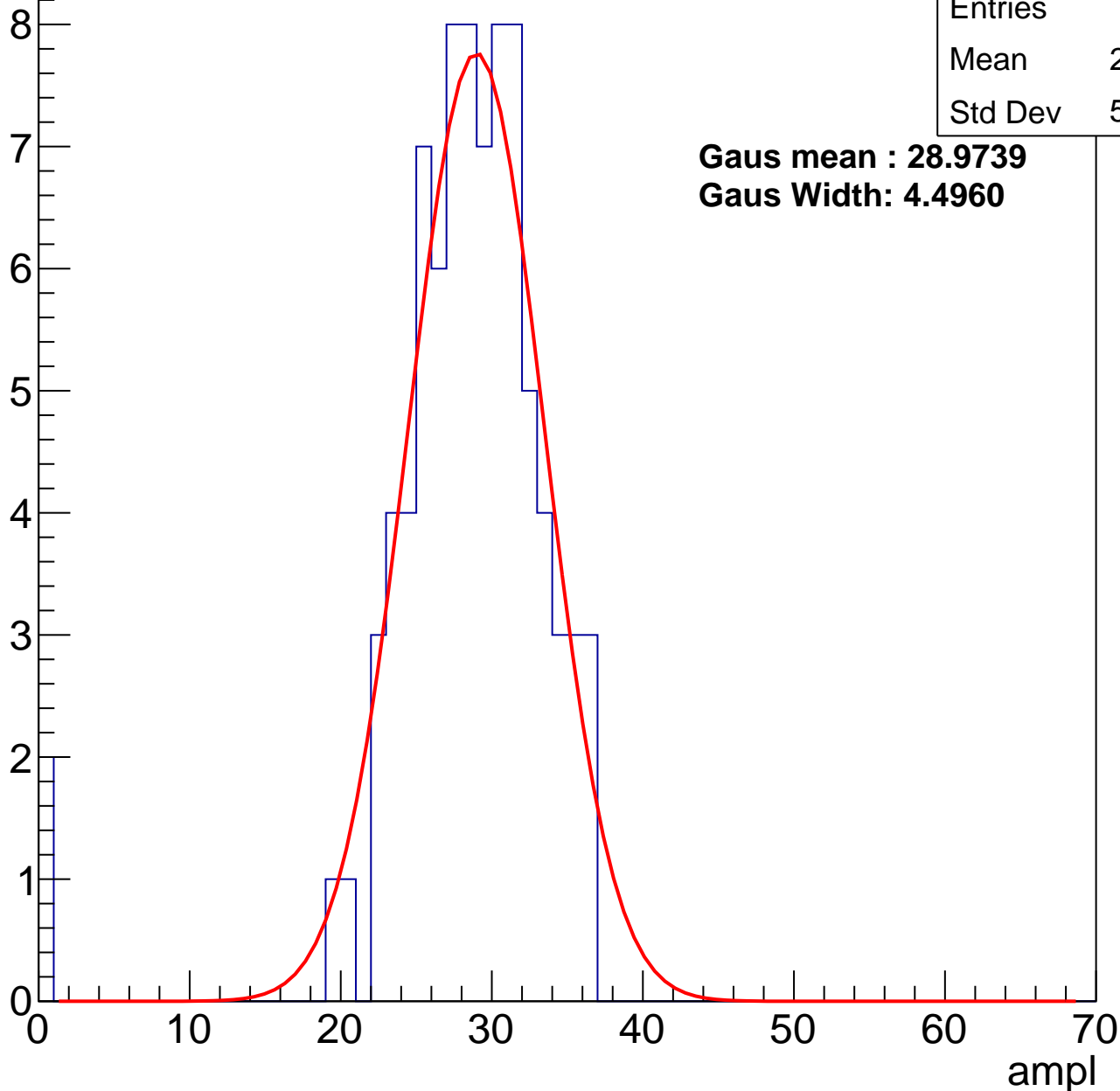
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	27.79
Std Dev	5.754

Gaus mean : 28.9739

Gaus Width: 4.4960



B1L103S, U24-ch31, adc1

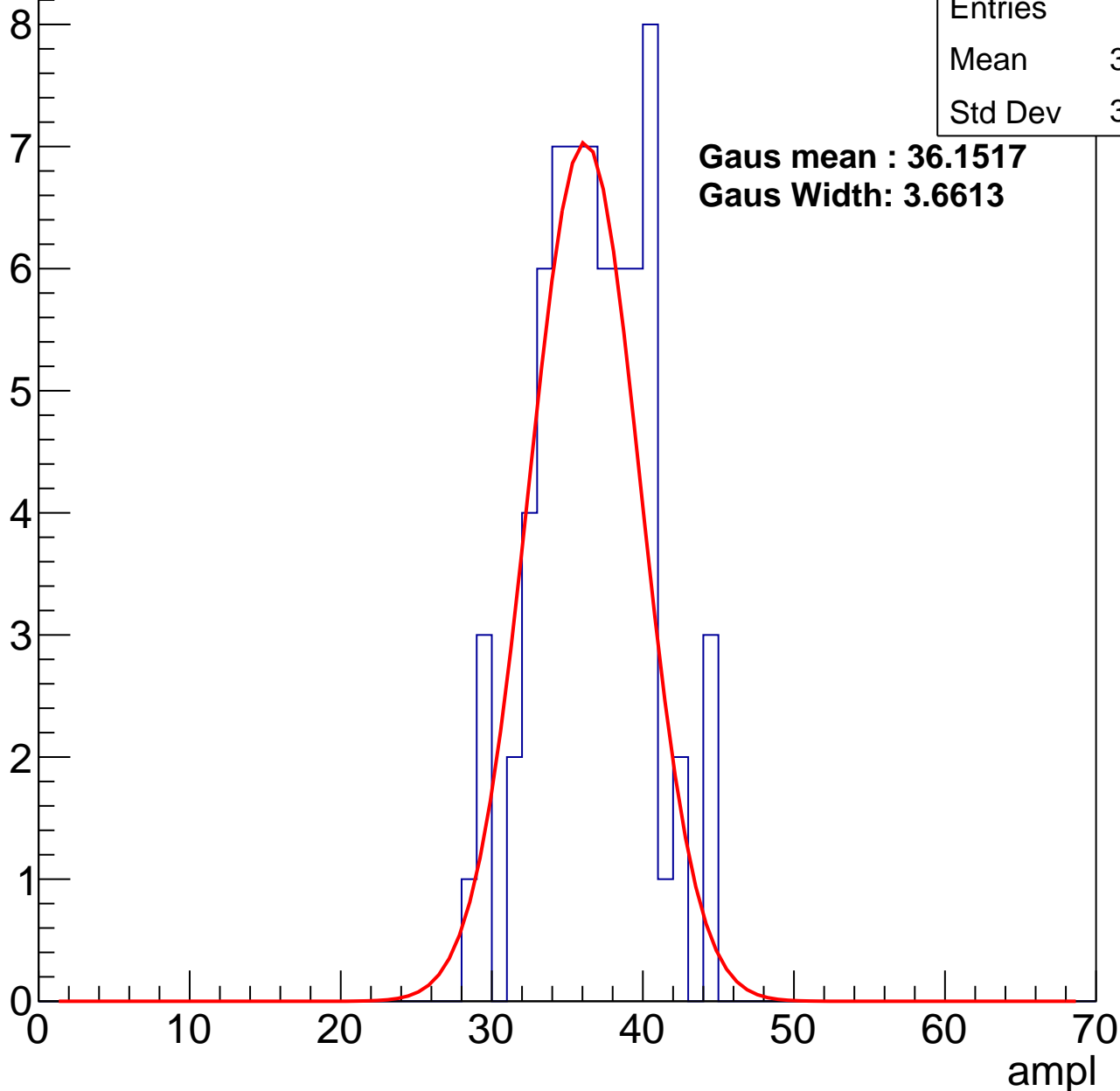
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.22
Std Dev	3.635

Gaus mean : 36.1517

Gaus Width: 3.6613



B1L103S, U24-ch31, adc2

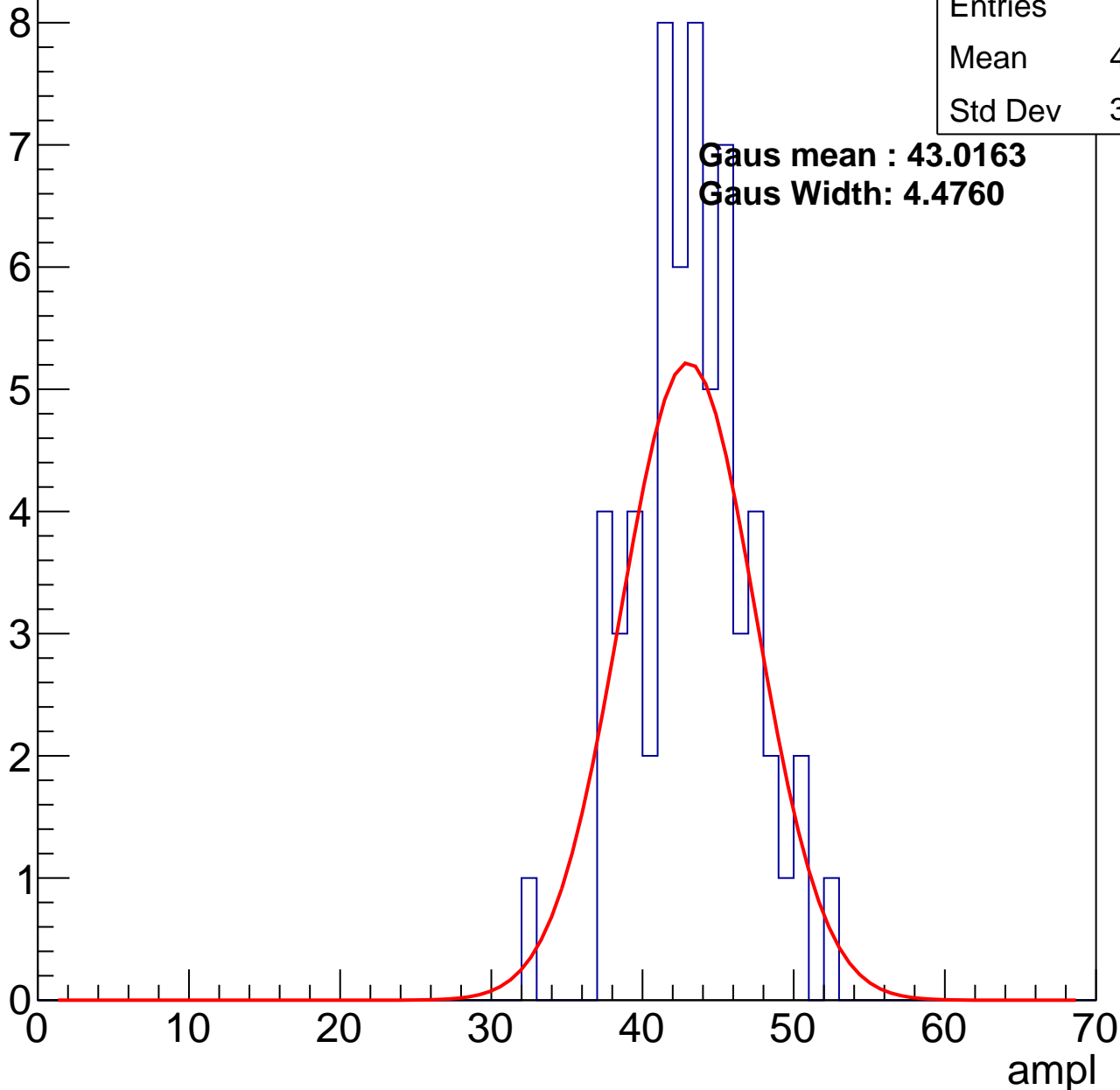
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.82
Std Dev	3.726

Gaus mean : 43.0163

Gaus Width: 4.4760

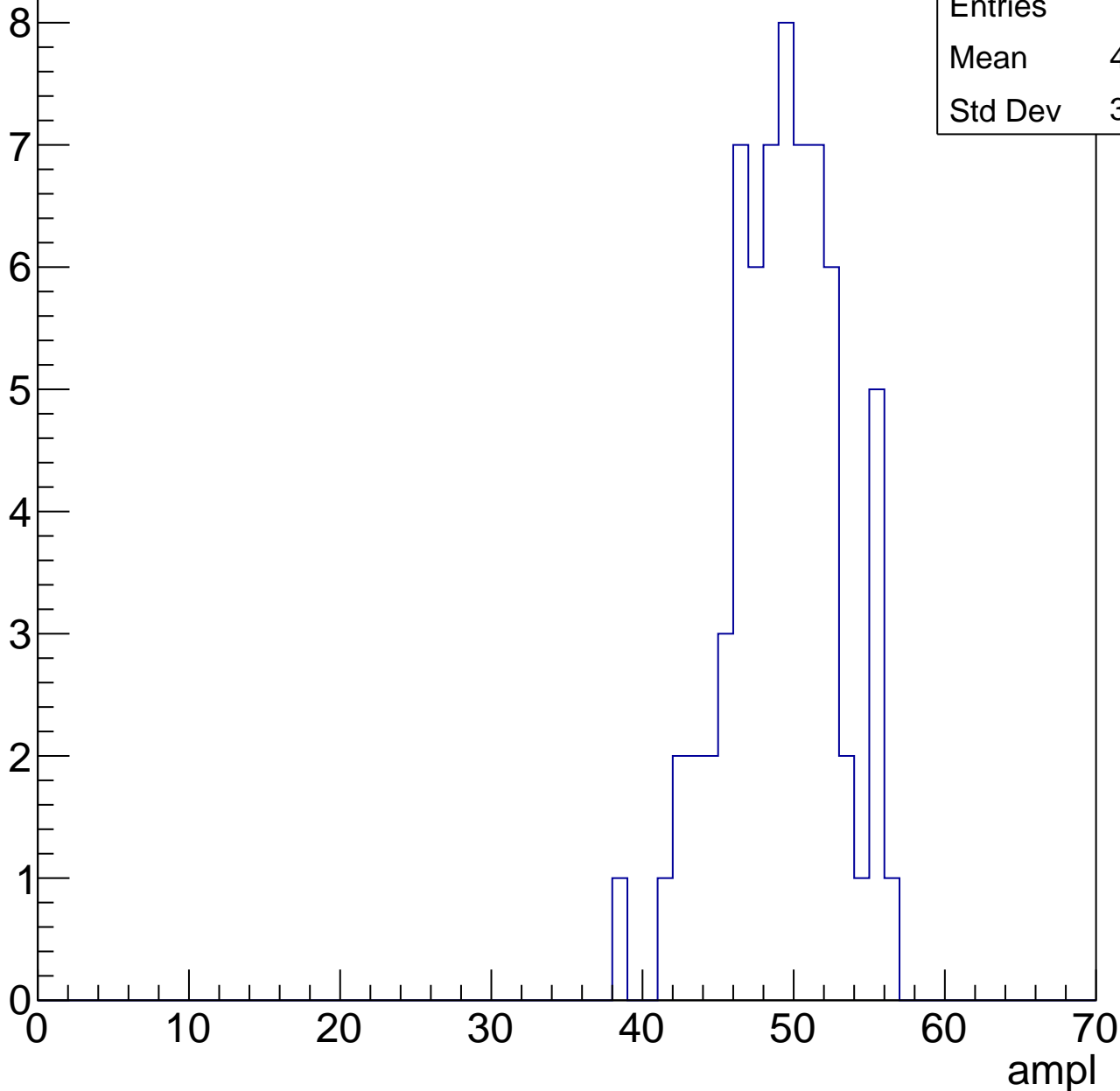


B1L103S, U24-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.74
Std Dev	3.673

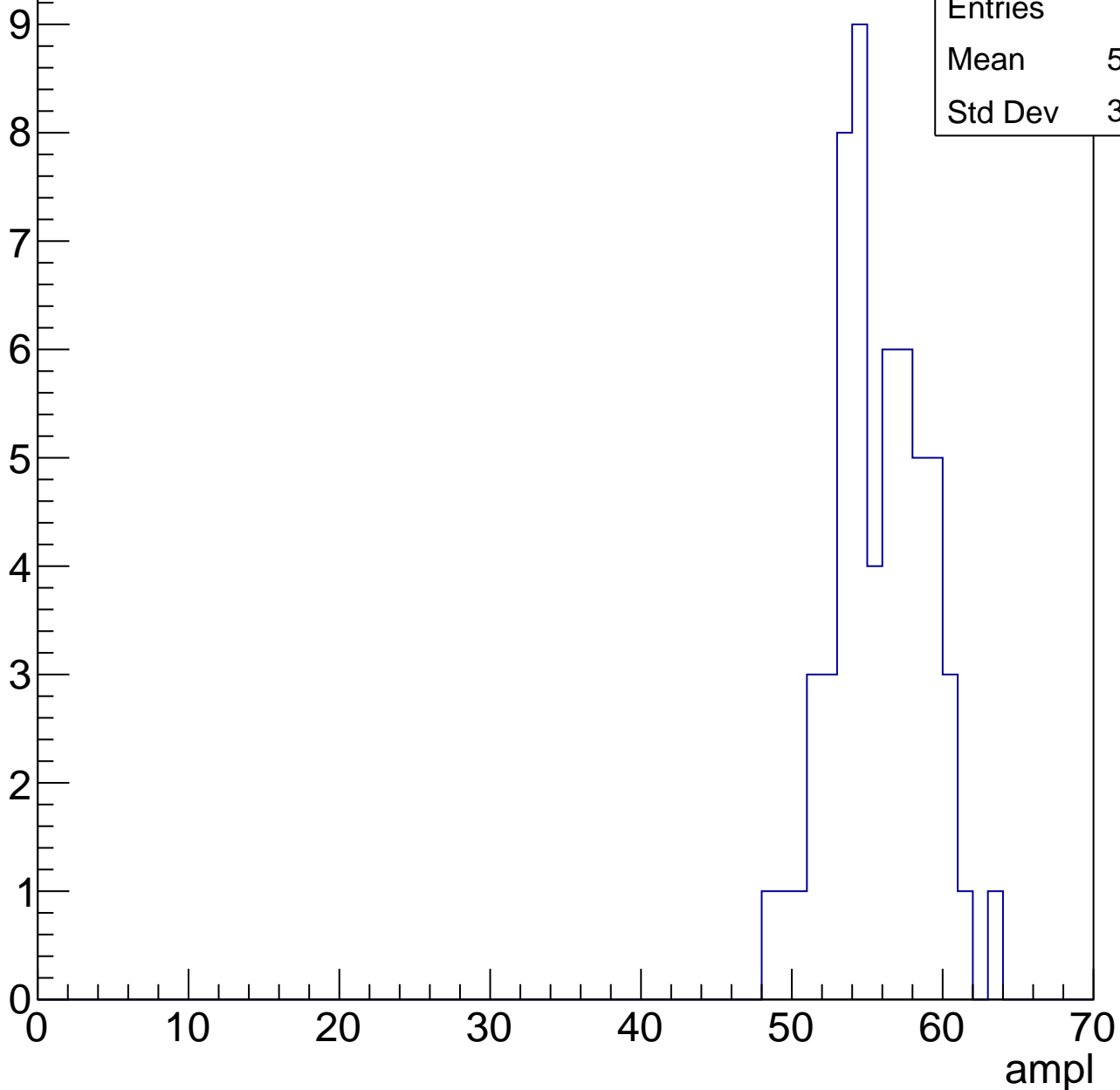


B1L103S, U24-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.32
Std Dev	3.107



B1L103S, U24-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	43
Mean	59.84
Std Dev	2.251

0

10

20

30

40

50

60

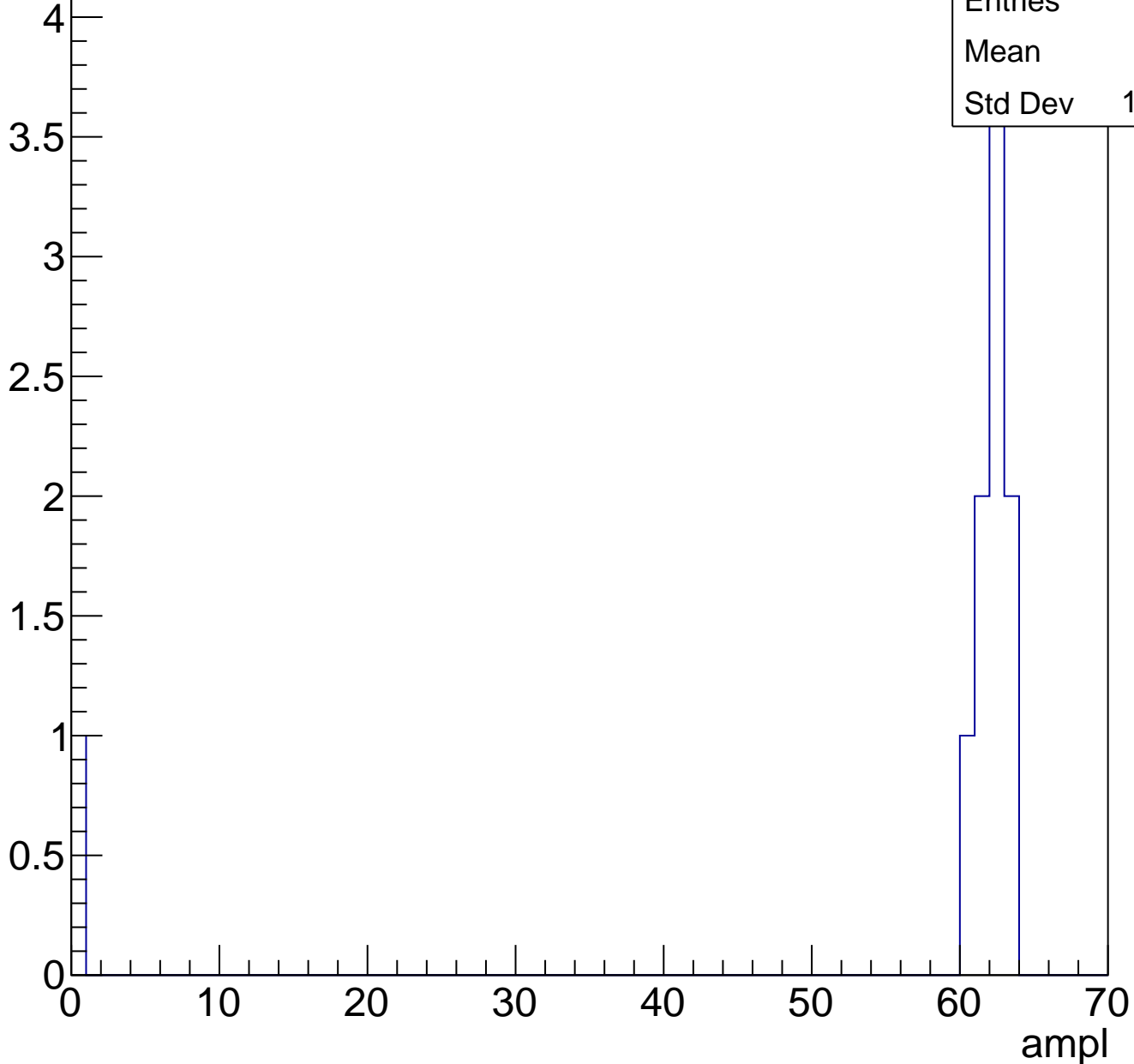
70

ampl

B1L103S, U24-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch32, adc0

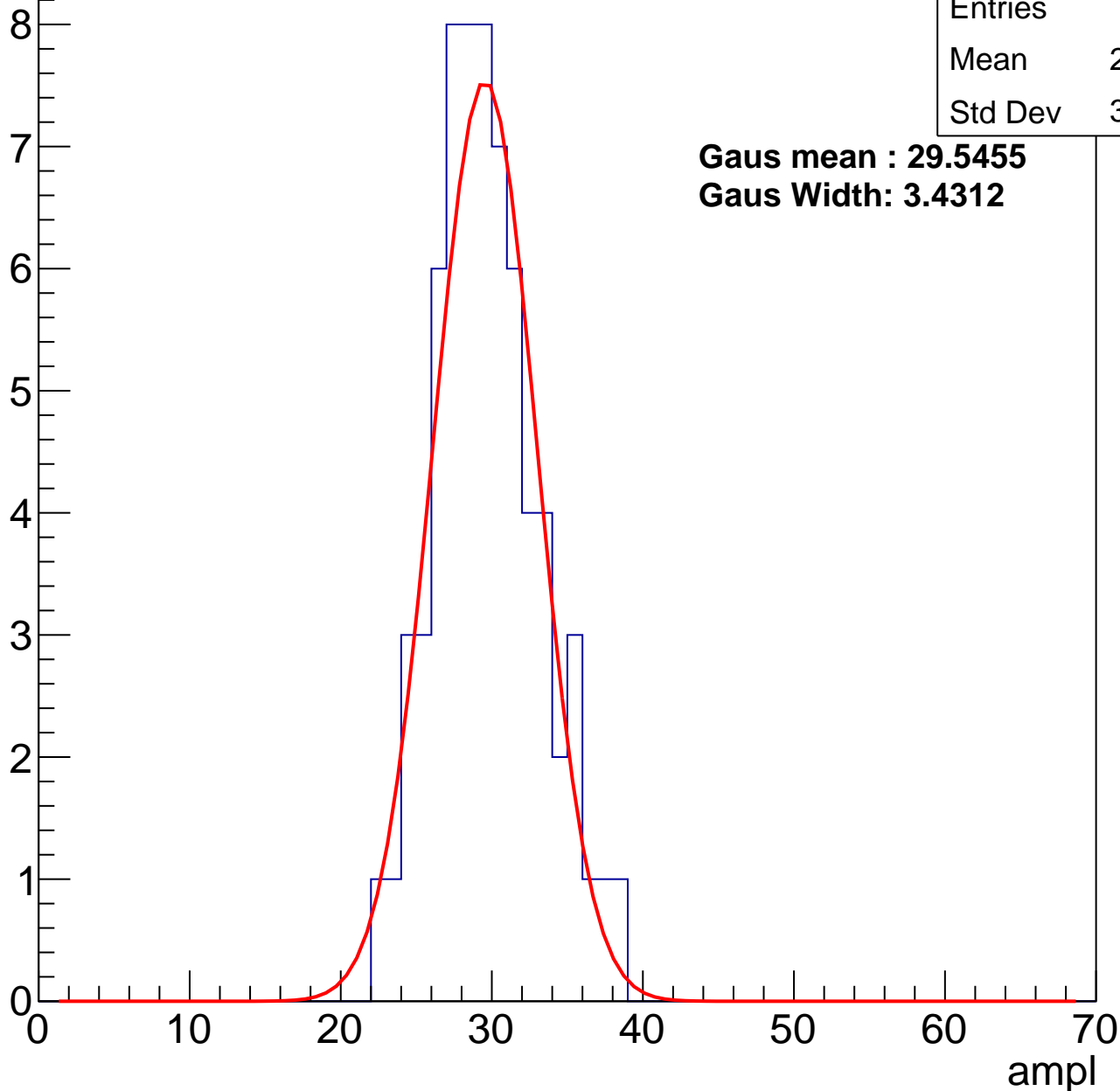
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.25
Std Dev	3.414

Gaus mean : 29.5455

Gaus Width: 3.4312



B1L103S, U24-ch32, adc1

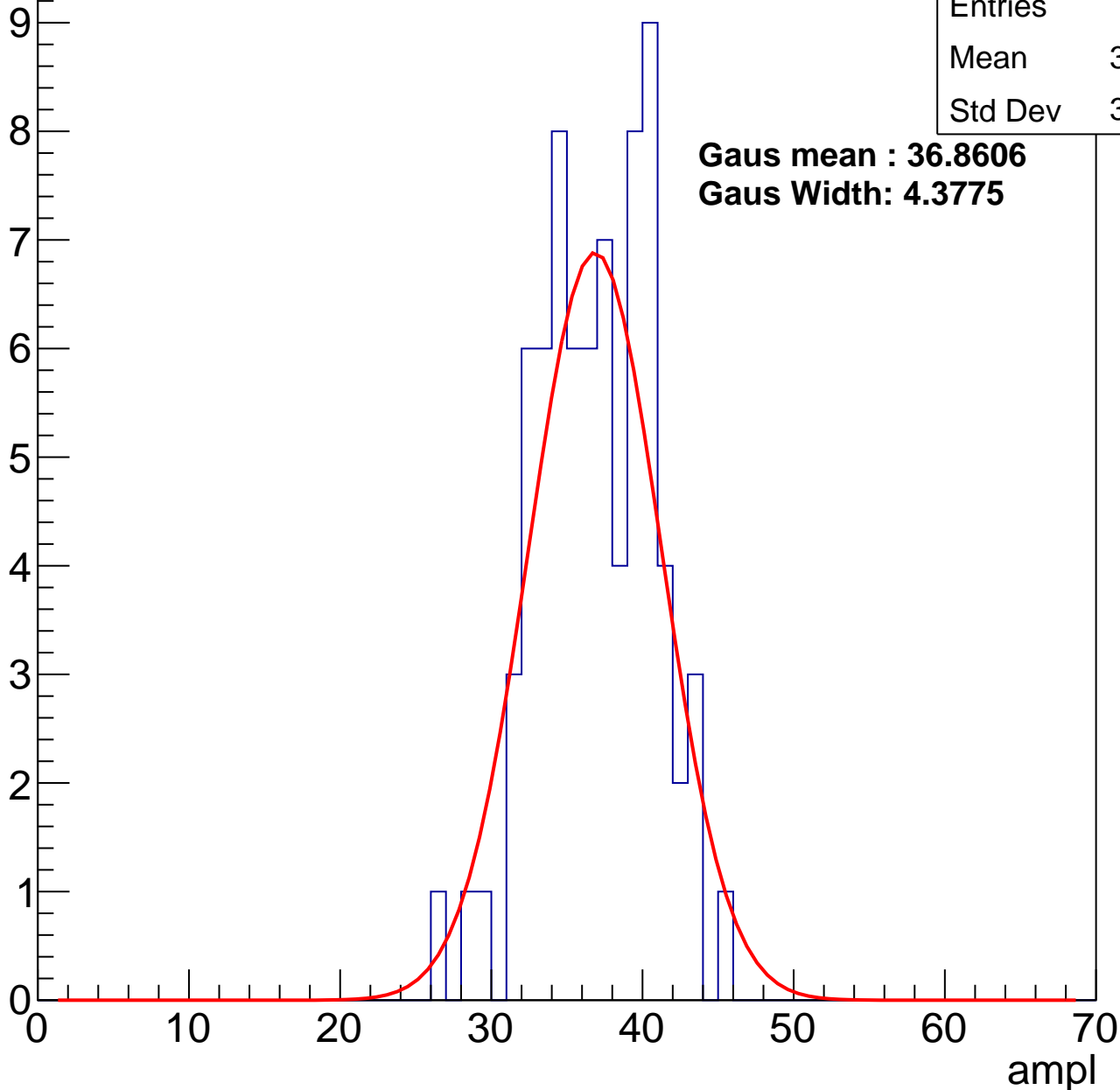
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.43
Std Dev	3.806

Gaus mean : 36.8606

Gaus Width: 4.3775



B1L103S, U24-ch32, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

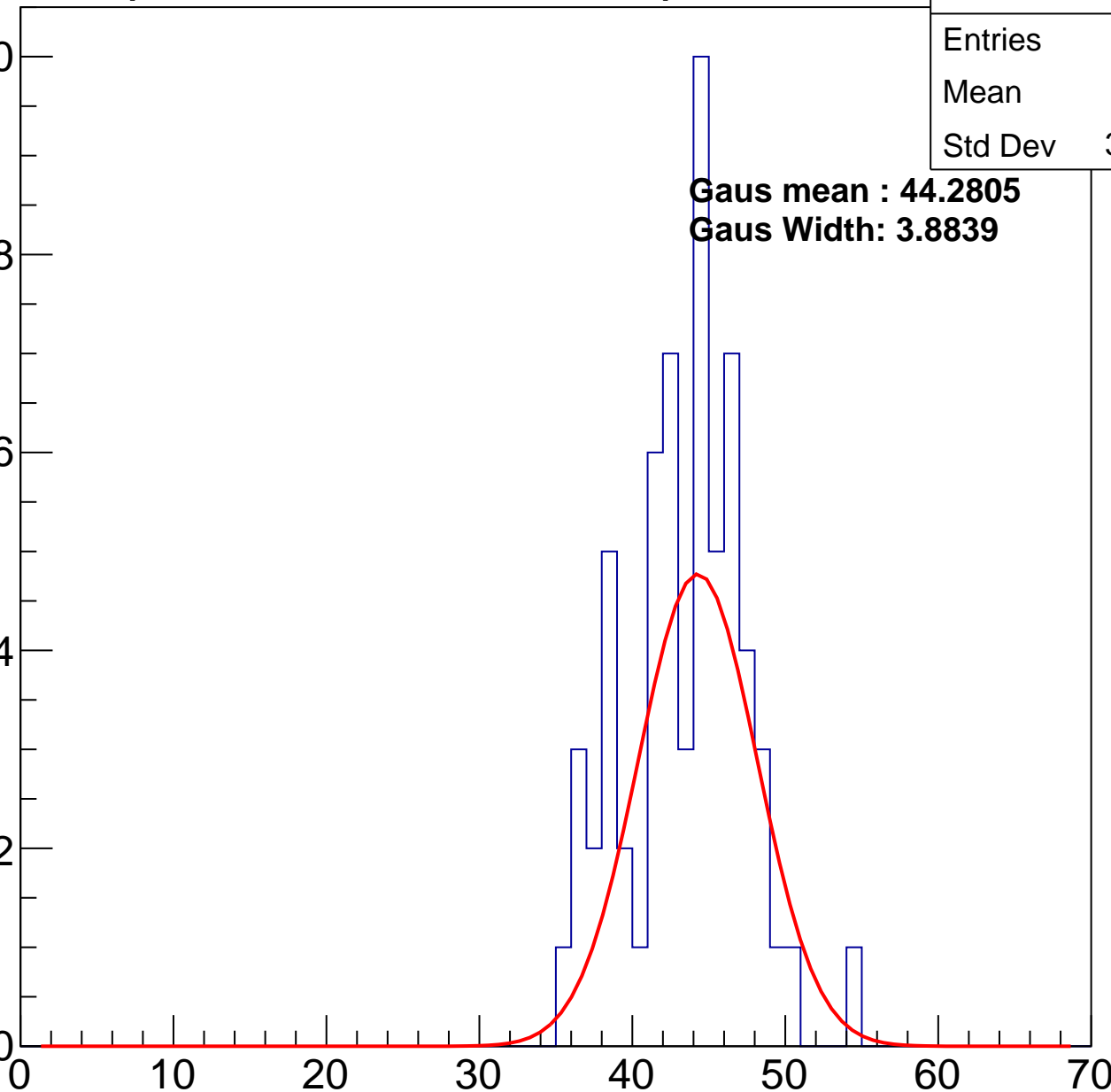
Entries	62
Mean	43
Std Dev	3.848

Gaus mean : 44.2805

Gaus Width: 3.8839

10
8
6
4
2
0

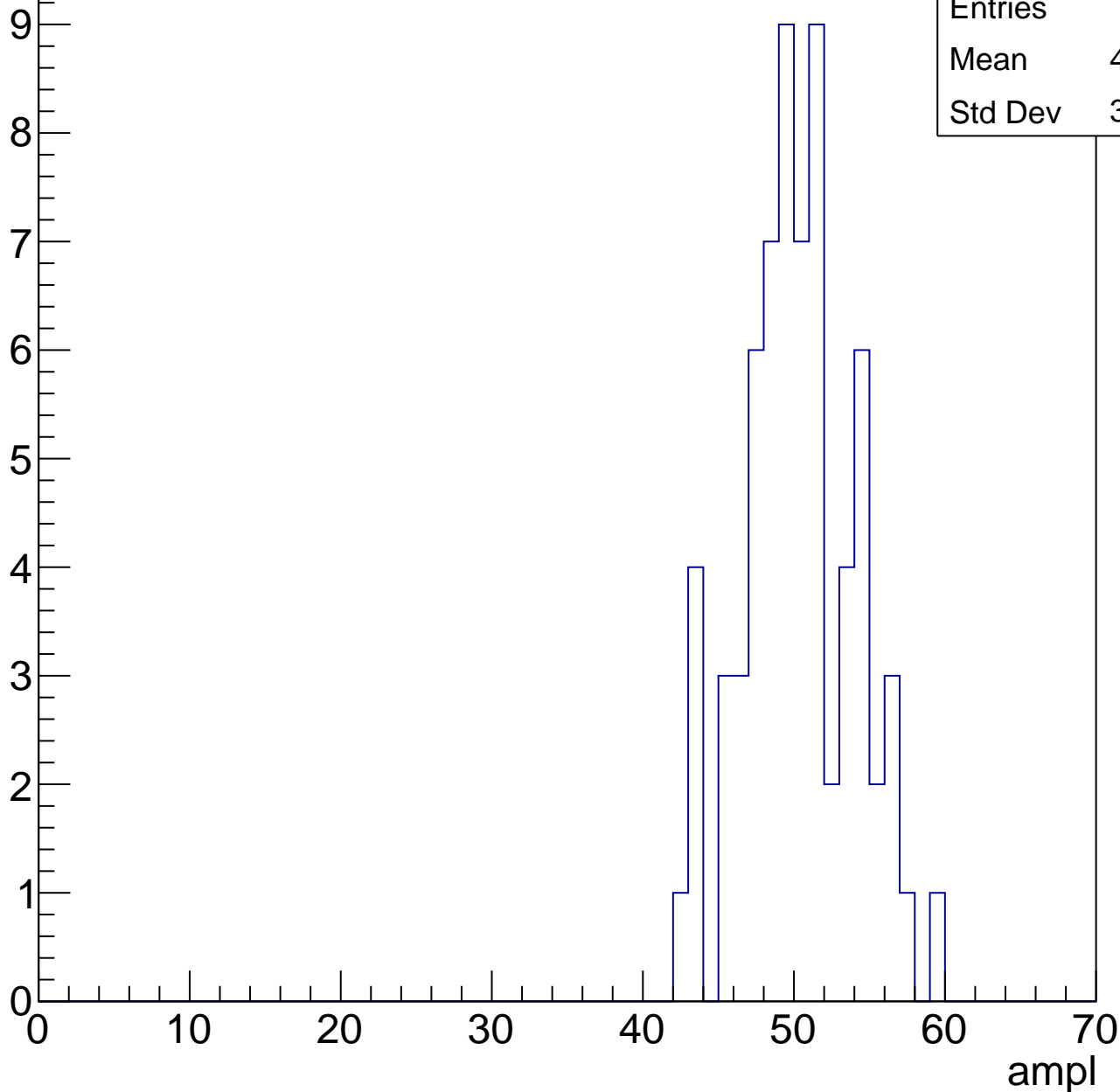
ampl



B1L103S, U24-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



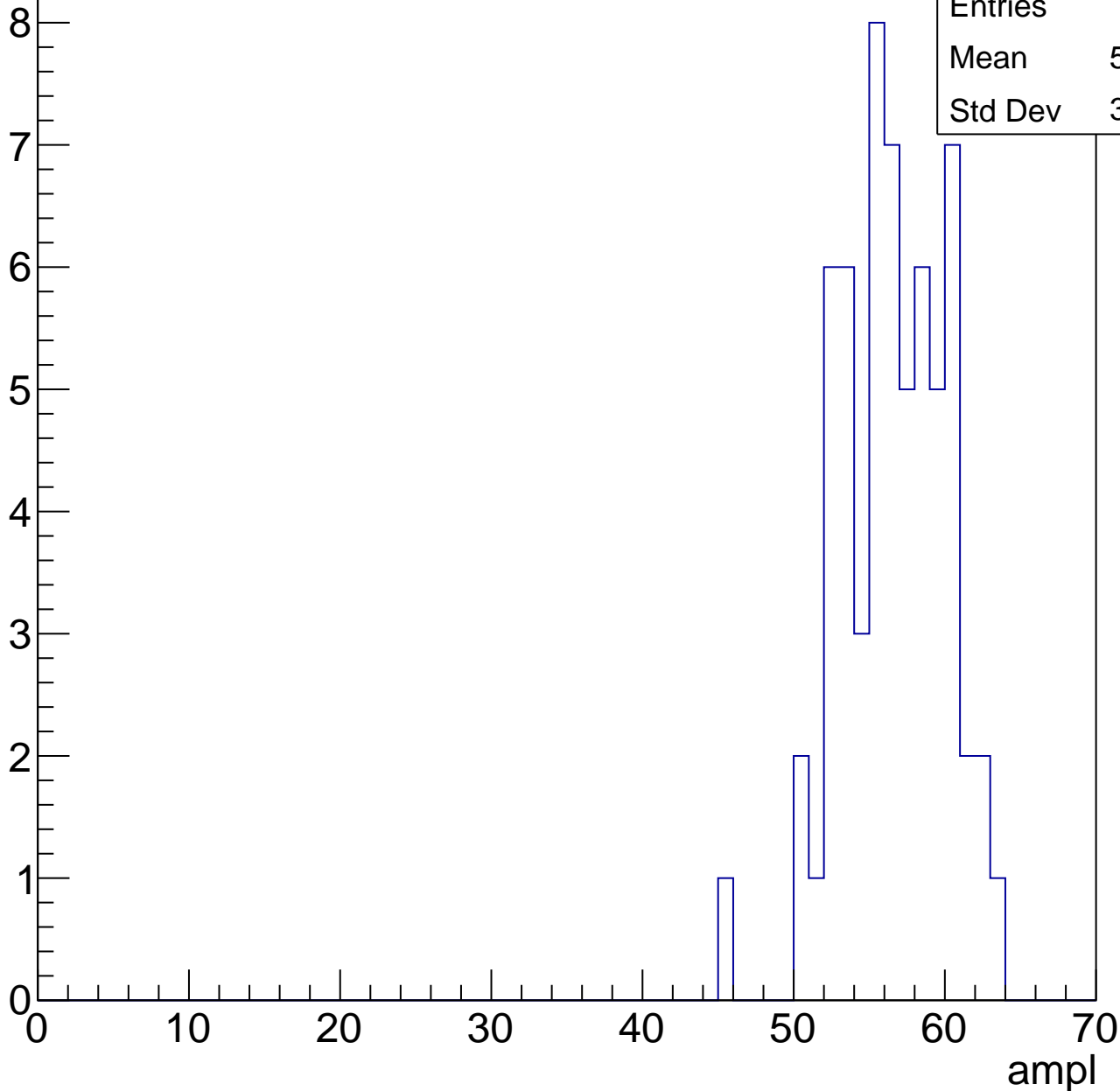
Entries	68
Mean	49.84
Std Dev	3.657

B1L103S, U24-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.08
Std Dev	3.465

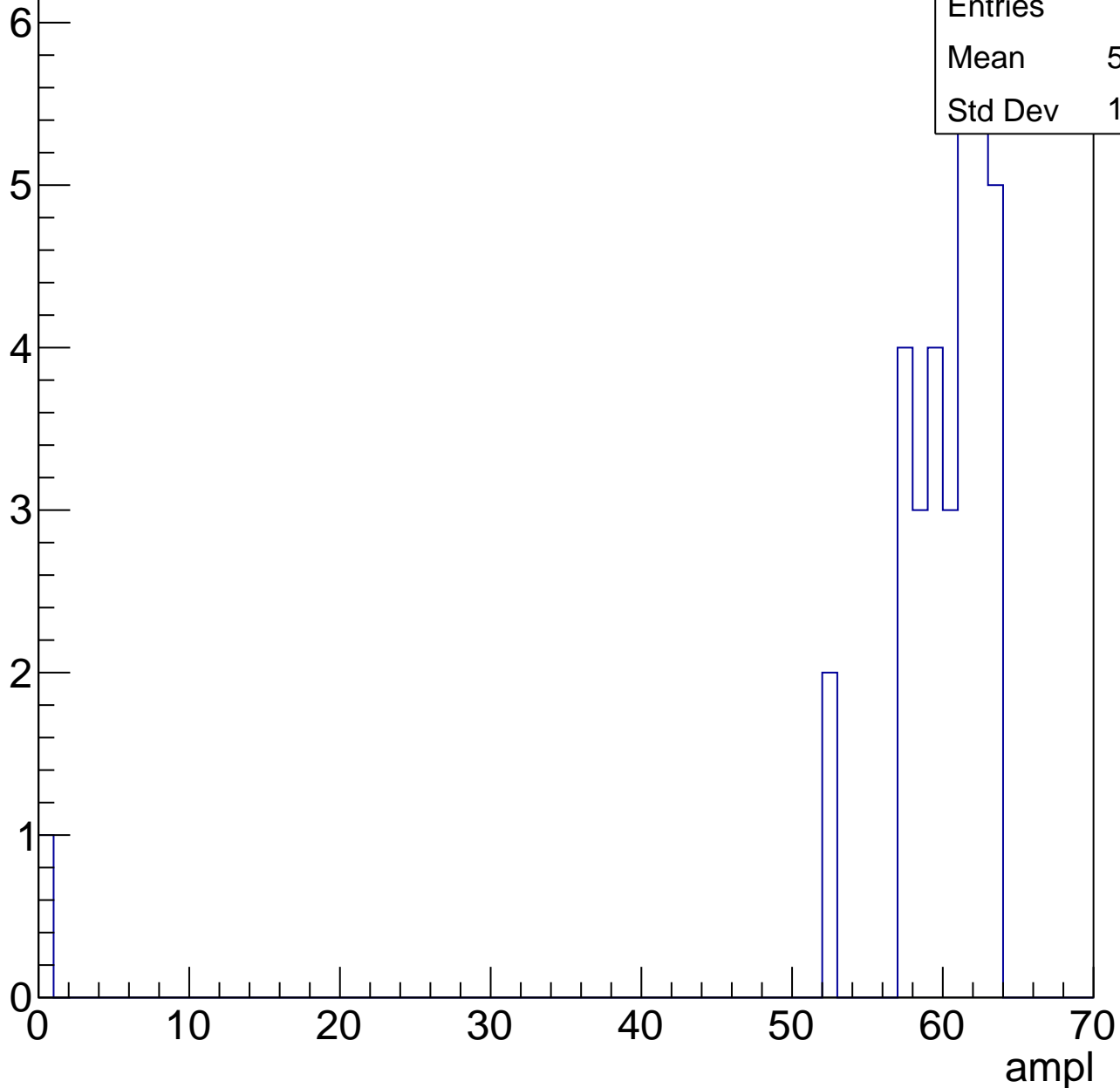


B1L103S, U24-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

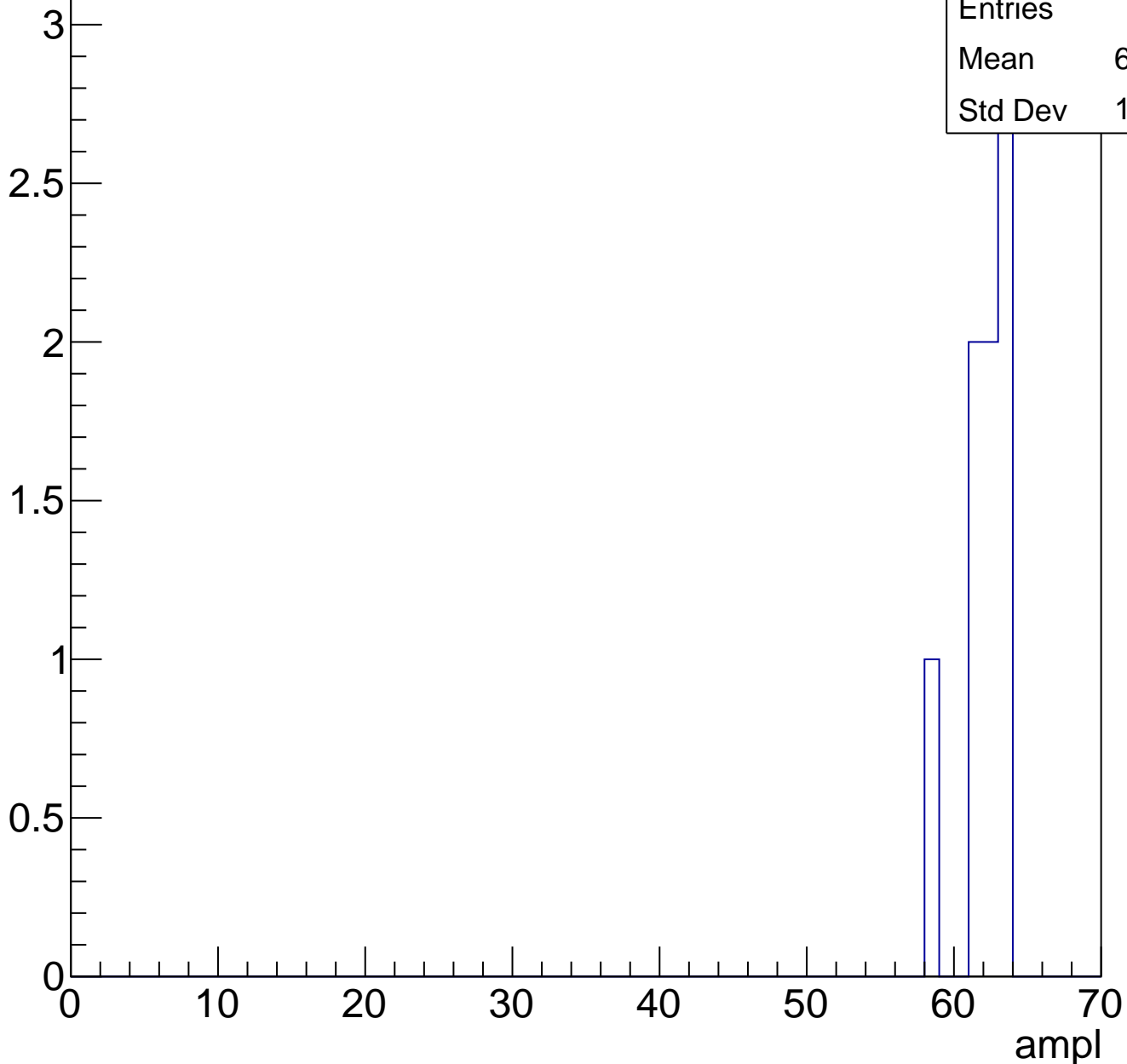
Entries	34
Mean	58.09
Std Dev	10.48



B1L103S, U24-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch33, adc0

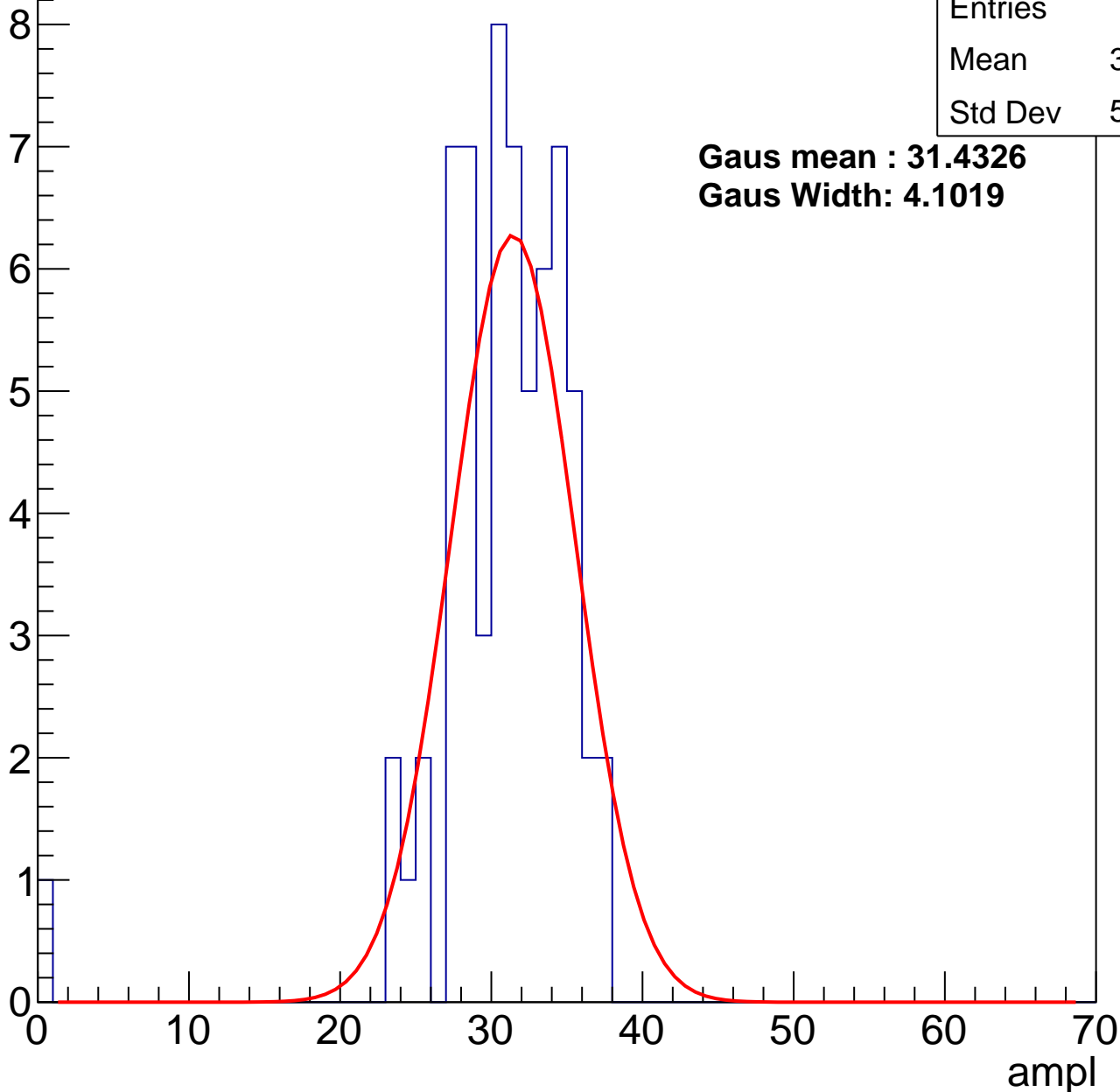
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.25
Std Dev	5.057

Gaus mean : 31.4326

Gaus Width: 4.1019



B1L103S, U24-ch33, adc1

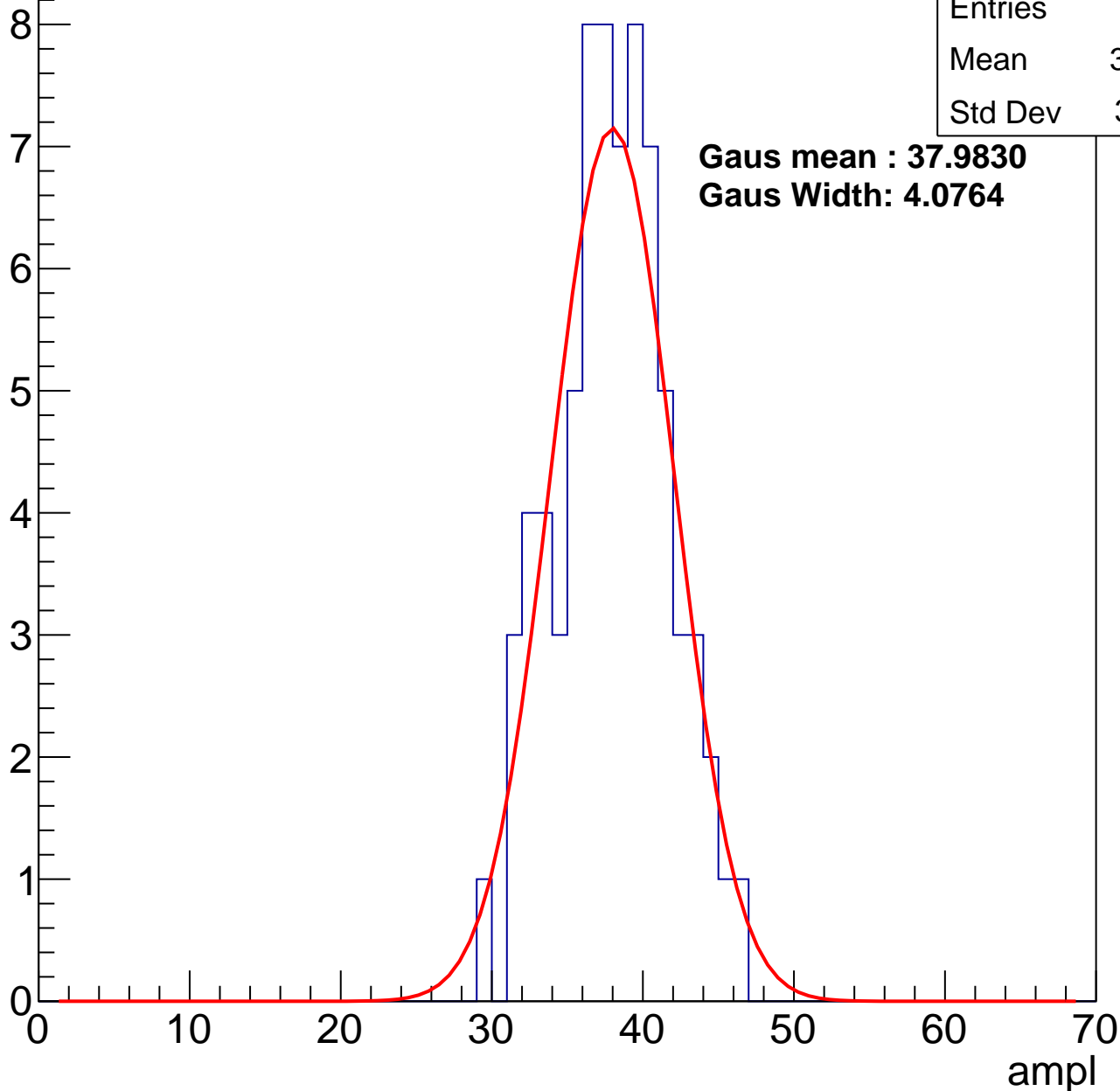
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	37.53
Std Dev	3.661

Gaus mean : 37.9830

Gaus Width: 4.0764



B1L103S, U24-ch33, adc2

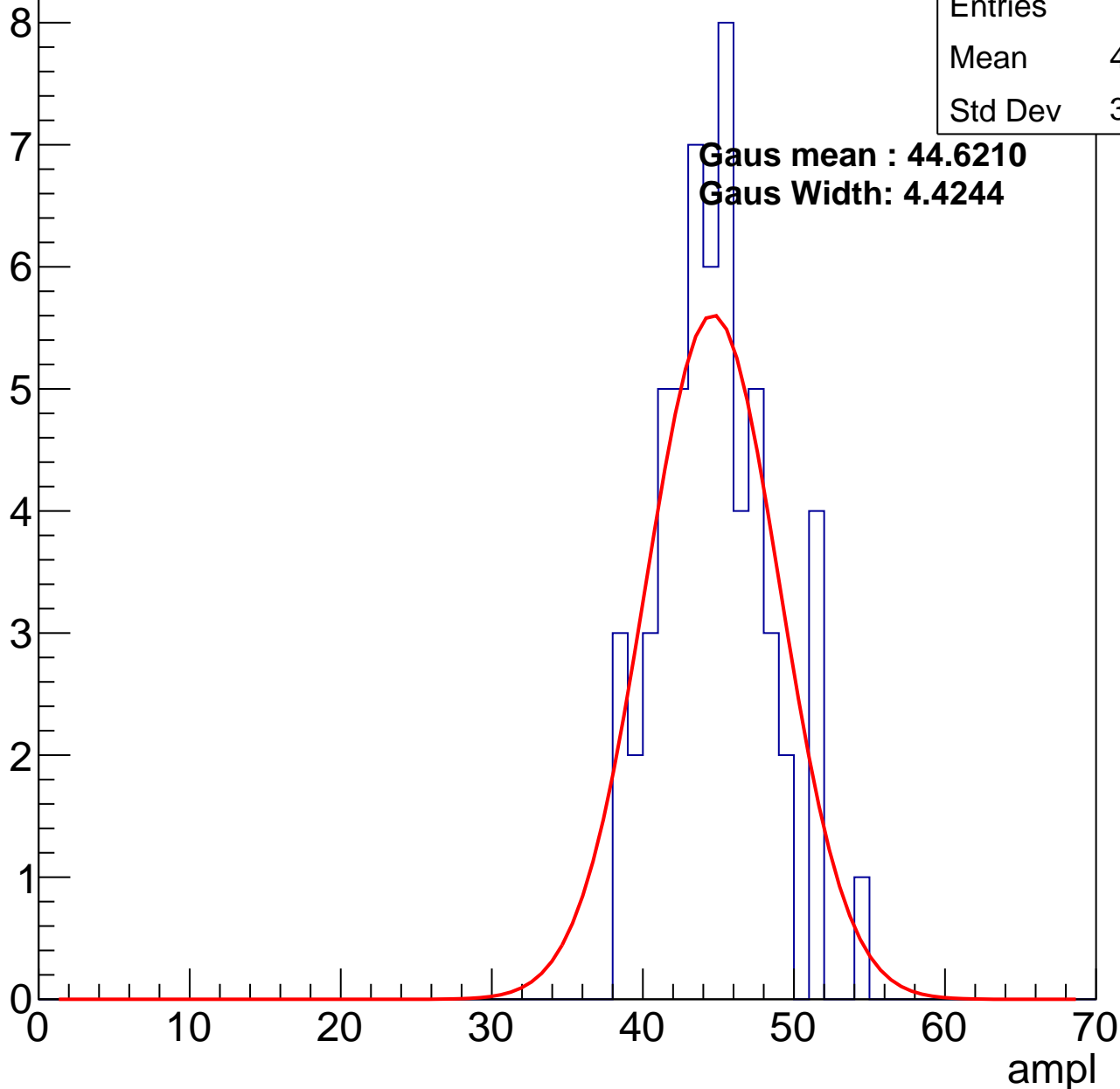
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.33
Std Dev	3.559

Gaus mean : 44.6210

Gaus Width: 4.4244

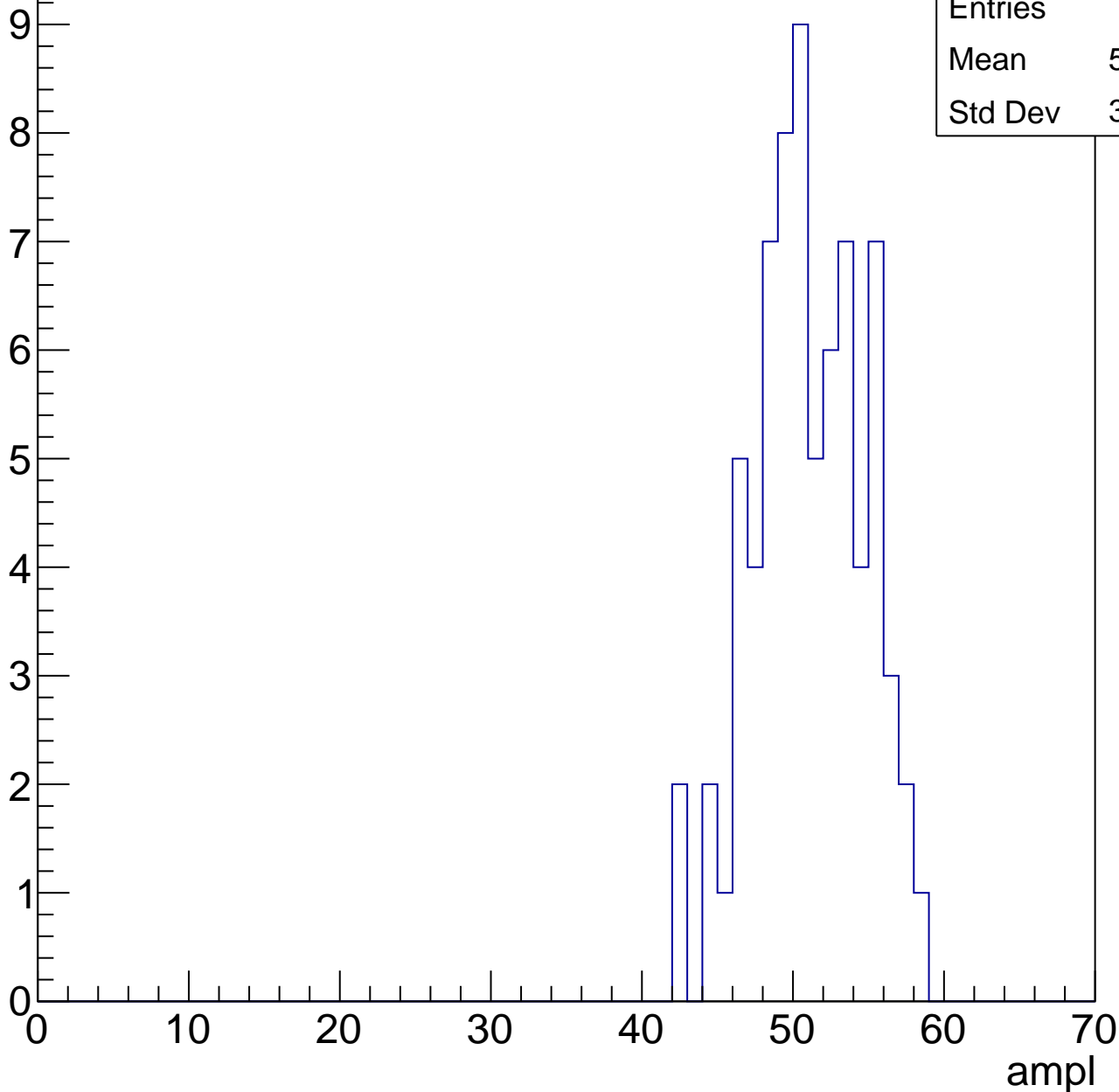


B1L103S, U24-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	50.58
Std Dev	3.615

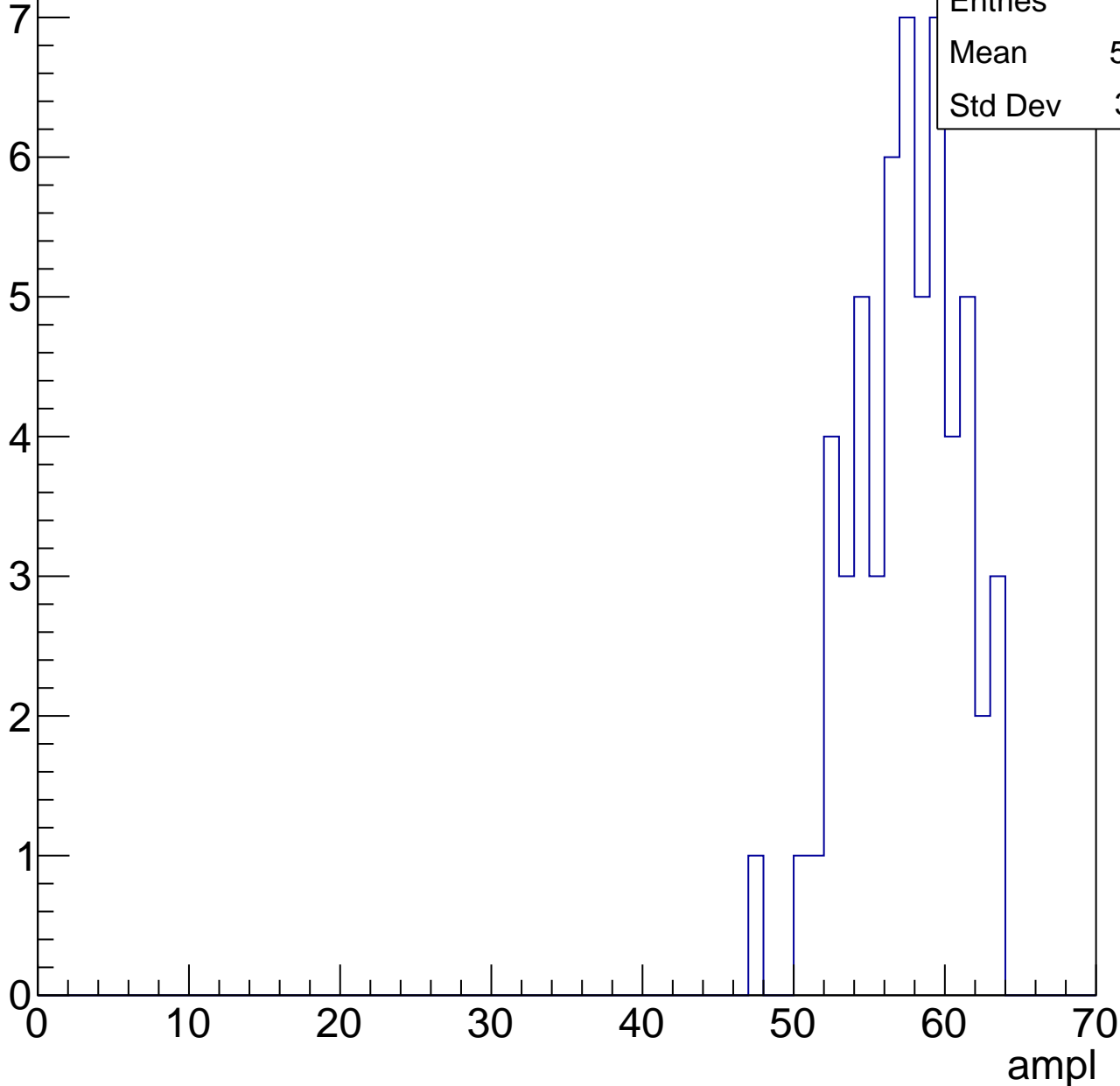


B1L103S, U24-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.95
Std Dev	3.511

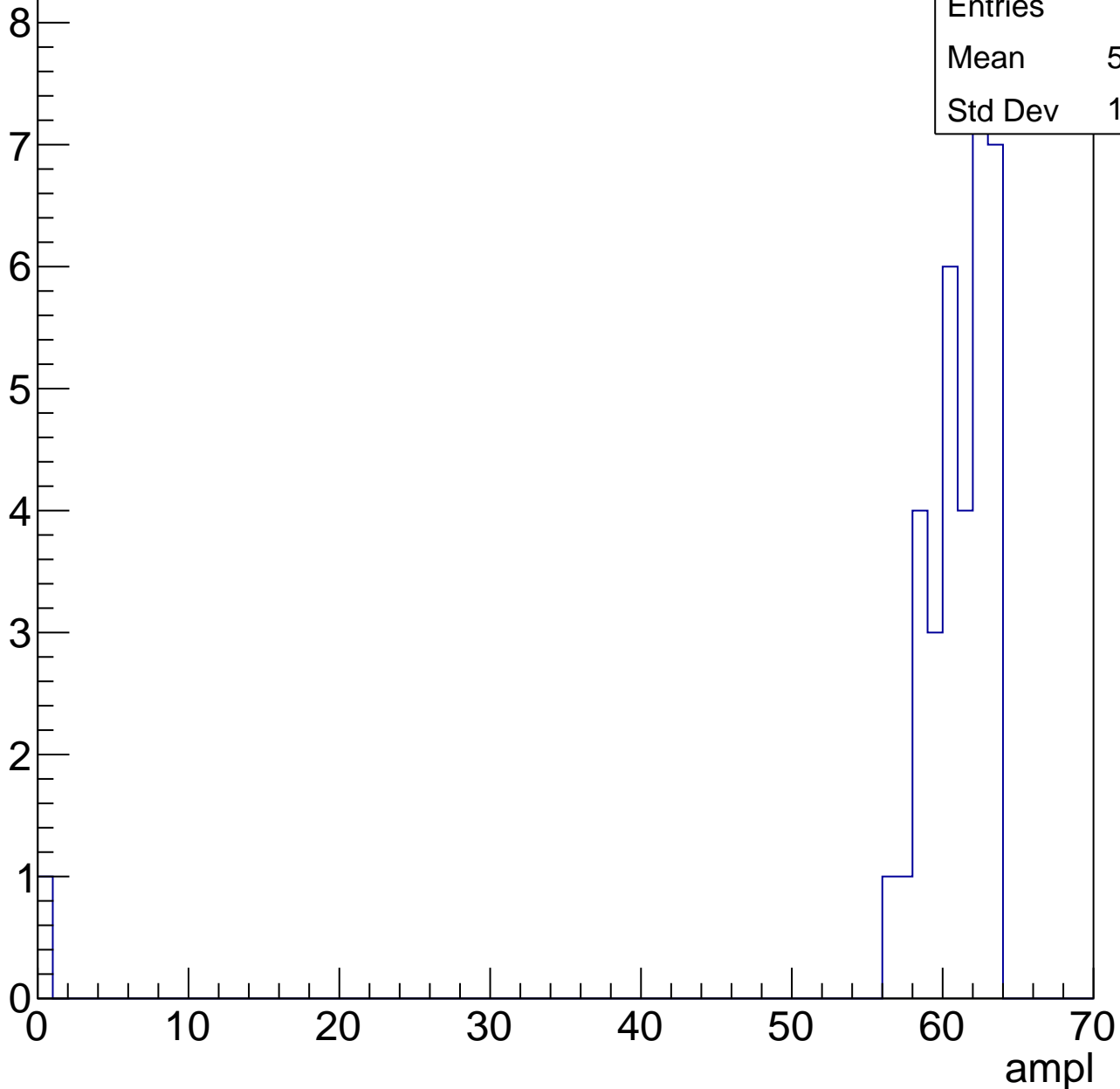


B1L103S, U24-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.94
Std Dev	10.29



B1L103S, U24-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch34, adc0

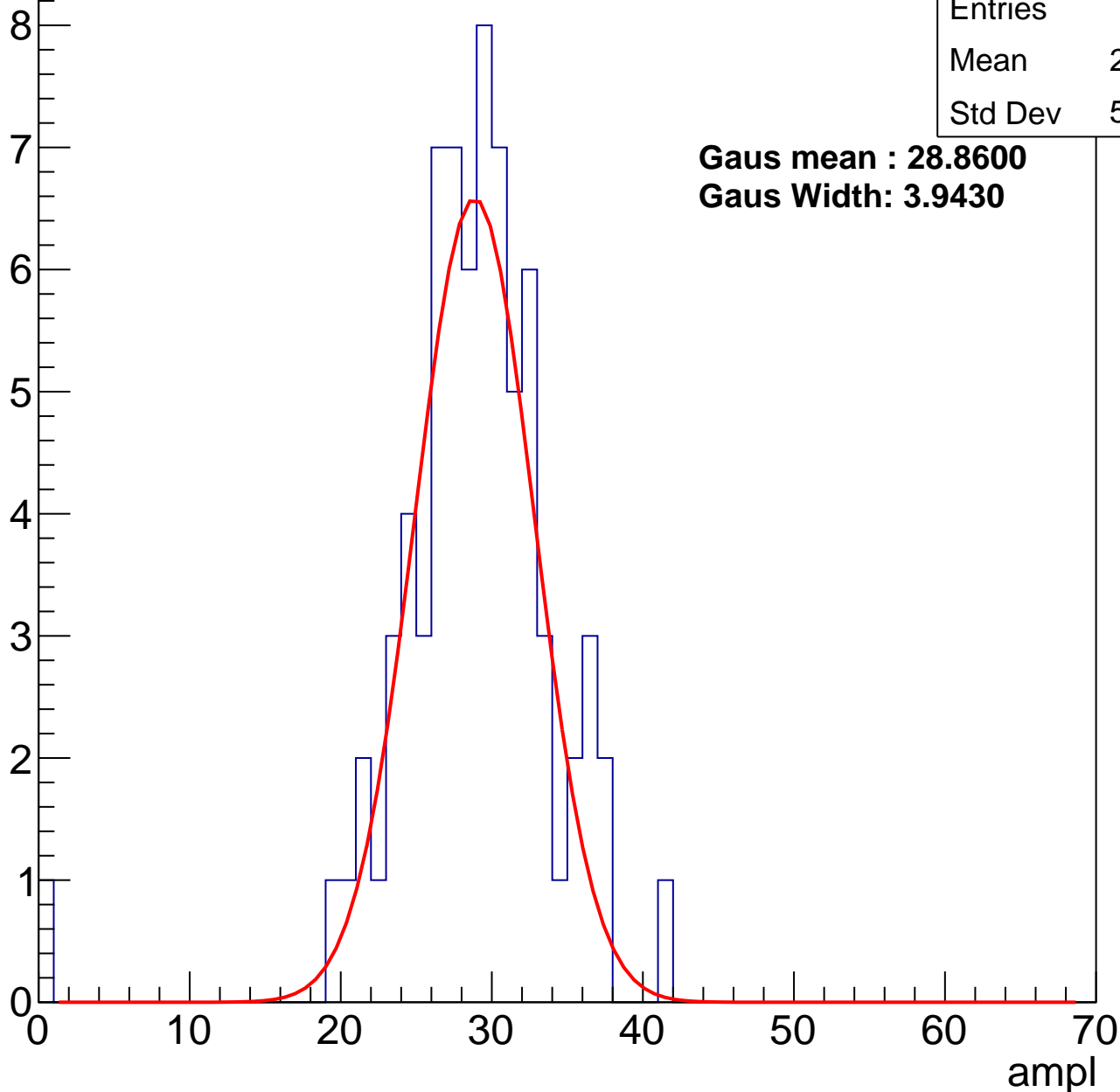
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.34
Std Dev	5.408

Gaus mean : 28.8600

Gaus Width: 3.9430



B1L103S, U24-ch34, adc1

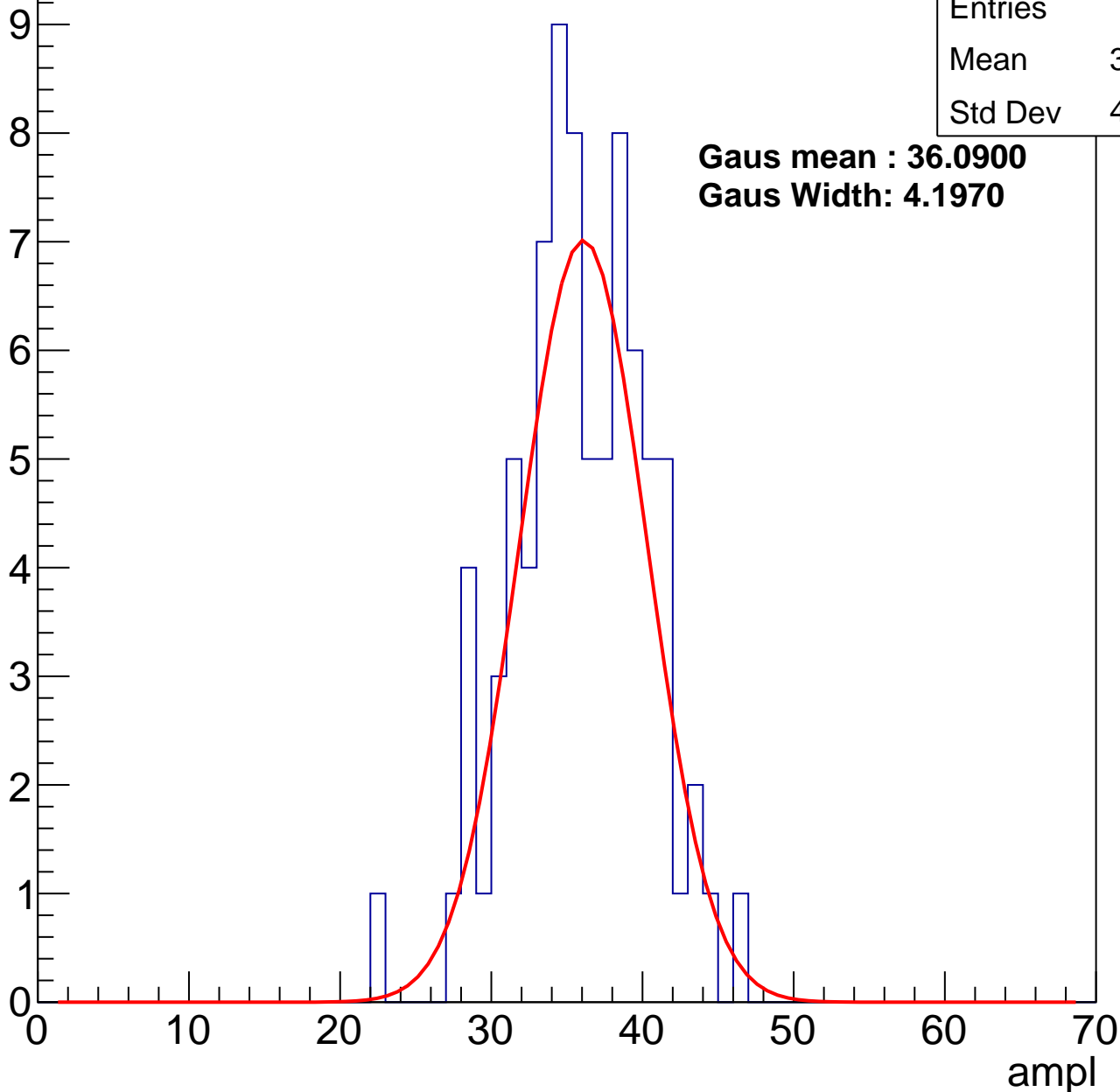
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	35.44
Std Dev	4.354

Gaus mean : 36.0900

Gaus Width: 4.1970



B1L103S, U24-ch34, adc2

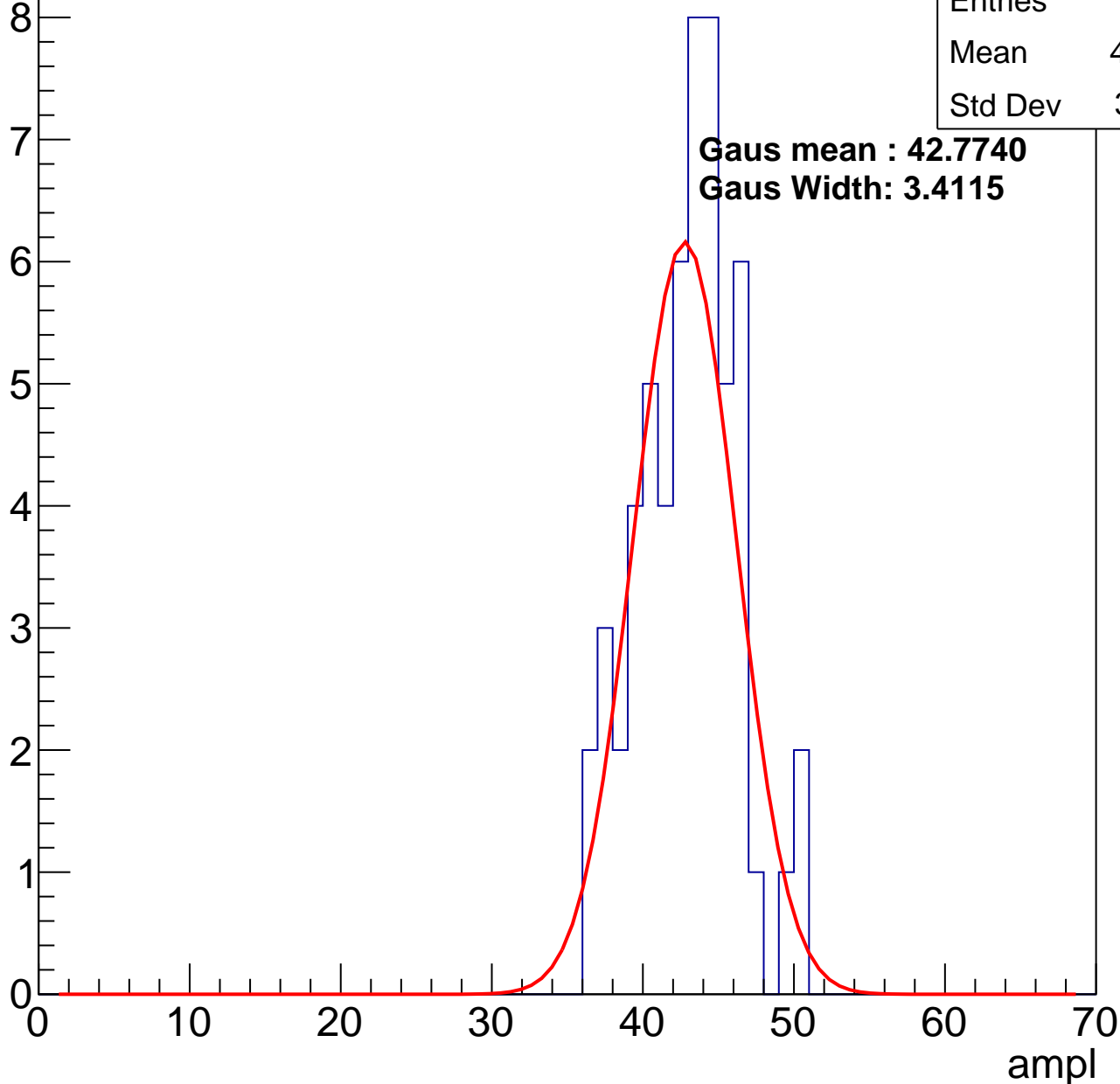
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.53
Std Dev	3.261

Gaus mean : 42.7740

Gaus Width: 3.4115

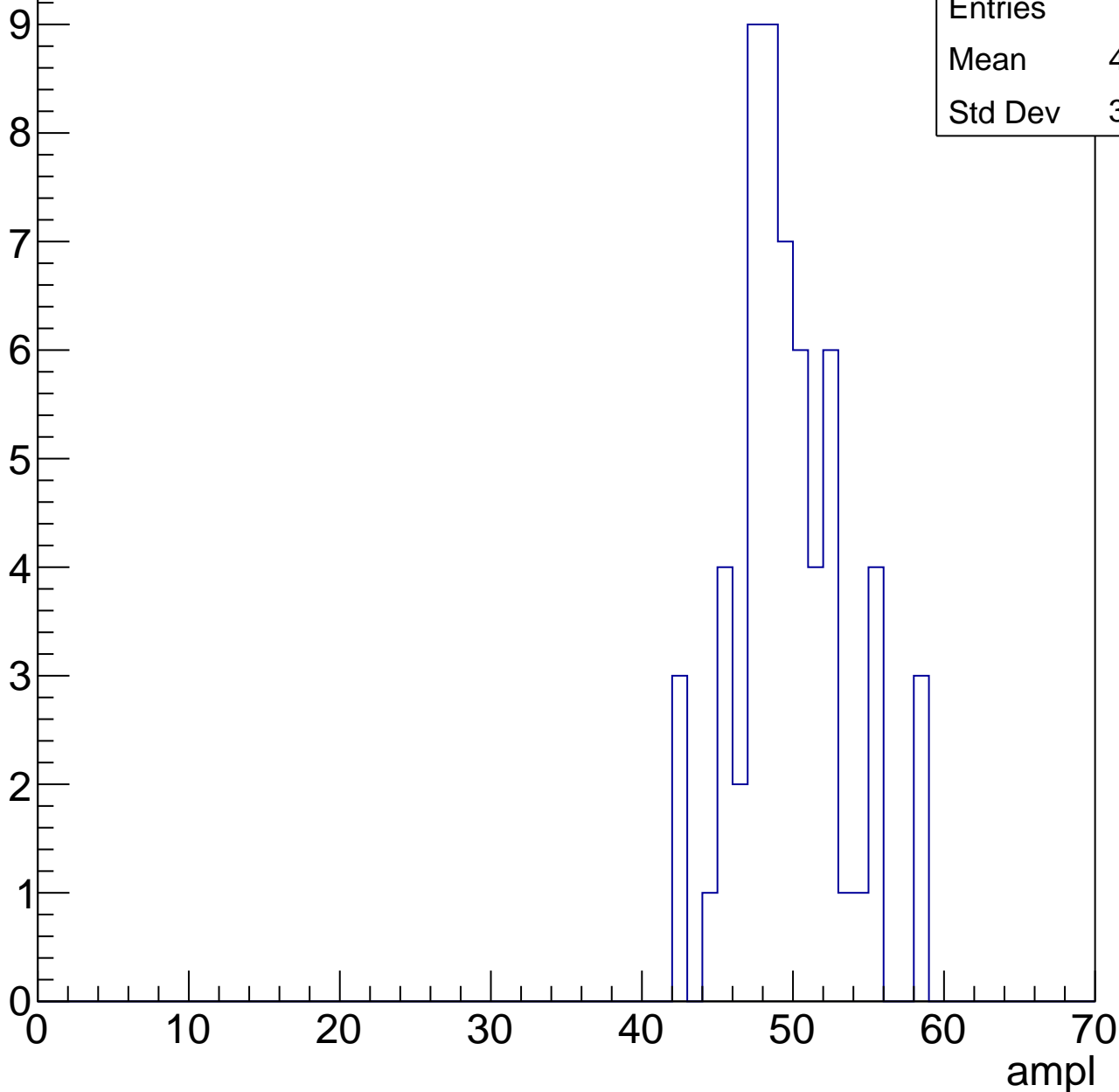


B1L103S, U24-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.28
Std Dev	3.647

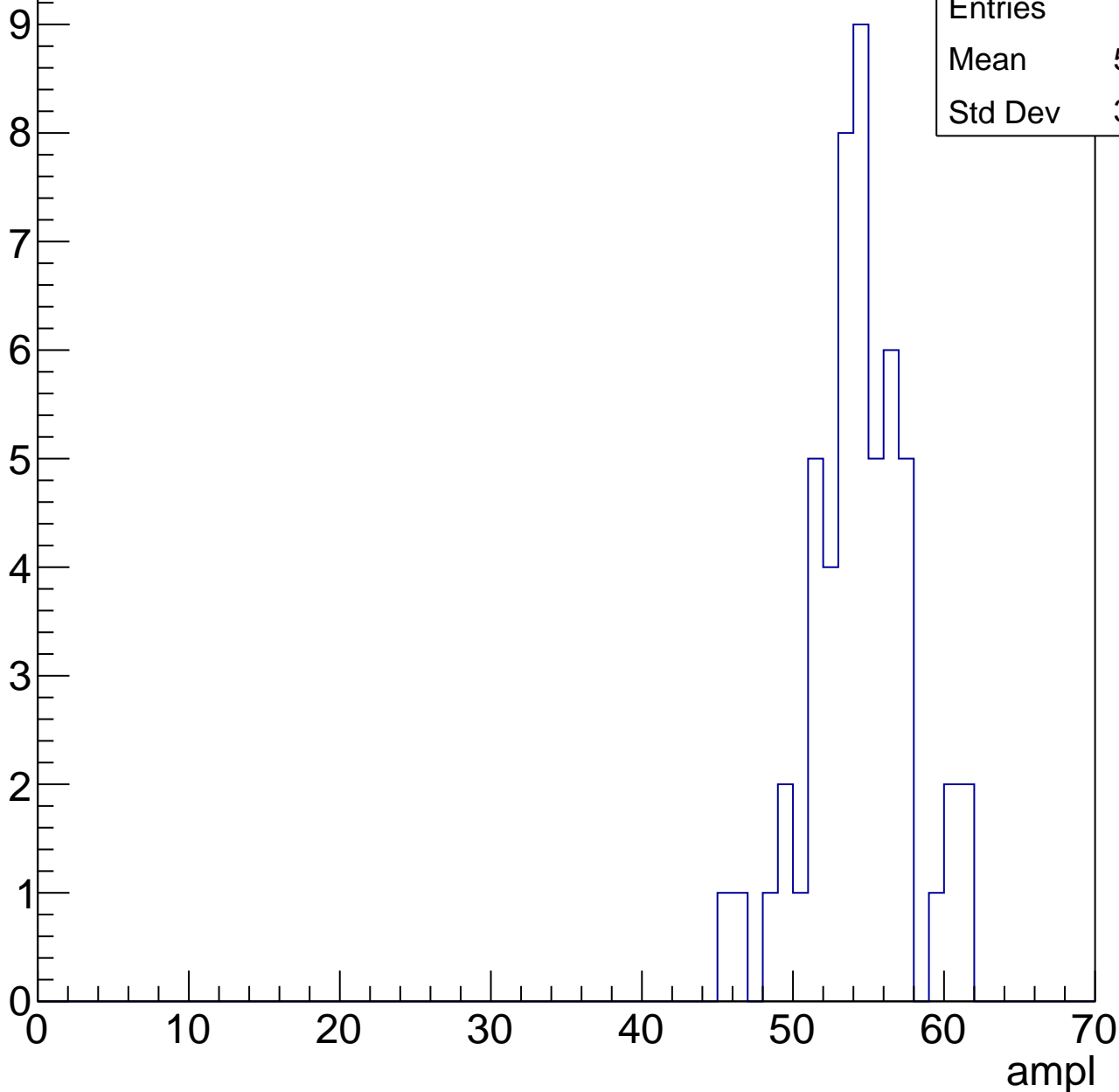


B1L103S, U24-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	53.91
Std Dev	3.321

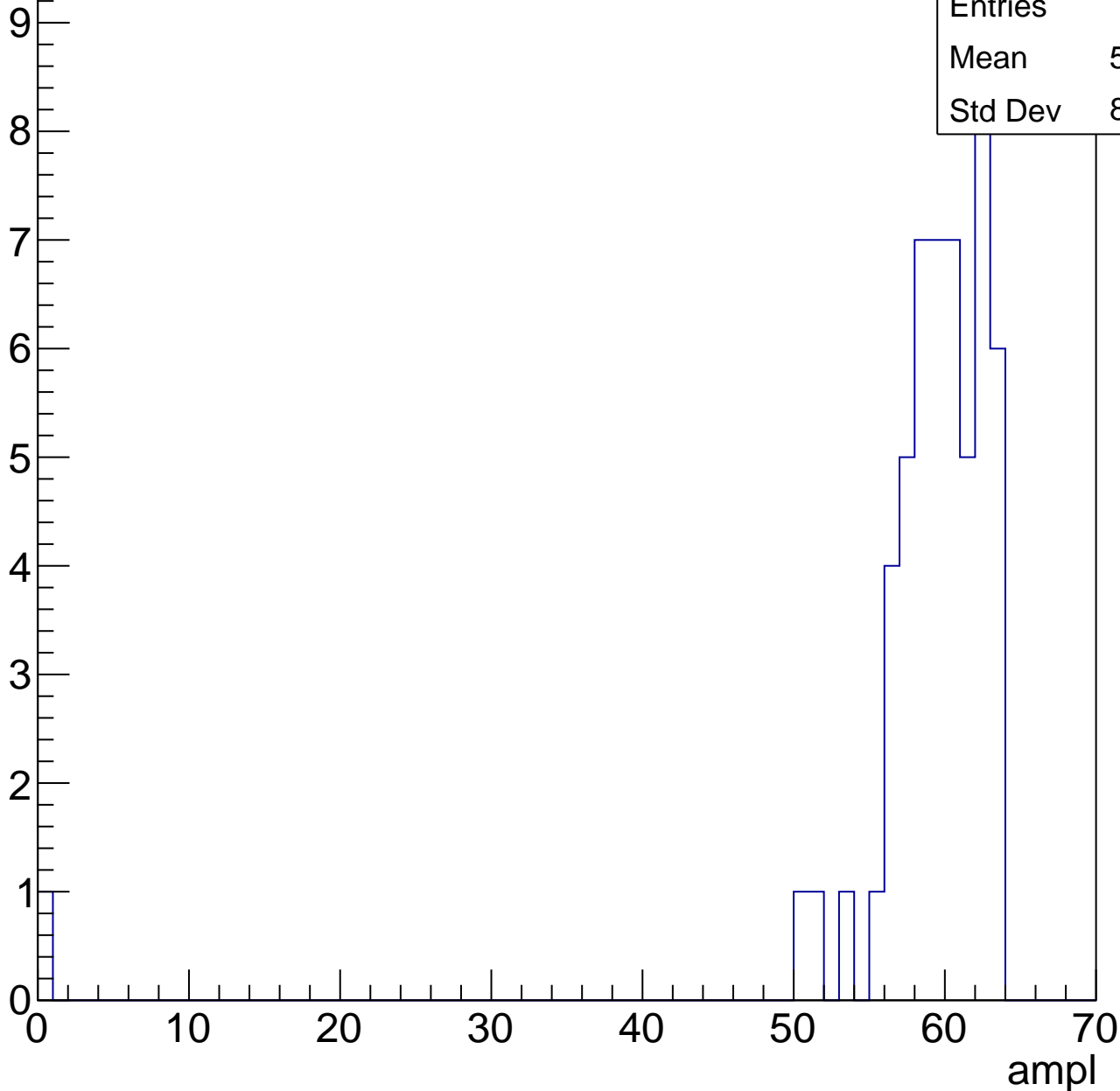


B1L103S, U24-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

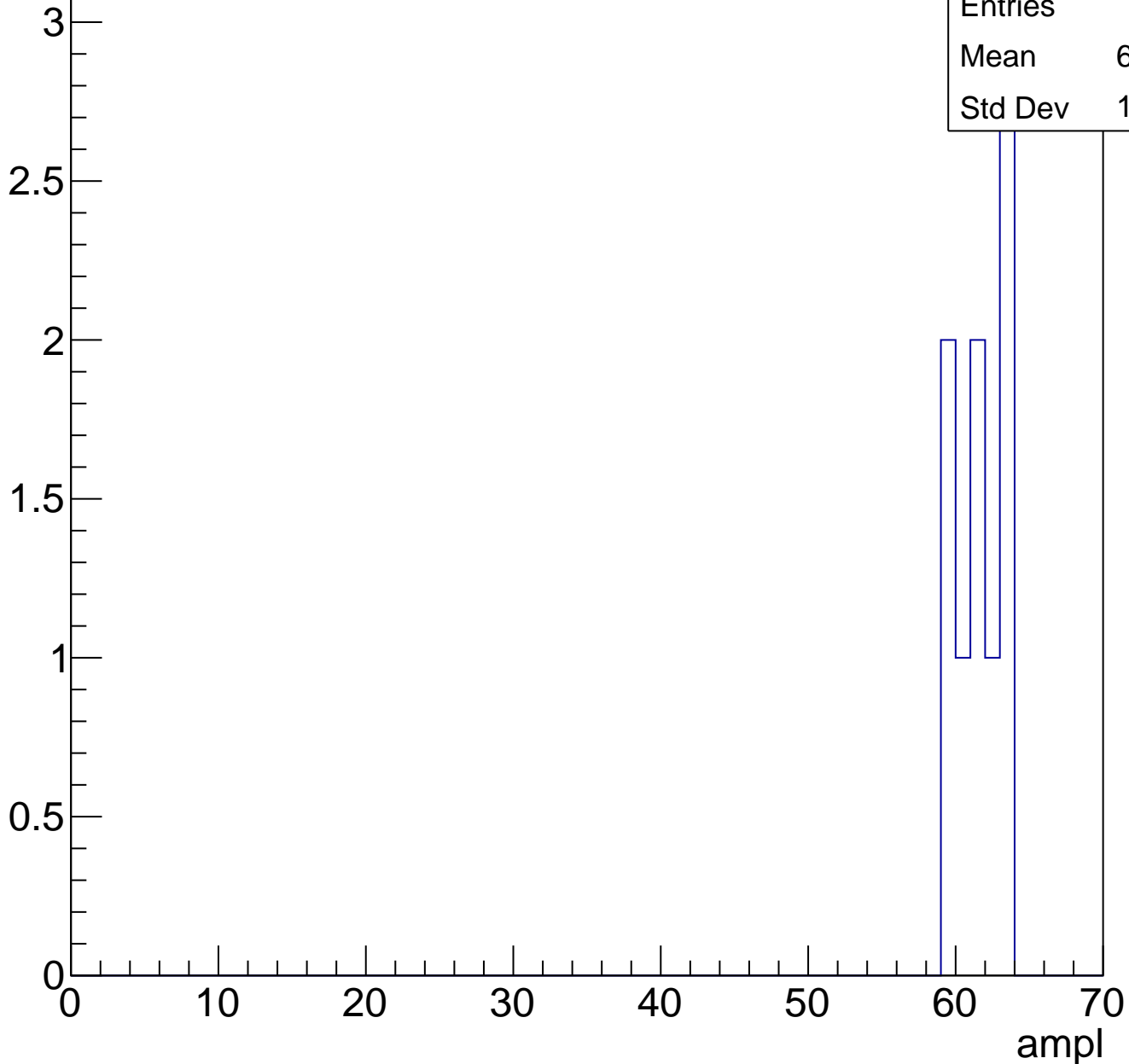
Entries	55
Mean	58.15
Std Dev	8.426



B1L103S, U24-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch35, adc0

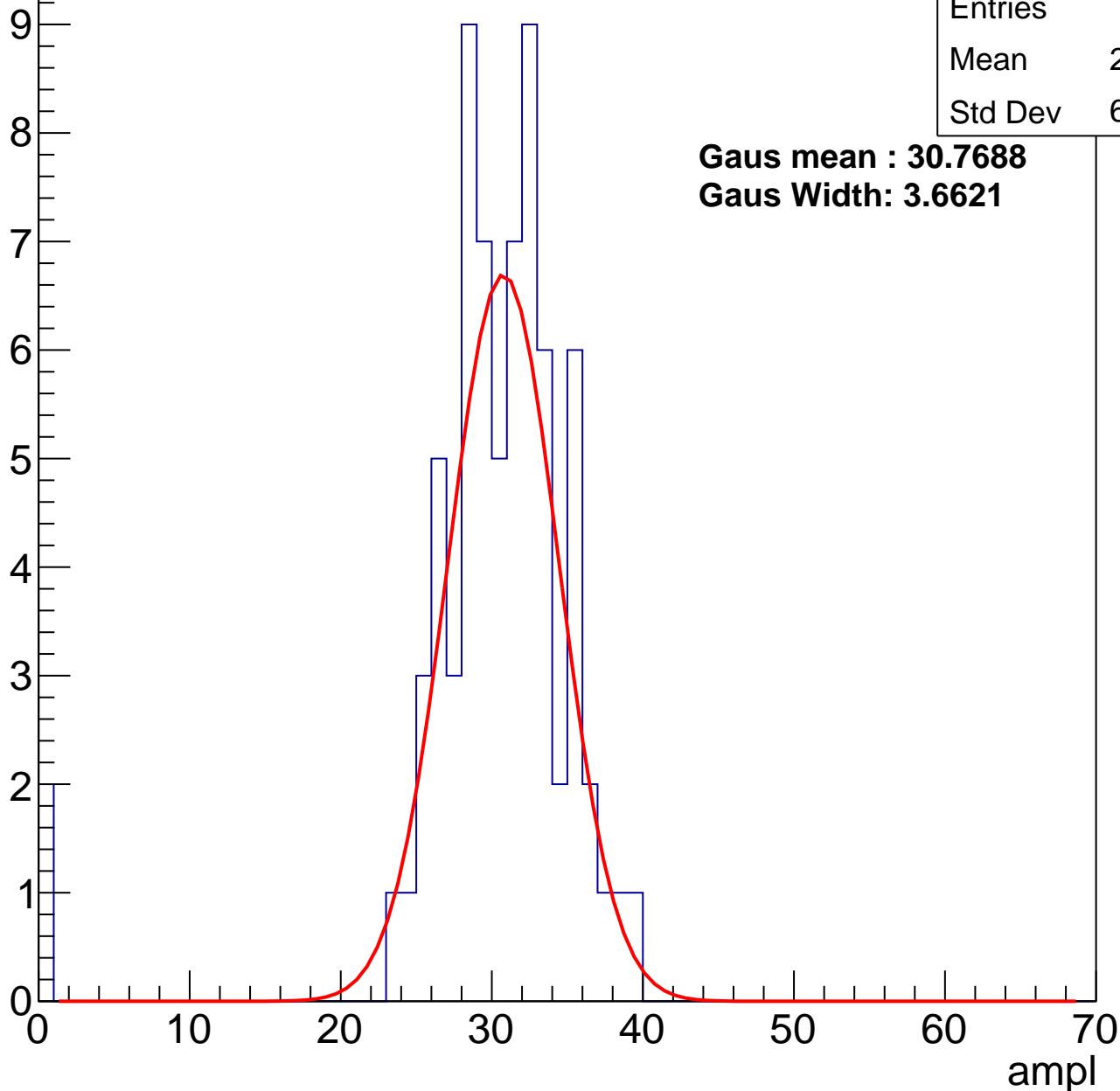
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.65
Std Dev	6.105

Gaus mean : 30.7688

Gaus Width: 3.6621



B1L103S, U24-ch35, adc1

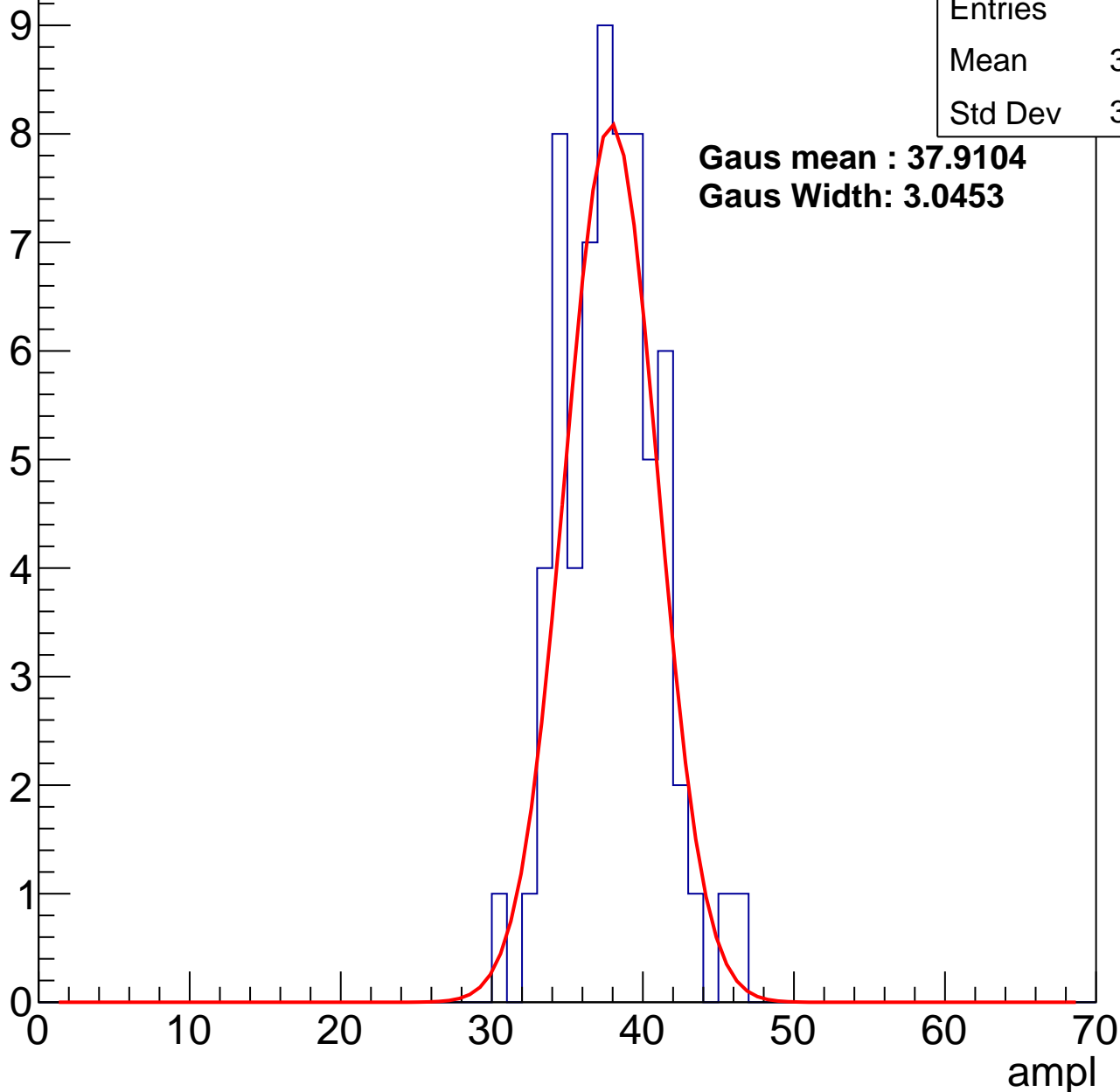
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	37.44
Std Dev	3.095

Gaus mean : 37.9104

Gaus Width: 3.0453



B1L103S, U24-ch35, adc2

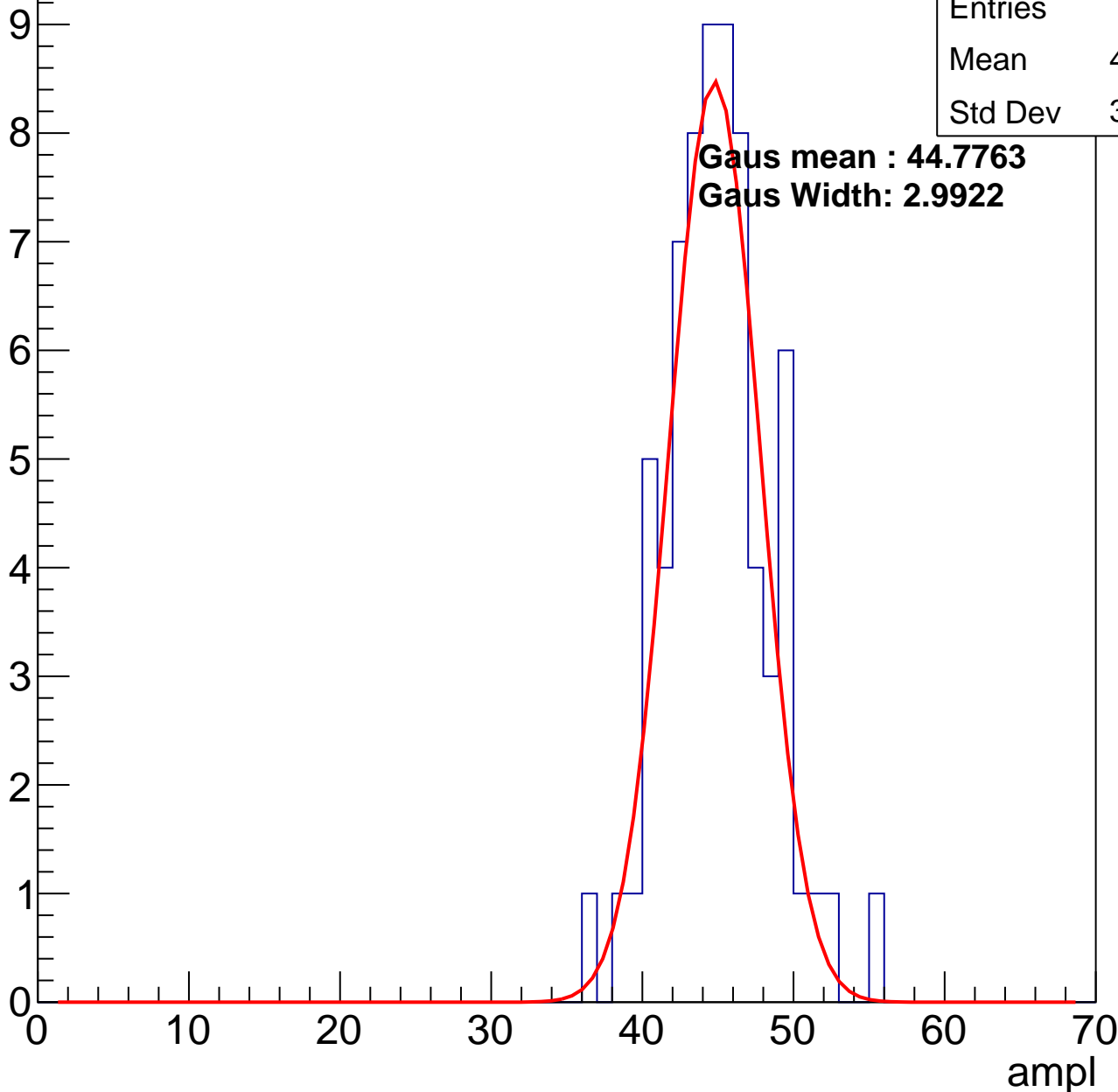
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.54
Std Dev	3.384

Gaus mean : 44.7763

Gaus Width: 2.9922

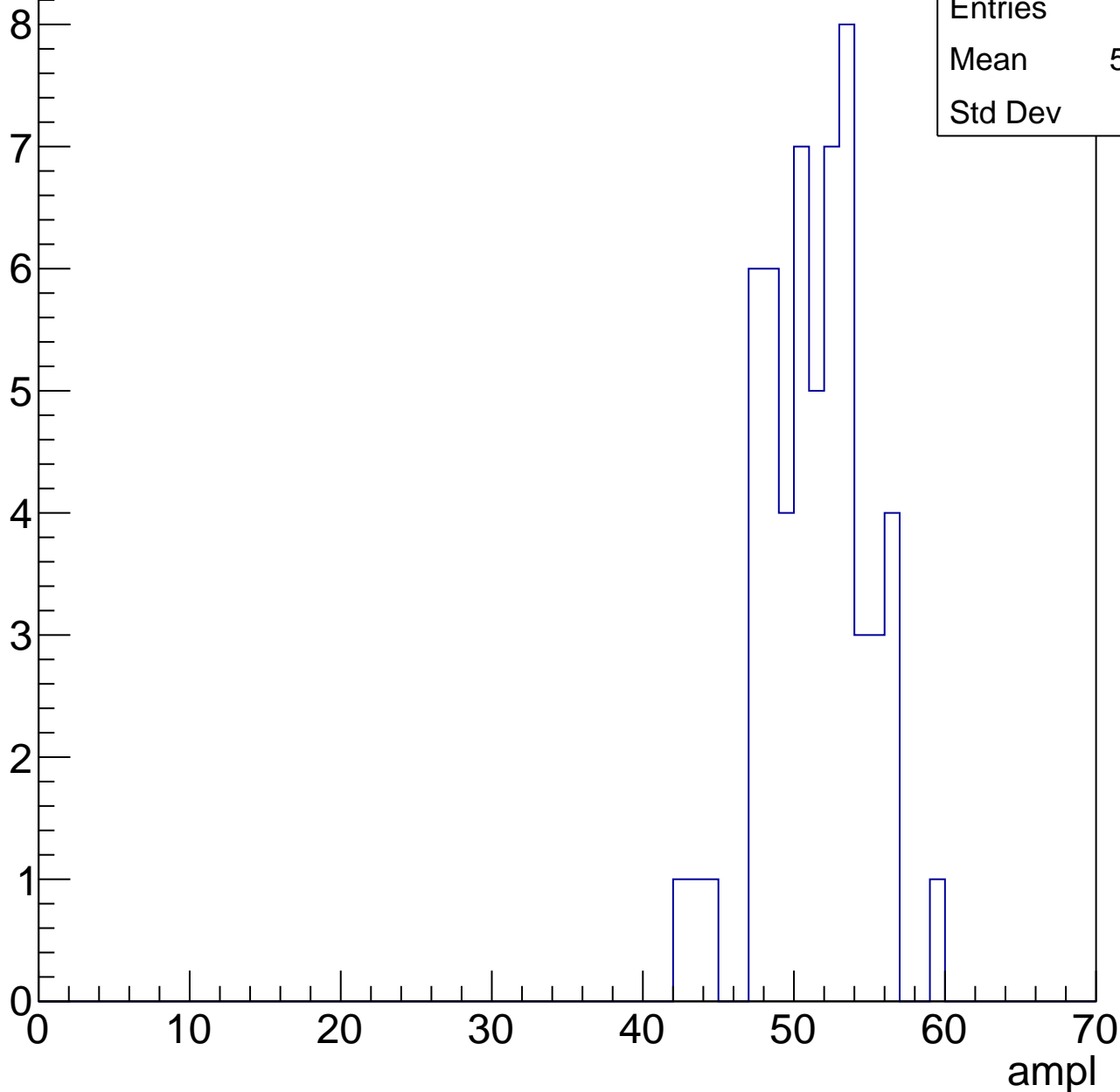


B1L103S, U24-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	50.84
Std Dev	3.36

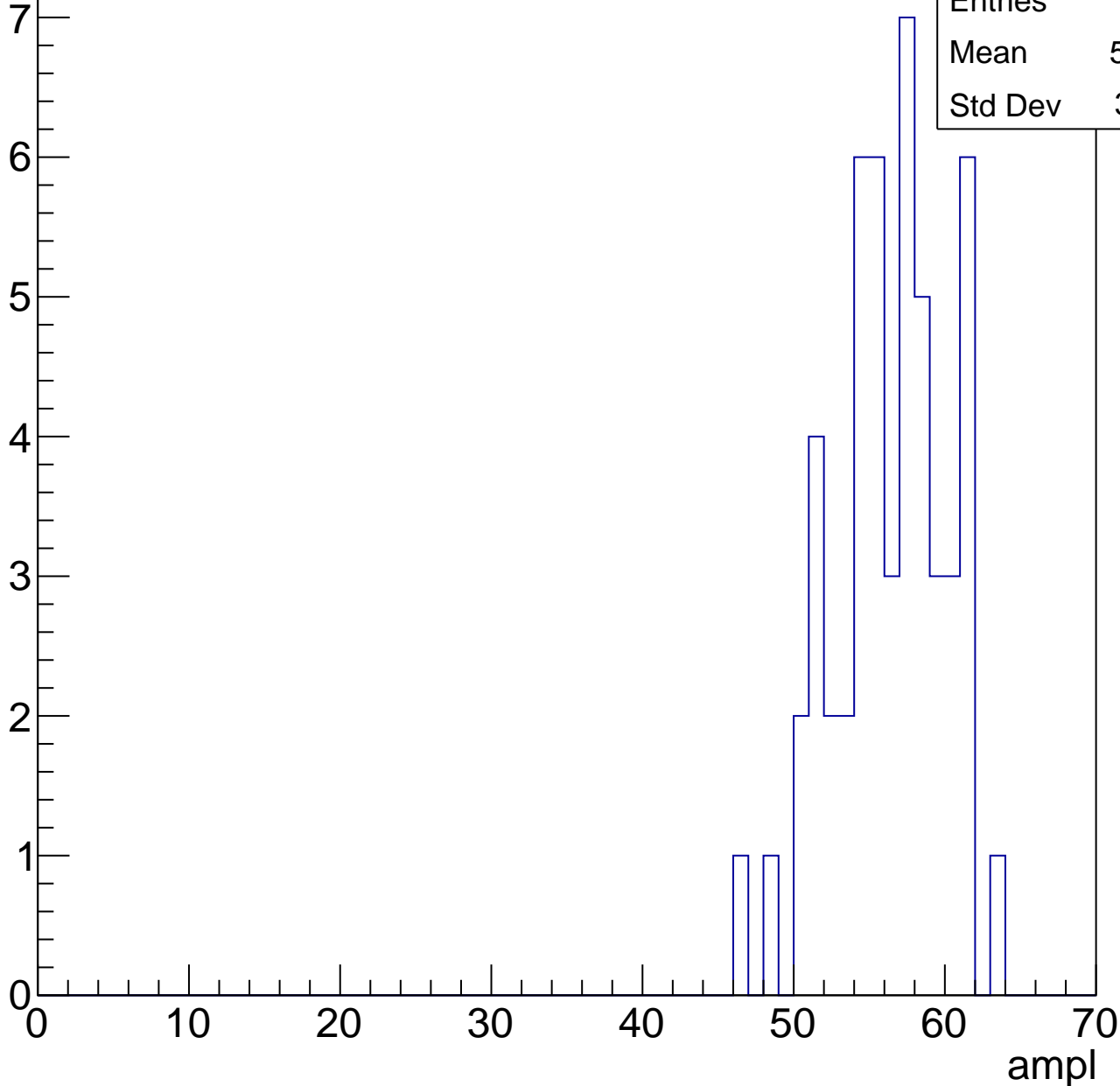


B1L103S, U24-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.87
Std Dev	3.711

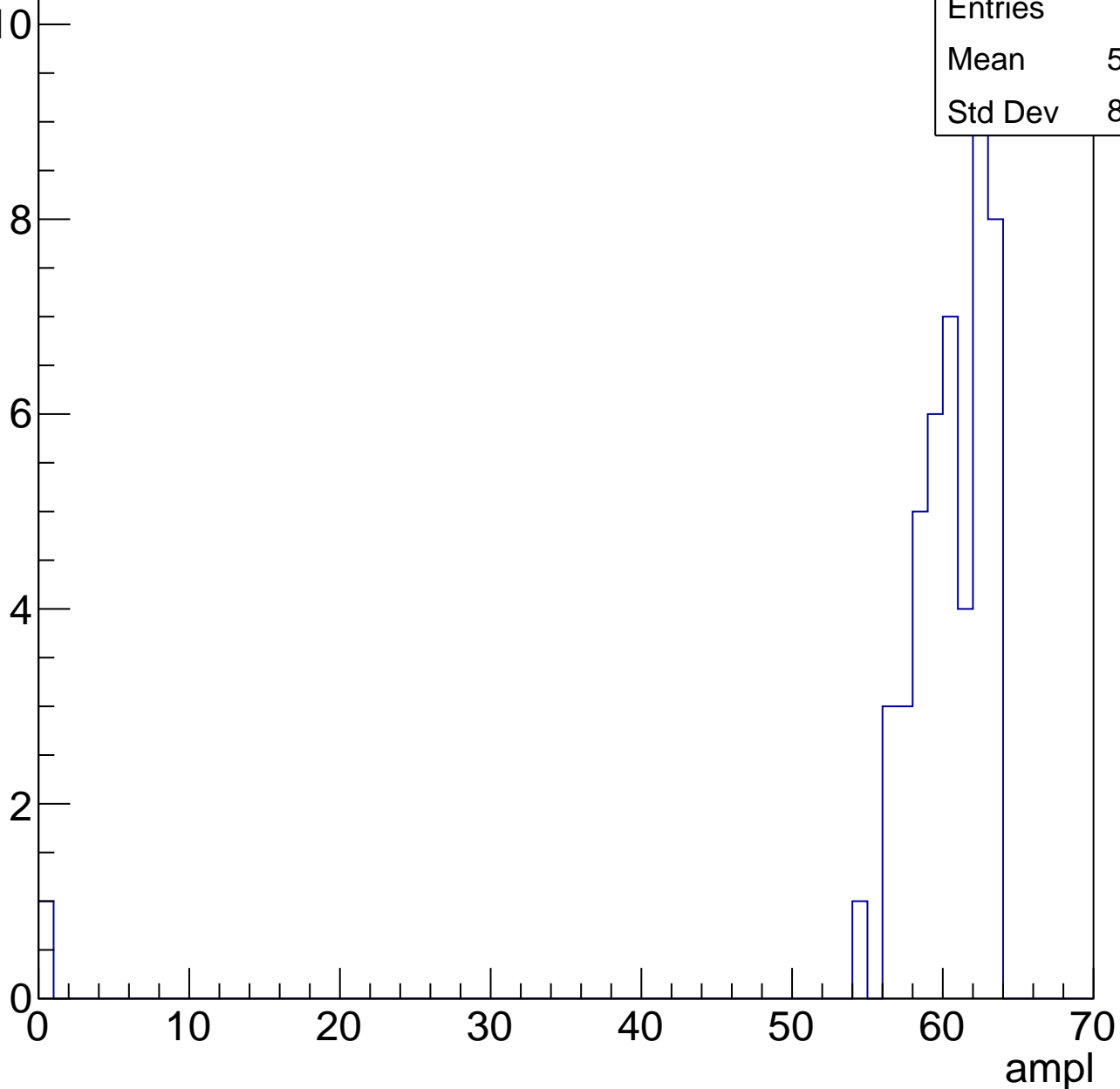


B1L103S, U24-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.85
Std Dev	8.886



B1L103S, U24-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U24-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch36, adc0

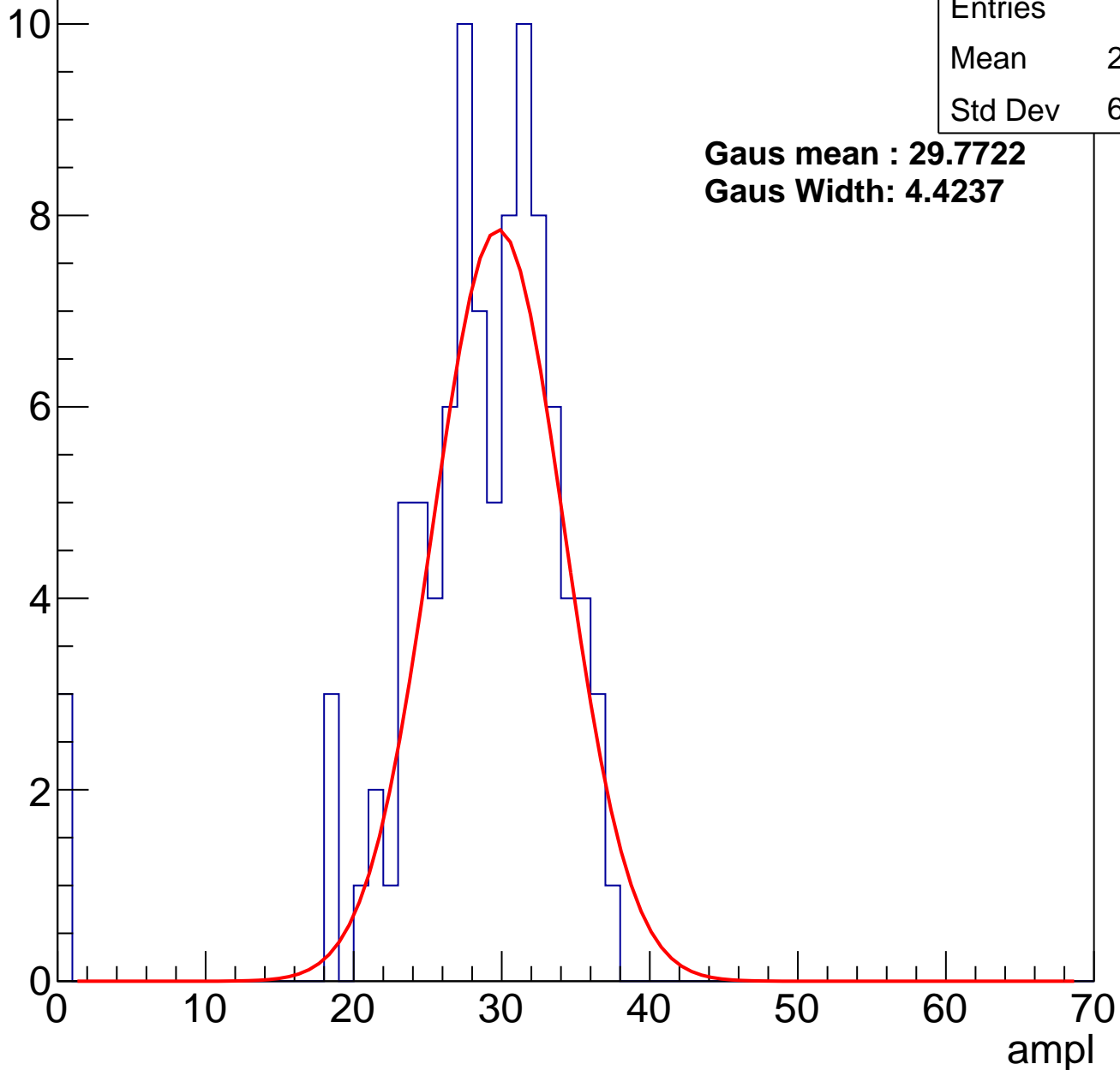
calib_packv5_042523_0143.root, FC#7, port C2

Entries	96
Mean	27.76
Std Dev	6.565

Gaus mean : 29.7722

Gaus Width: 4.4237

Entry



B1L103S, U24-ch36, adc1

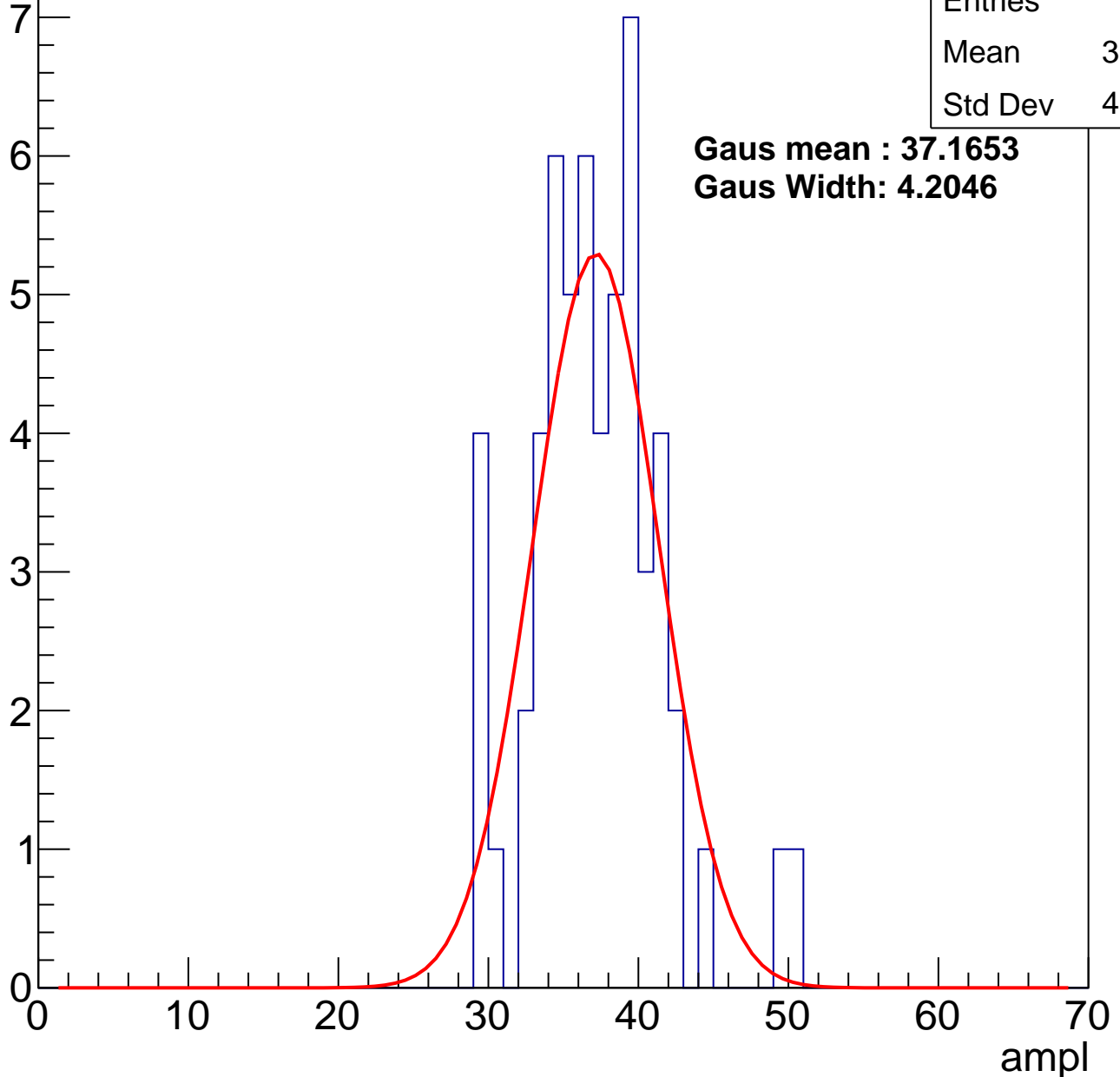
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.77
Std Dev	4.293

Gaus mean : 37.1653

Gaus Width: 4.2046

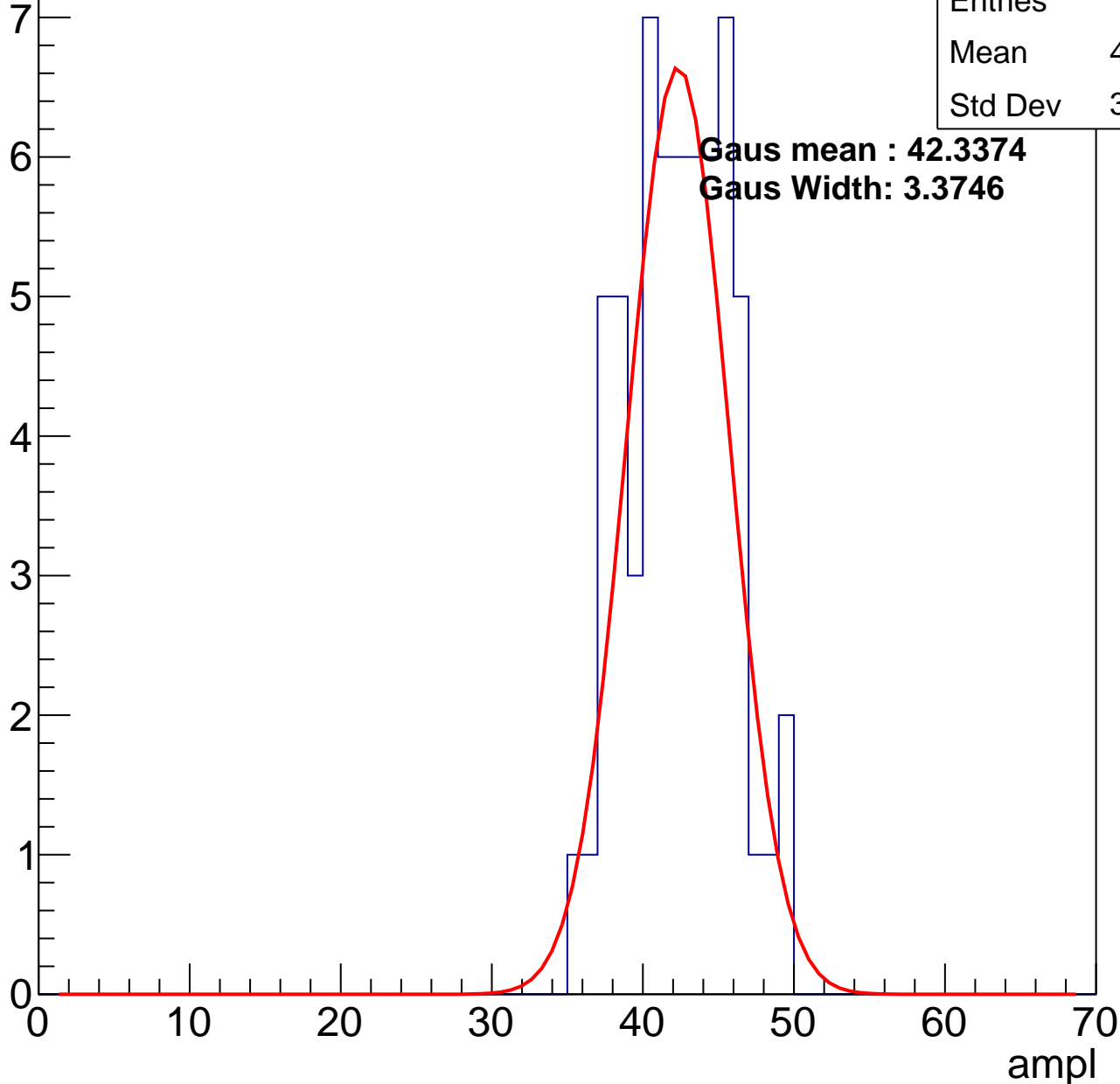


B1L103S, U24-ch36, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.95
Std Dev	3.319

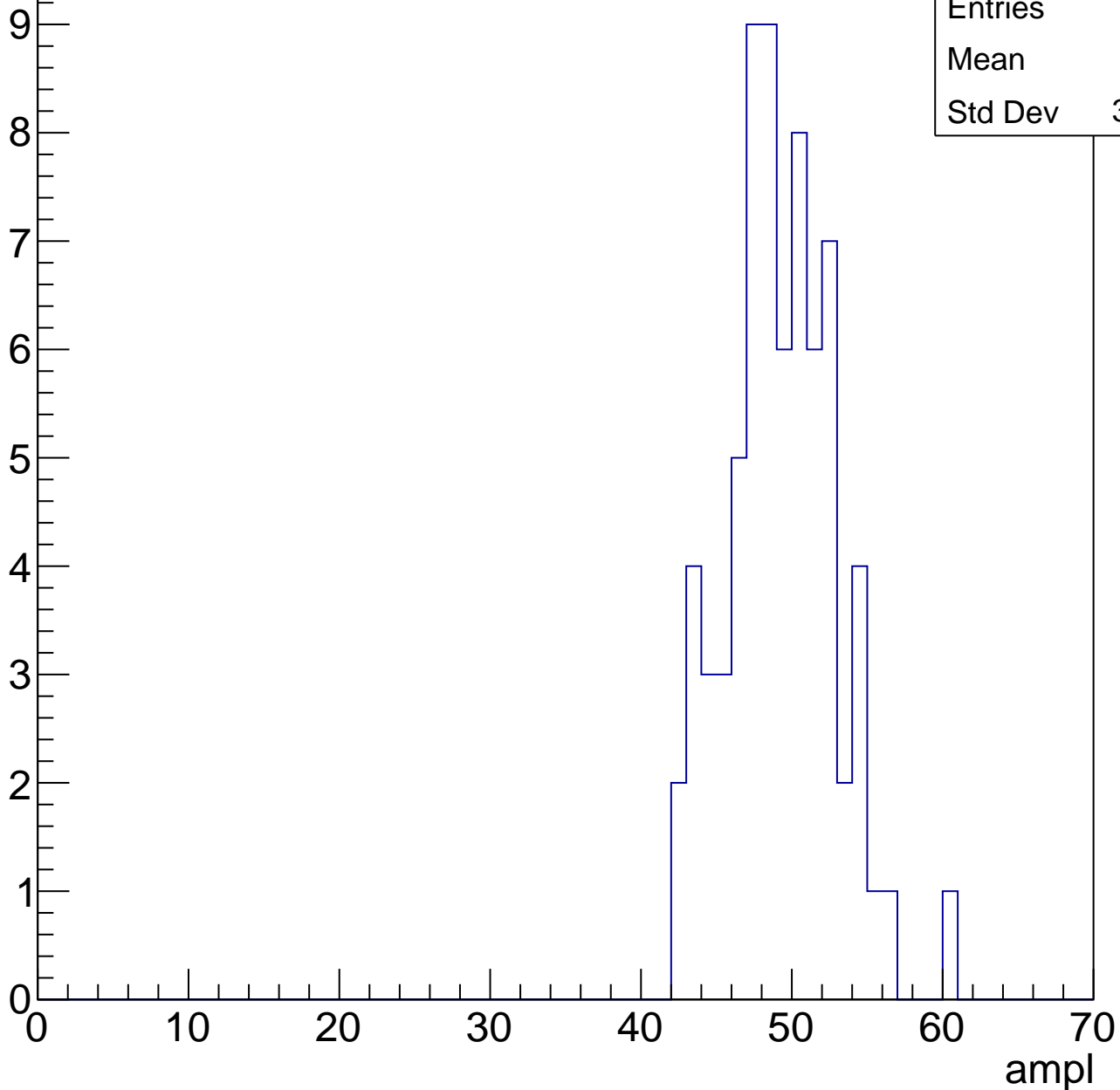


B1L103S, U24-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.8
Std Dev	3.531

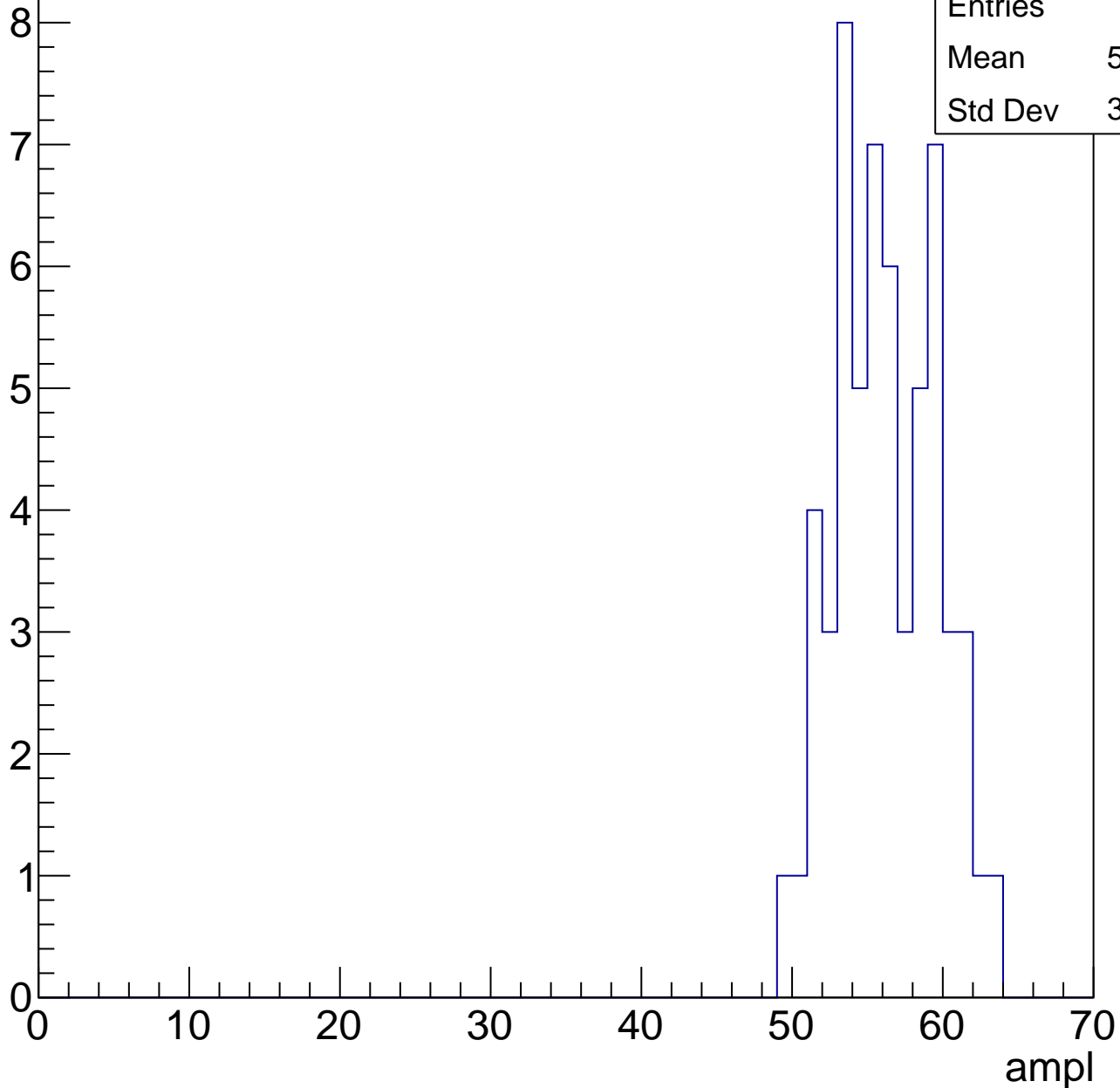


B1L103S, U24-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

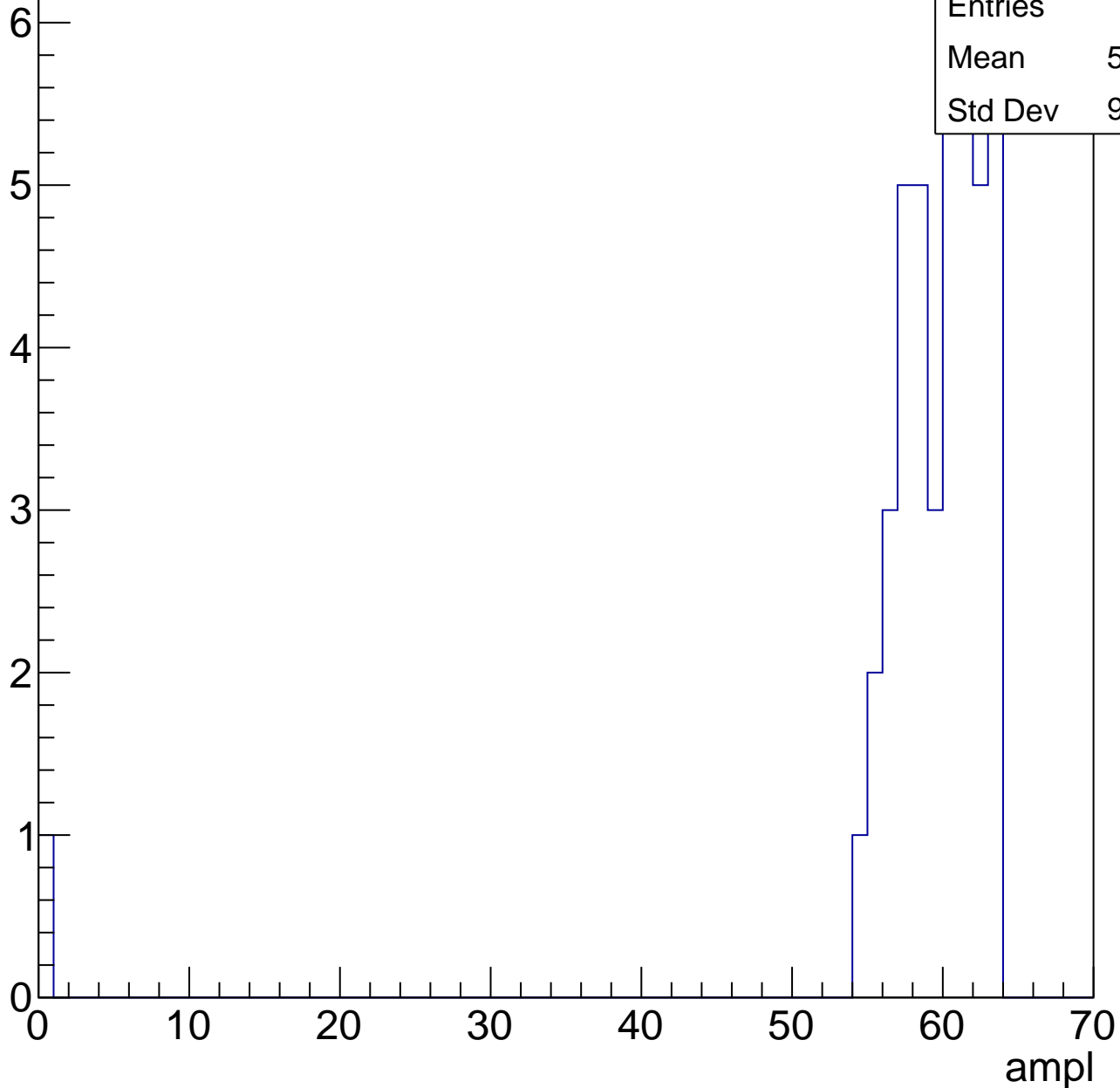
Entries	58
Mean	55.79
Std Dev	3.279



B1L103S, U24-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

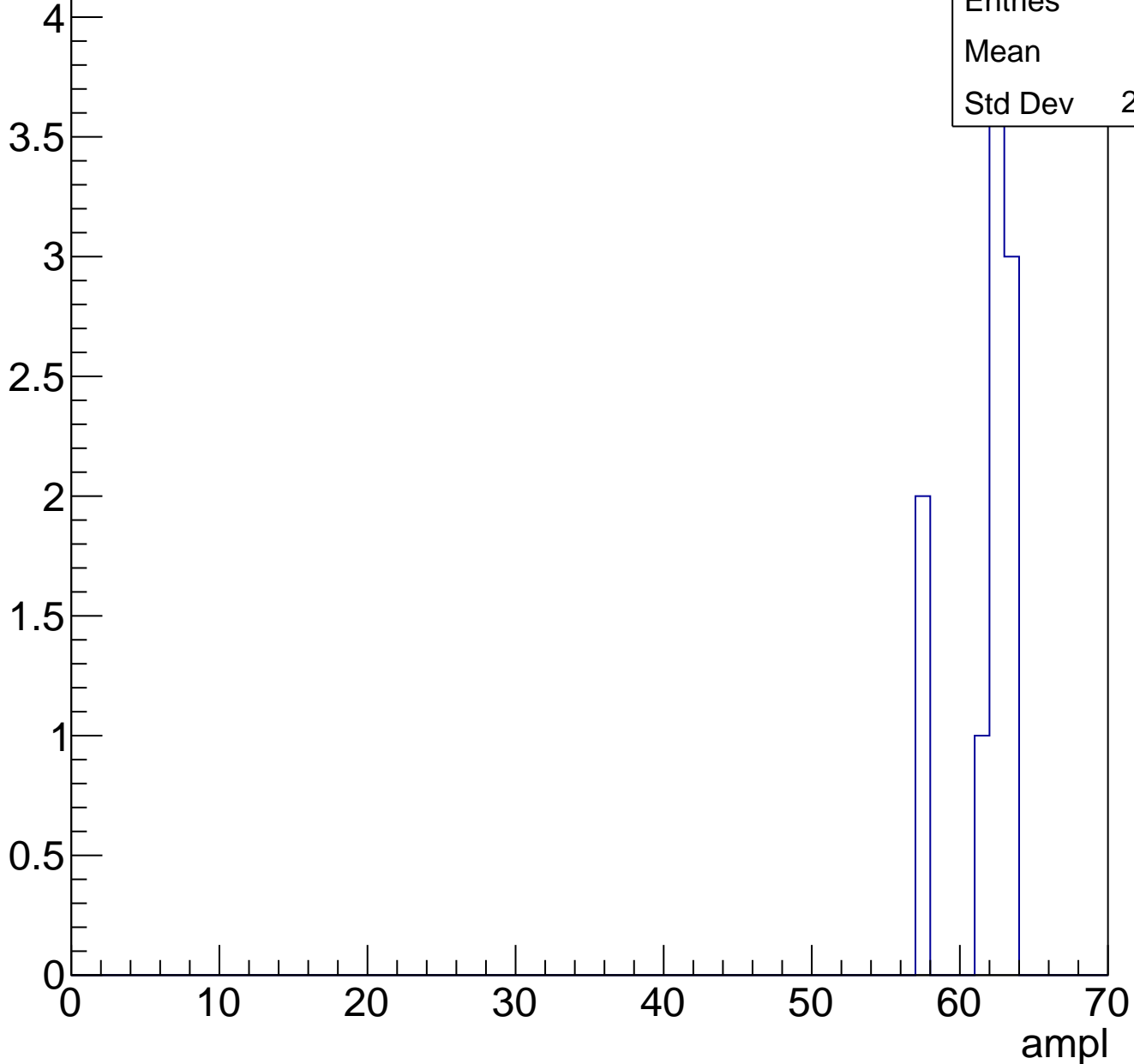


Entries	43
Mean	58.09
Std Dev	9.308

B1L103S, U24-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

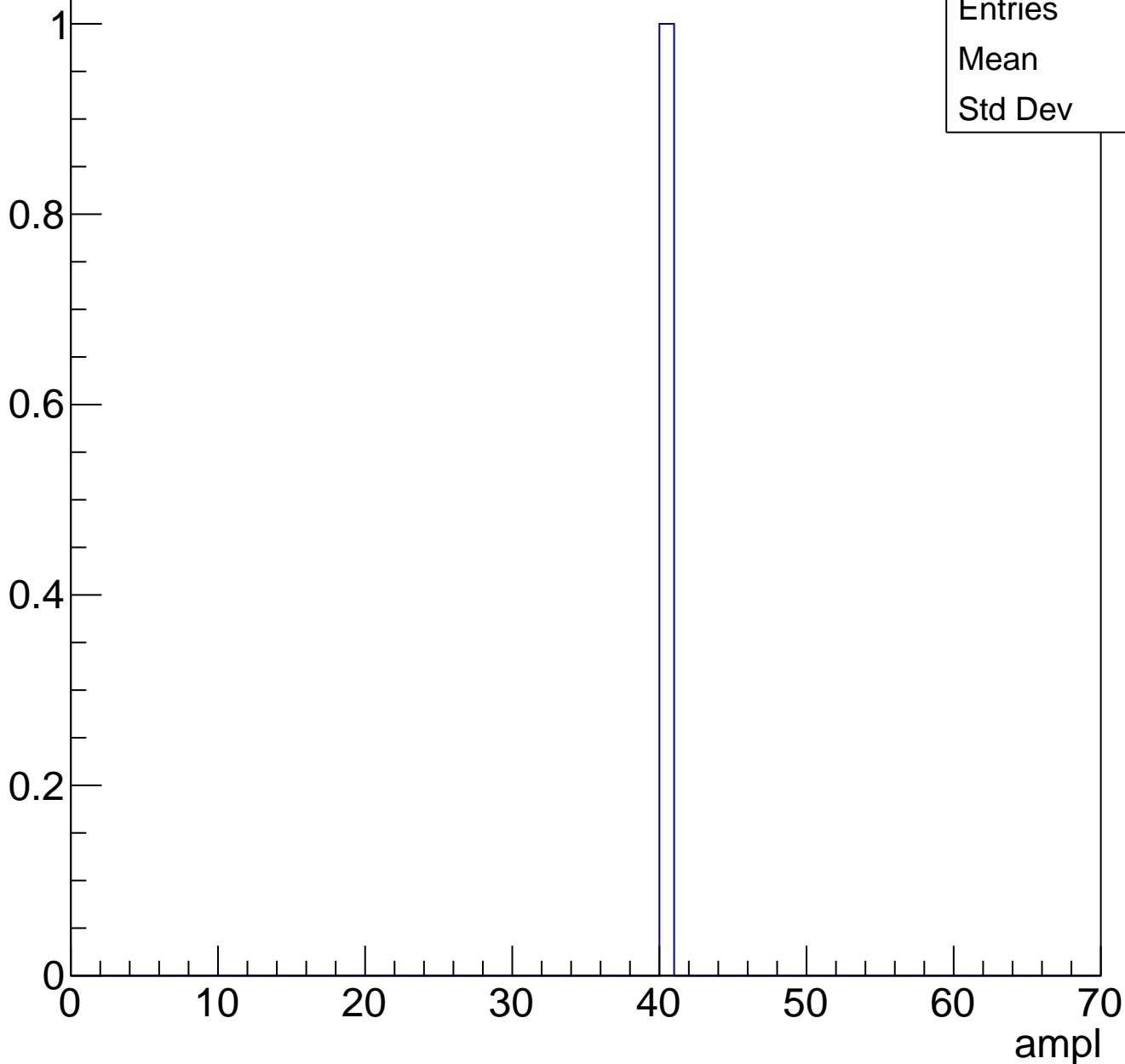


Entries	10
Mean	61.2
Std Dev	2.182

B1L103S, U24-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch37, adc0

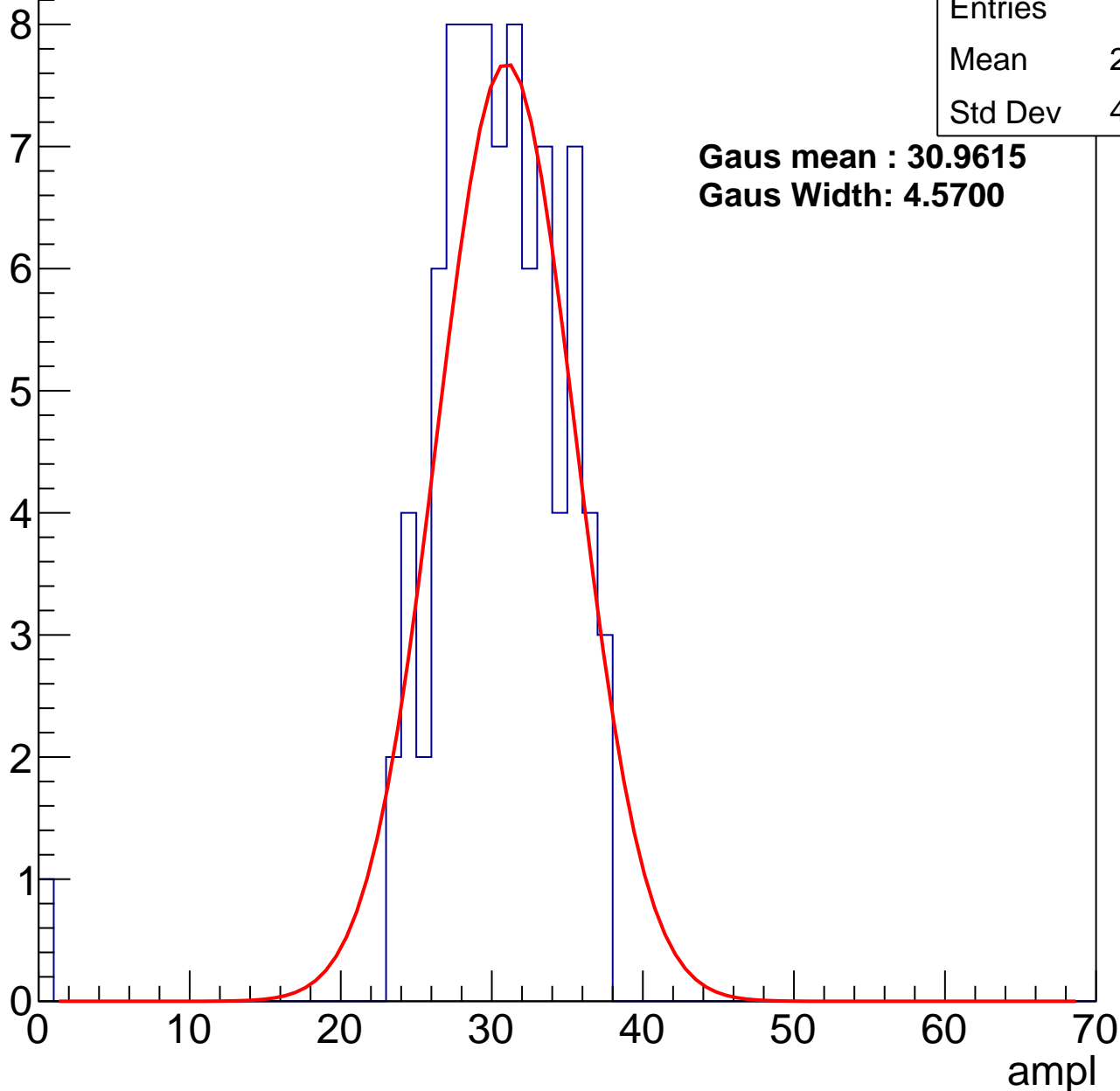
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	29.85
Std Dev	4.879

Gaus mean : 30.9615

Gaus Width: 4.5700



B1L103S, U24-ch37, adc1

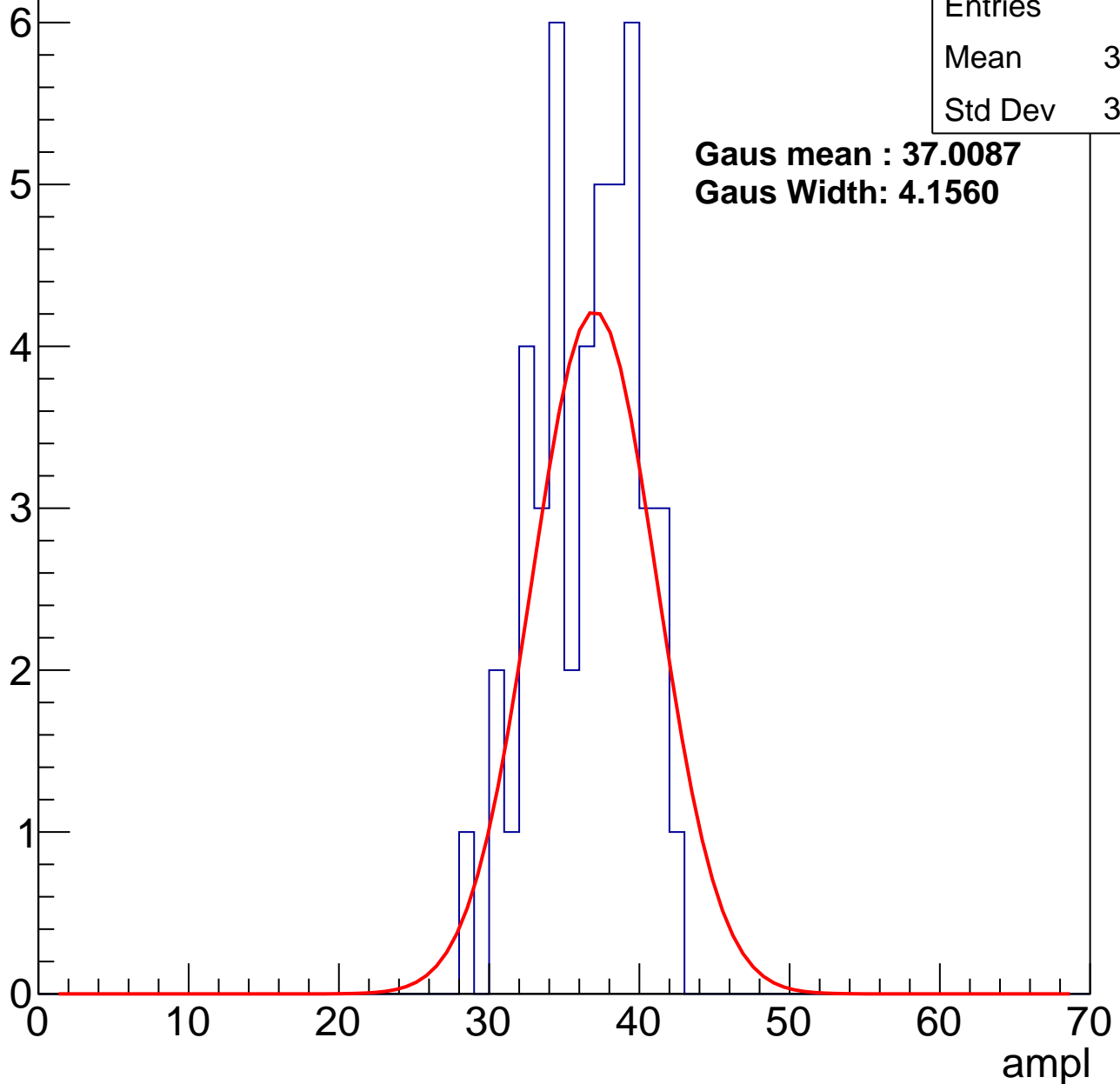
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	36.04
Std Dev	3.349

Gaus mean : 37.0087

Gaus Width: 4.1560



B1L103S, U24-ch37, adc2

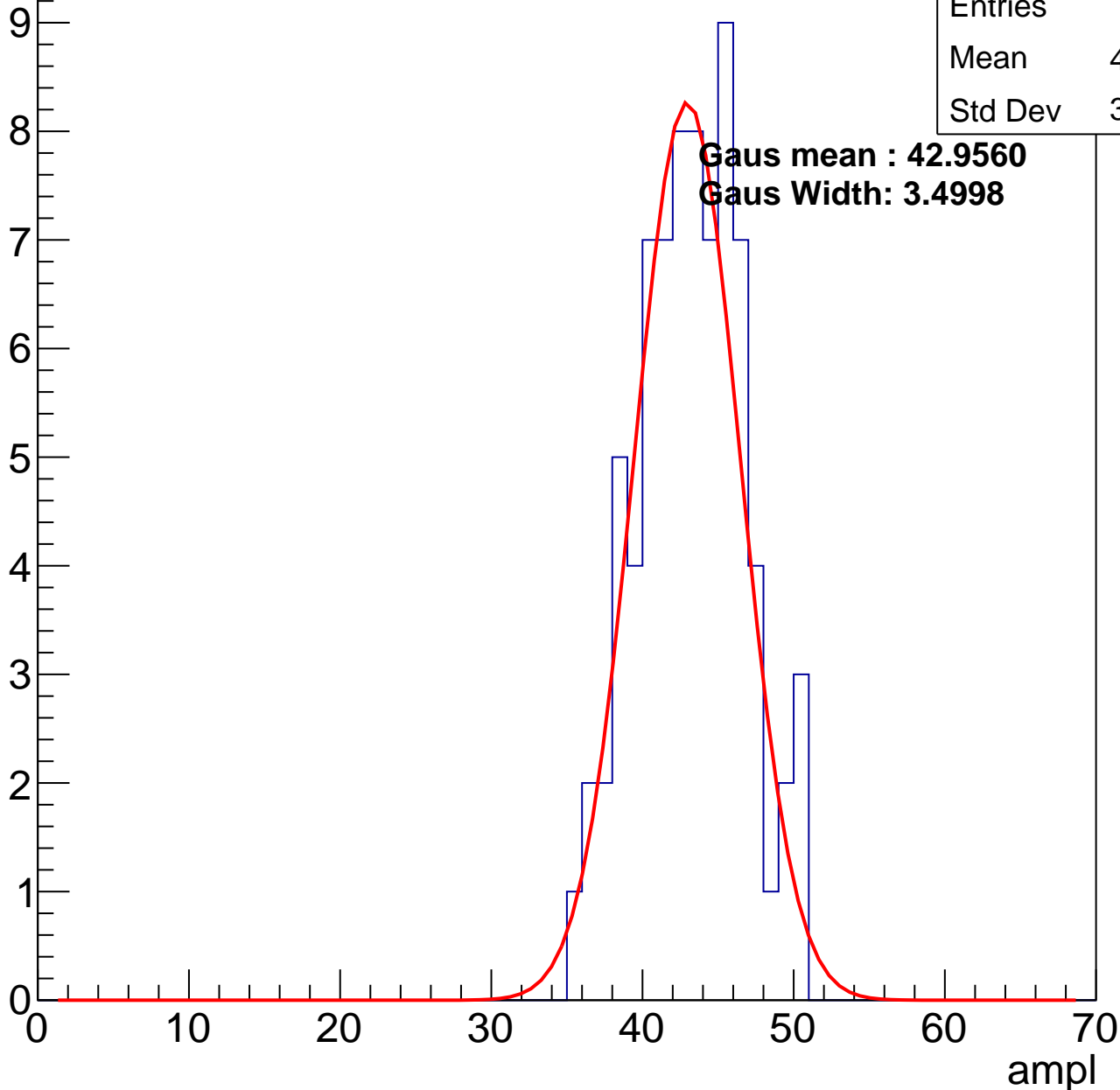
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	42.77
Std Dev	3.479

Gaus mean : 42.9560

Gaus Width: 3.4998

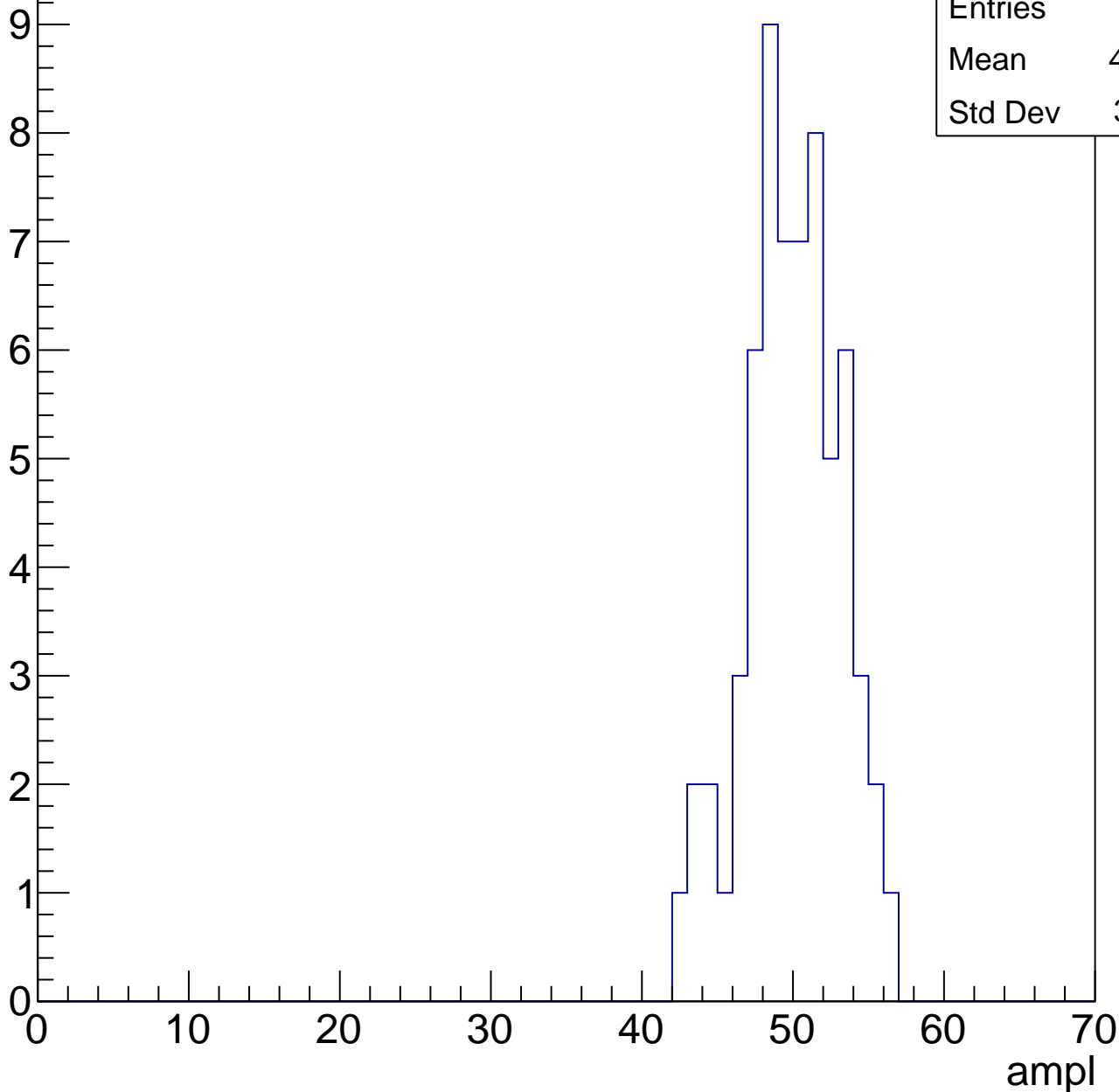


B1L103S, U24-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.52
Std Dev	3.101

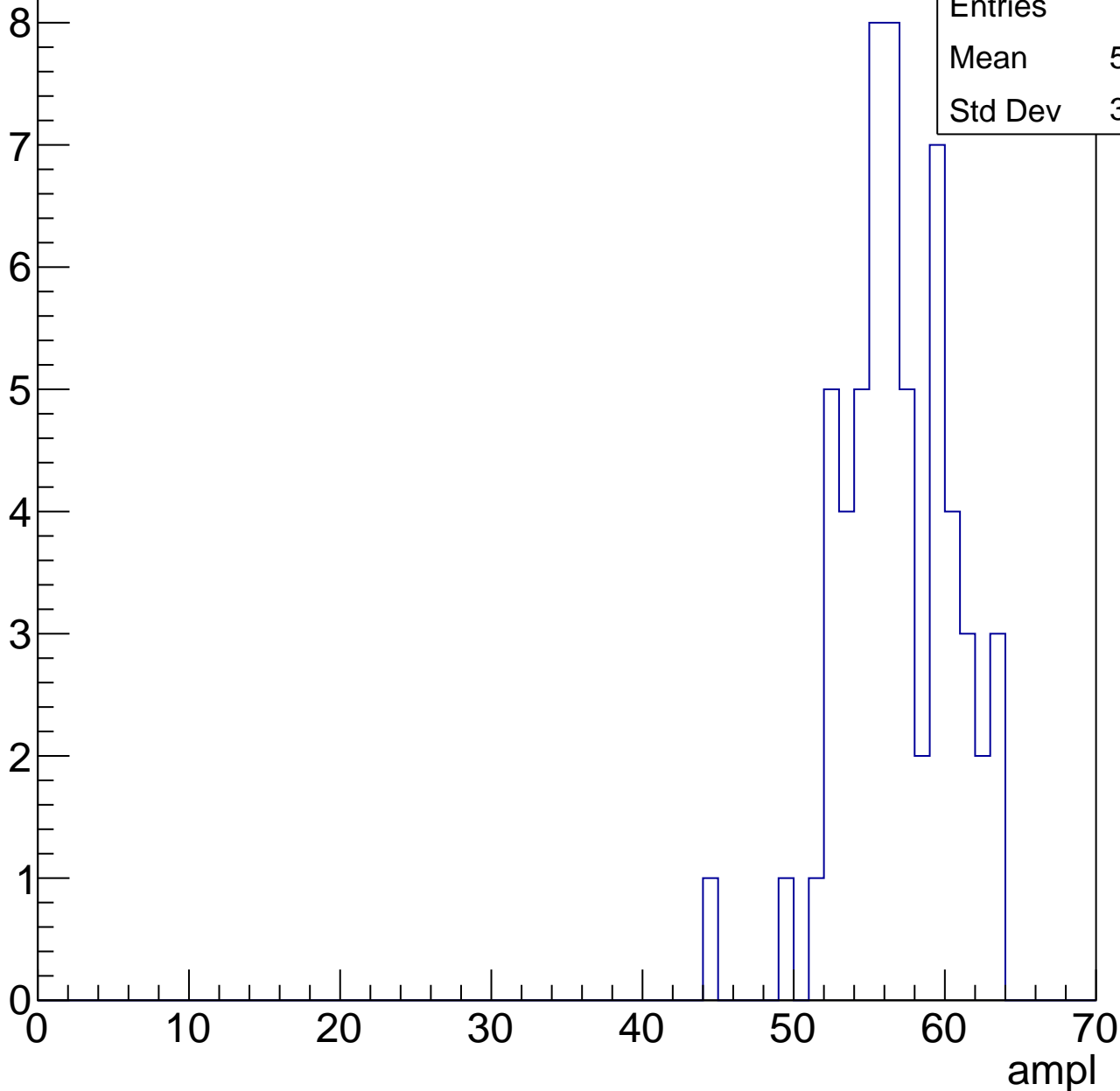


B1L103S, U24-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	56.34
Std Dev	3.657

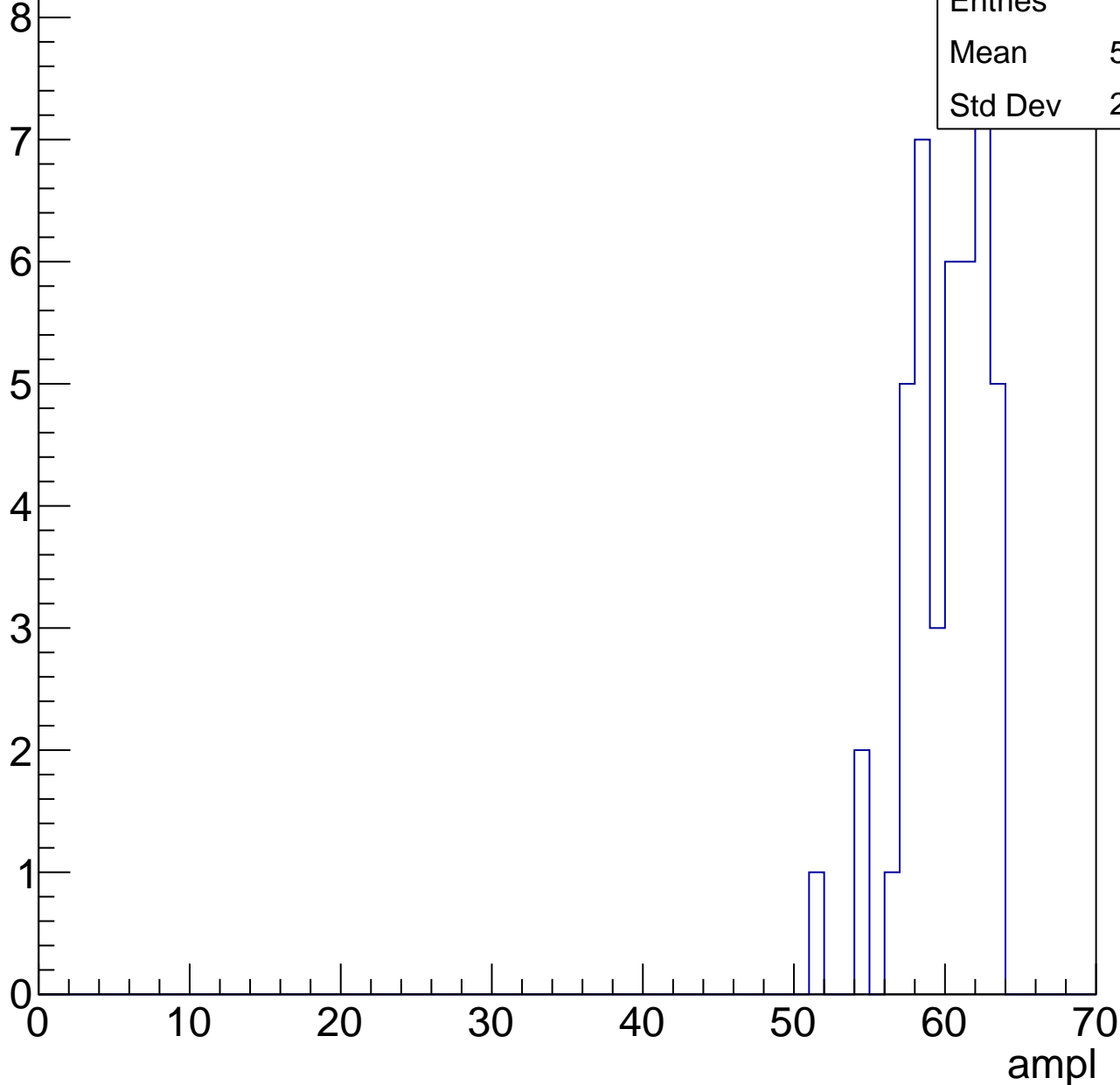


B1L103S, U24-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

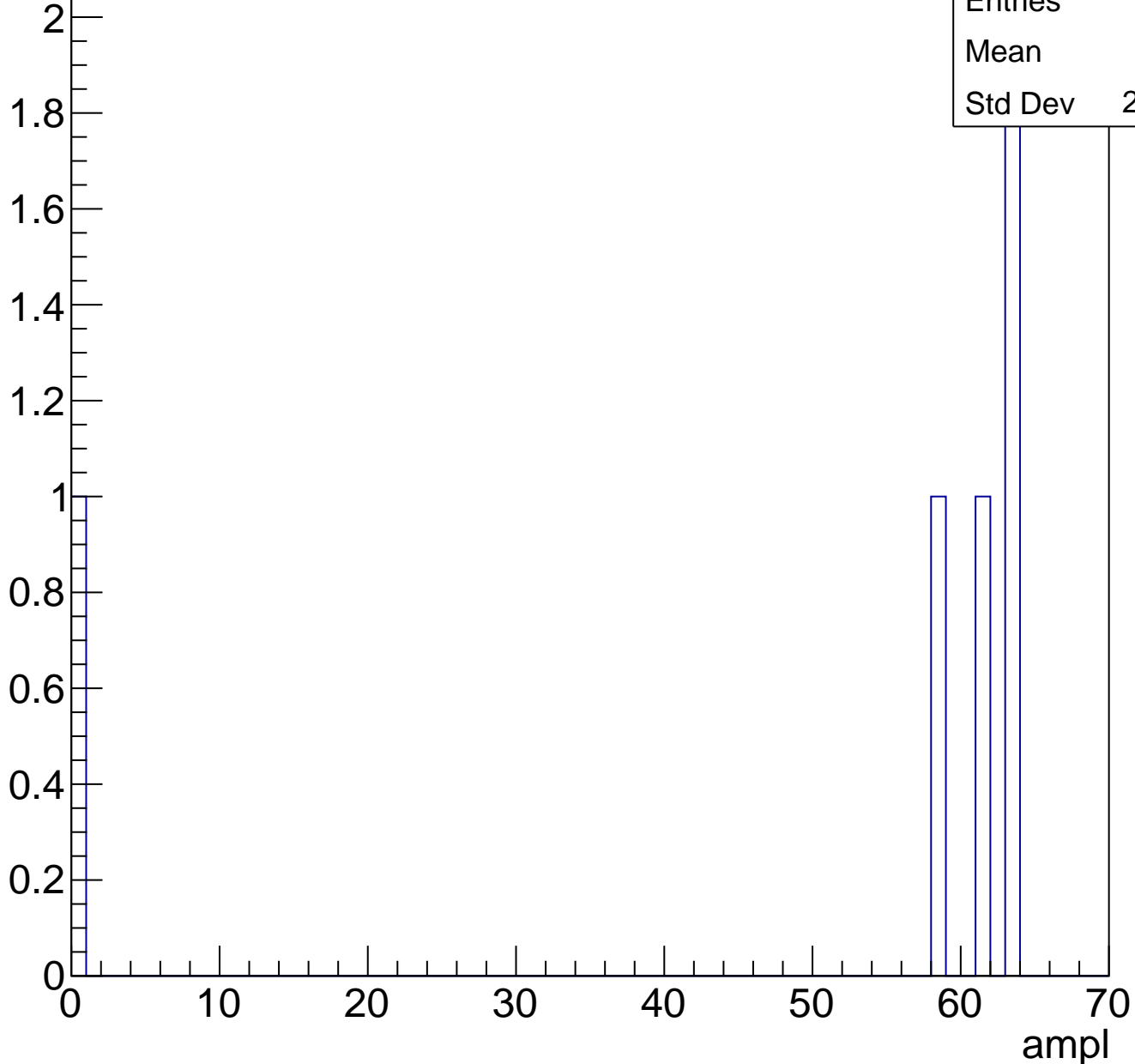
Entries	44
Mean	59.55
Std Dev	2.692



B1L103S, U24-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

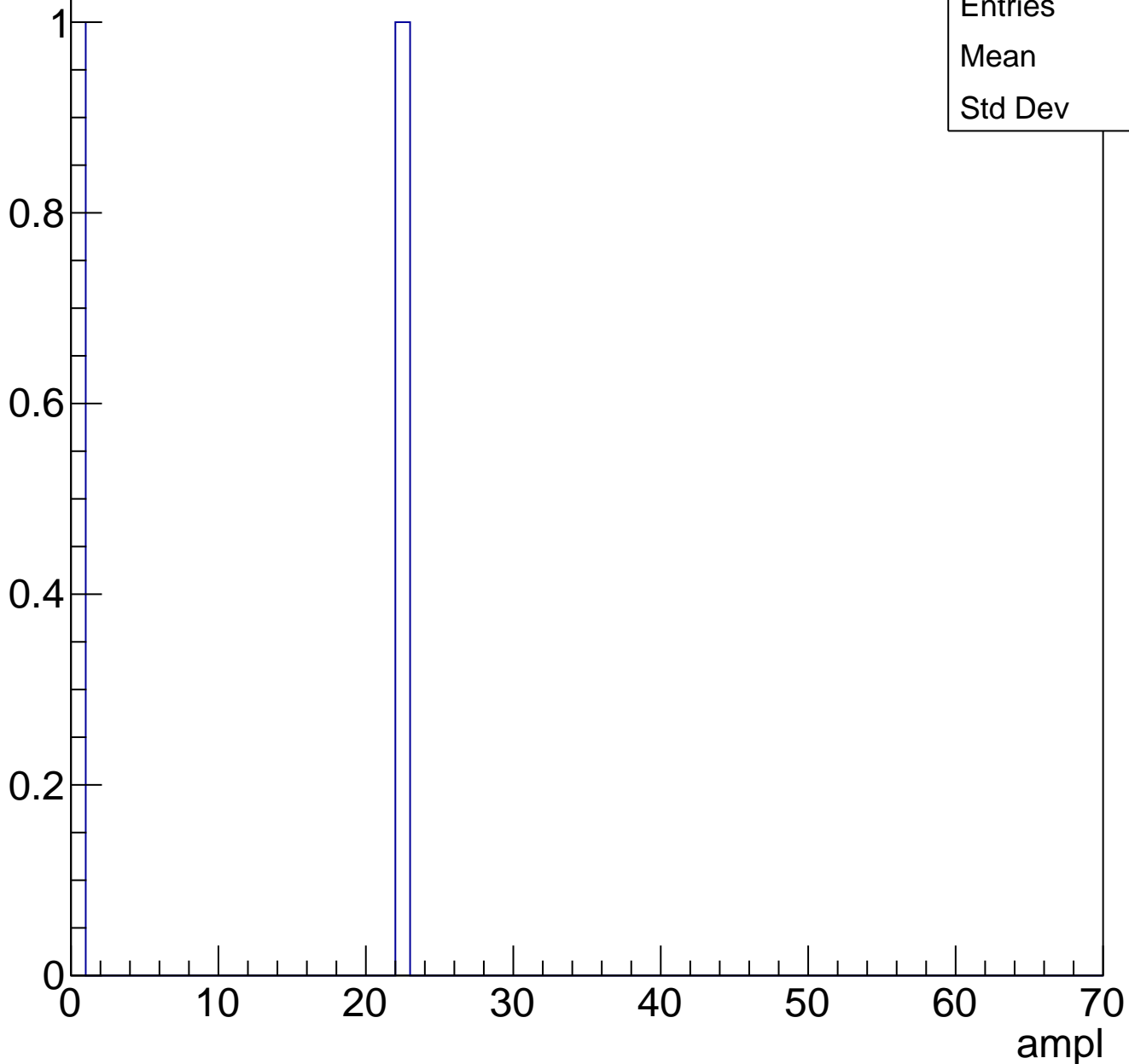


Entries	5
Mean	49
Std Dev	24.57

B1L103S, U24-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch38, adc0

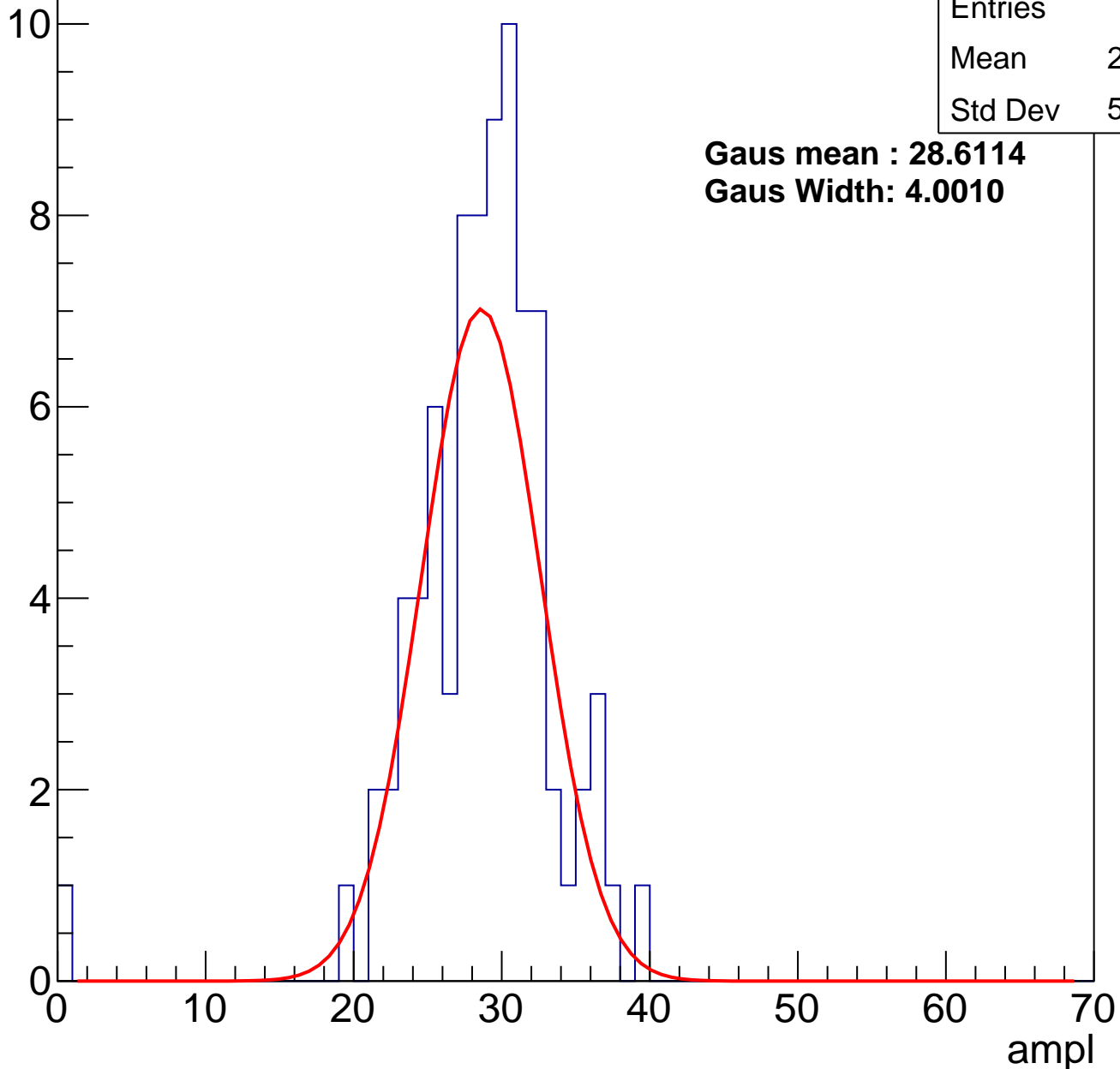
calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	28.26
Std Dev	5.024

Gaus mean : 28.6114

Gaus Width: 4.0010

Entry



B1L103S, U24-ch38, adc1

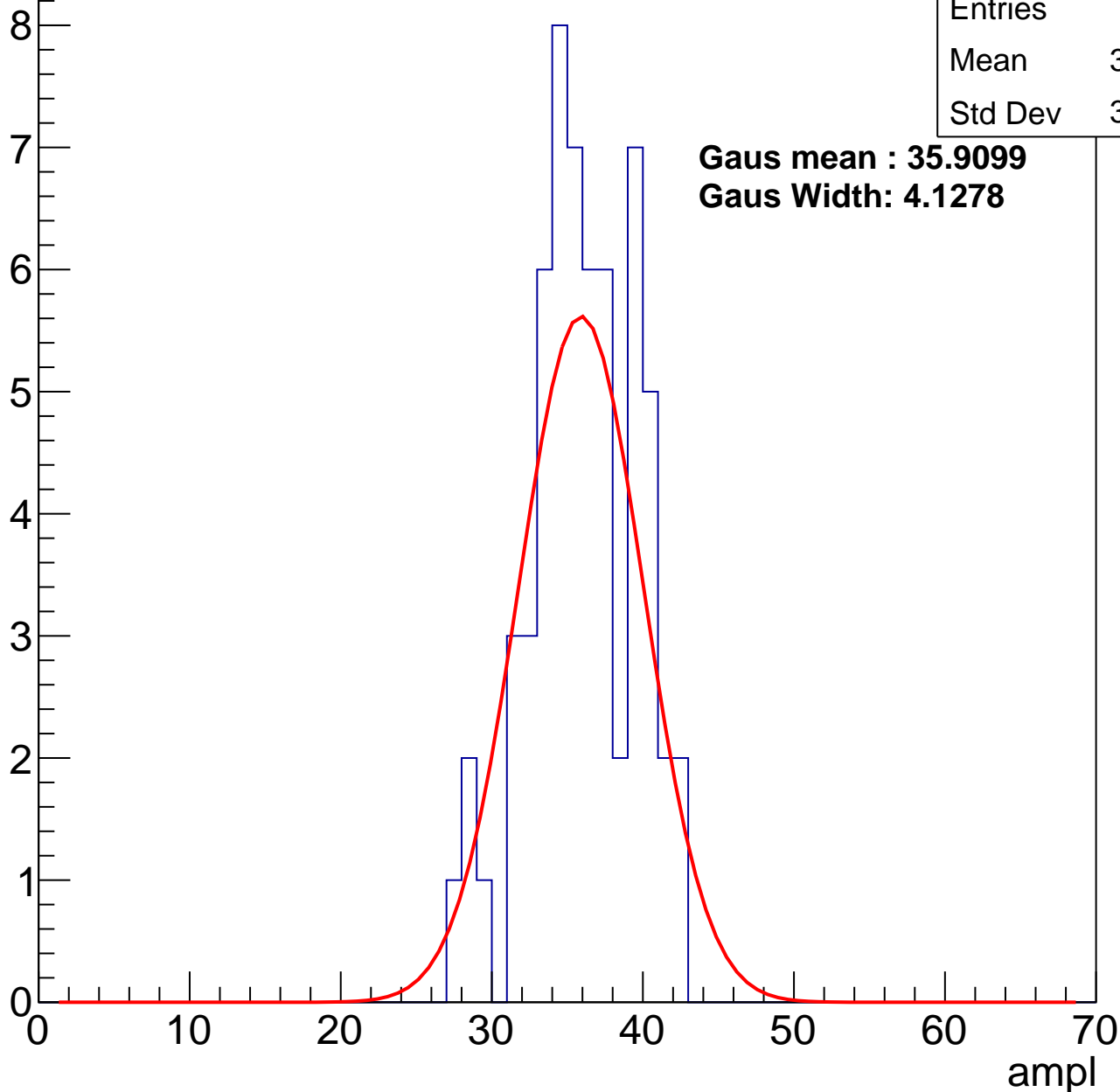
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.56
Std Dev	3.485

Gaus mean : 35.9099

Gaus Width: 4.1278



B1L103S, U24-ch38, adc2

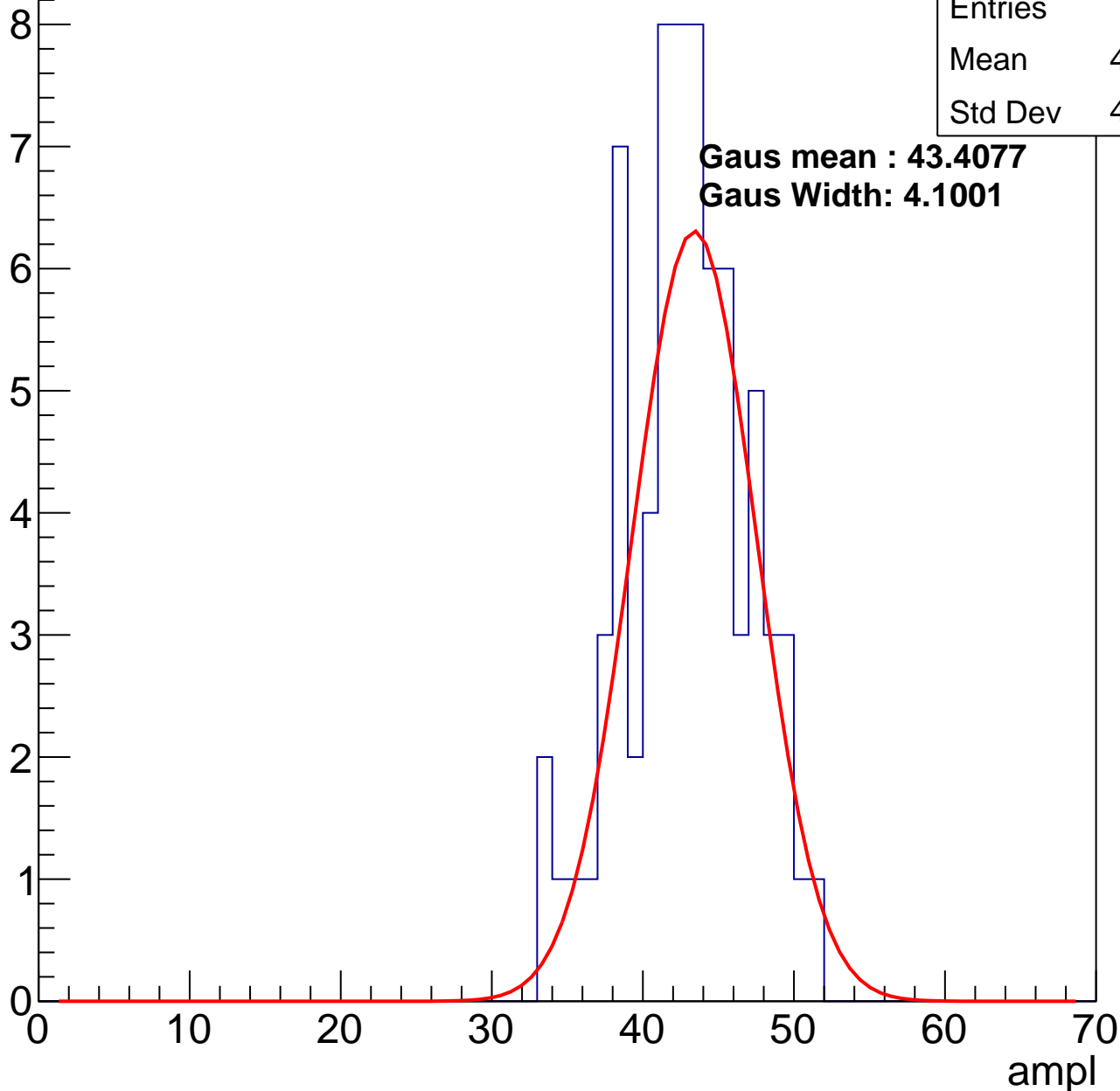
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.37
Std Dev	4.036

Gaus mean : 43.4077

Gaus Width: 4.1001

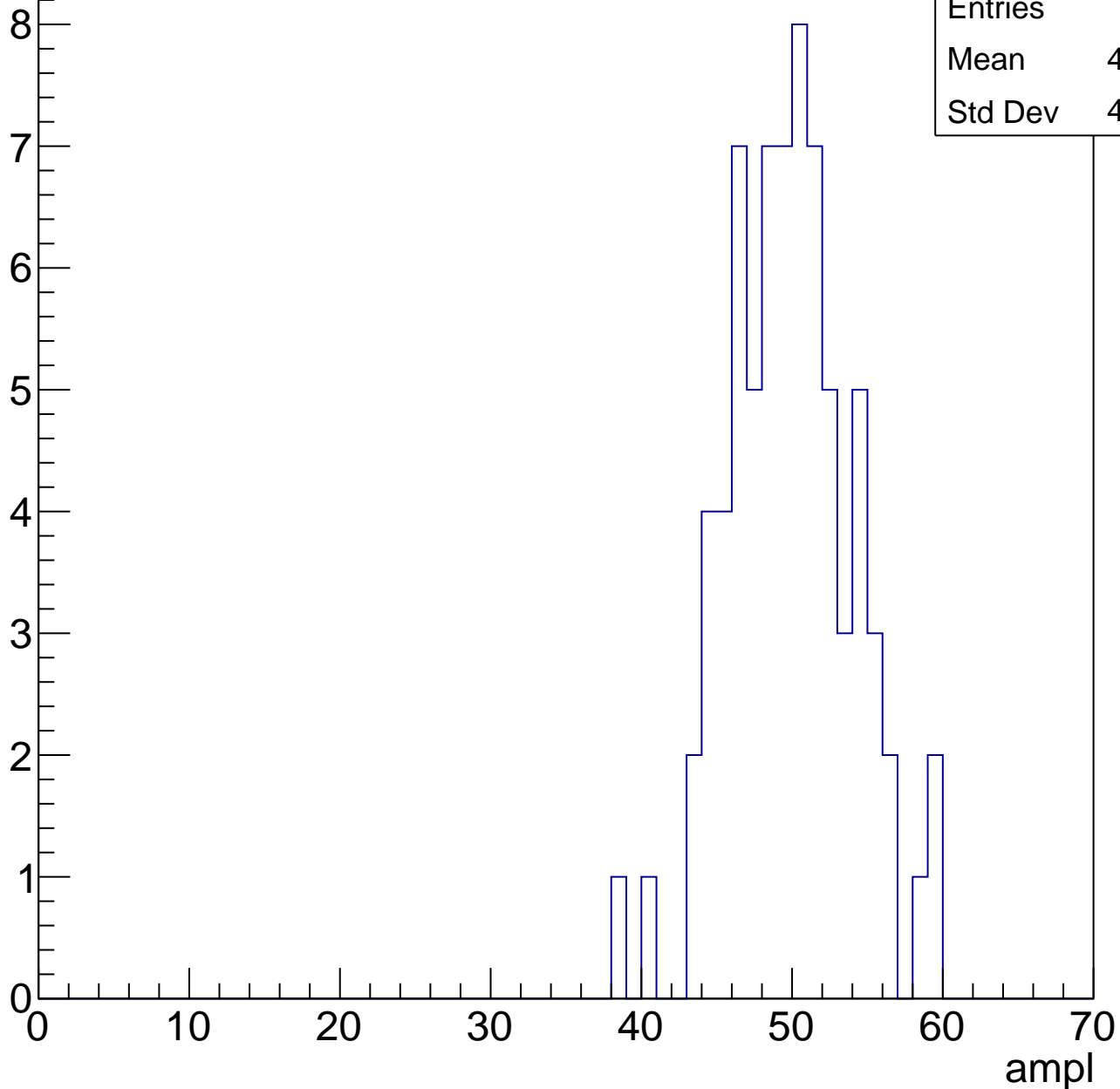


B1L103S, U24-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.39
Std Dev	4.123

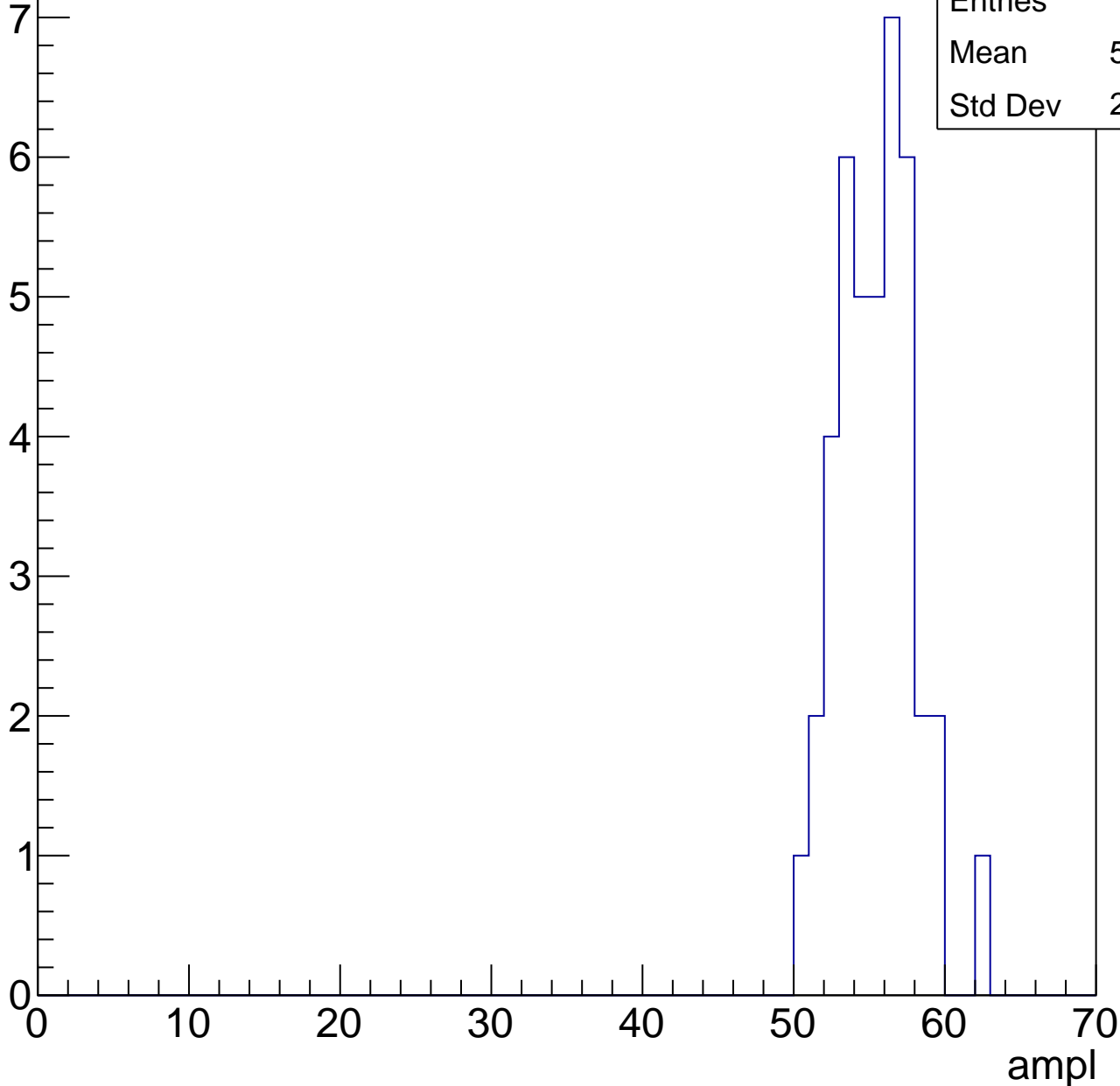


B1L103S, U24-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

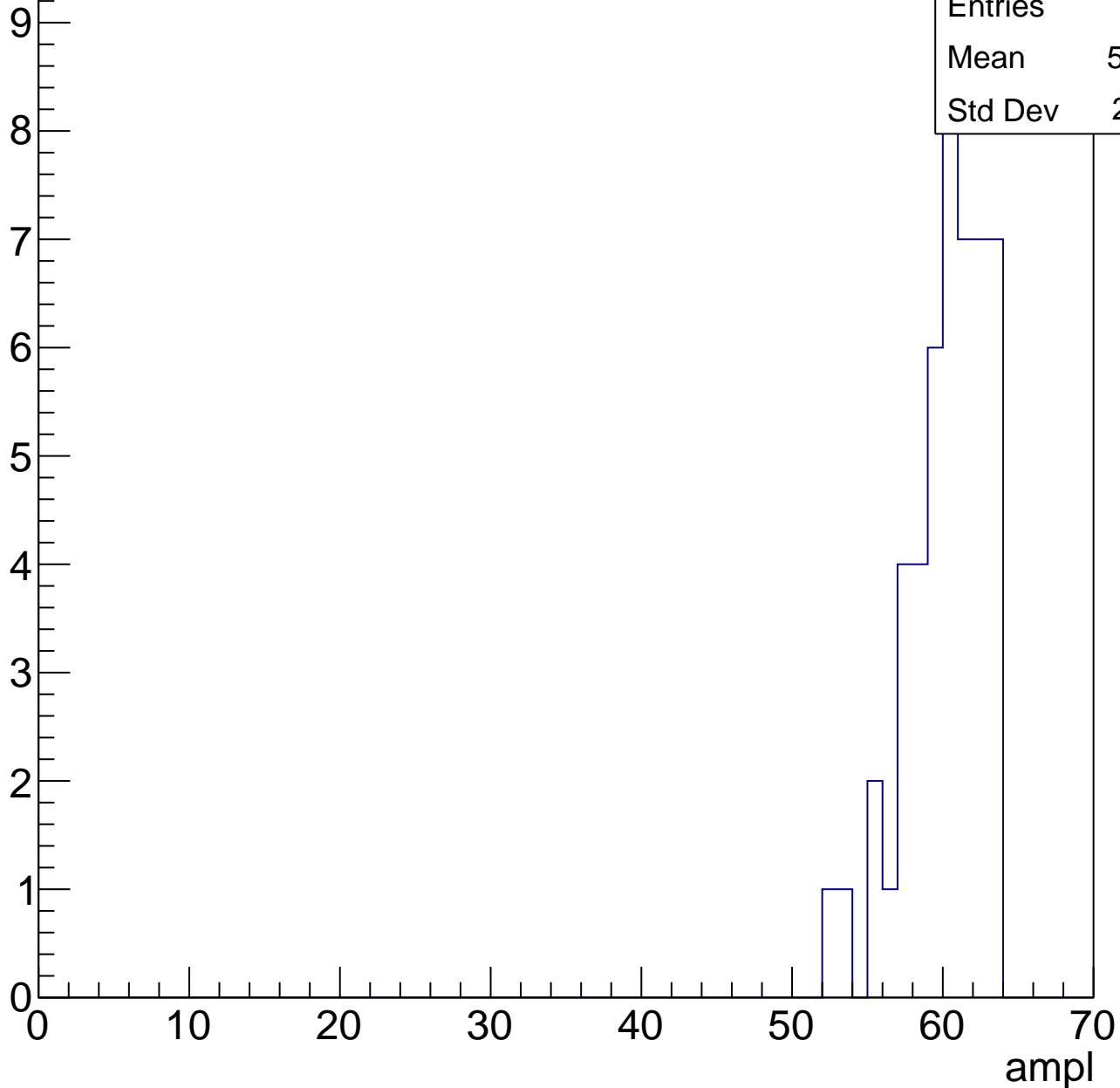
Entries	41
Mean	54.95
Std Dev	2.479



B1L103S, U24-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

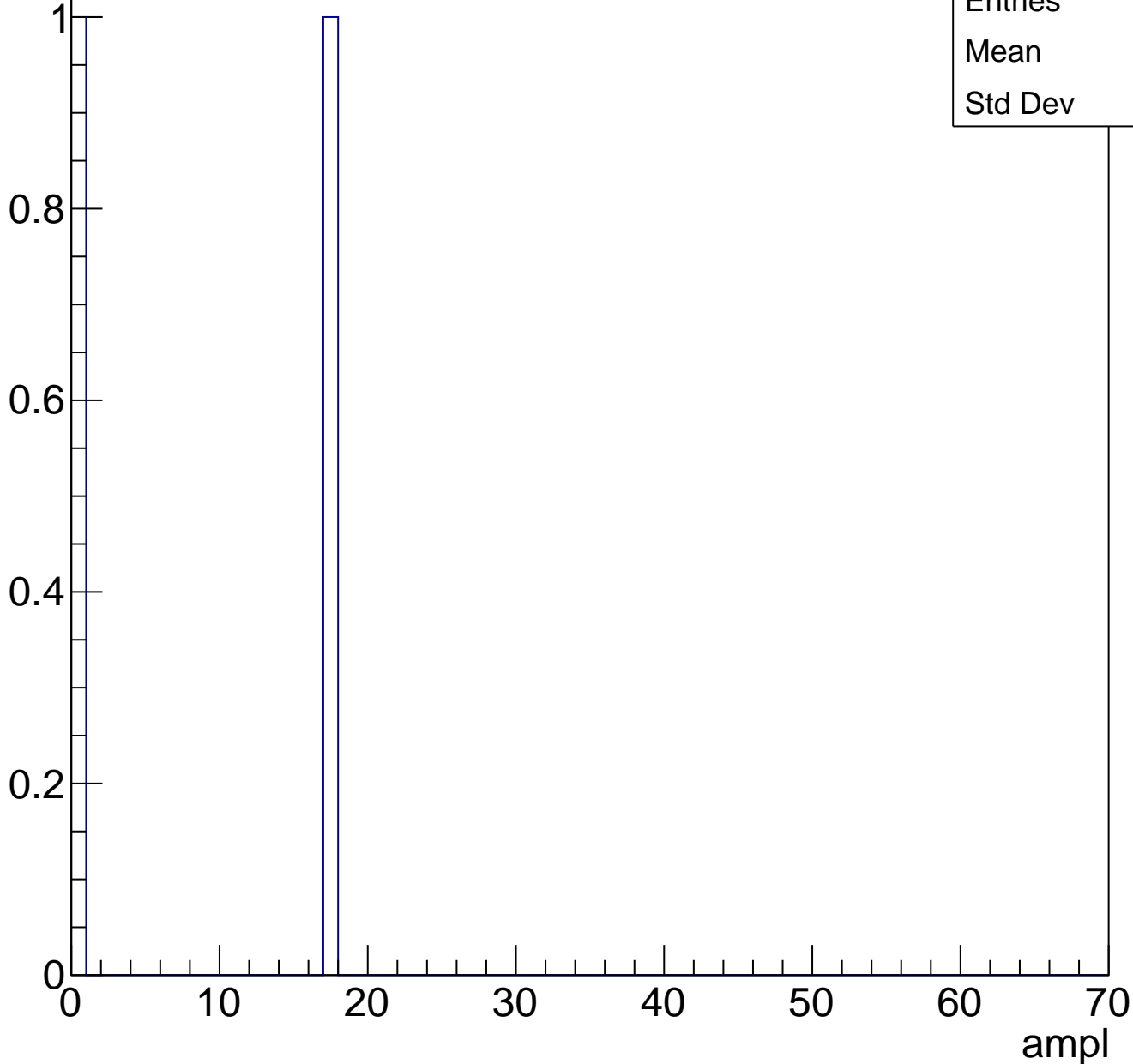
ampl

Entries	12
Mean	60.83
Std Dev	1.863

B1L103S, U24-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L103S, U24-ch39, adc0

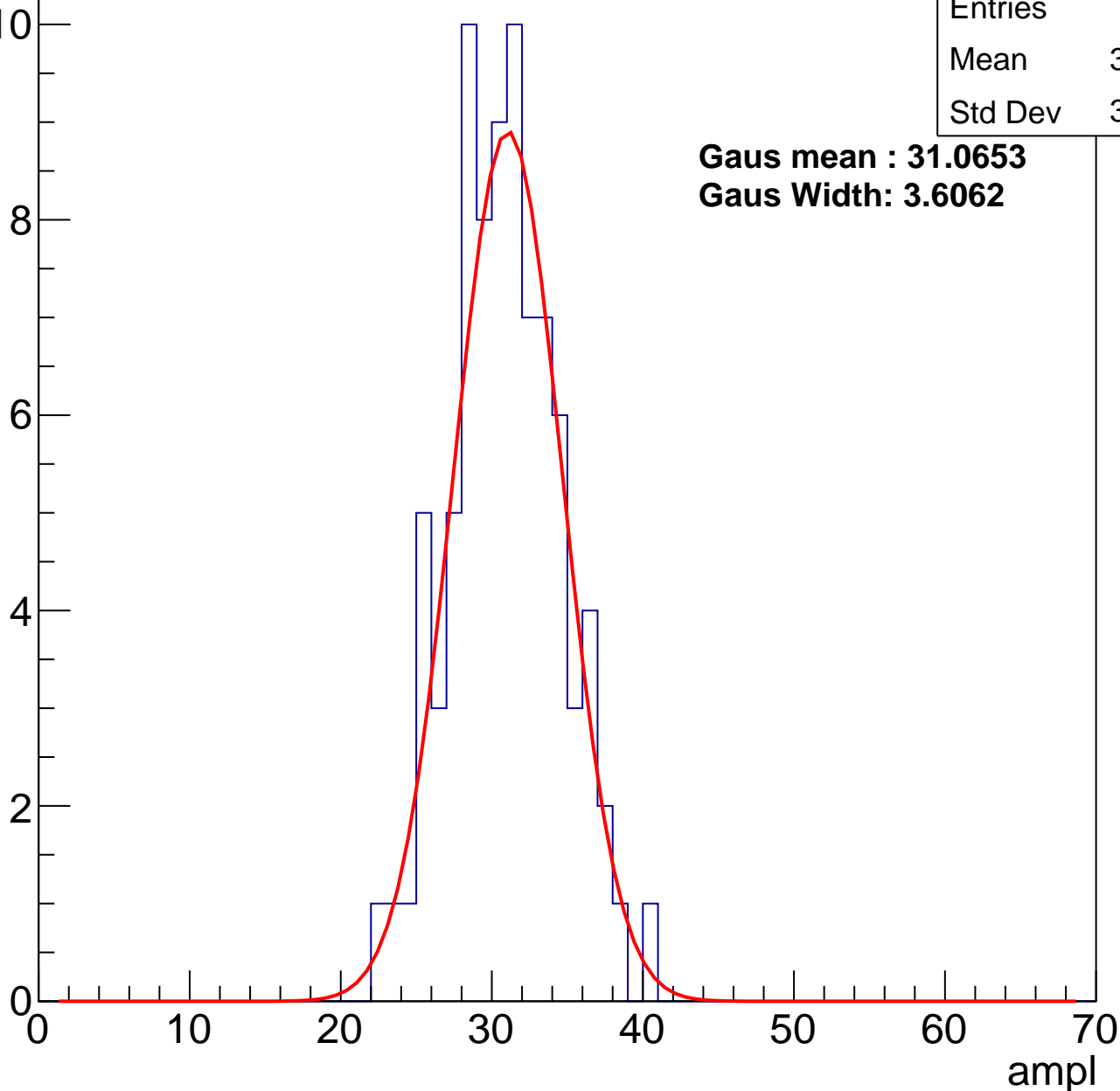
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	30.46
Std Dev	3.577

Gaus mean : 31.0653

Gaus Width: 3.6062



B1L103S, U24-ch39, adc1

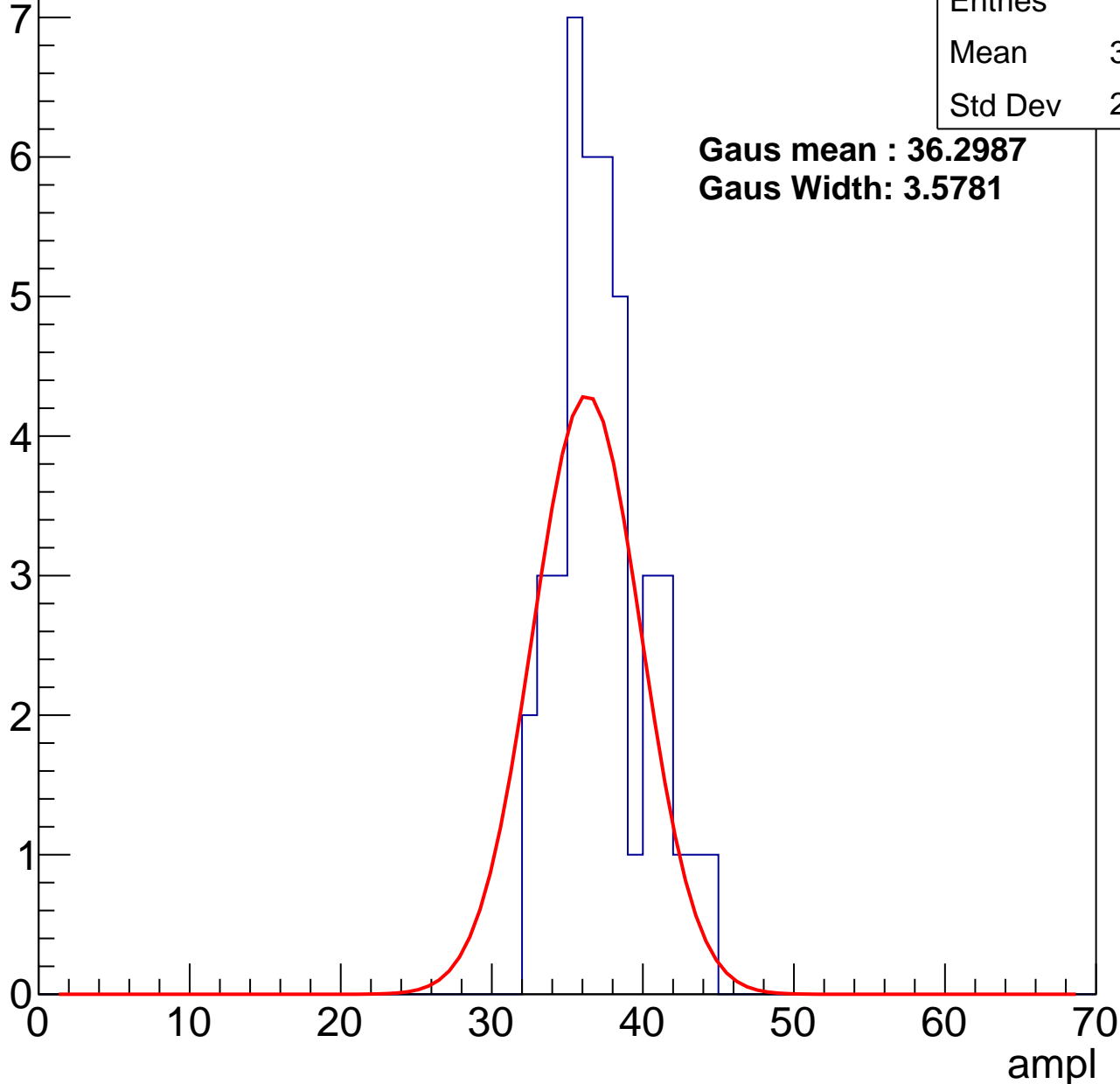
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	36.88
Std Dev	2.897

Gaus mean : 36.2987

Gaus Width: 3.5781



B1L103S, U24-ch39, adc2

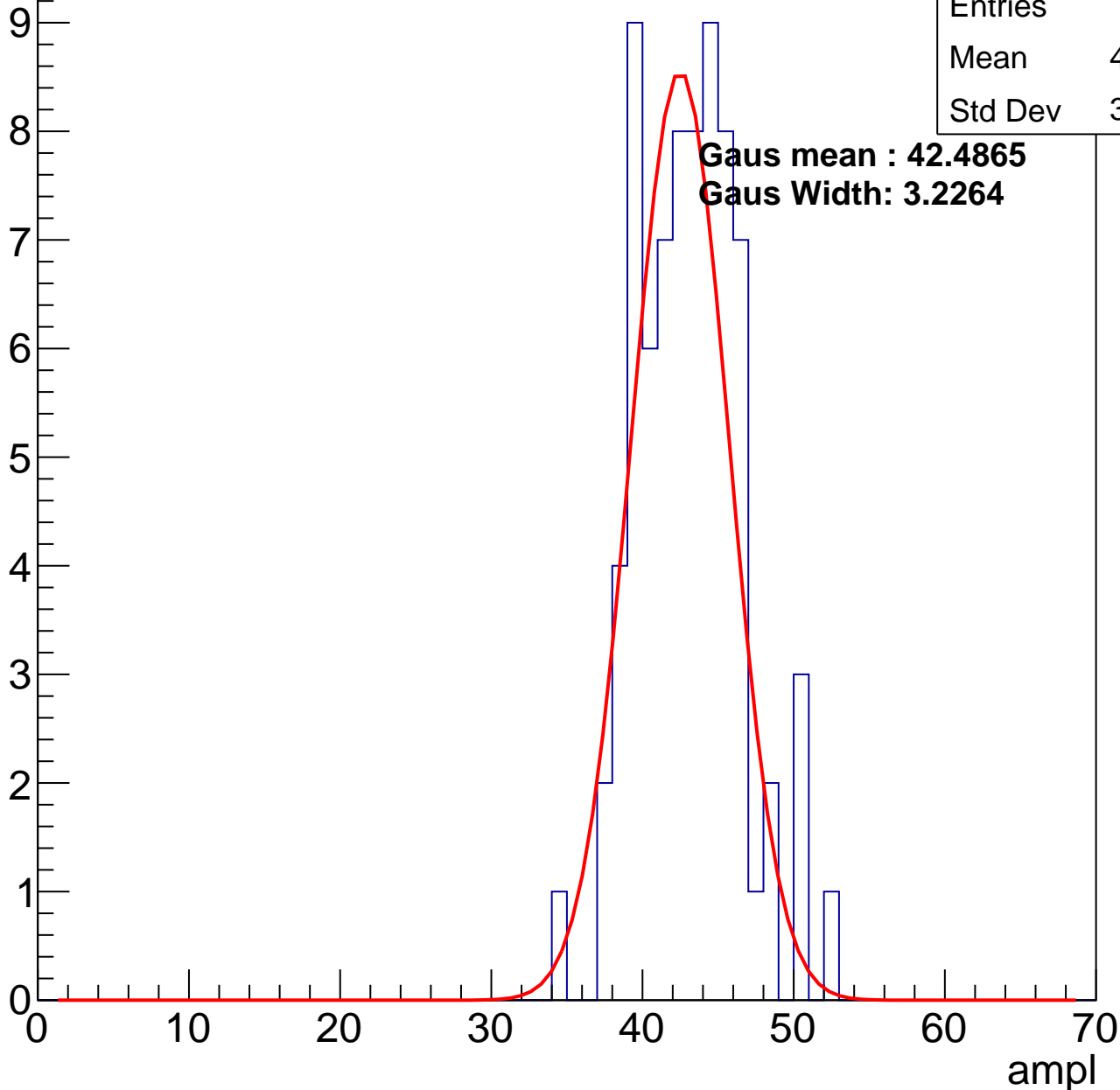
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	42.64
Std Dev	3.394

Gaus mean : 42.4865

Gaus Width: 3.2264



B1L103S, U24-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	50.43
Std Dev	3.408

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

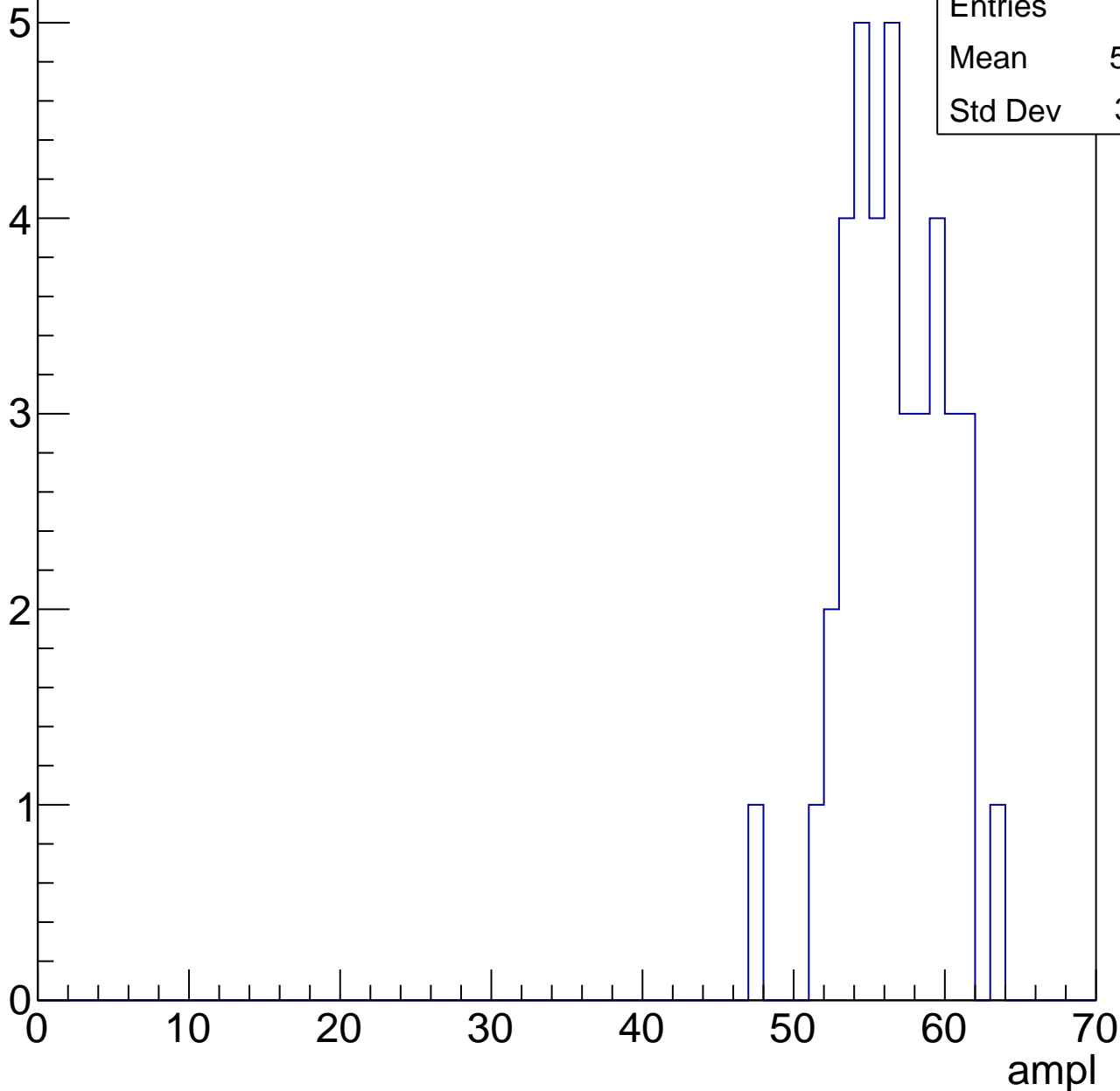
10

B1L103S, U24-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	56.18
Std Dev	3.281

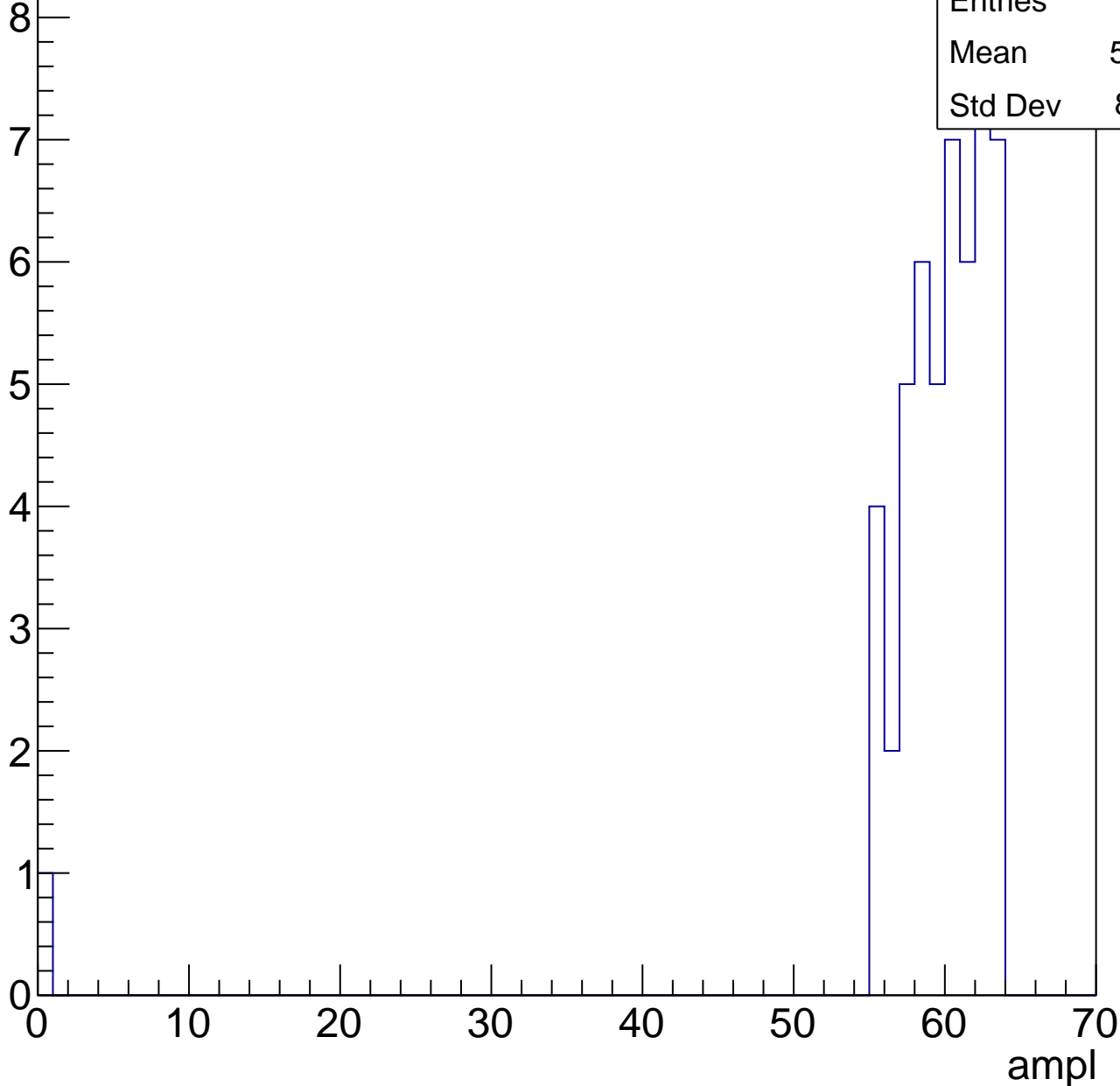


B1L103S, U24-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.49
Std Dev	8.621



B1L103S, U24-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch40, adc0

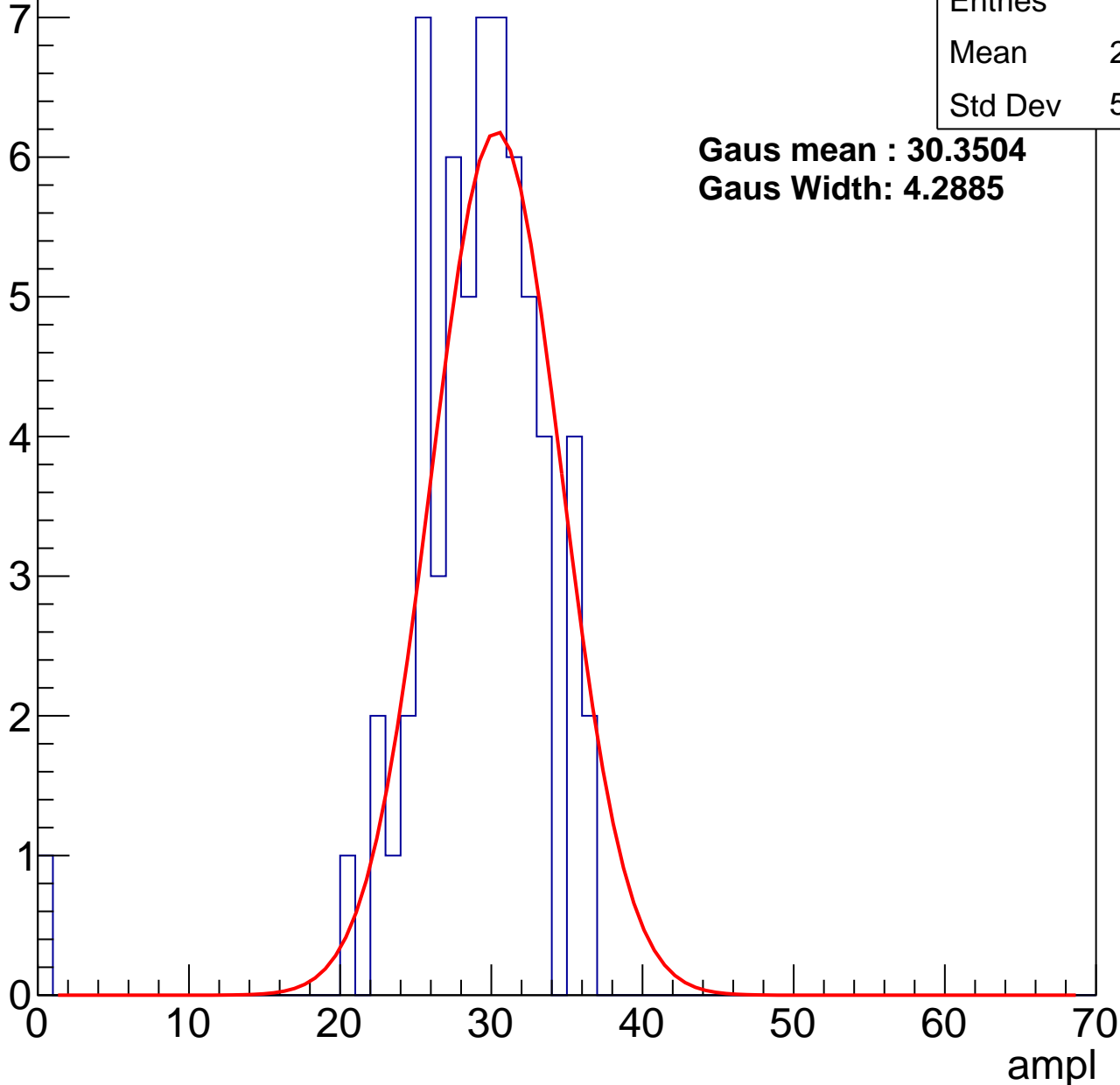
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.46
Std Dev	5.098

Gaus mean : 30.3504

Gaus Width: 4.2885



B1L103S, U24-ch40, adc1

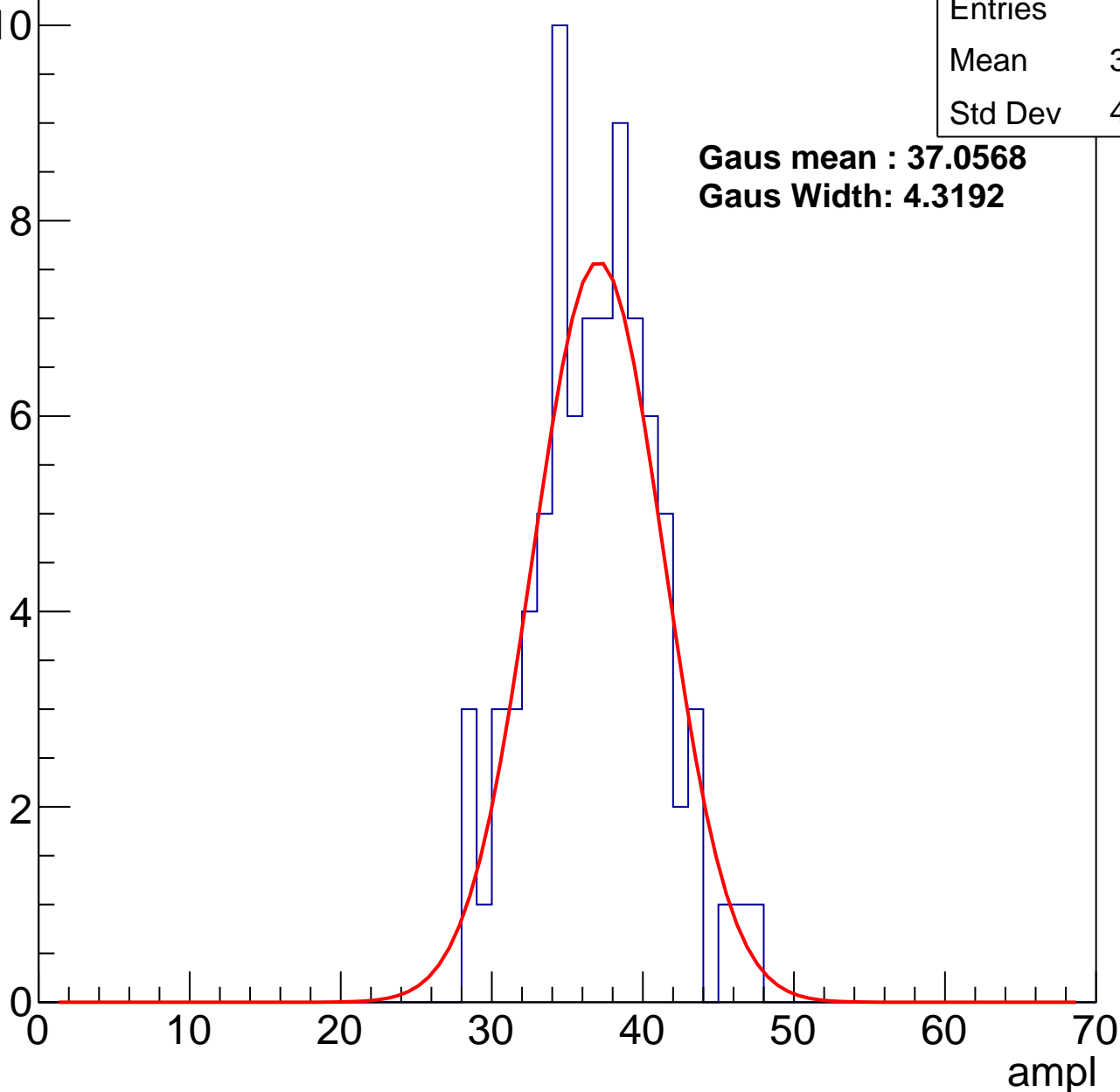
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	36.44
Std Dev	4.098

Gaus mean : 37.0568

Gaus Width: 4.3192



B1L103S, U24-ch40, adc2

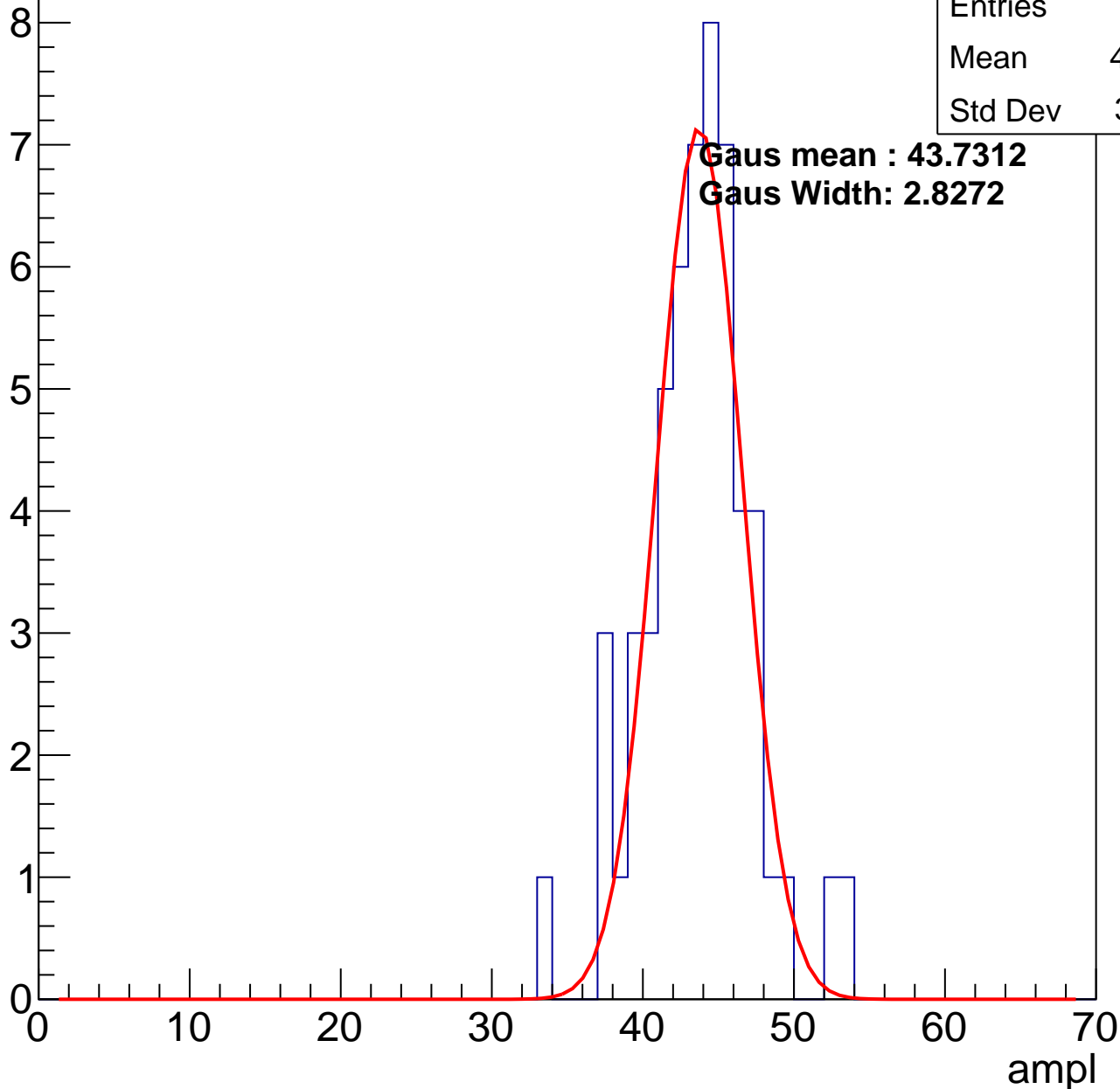
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.18
Std Dev	3.561

Gaus mean : 43.7312

Gaus Width: 2.8272

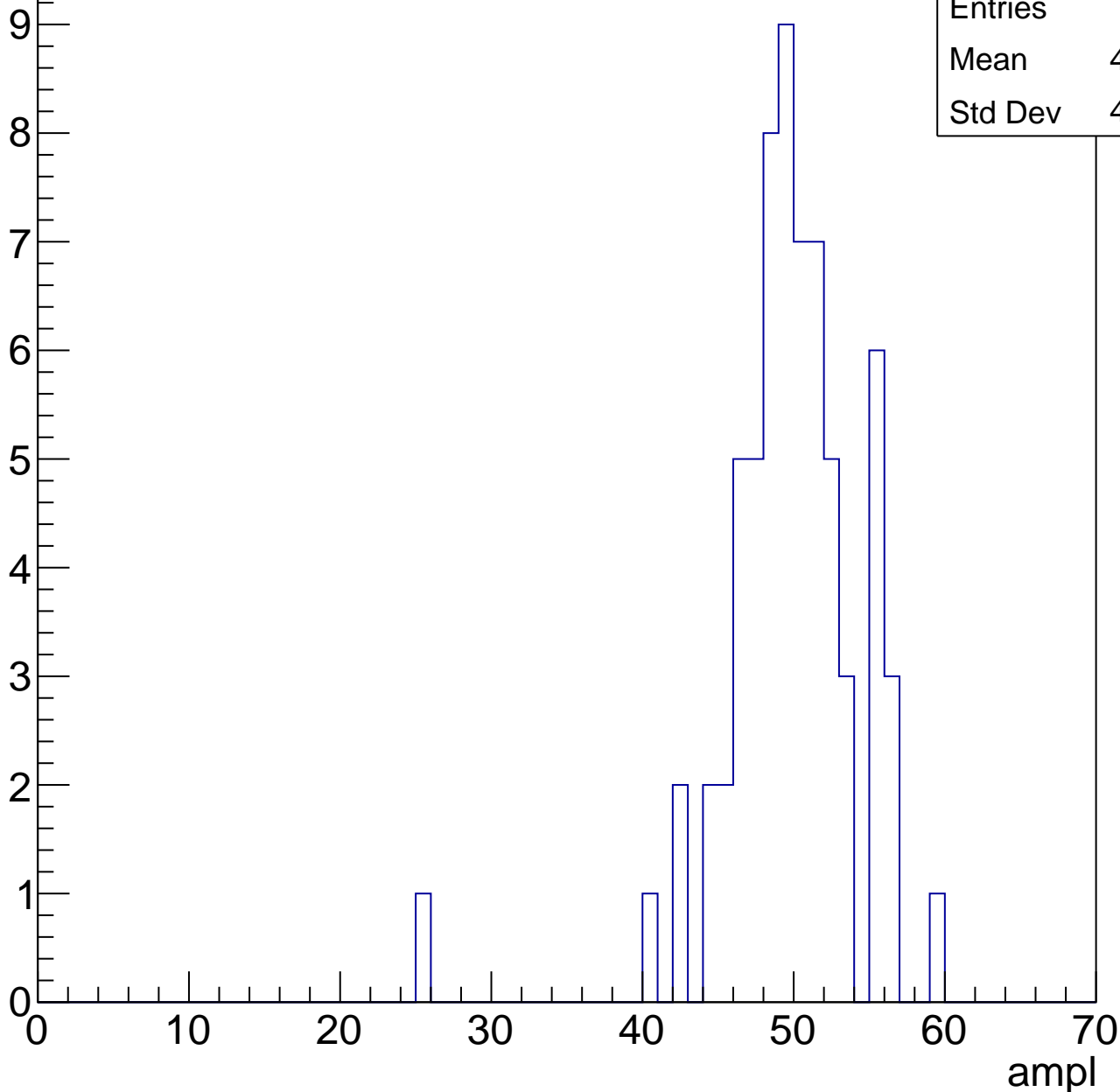


B1L103S, U24-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.25
Std Dev	4.726

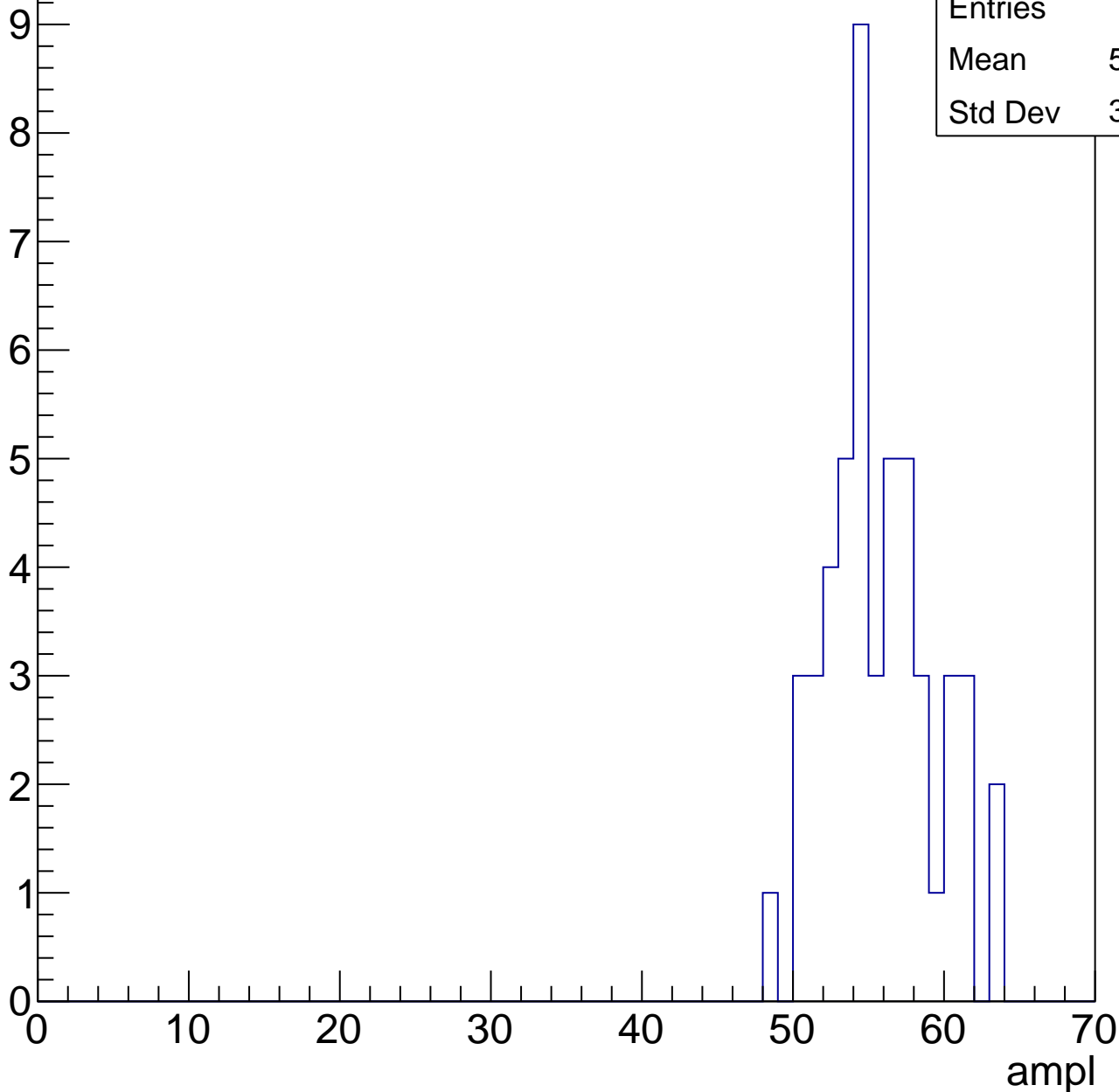


B1L103S, U24-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.24
Std Dev	3.502



B1L103S, U24-ch40, adc5

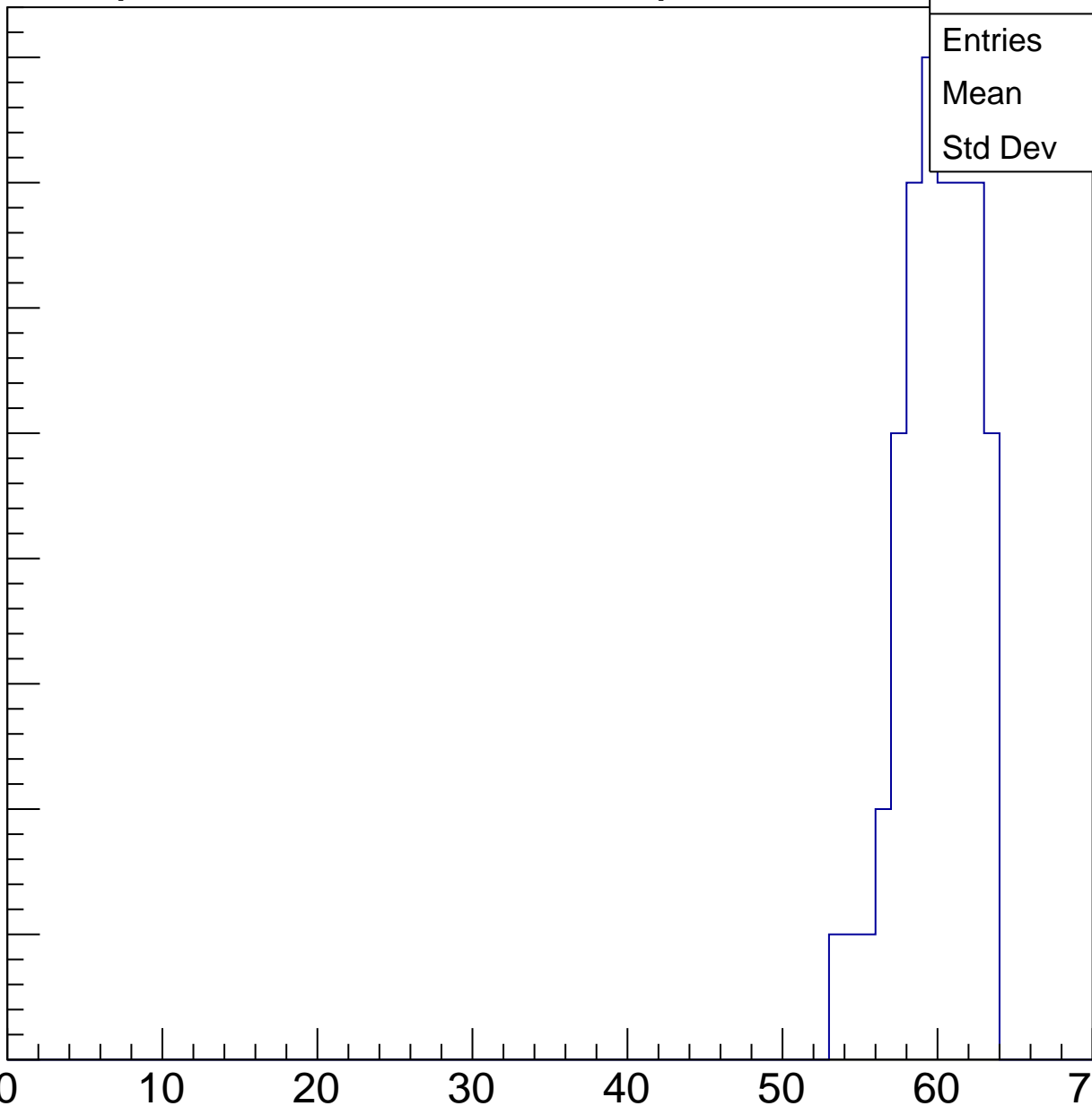
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.47
Std Dev	2.379

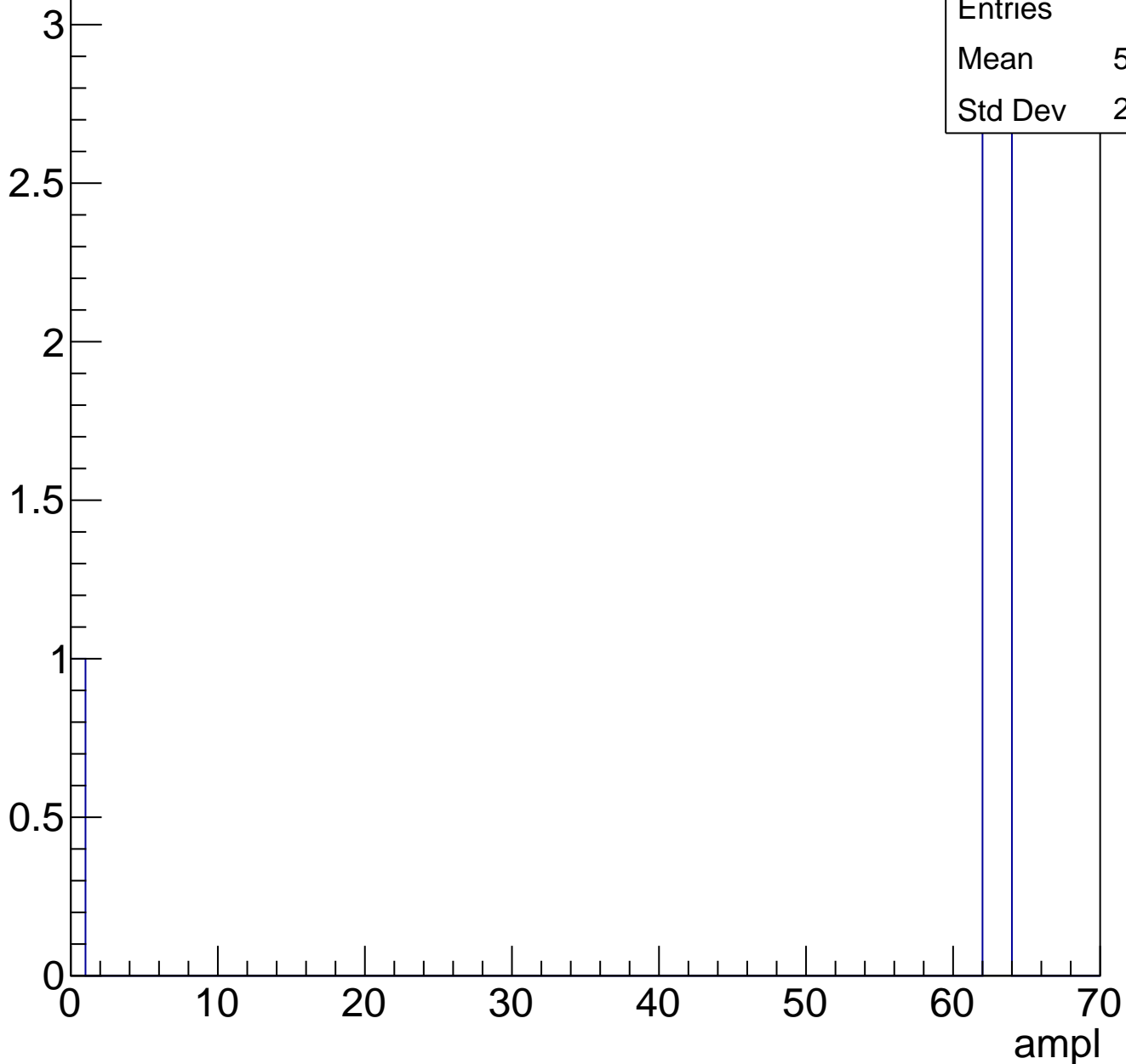
ampl



B1L103S, U24-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch41, adc0

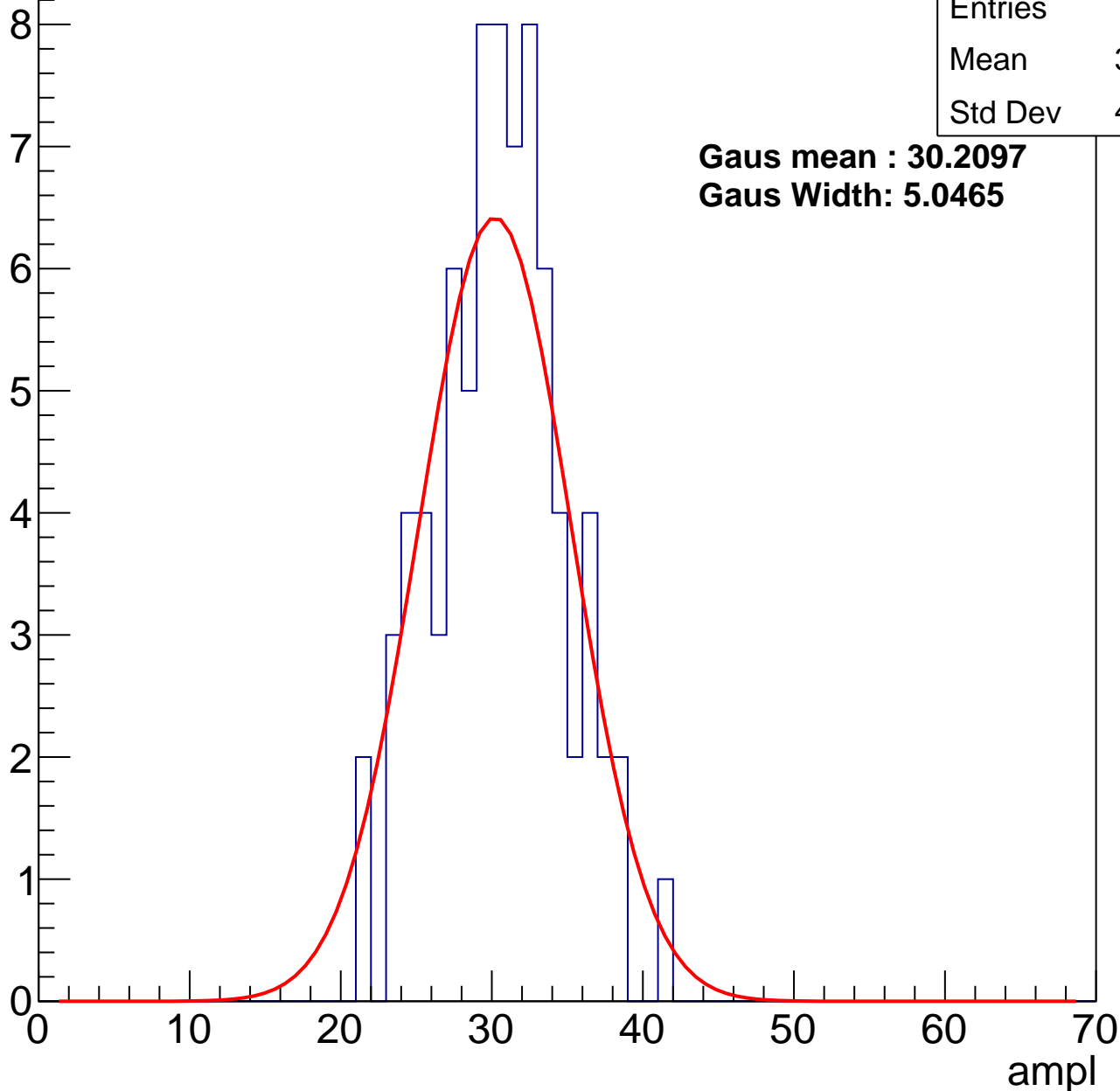
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	30.01
Std Dev	4.181

Gaus mean : 30.2097

Gaus Width: 5.0465



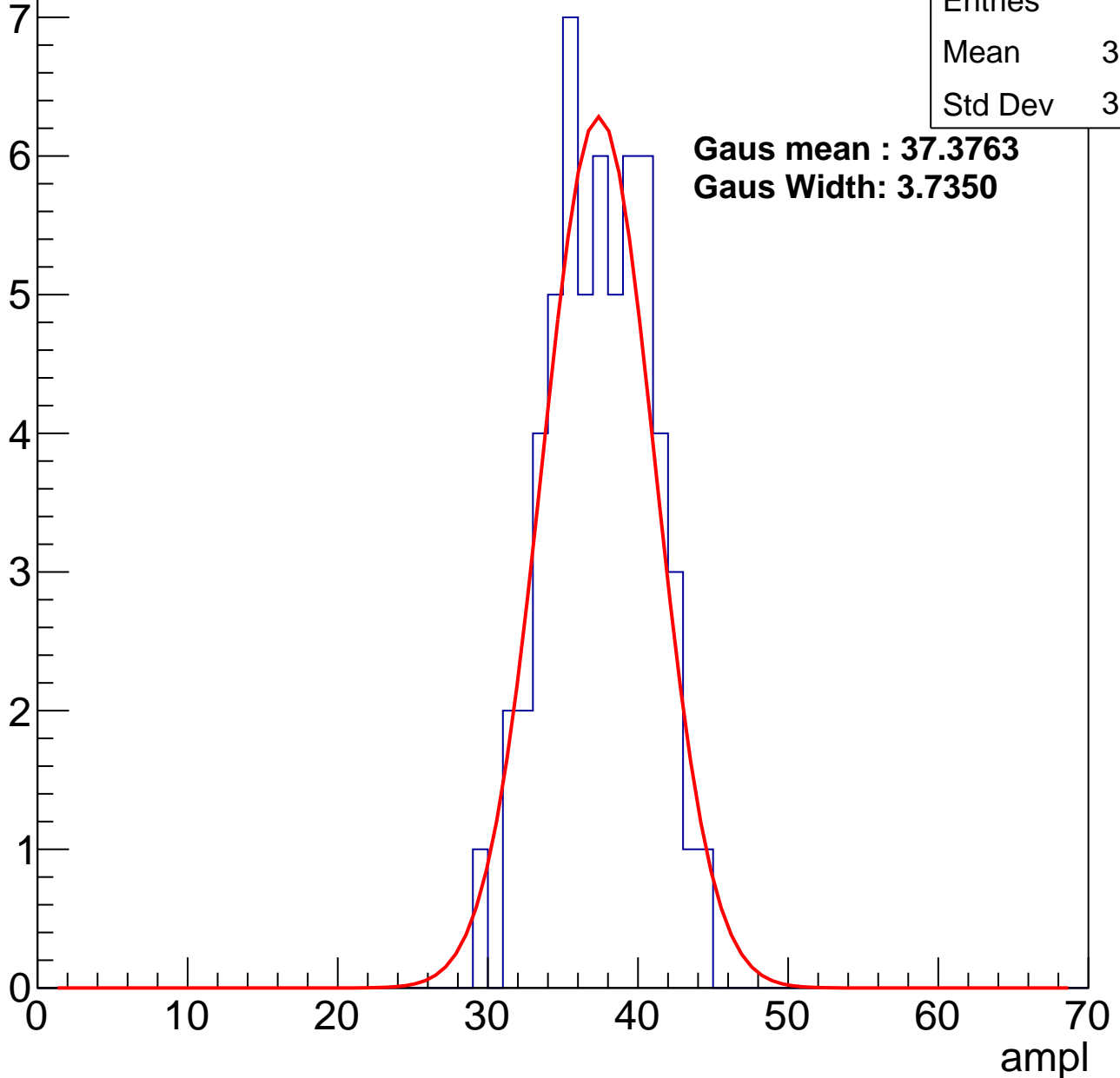
B1L103S, U24-ch41, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.98
Std Dev	3.309

Gaus mean : 37.3763
Gaus Width: 3.7350



B1L103S, U24-ch41, adc2

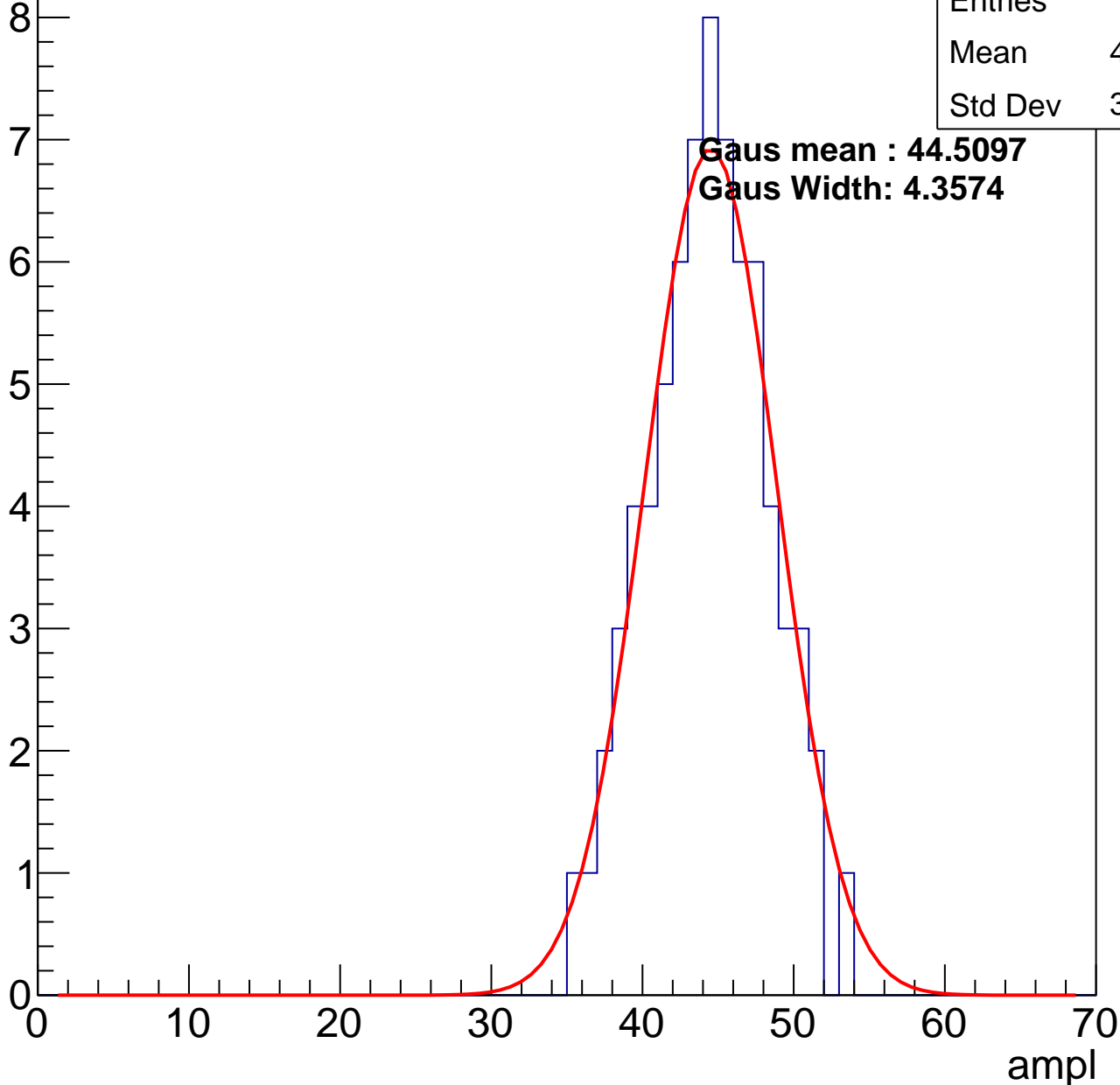
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.86
Std Dev	3.883

Gaus mean : 44.5097

Gaus Width: 4.3574

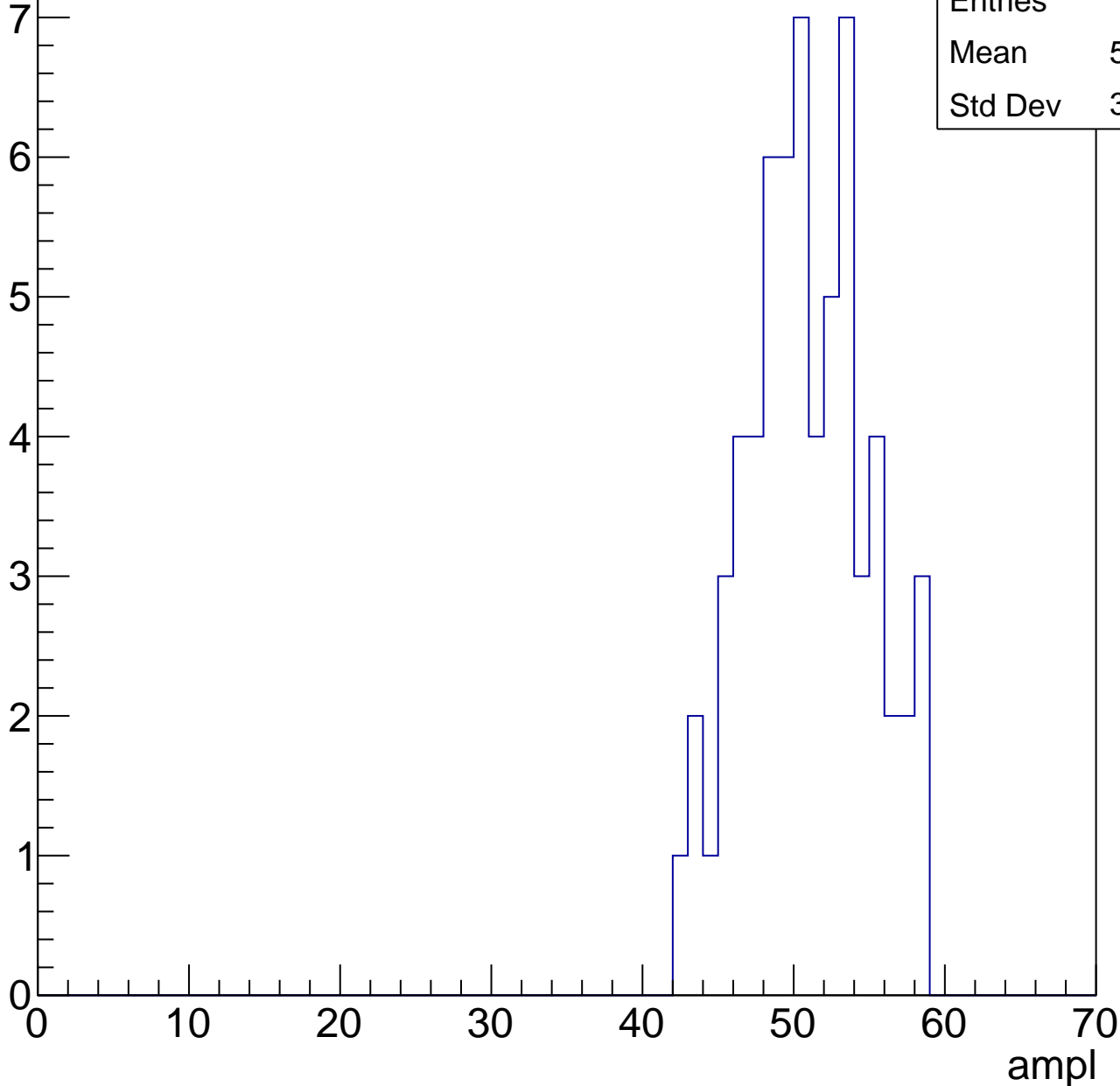


B1L103S, U24-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.44
Std Dev	3.929

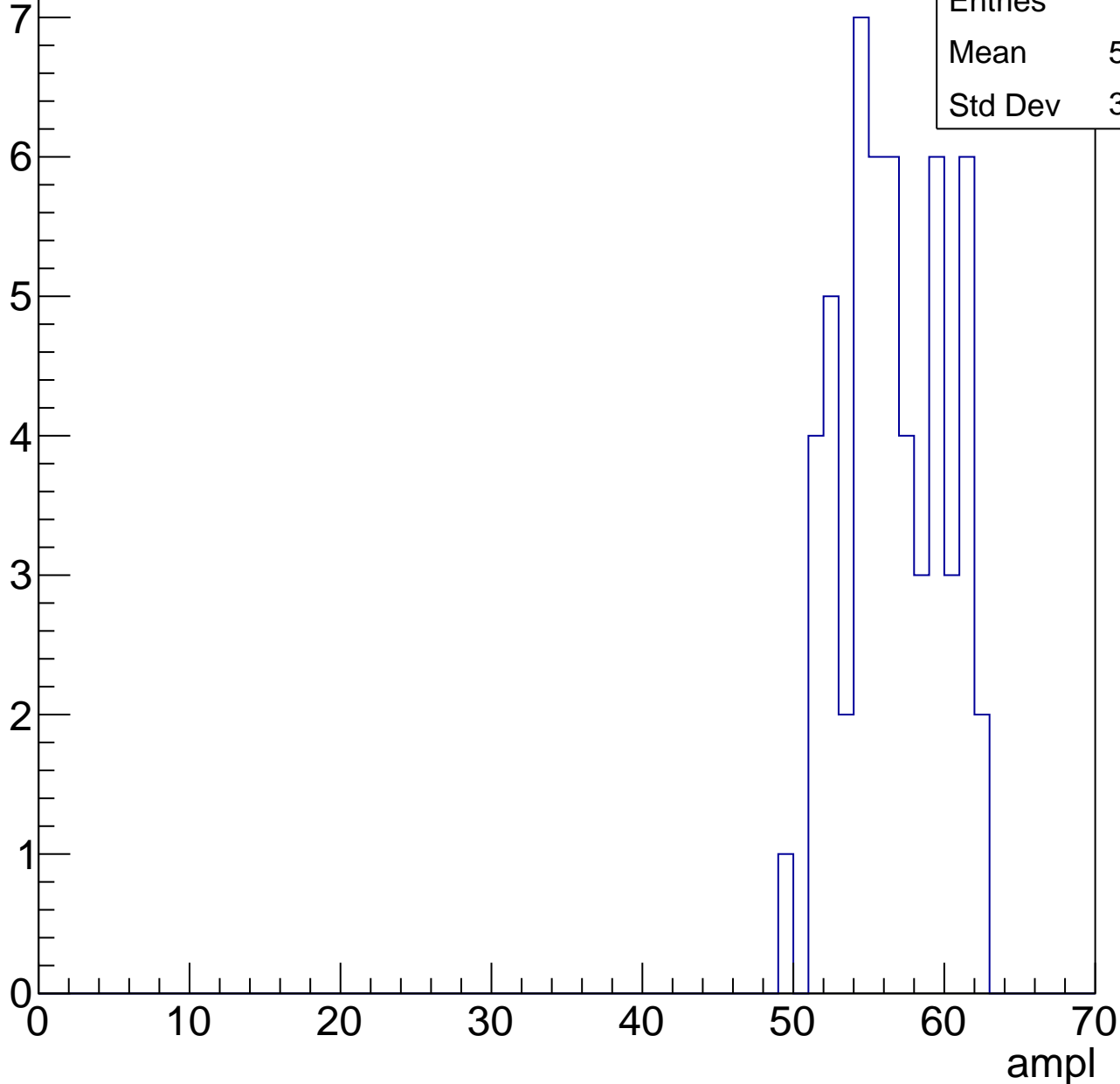


B1L103S, U24-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.16
Std Dev	3.356

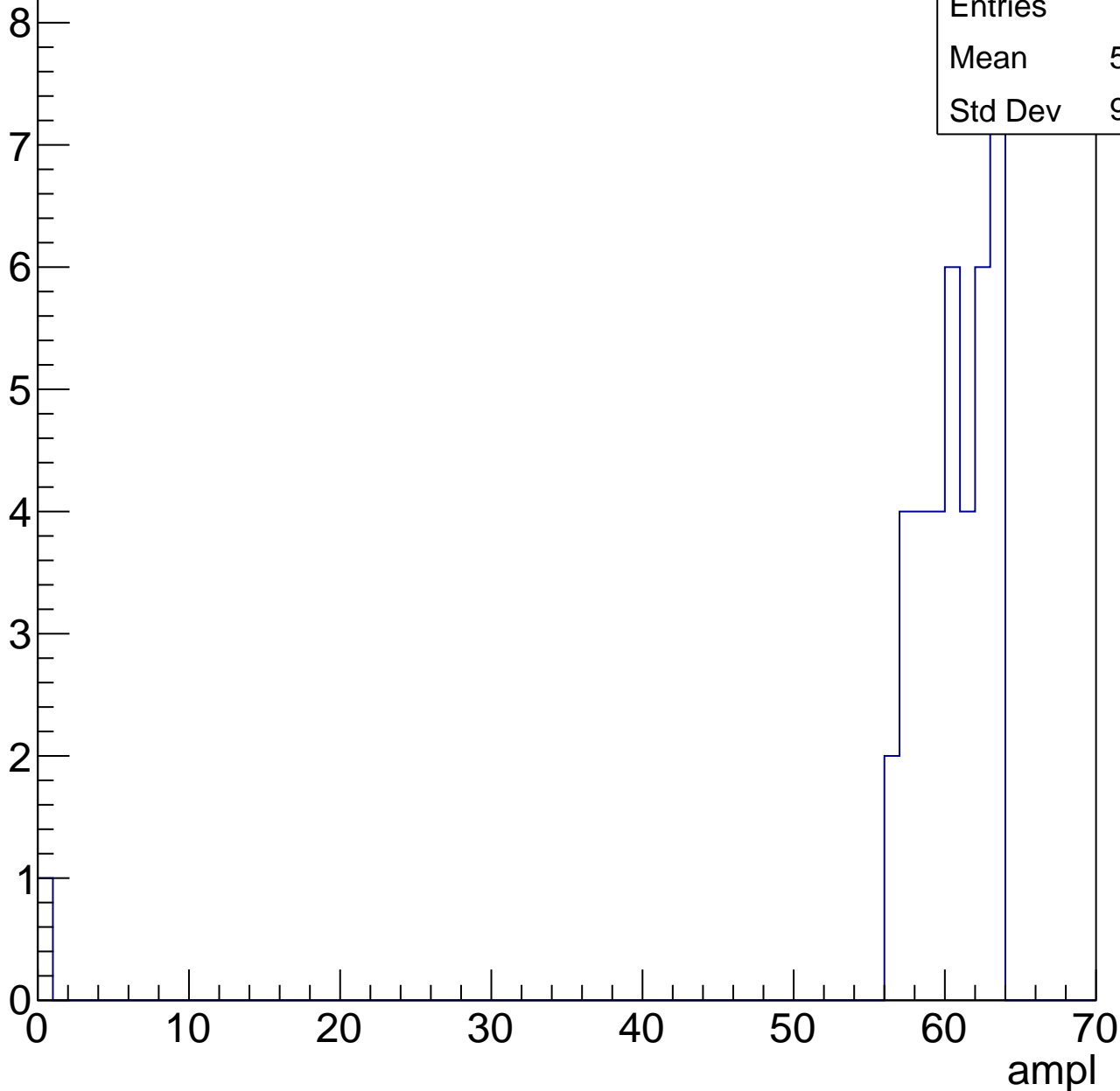


B1L103S, U24-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.67
Std Dev	9.765



B1L103S, U24-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch42, adc0

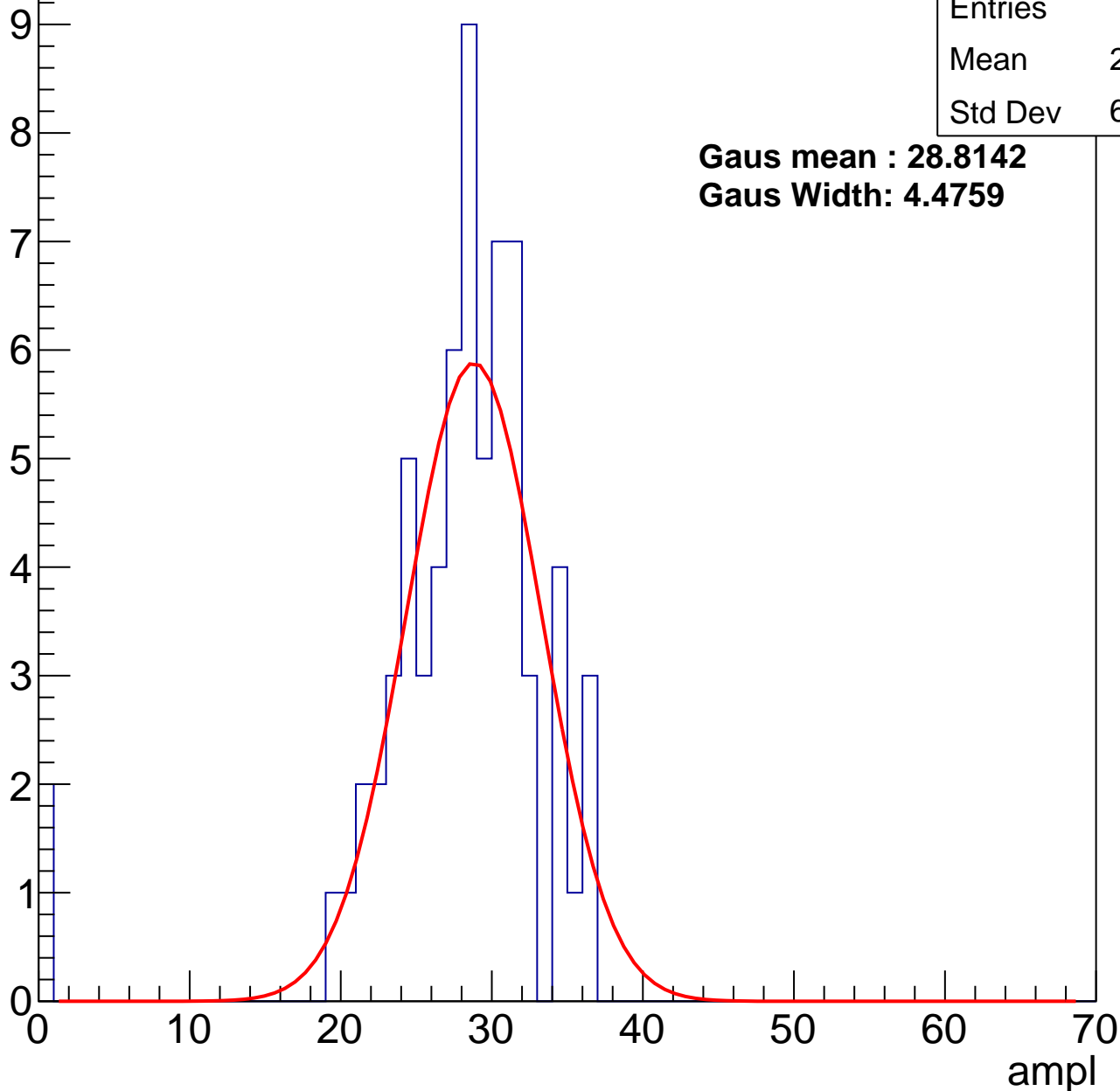
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.26
Std Dev	6.159

Gaus mean : 28.8142

Gaus Width: 4.4759



B1L103S, U24-ch42, adc1

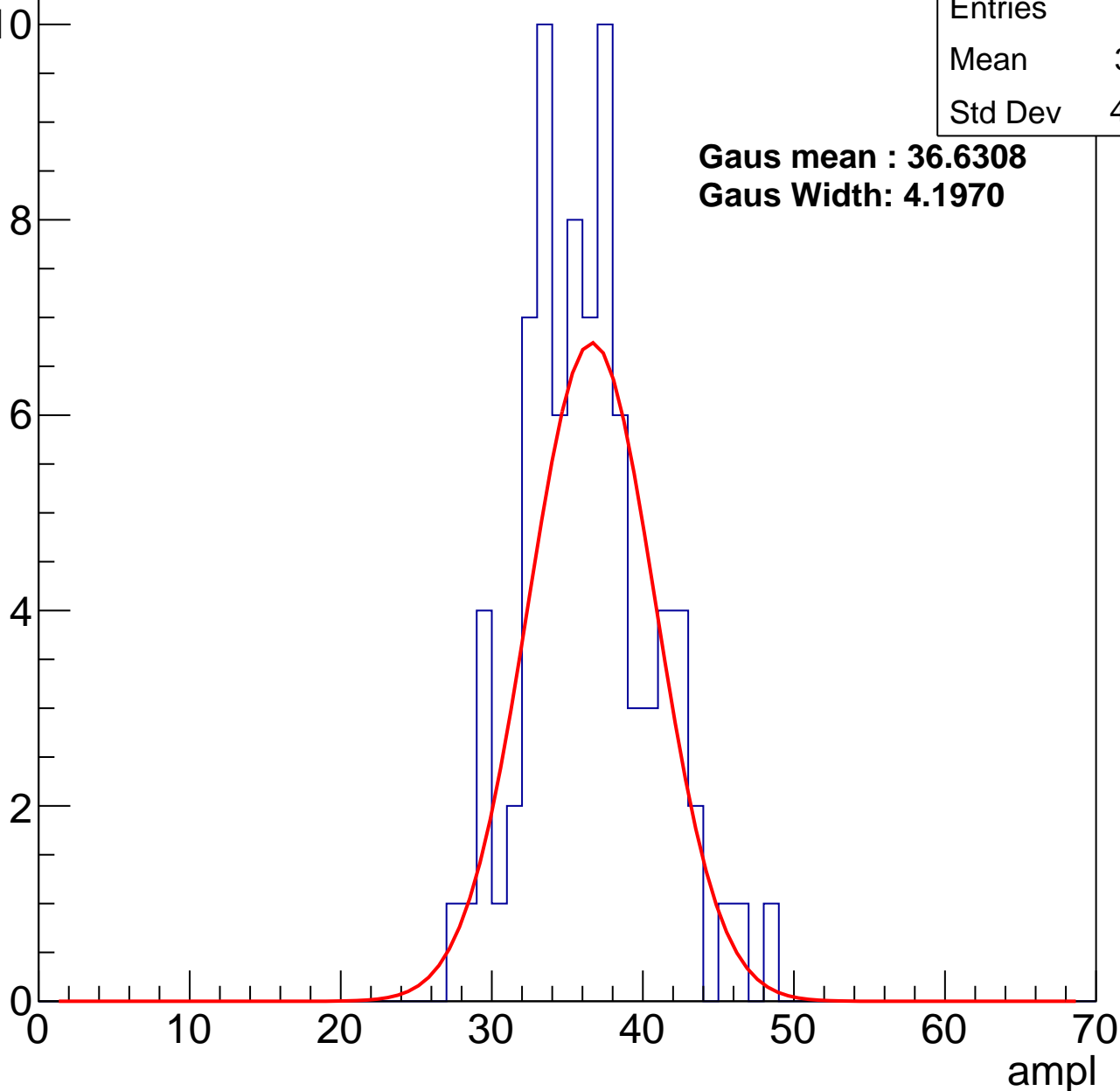
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	35.91
Std Dev	4.185

Gaus mean : 36.6308

Gaus Width: 4.1970



B1L103S, U24-ch42, adc2

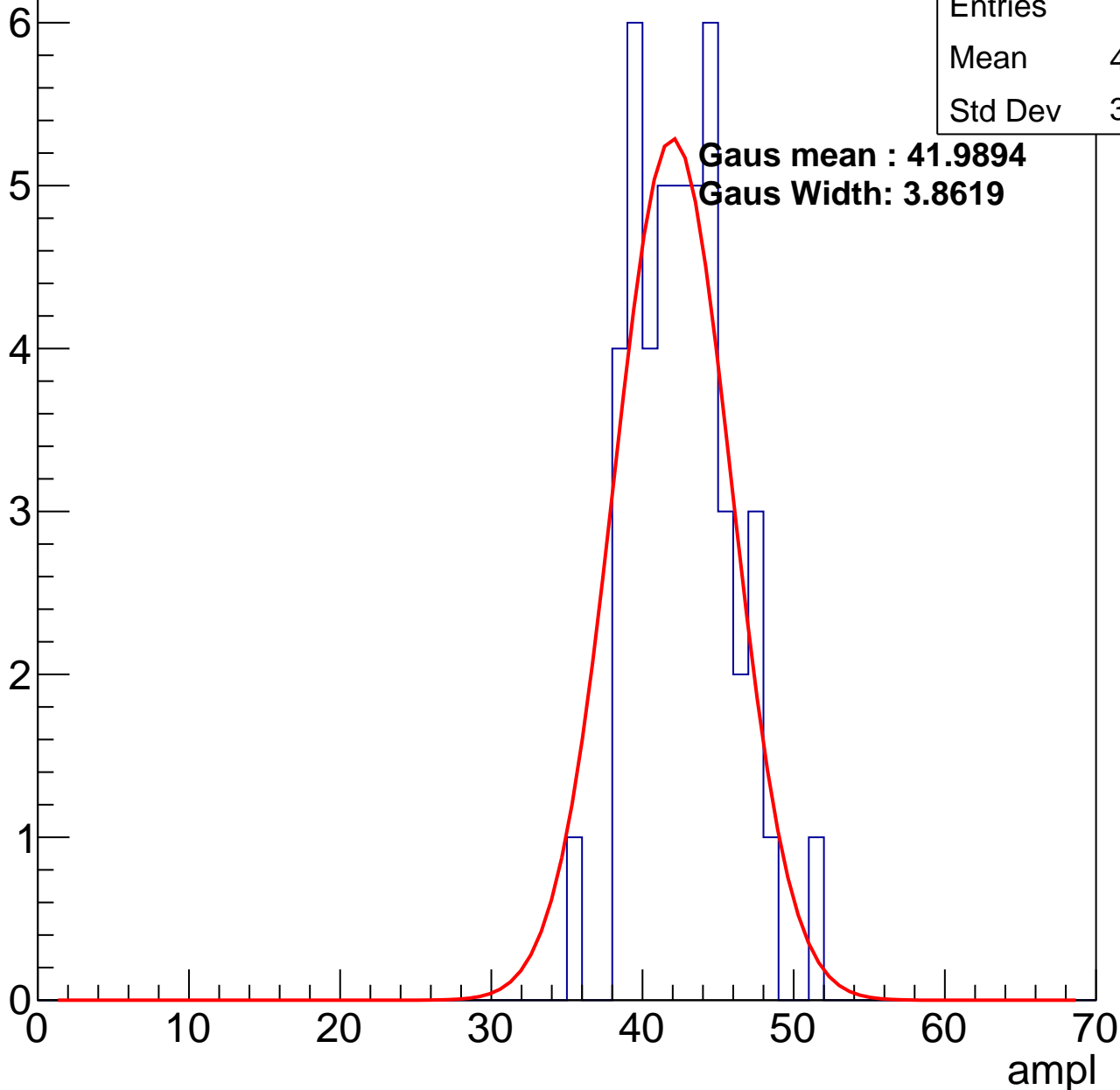
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	42.22
Std Dev	3.182

Gaus mean : 41.9894

Gaus Width: 3.8619

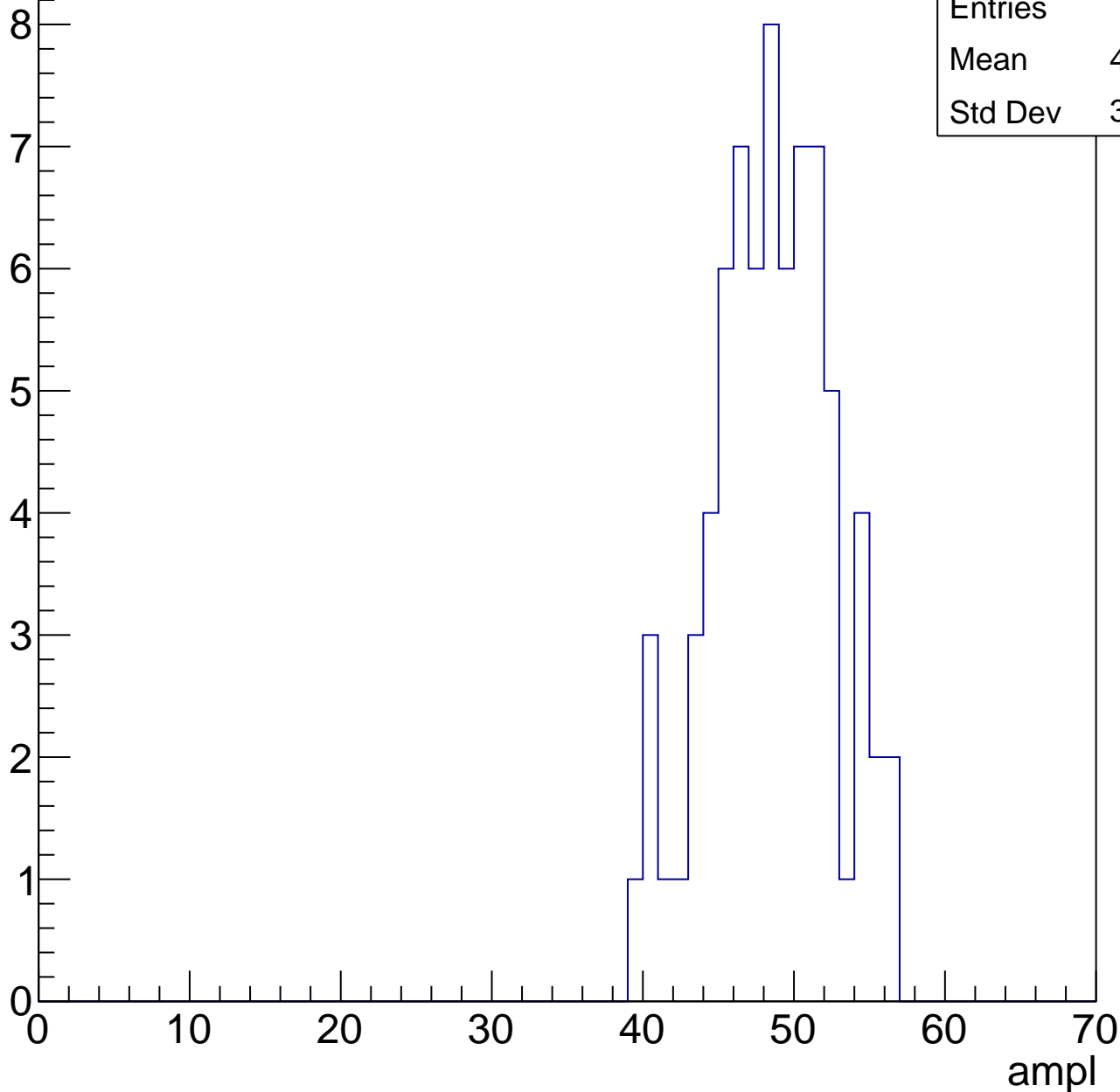


B1L103S, U24-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	48.07
Std Dev	3.954

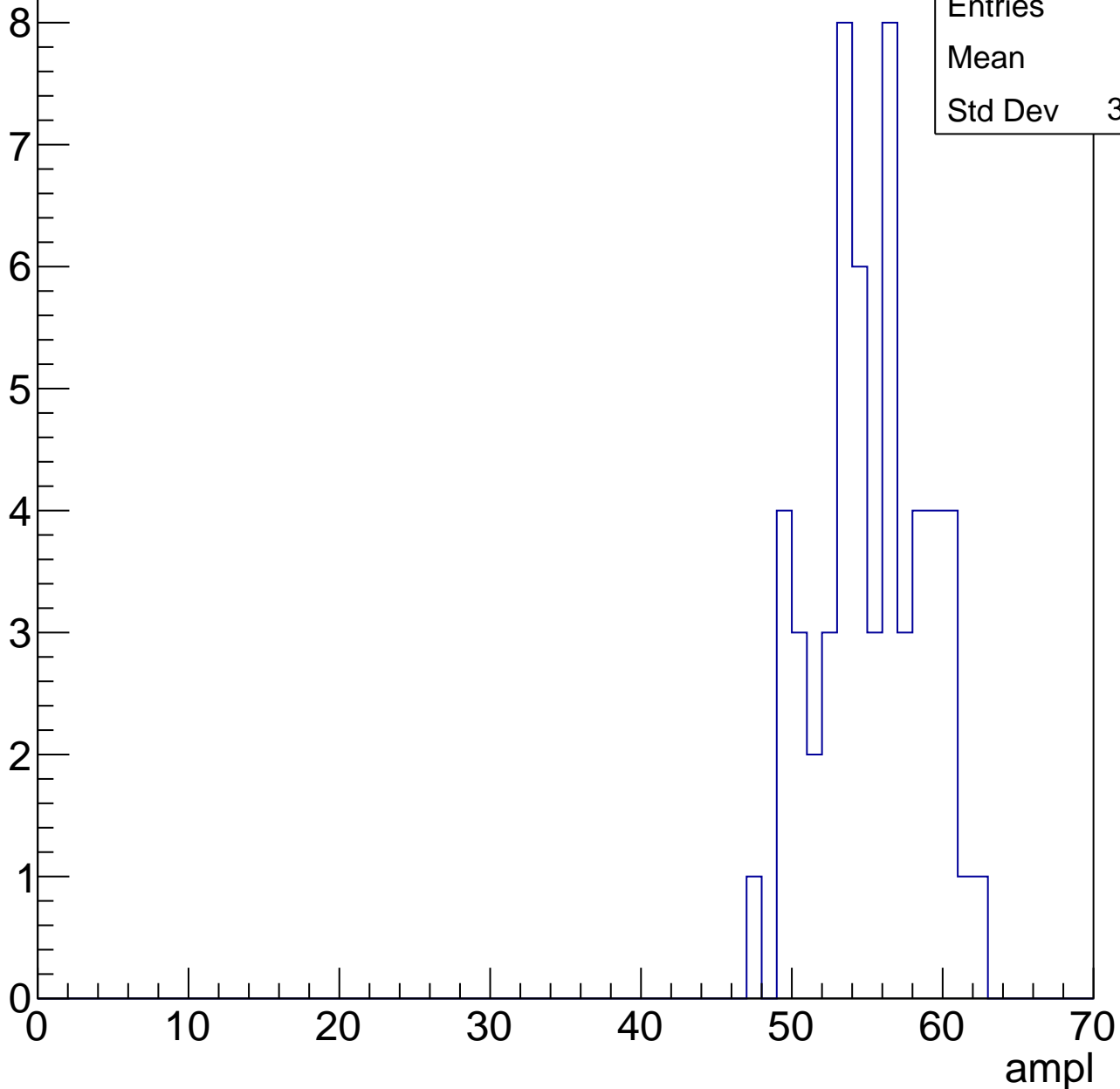


B1L103S, U24-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.8
Std Dev	3.523

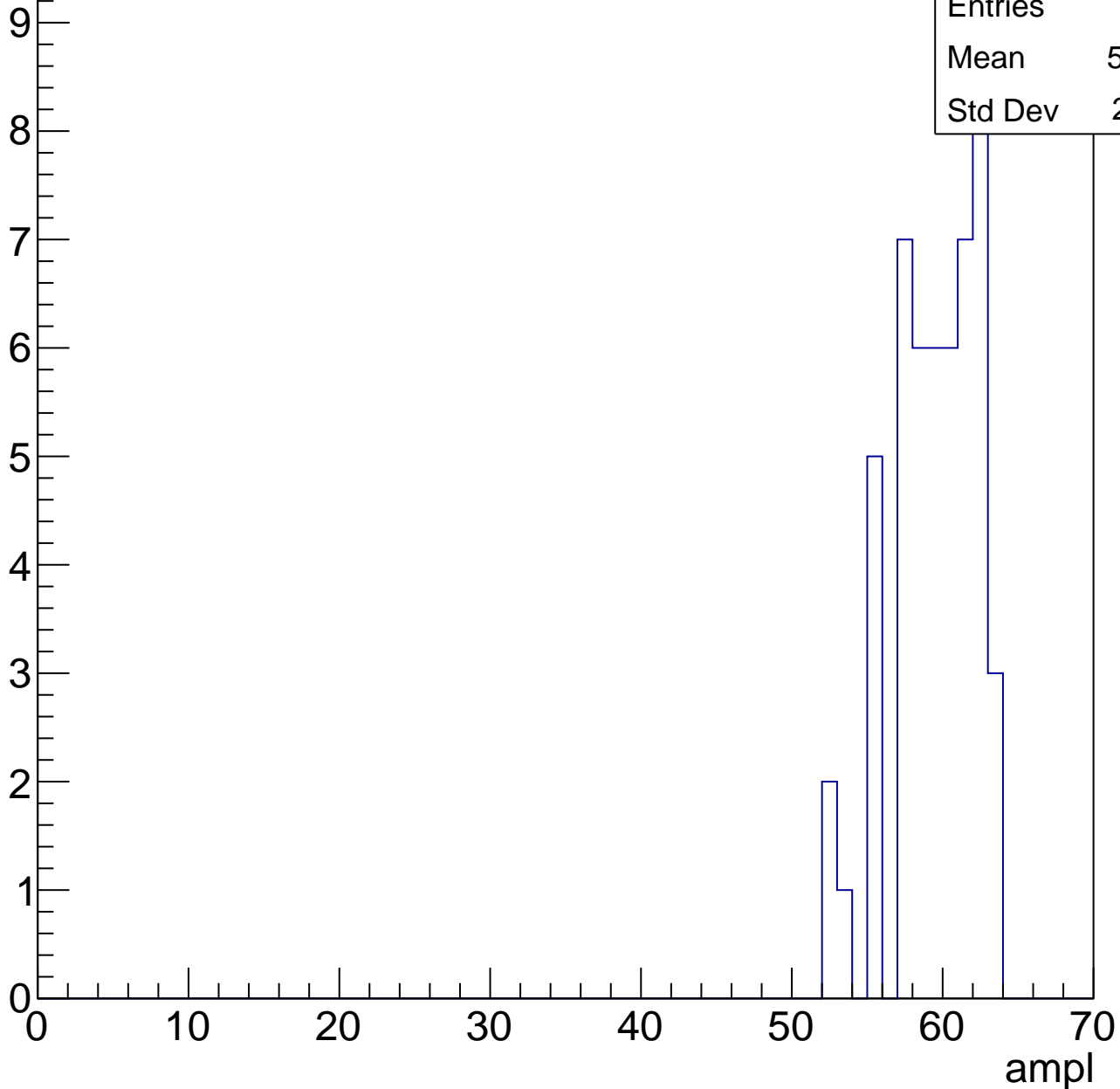


B1L103S, U24-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.98
Std Dev	2.811

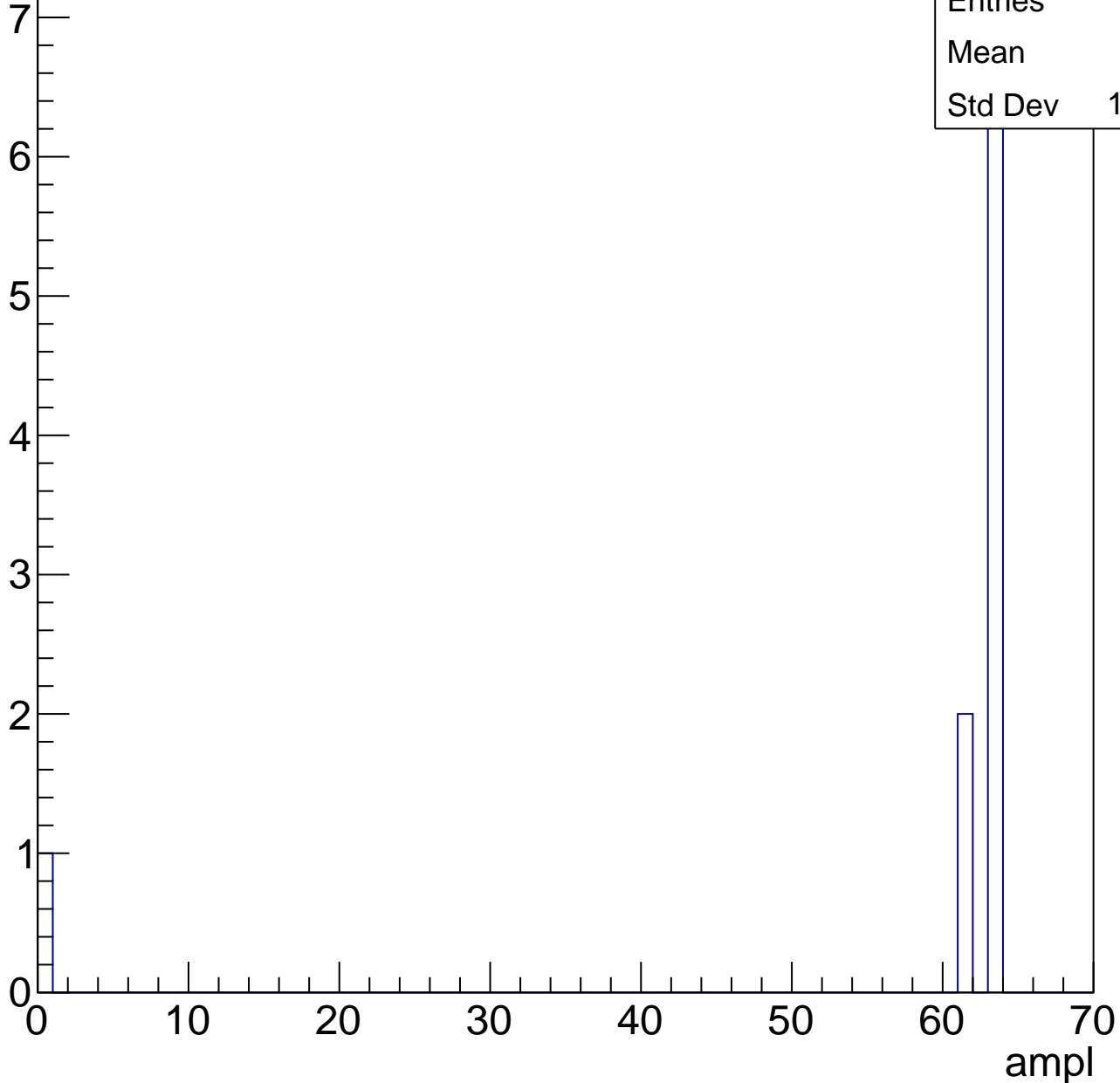


B1L103S, U24-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

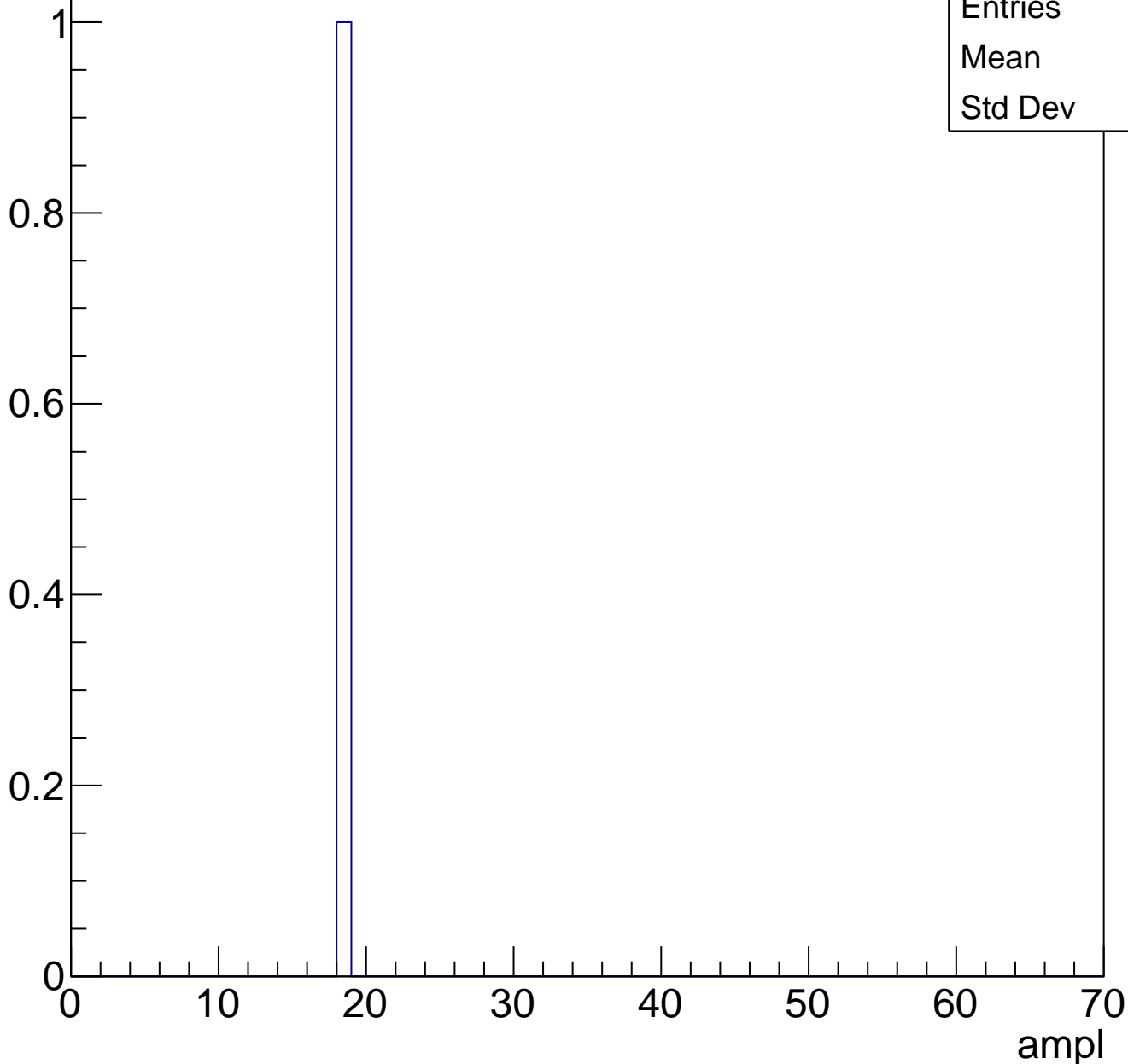
Entries	10
Mean	56.3
Std Dev	18.78



B1L103S, U24-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch43, adc0

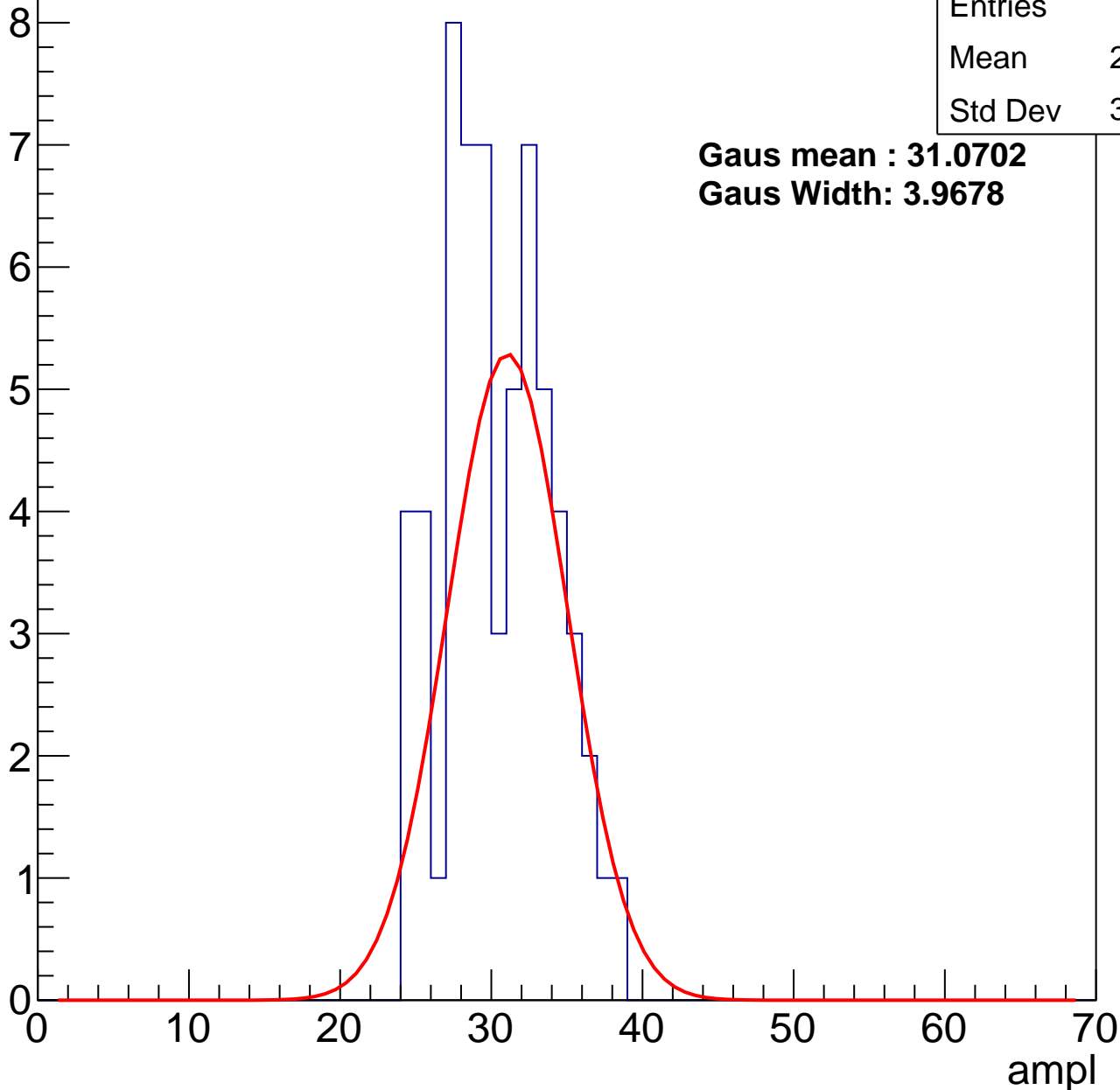
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.98
Std Dev	3.522

Gaus mean : 31.0702

Gaus Width: 3.9678



B1L103S, U24-ch43, adc1

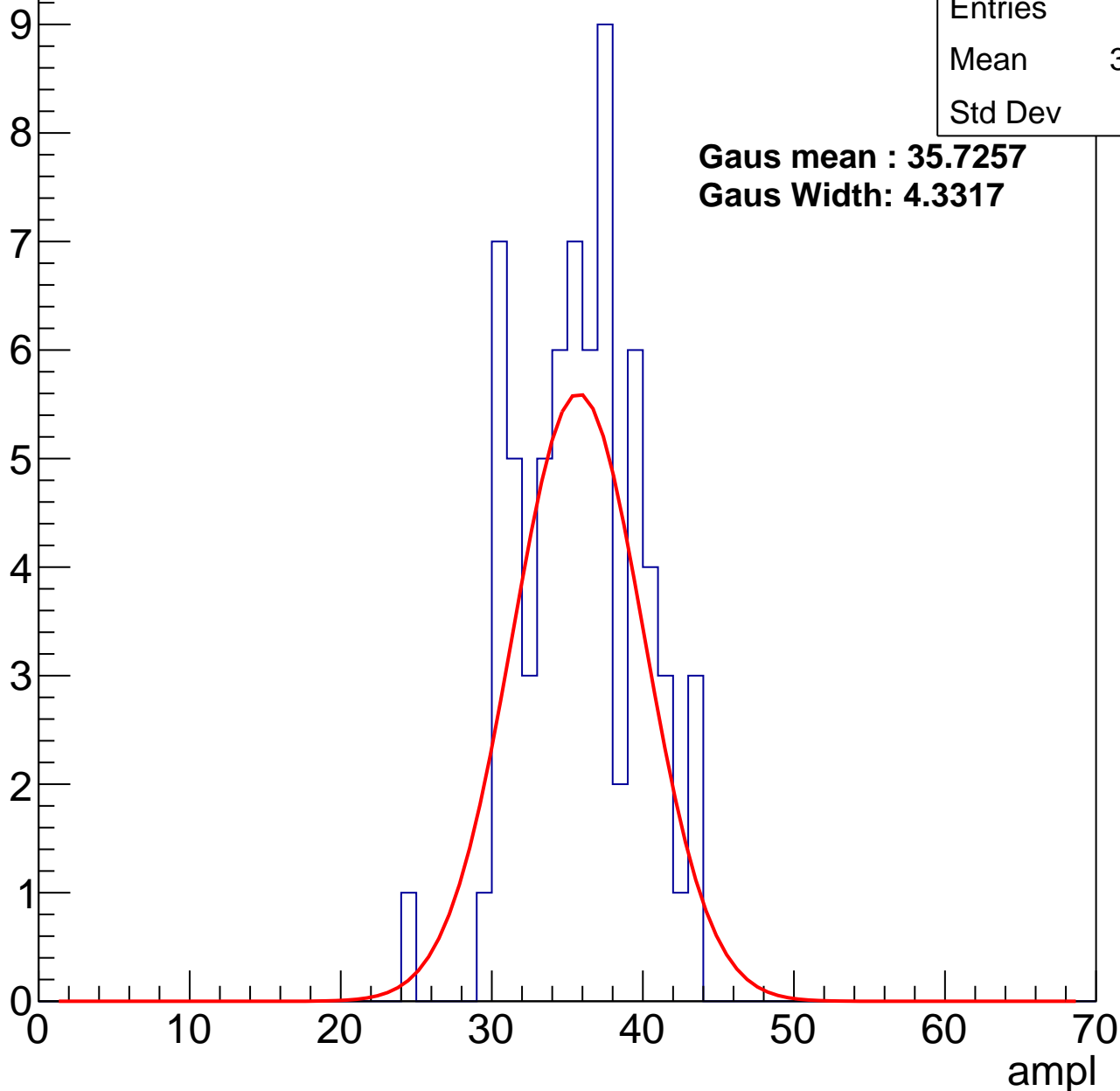
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.38
Std Dev	3.92

Gaus mean : 35.7257

Gaus Width: 4.3317



B1L103S, U24-ch43, adc2

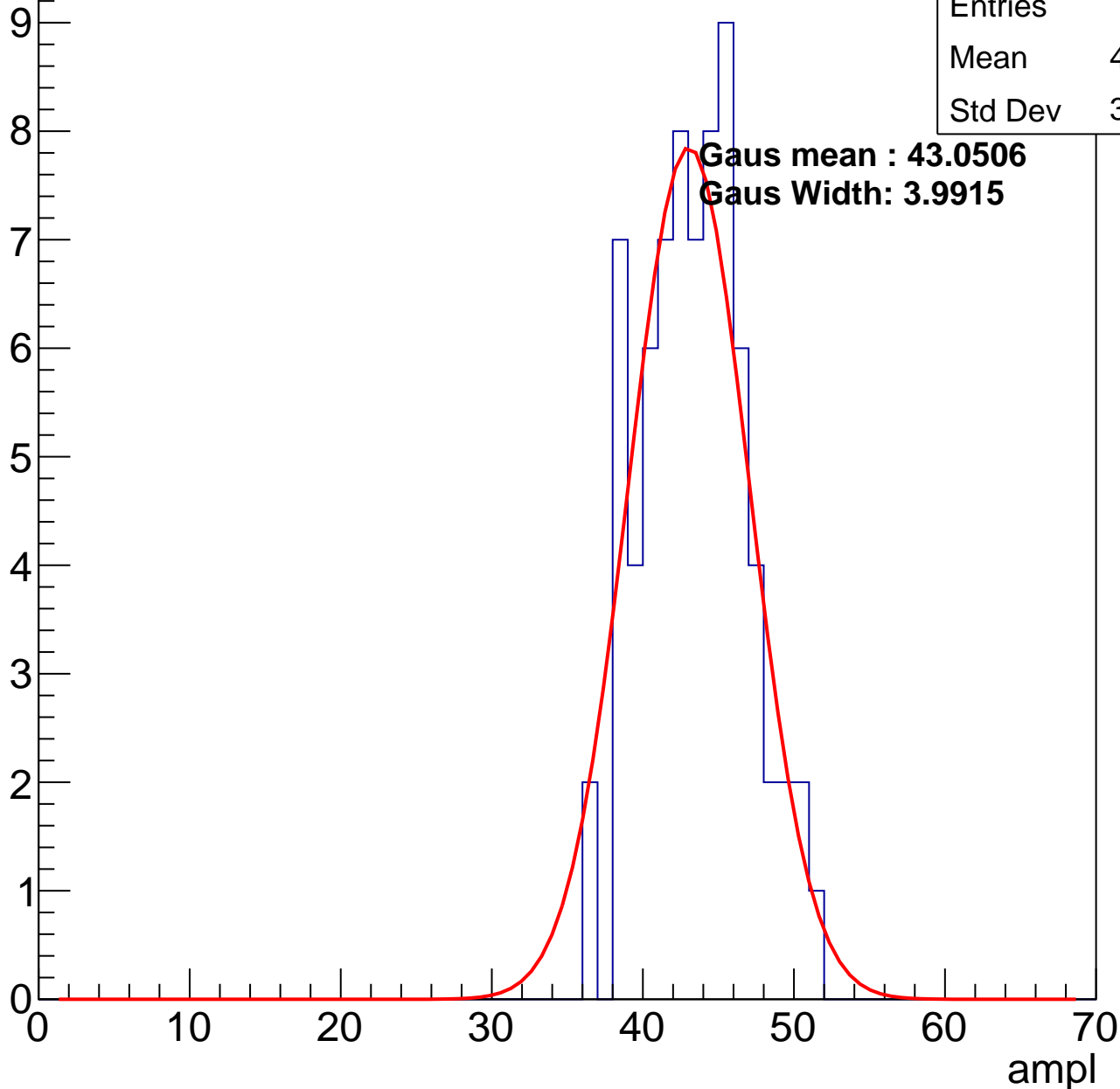
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.99
Std Dev	3.412

Gaus mean : 43.0506

Gaus Width: 3.9915

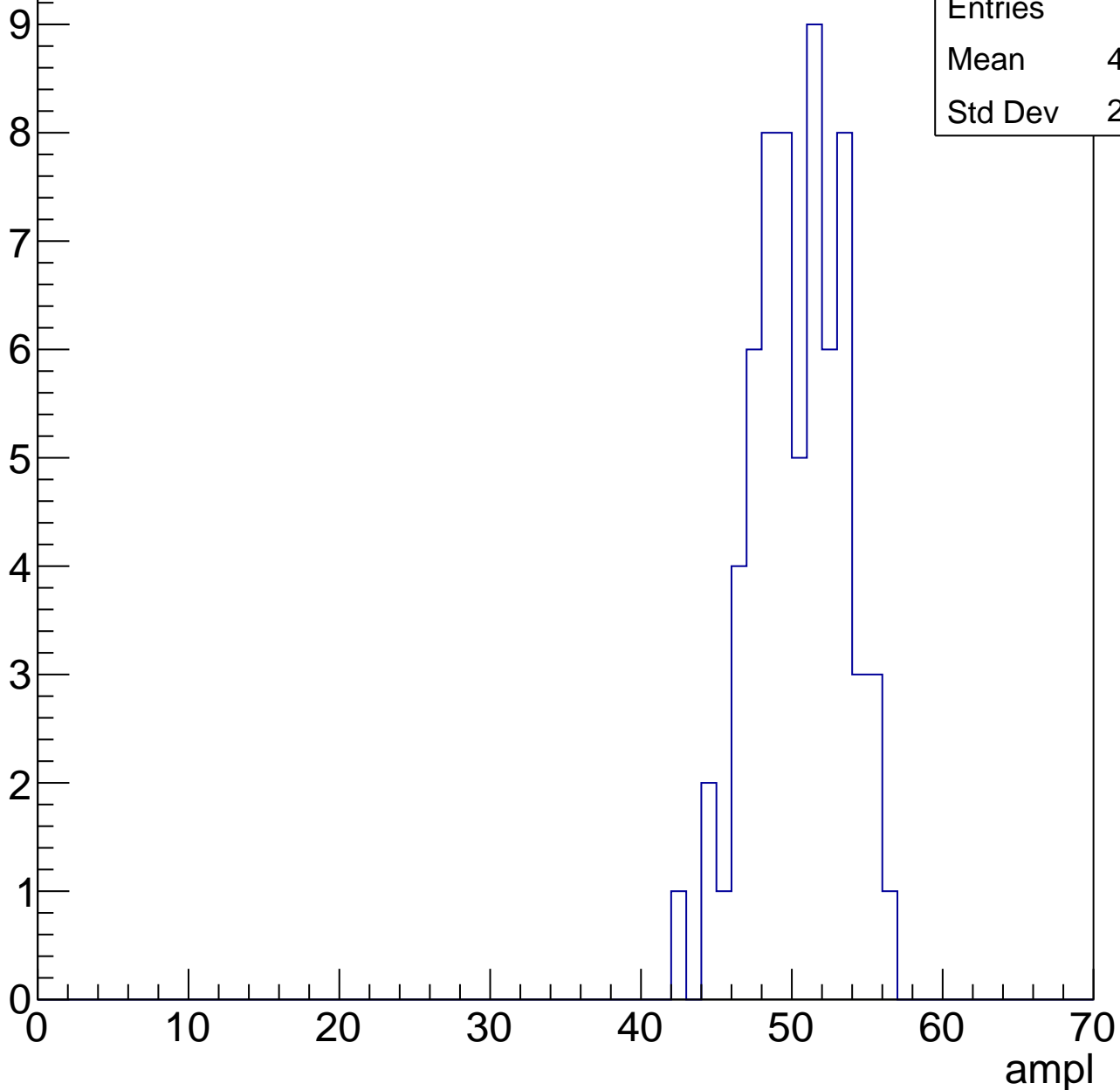


B1L103S, U24-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.92
Std Dev	2.994

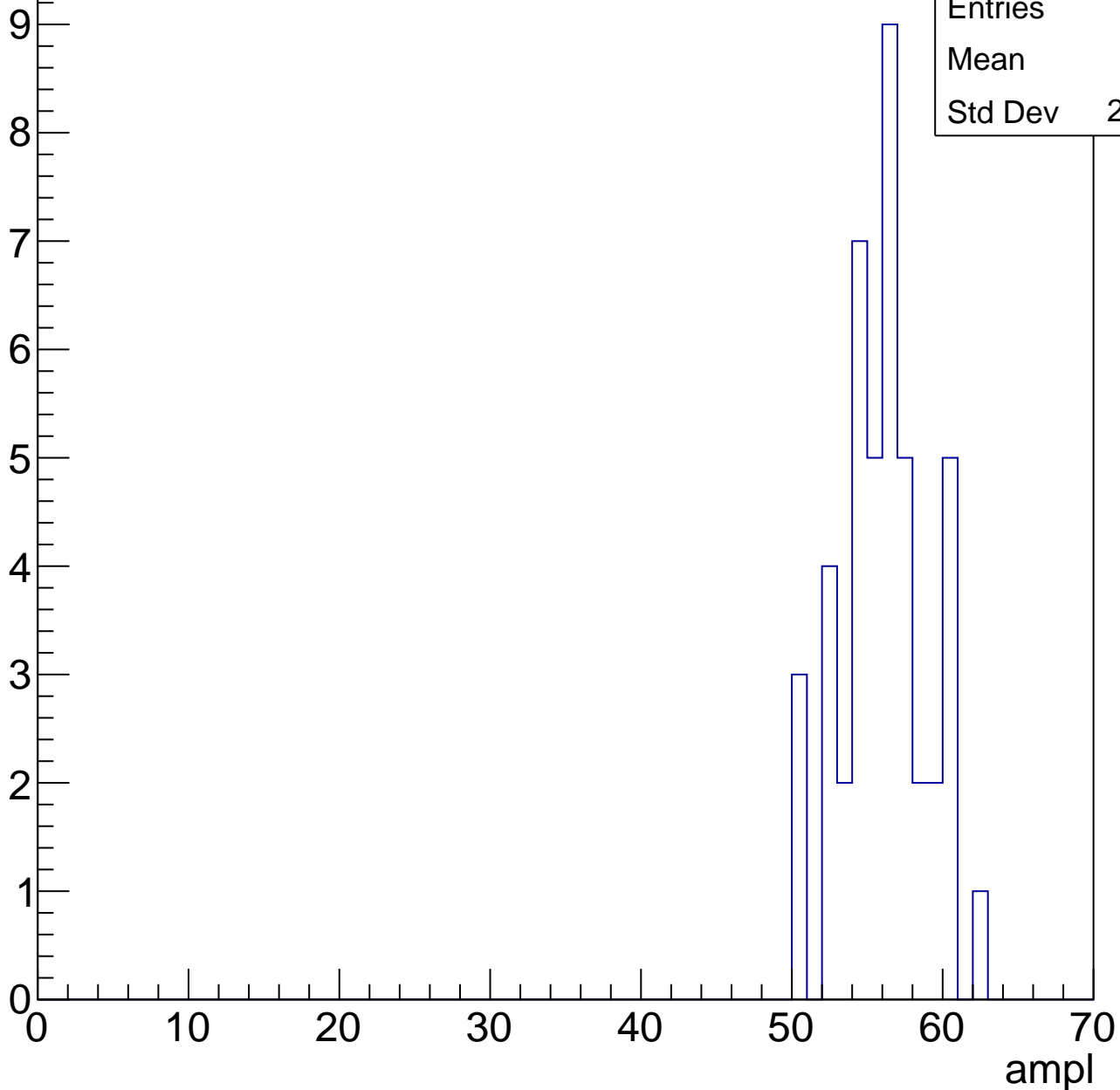


B1L103S, U24-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.6
Std Dev	2.839



B1L103S, U24-ch43, adc5

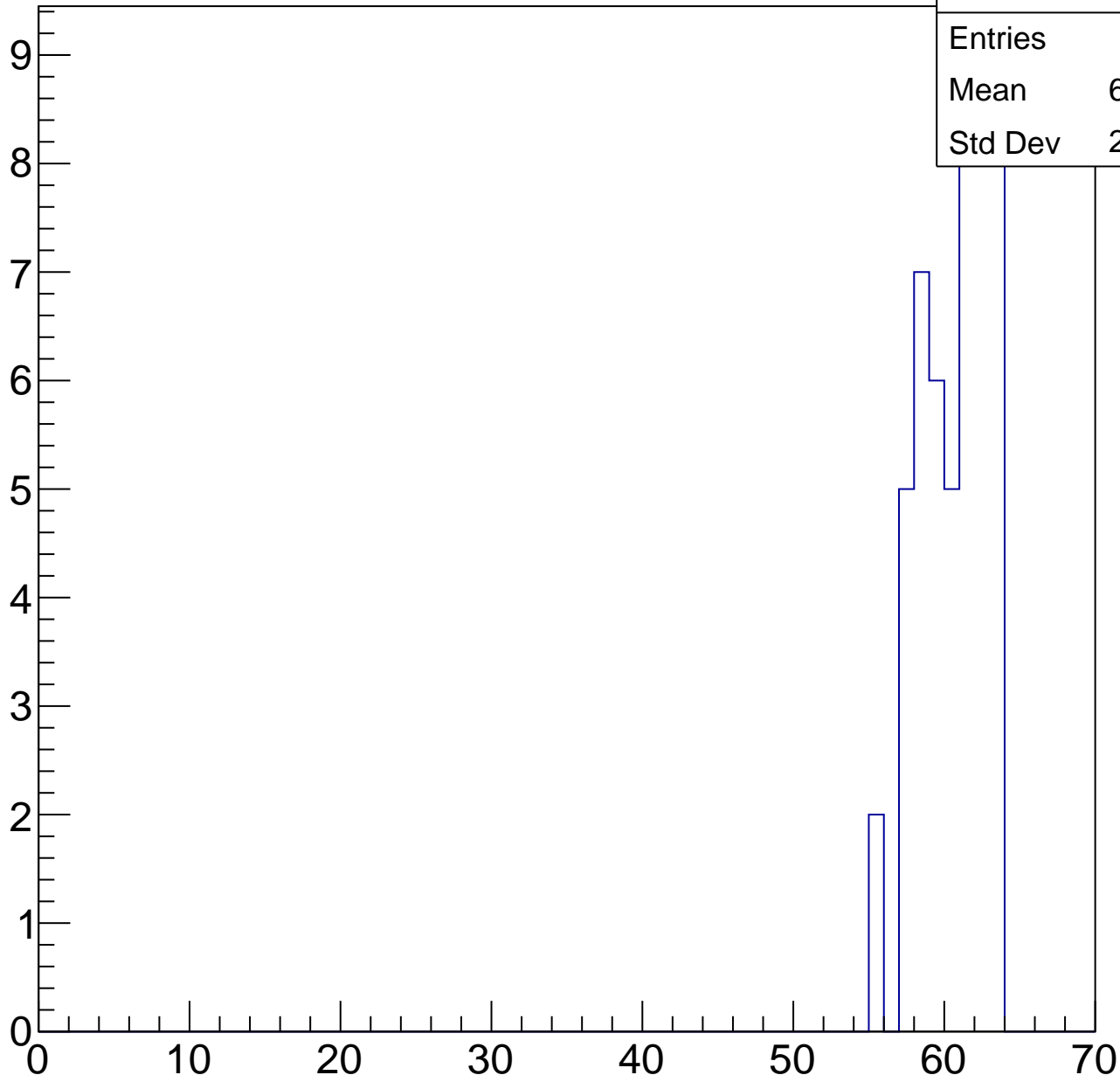
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	60.08
Std Dev	2.199

ampl



B1L103S, U24-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

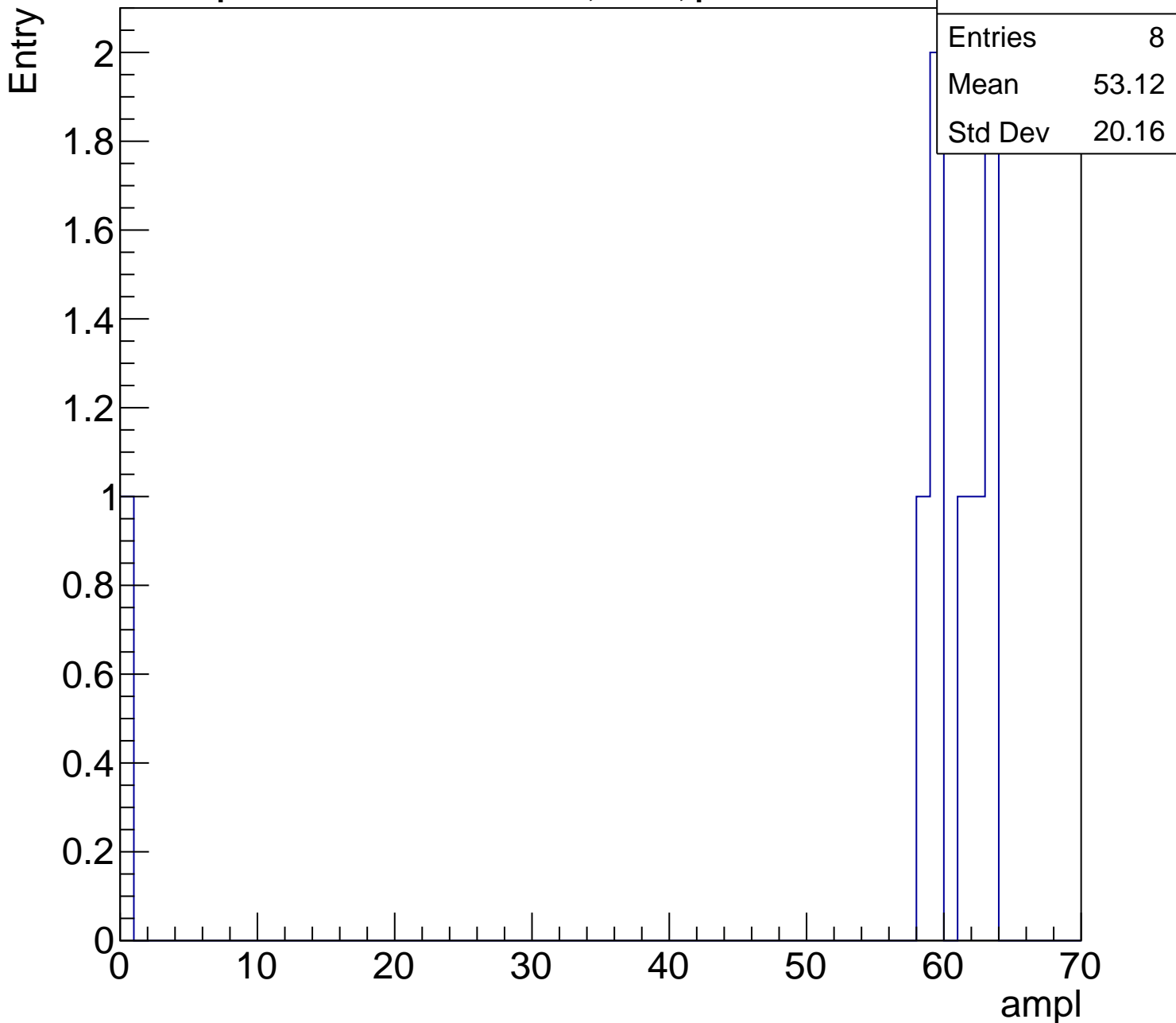
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.12
Std Dev	20.16

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch44, adc0

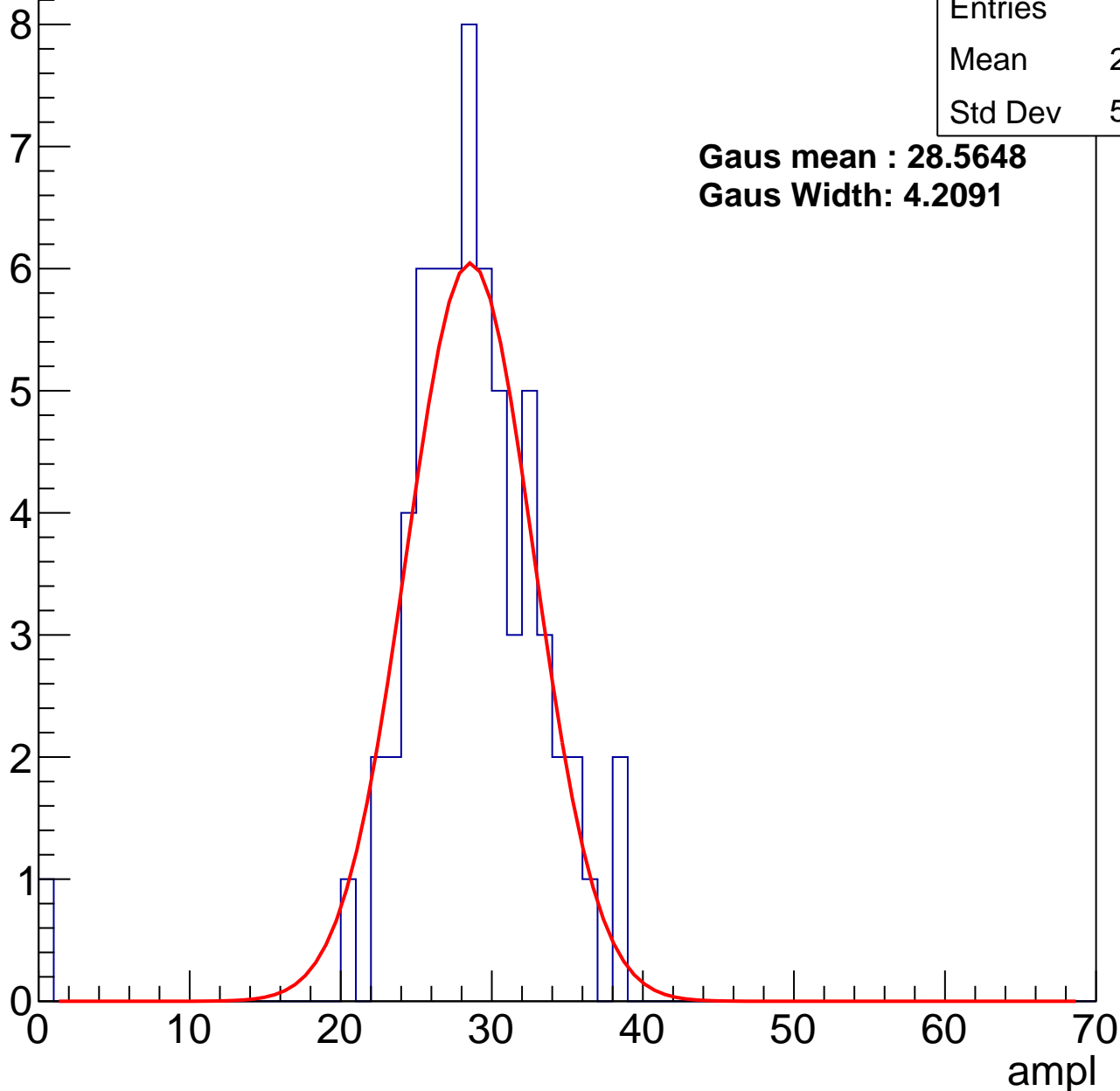
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.06
Std Dev	5.203

Gaus mean : 28.5648

Gaus Width: 4.2091



B1L103S, U24-ch44, adc1

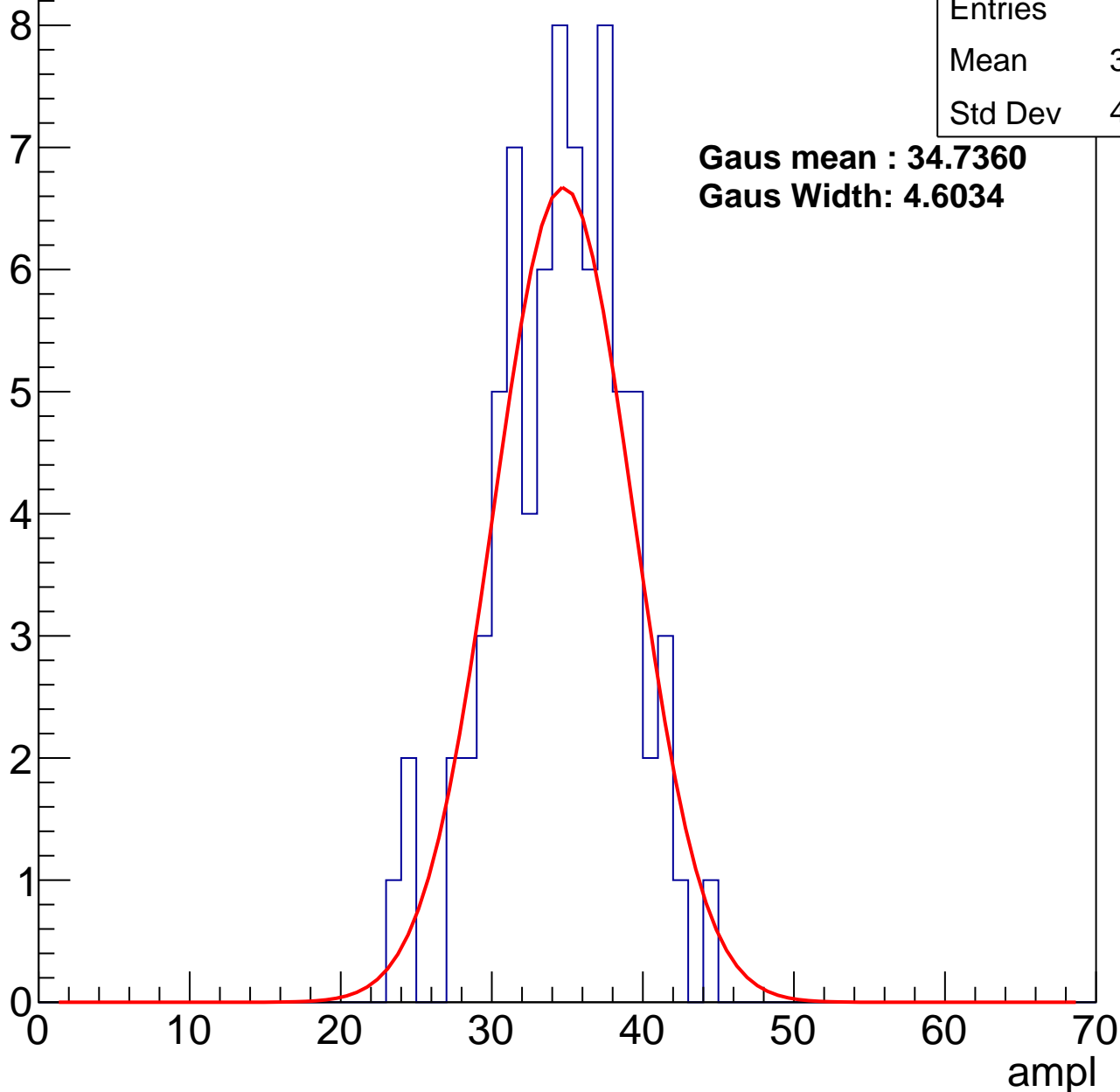
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.15
Std Dev	4.267

Gaus mean : 34.7360

Gaus Width: 4.6034



B1L103S, U24-ch44, adc2

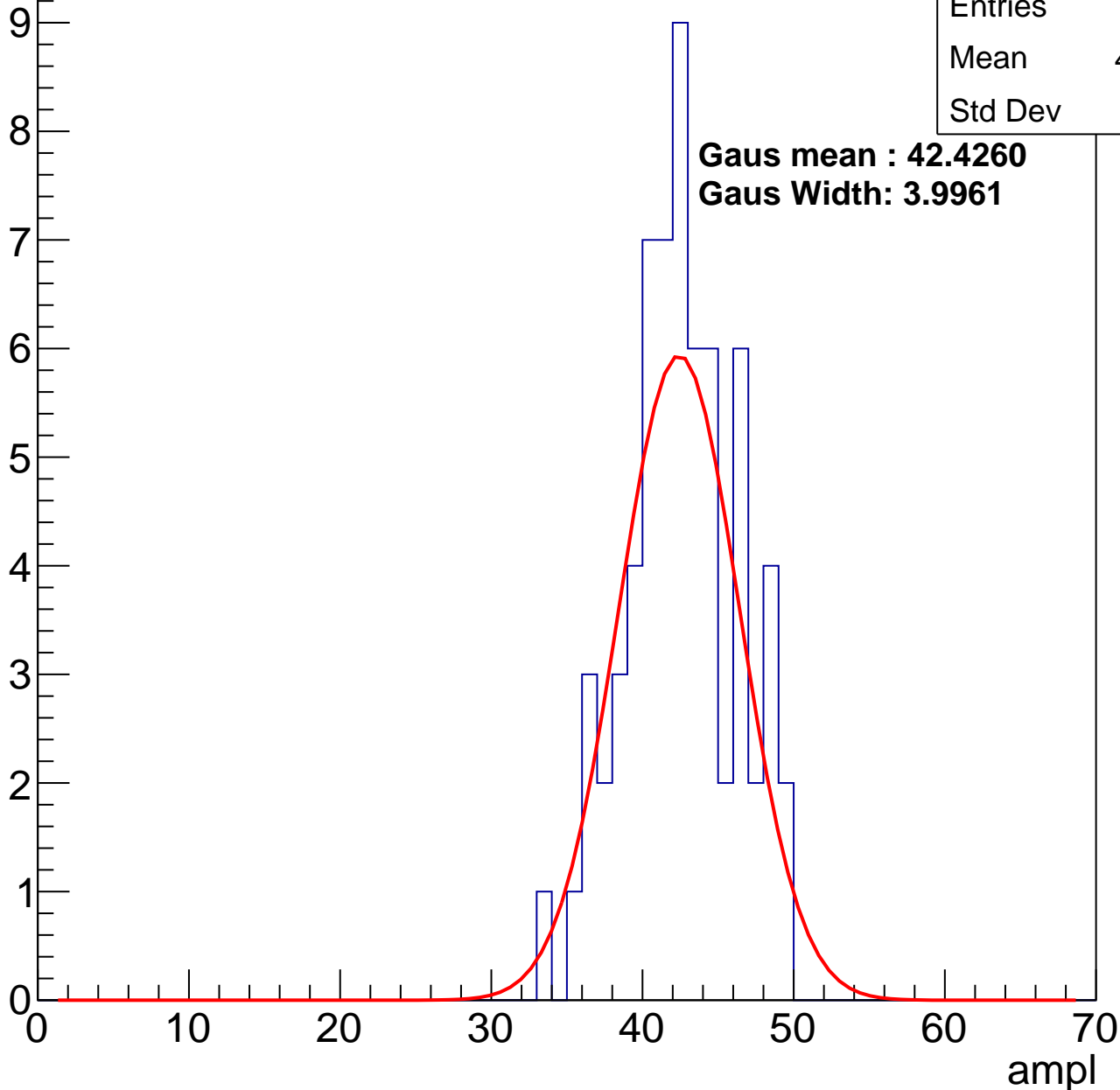
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.11
Std Dev	3.6

Gaus mean : 42.4260

Gaus Width: 3.9961

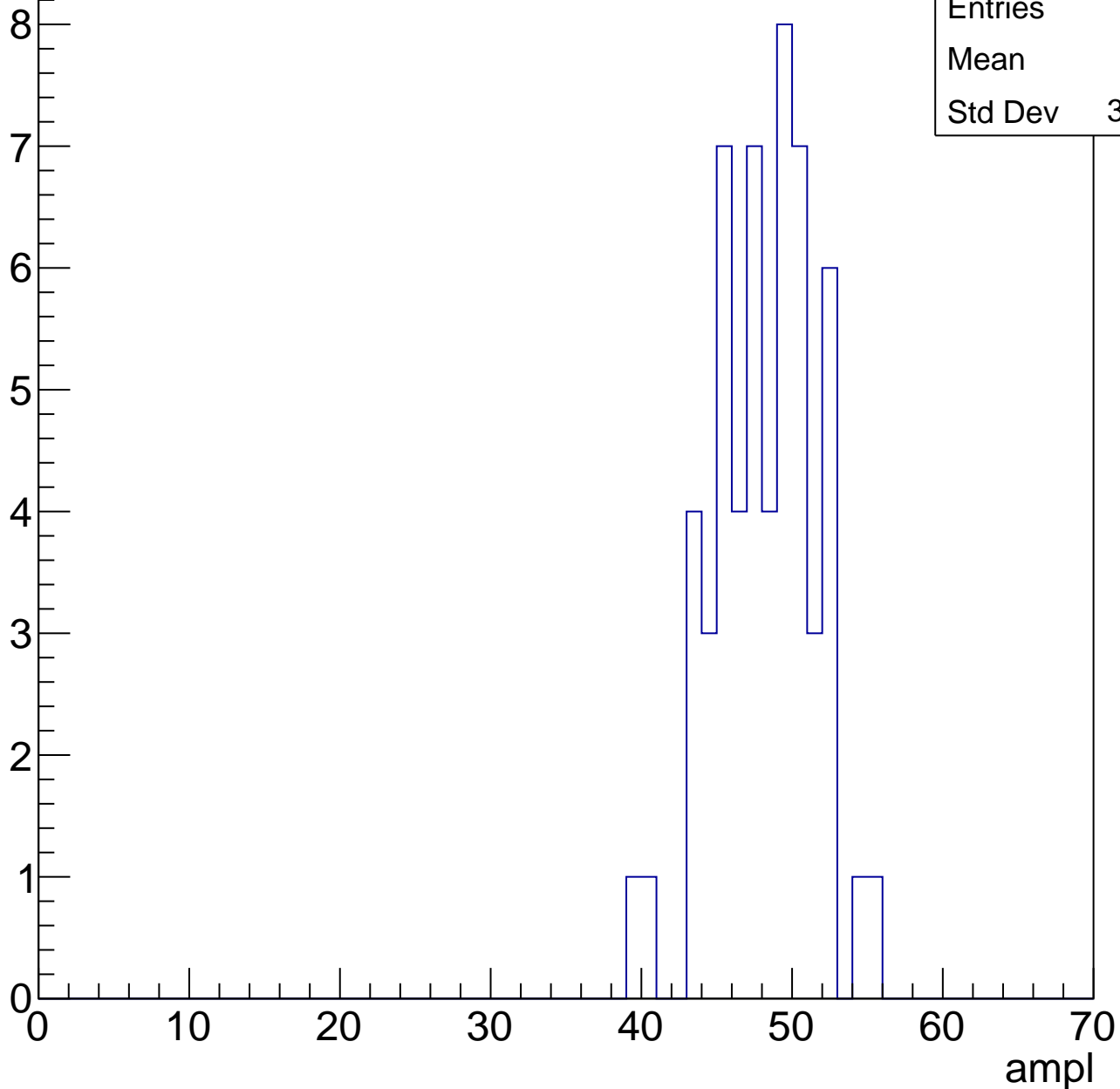


B1L103S, U24-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	47.7
Std Dev	3.293

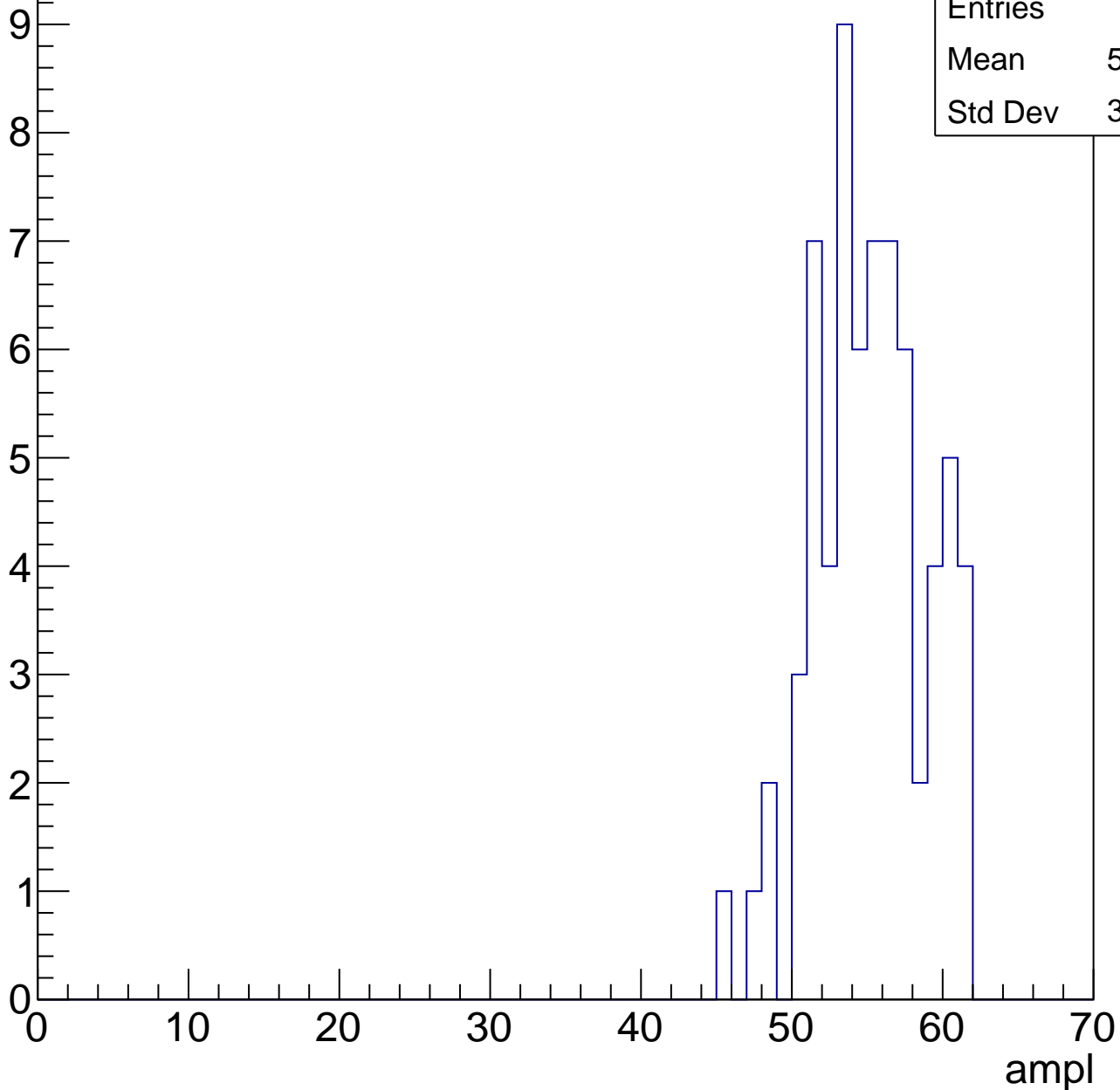


B1L103S, U24-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	54.69
Std Dev	3.643

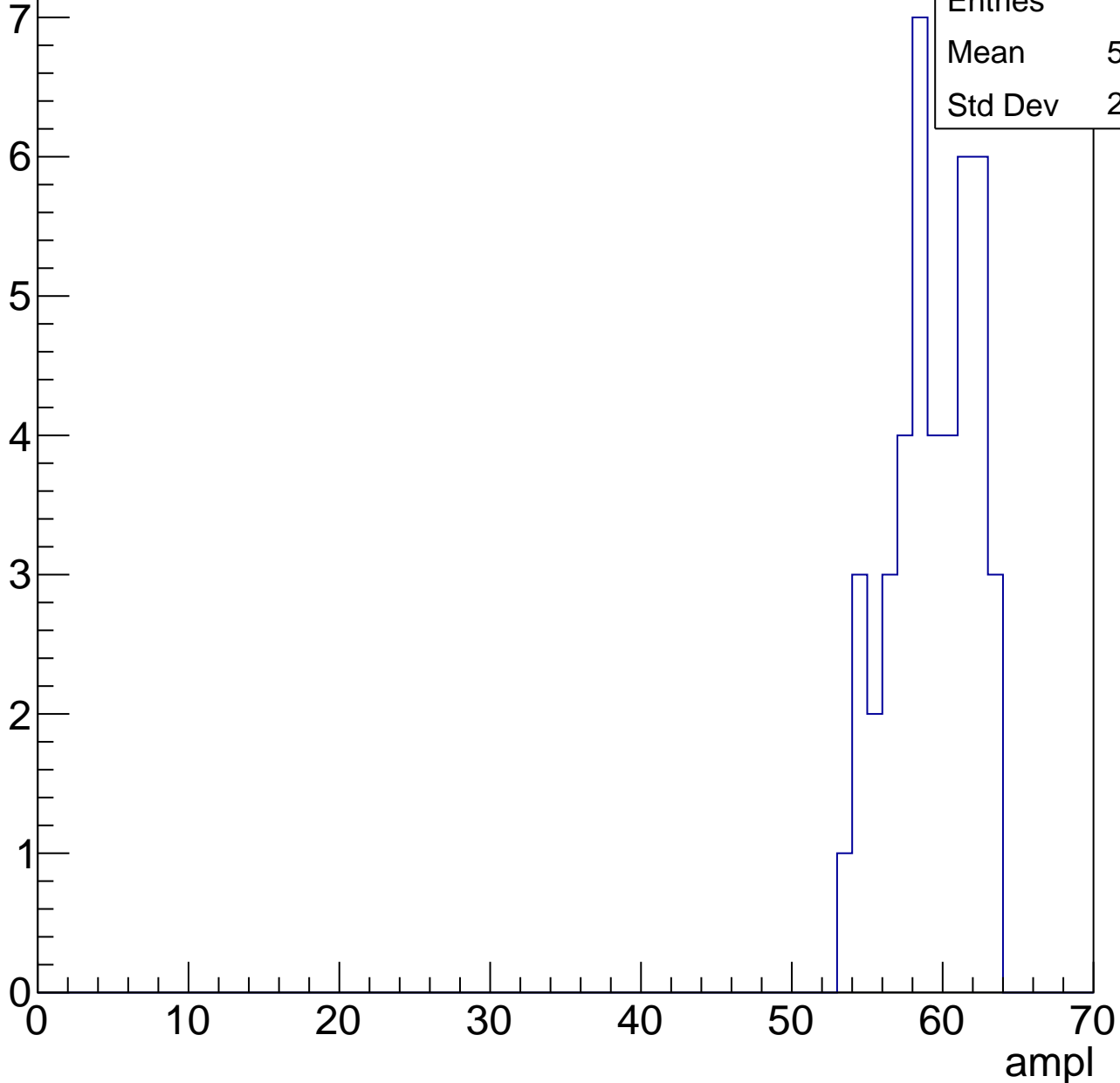


B1L103S, U24-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.84
Std Dev	2.736

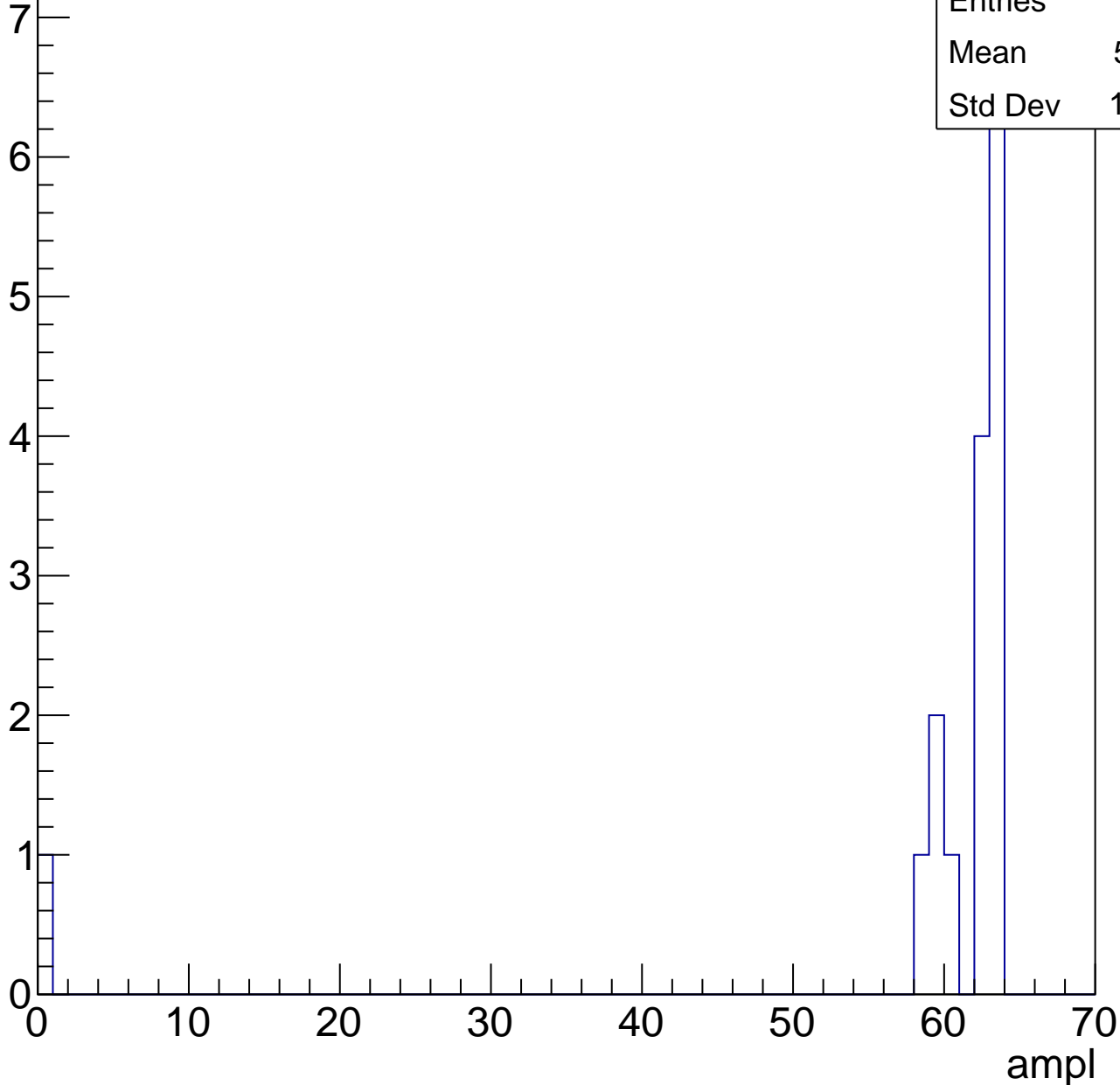


B1L103S, U24-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.81
Std Dev	15.02



B1L103S, U24-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch45, adc0

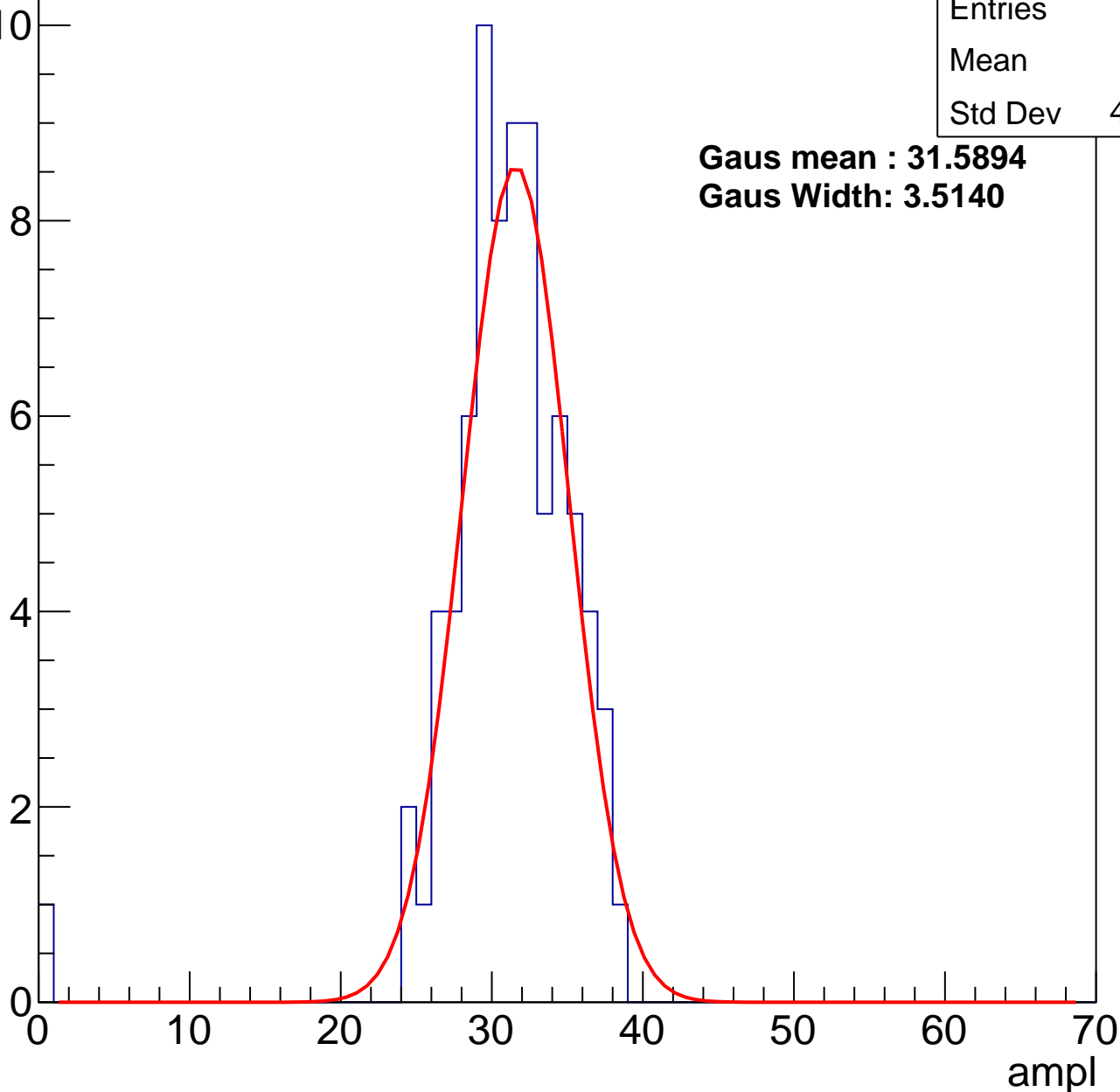
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	30.6
Std Dev	4.762

Gaus mean : 31.5894

Gaus Width: 3.5140



B1L103S, U24-ch45, adc1

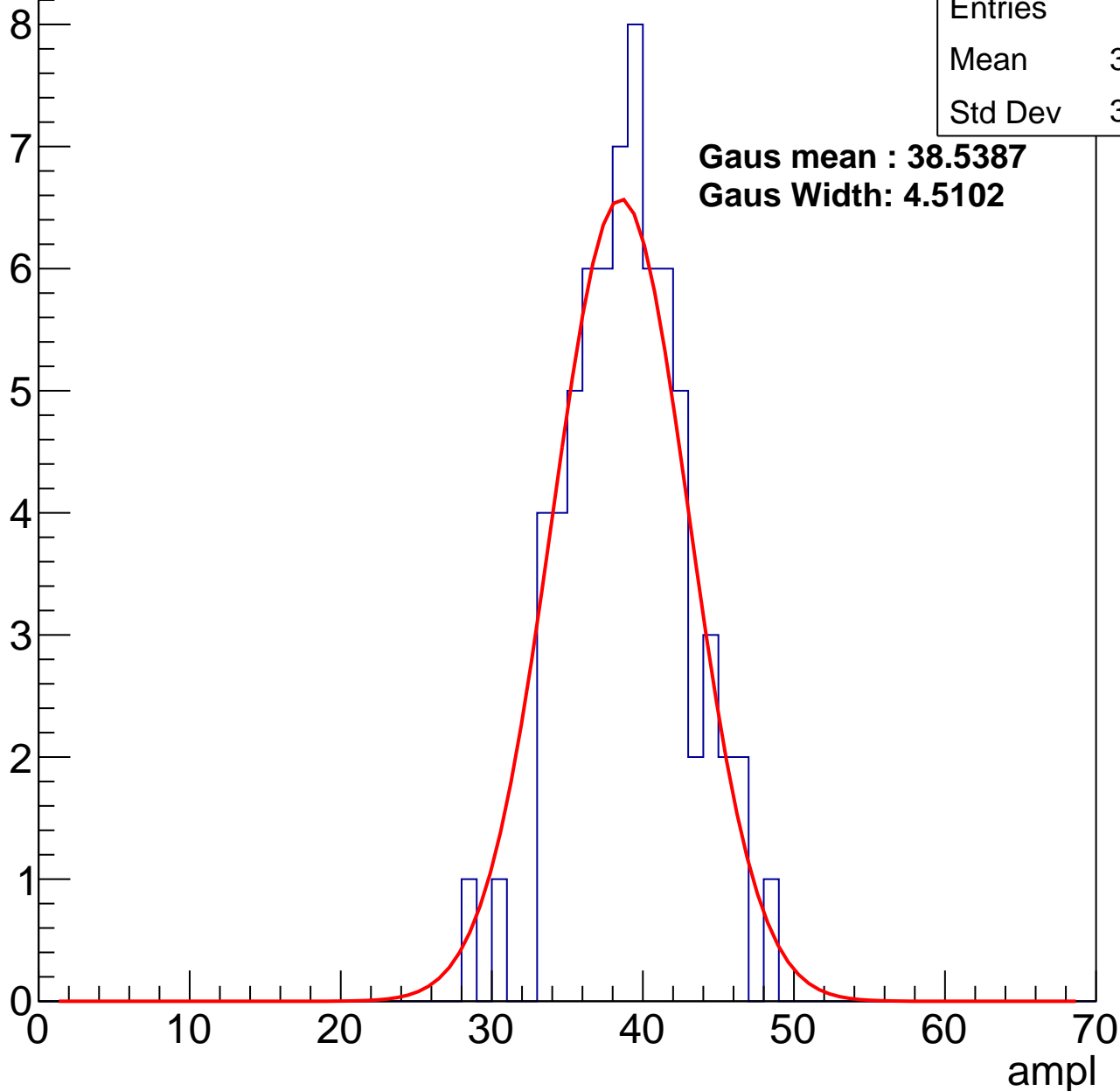
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	38.57
Std Dev	3.865

Gaus mean : 38.5387

Gaus Width: 4.5102



B1L103S, U24-ch45, adc2

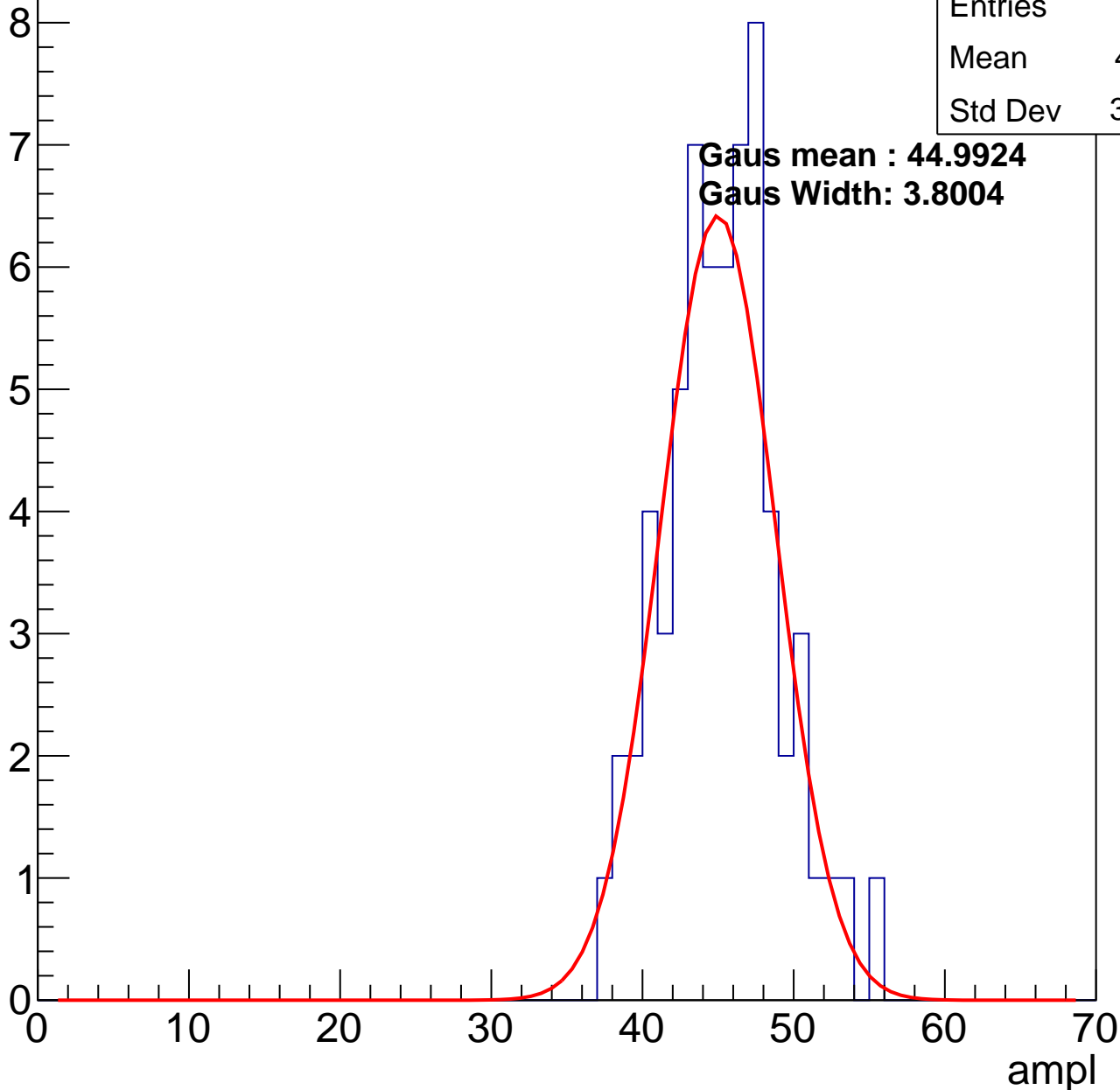
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	44.81
Std Dev	3.729

Gaus mean : 44.9924

Gaus Width: 3.8004

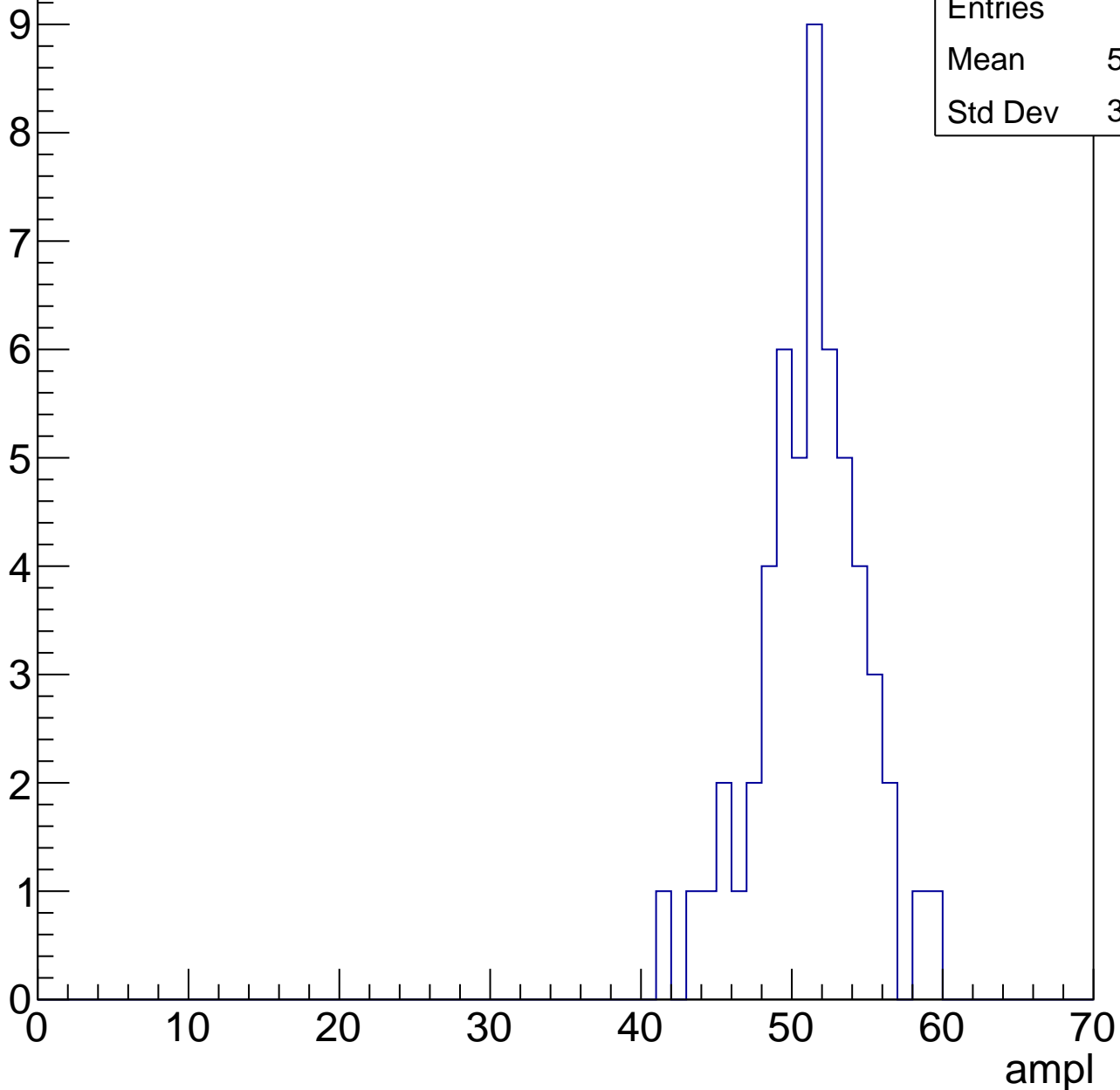


B1L103S, U24-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

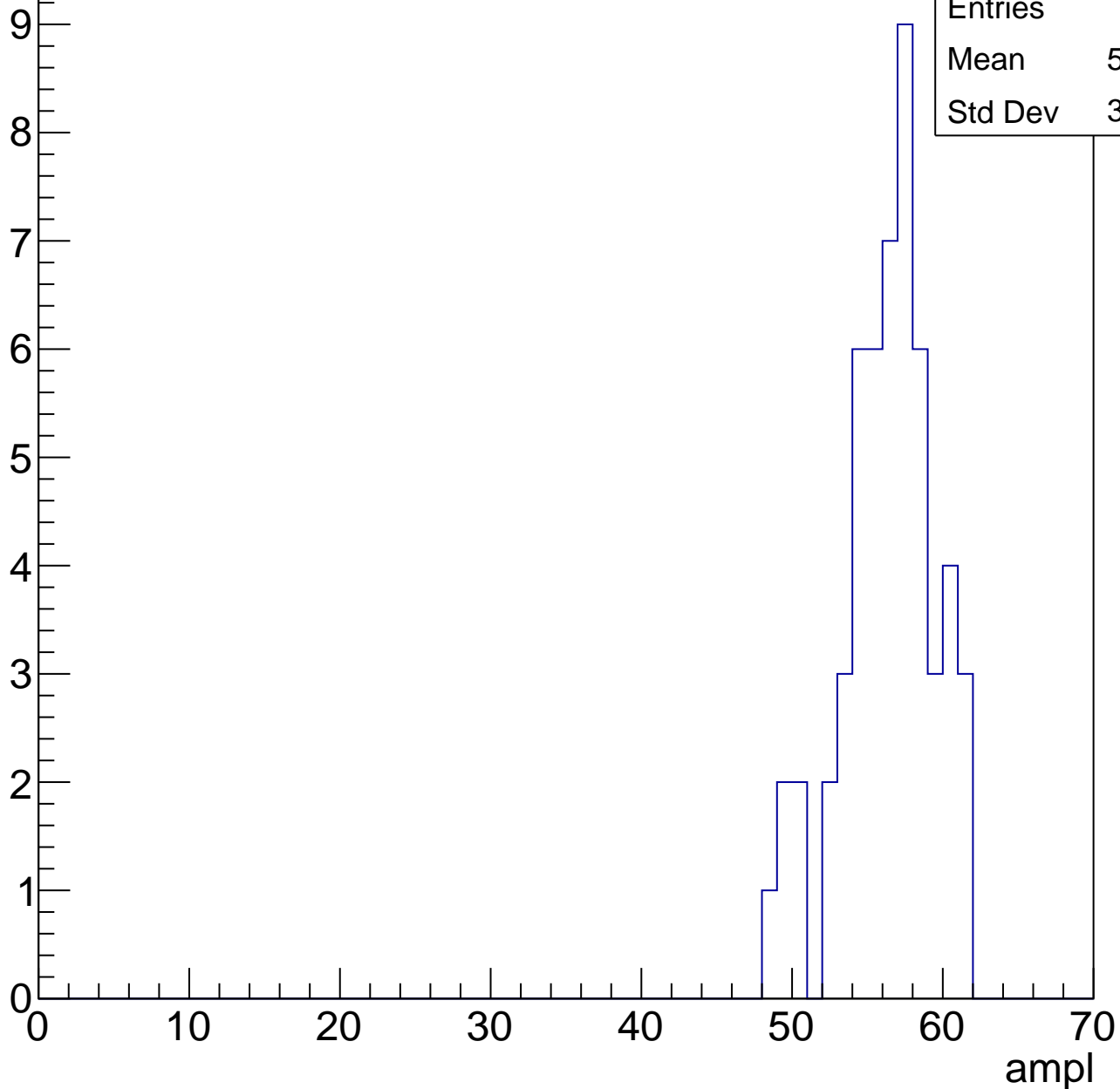
Entries	54
Mean	50.74
Std Dev	3.539



B1L103S, U24-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

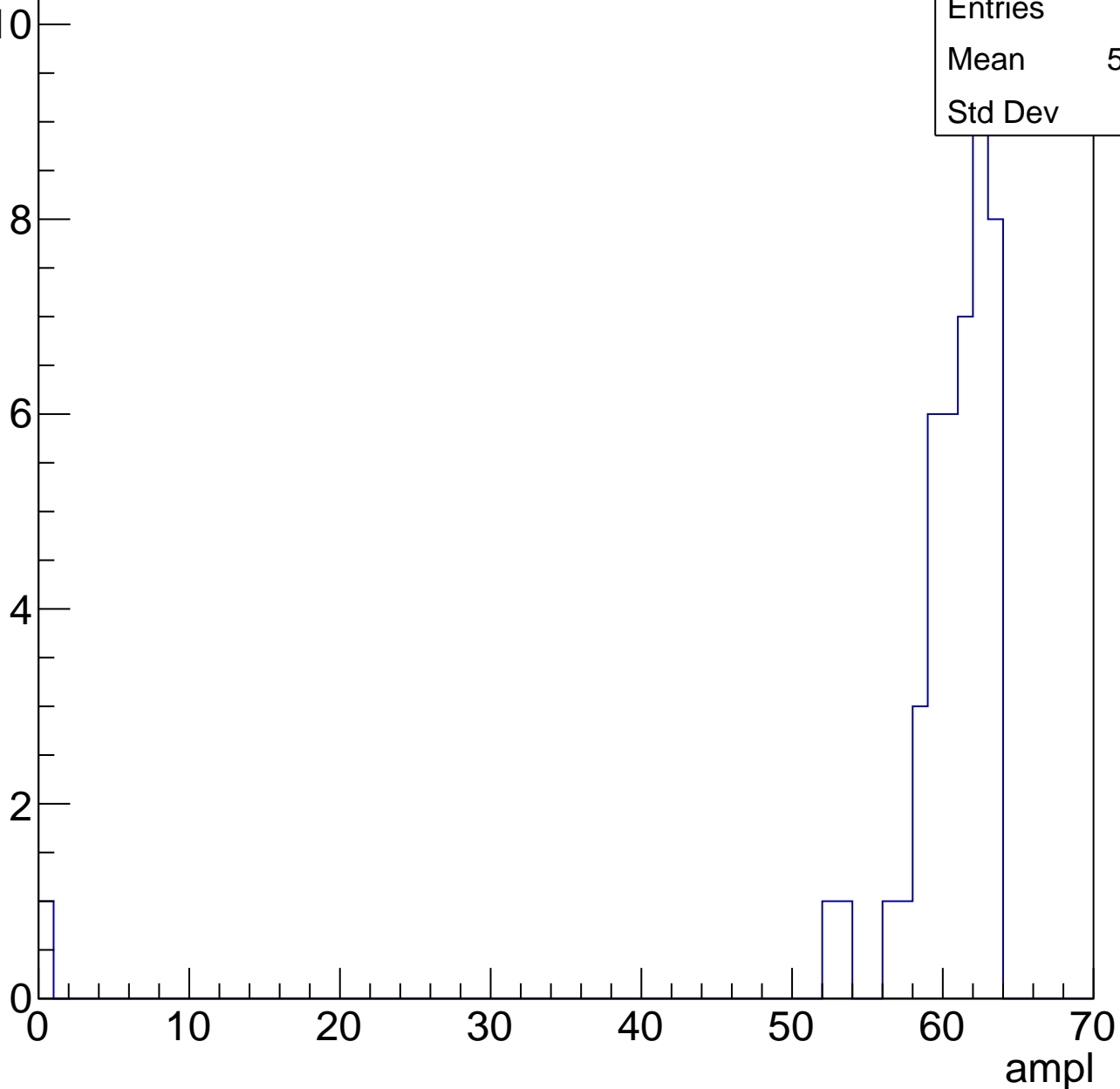


B1L103S, U24-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	59.04
Std Dev	9.23



B1L103S, U24-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch46, adc0

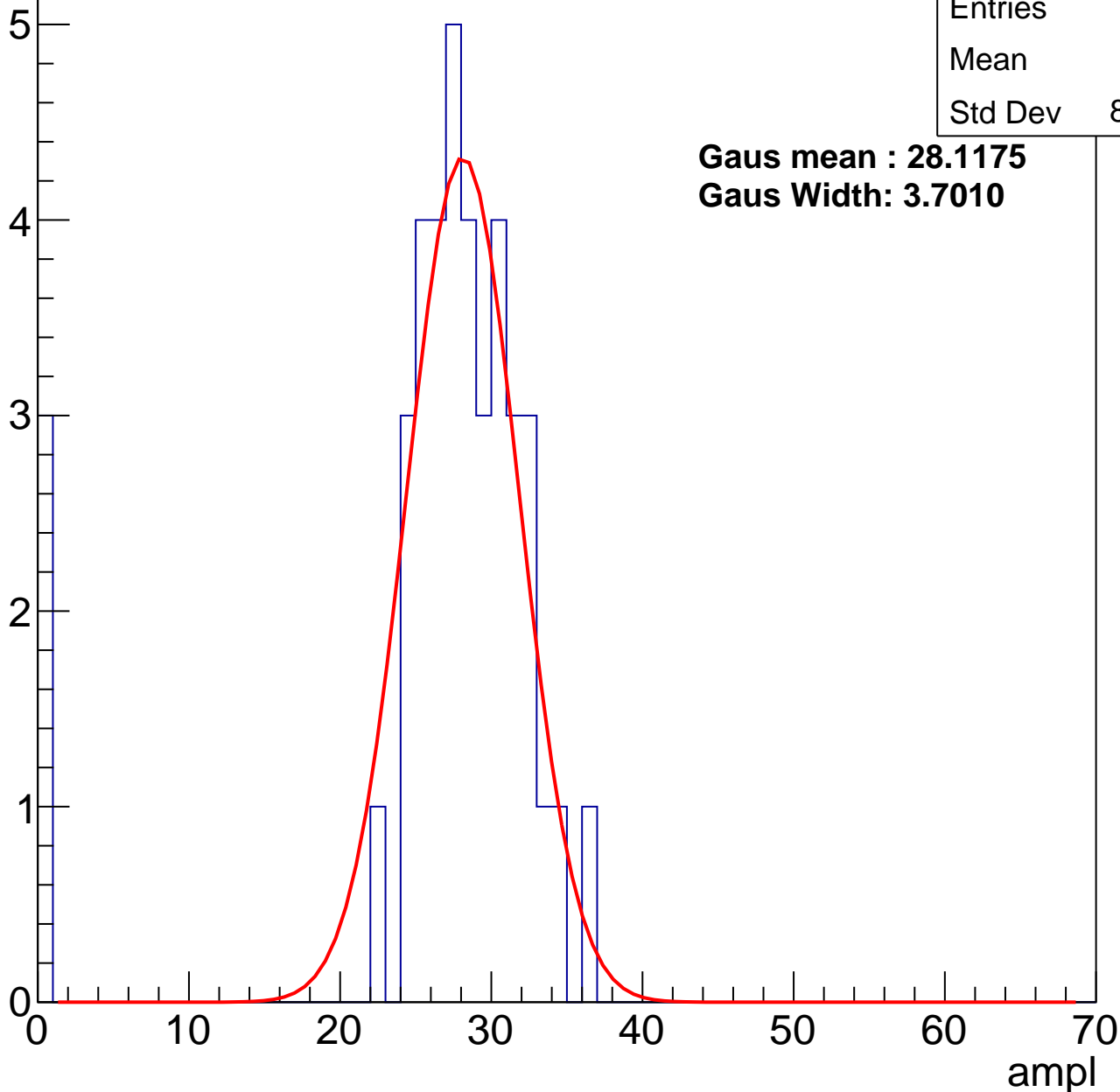
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	26.1
Std Dev	8.012

Gaus mean : 28.1175

Gaus Width: 3.7010



B1L103S, U24-ch46, adc1

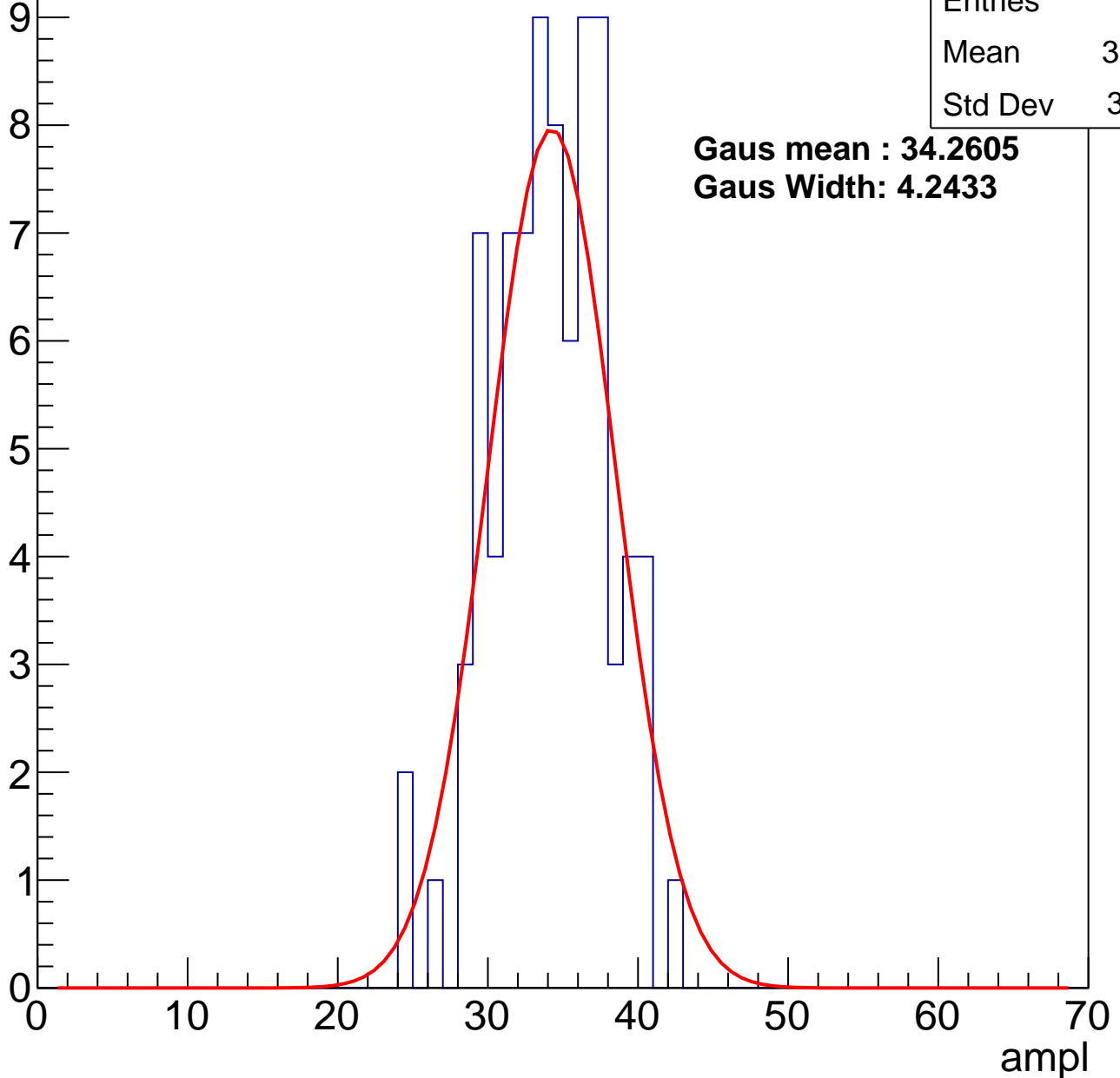
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	33.69
Std Dev	3.751

Gaus mean : 34.2605

Gaus Width: 4.2433



B1L103S, U24-ch46, adc2

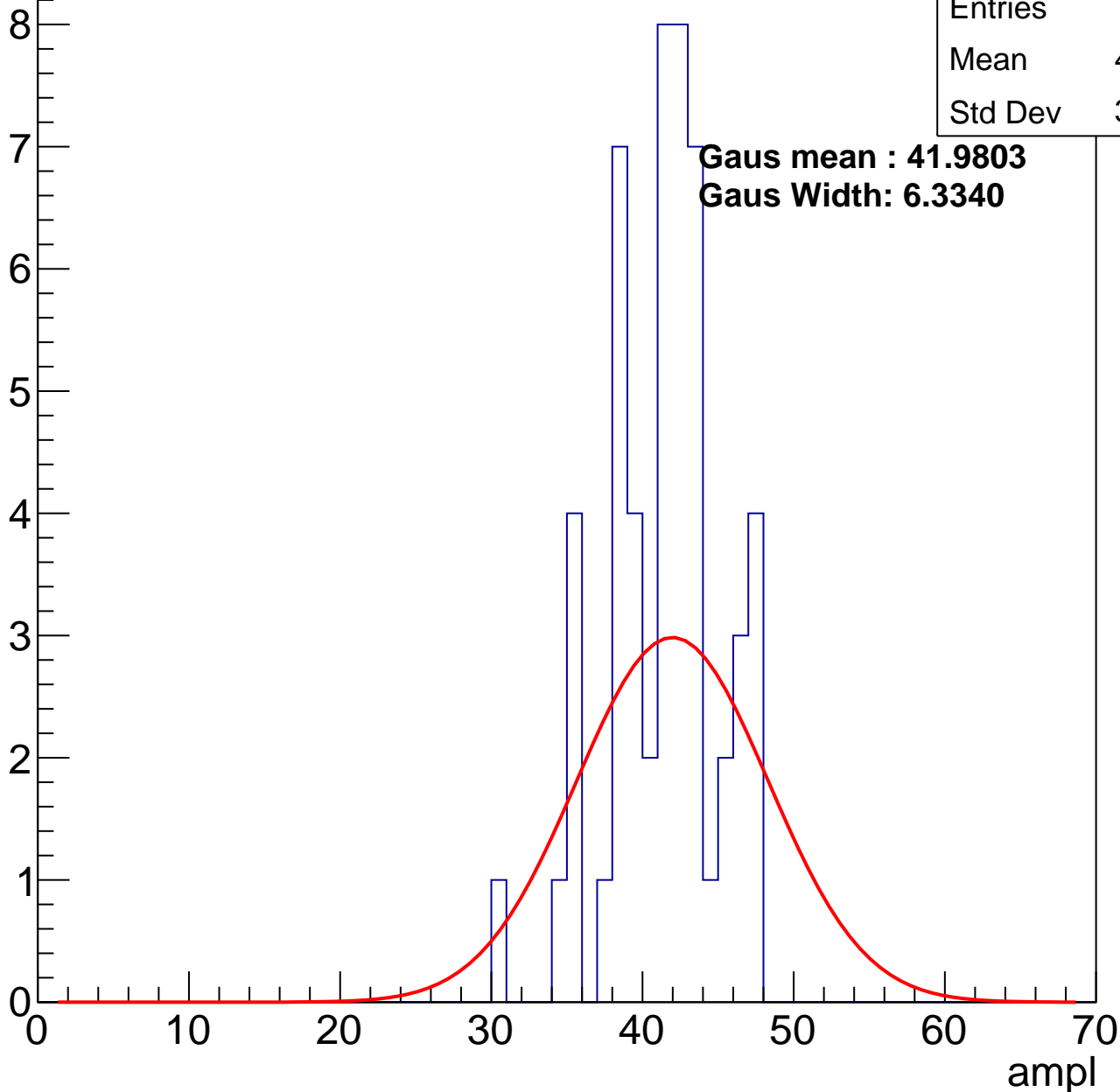
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	40.91
Std Dev	3.651

Gaus mean : 41.9803

Gaus Width: 6.3340

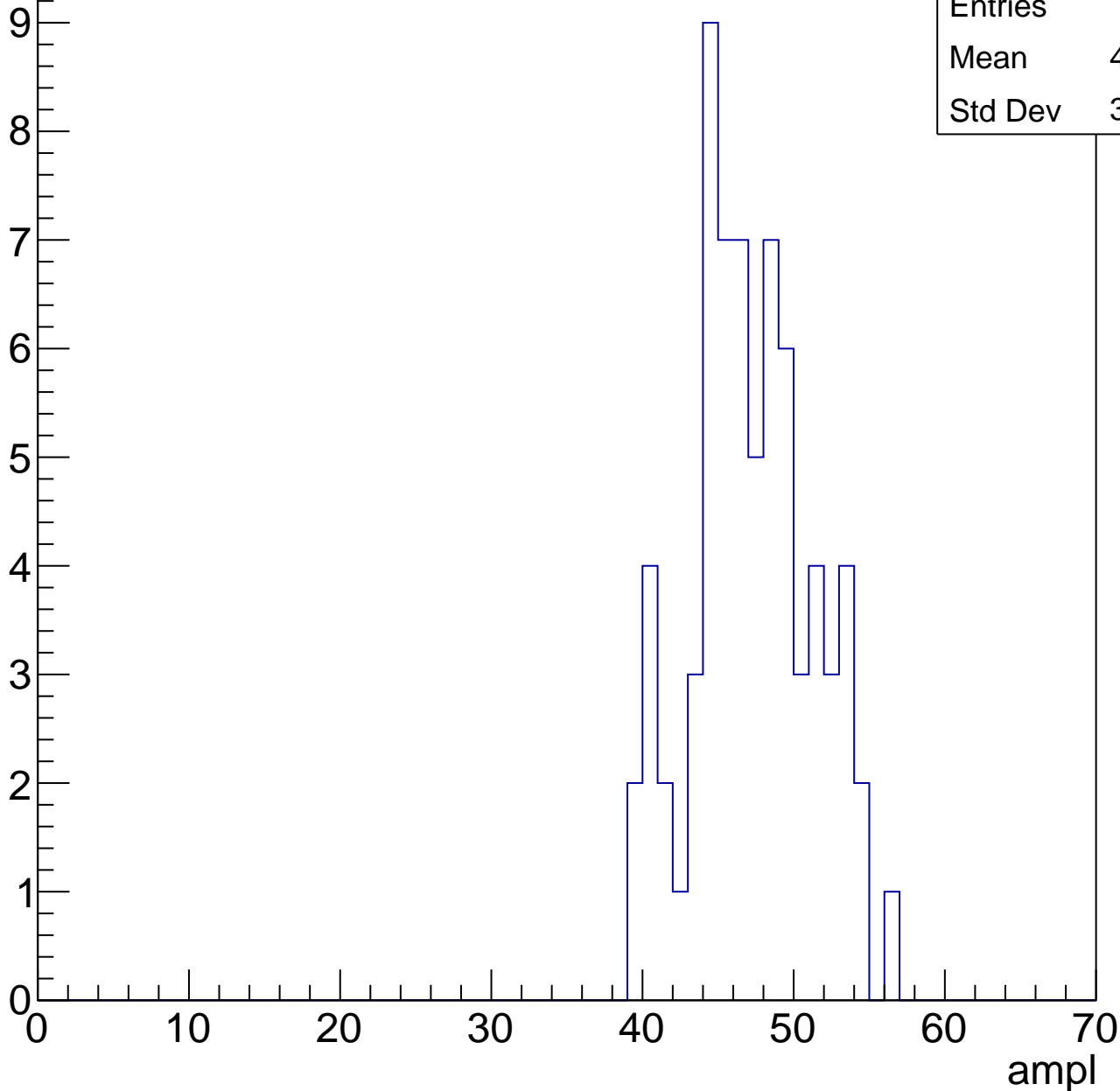


B1L103S, U24-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	46.79
Std Dev	3.967

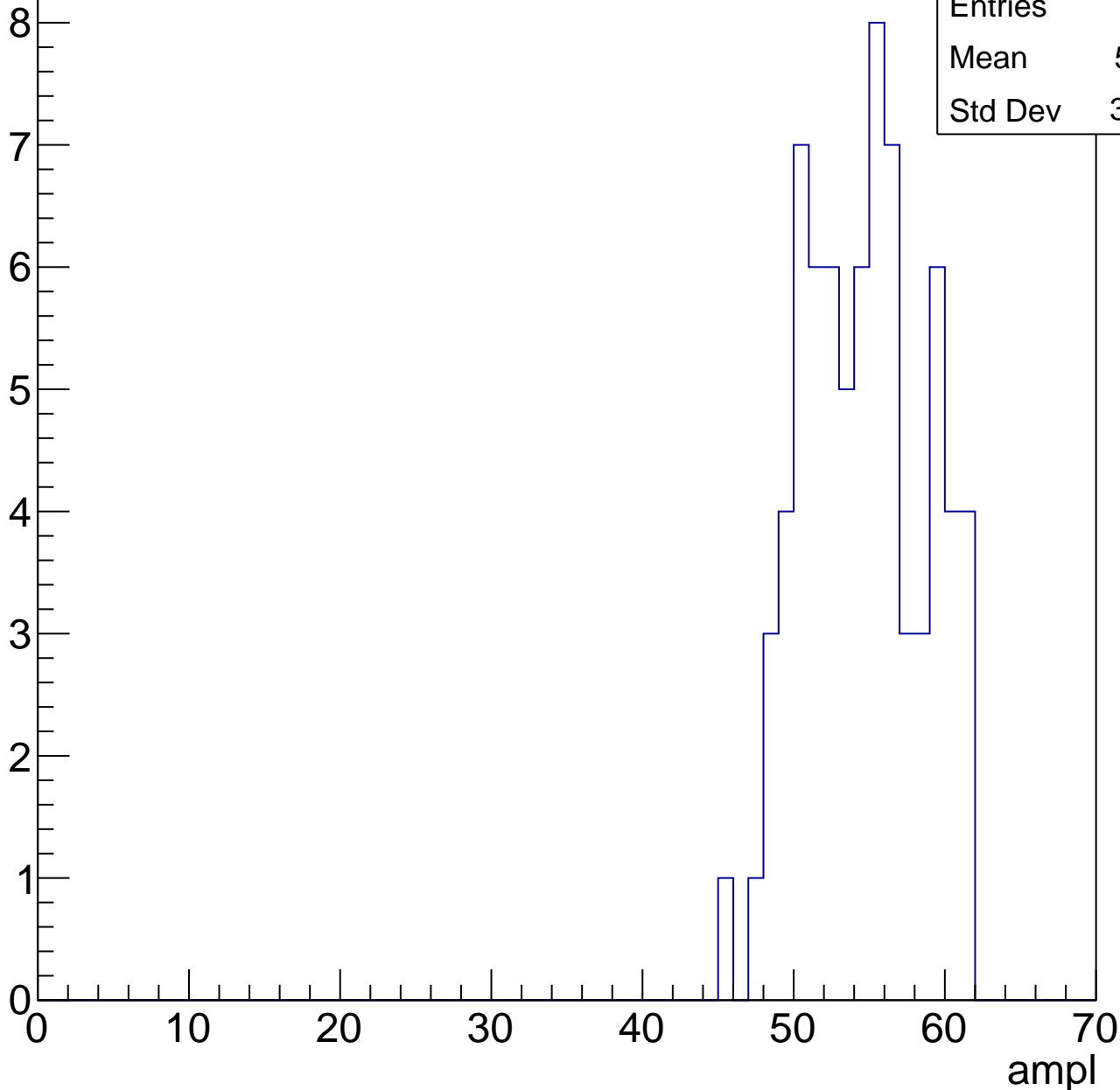


B1L103S, U24-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	54.11
Std Dev	3.913

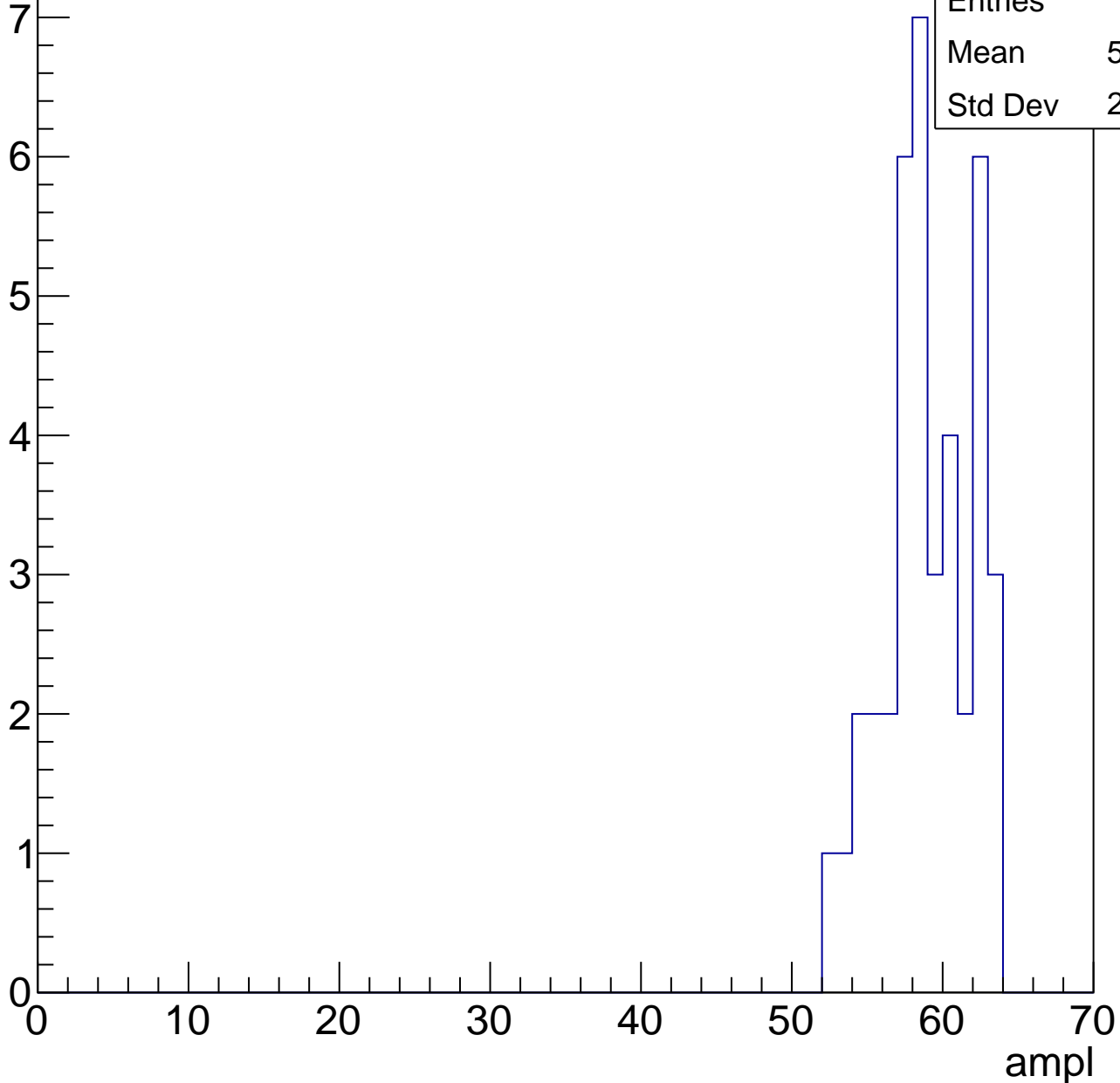


B1L103S, U24-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.54
Std Dev	2.872

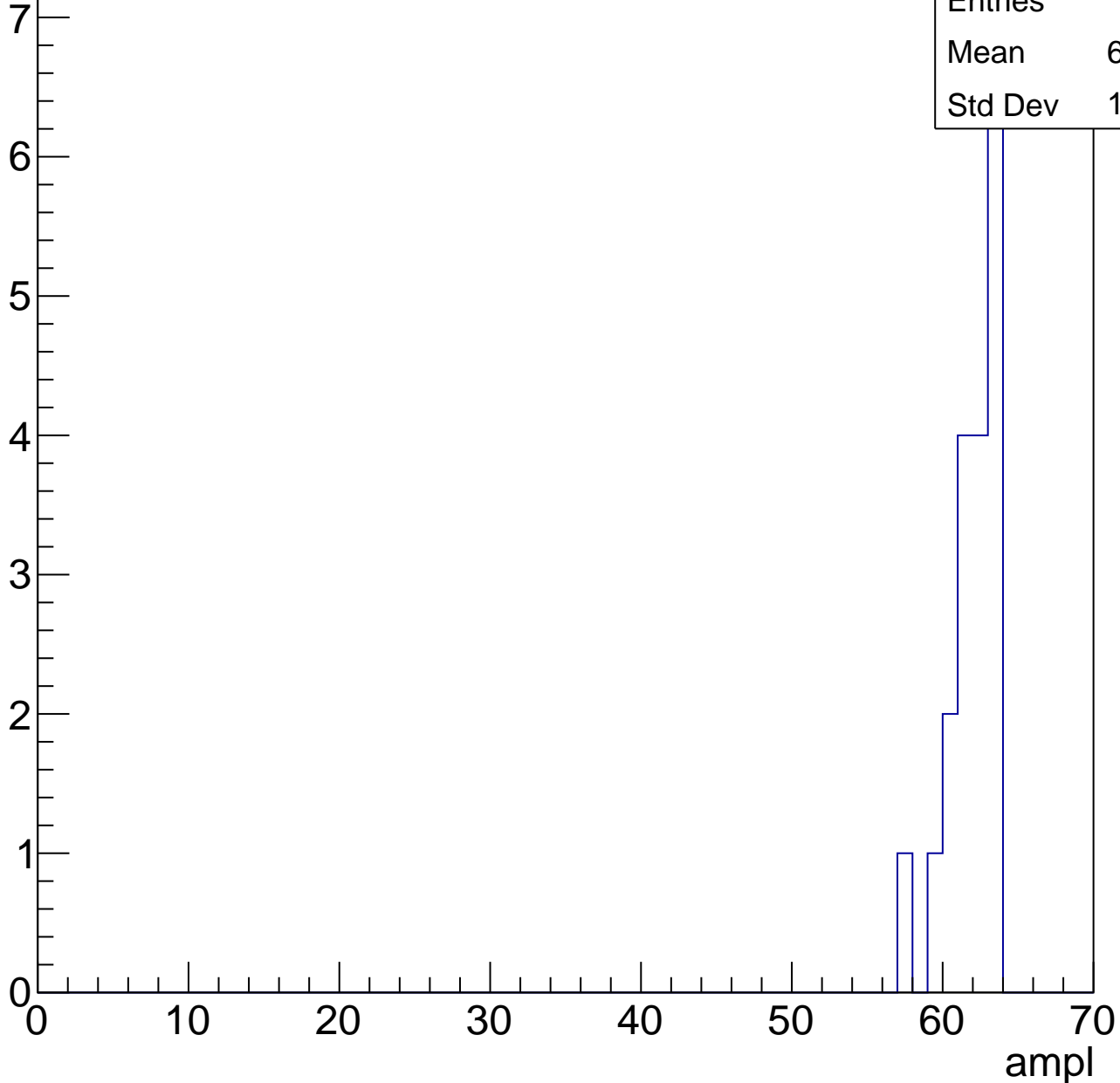


B1L103S, U24-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	61.53
Std Dev	1.602



B1L103S, U24-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch47, adc0

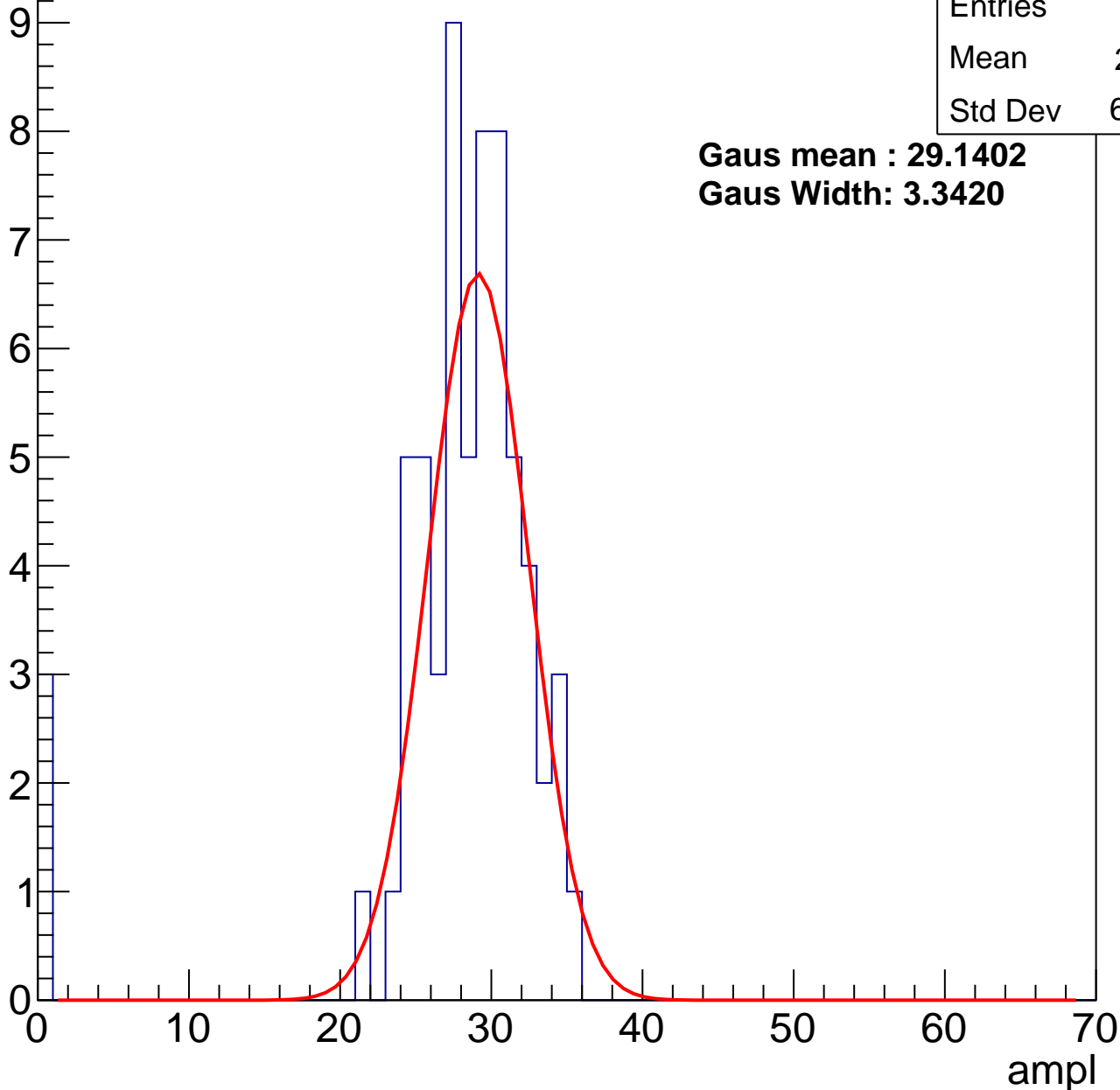
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.11
Std Dev	6.759

Gaus mean : 29.1402

Gaus Width: 3.3420



B1L103S, U24-ch47, adc1

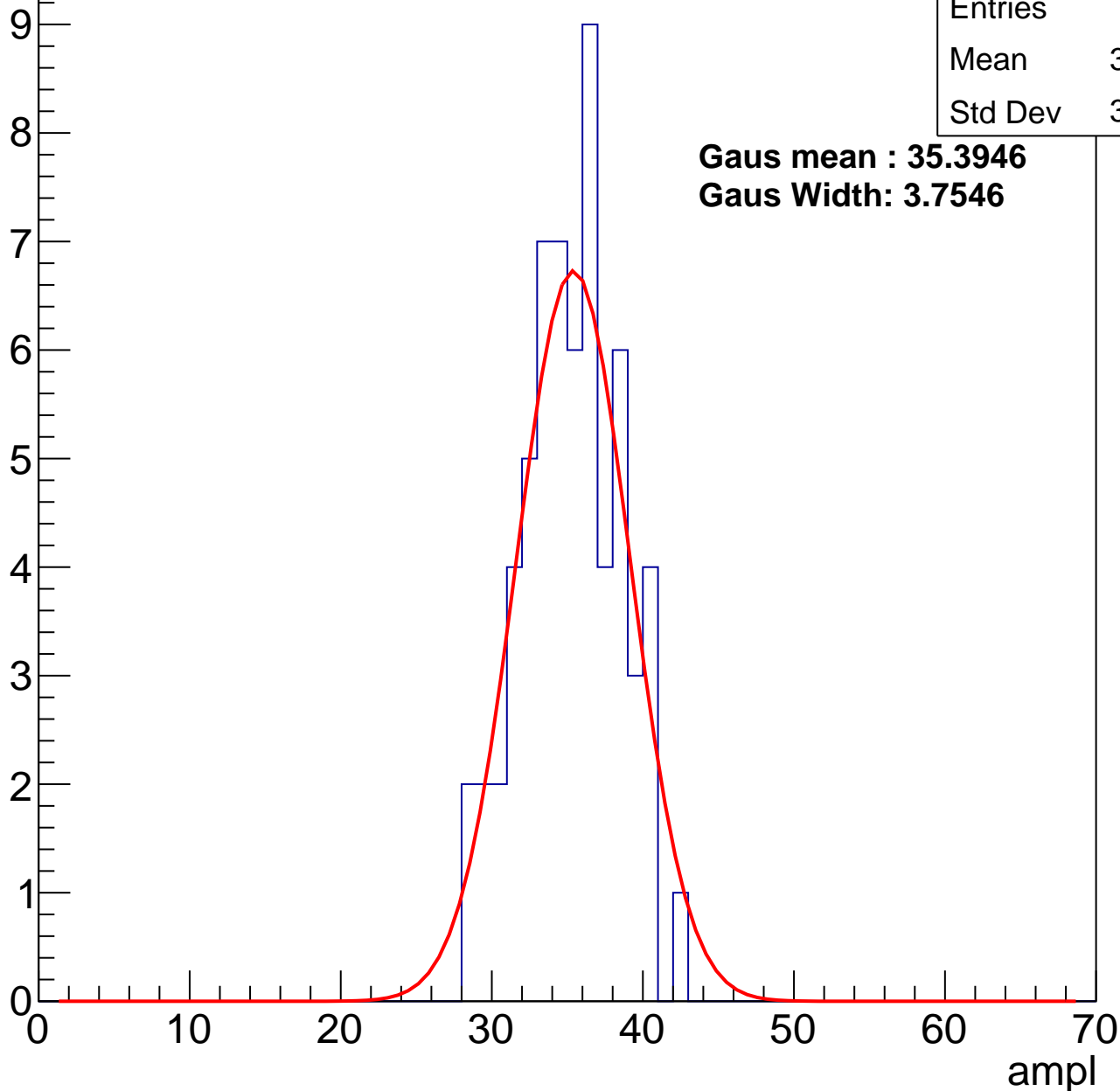
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	34.77
Std Dev	3.205

Gaus mean : 35.3946

Gaus Width: 3.7546



B1L103S, U24-ch47, adc2

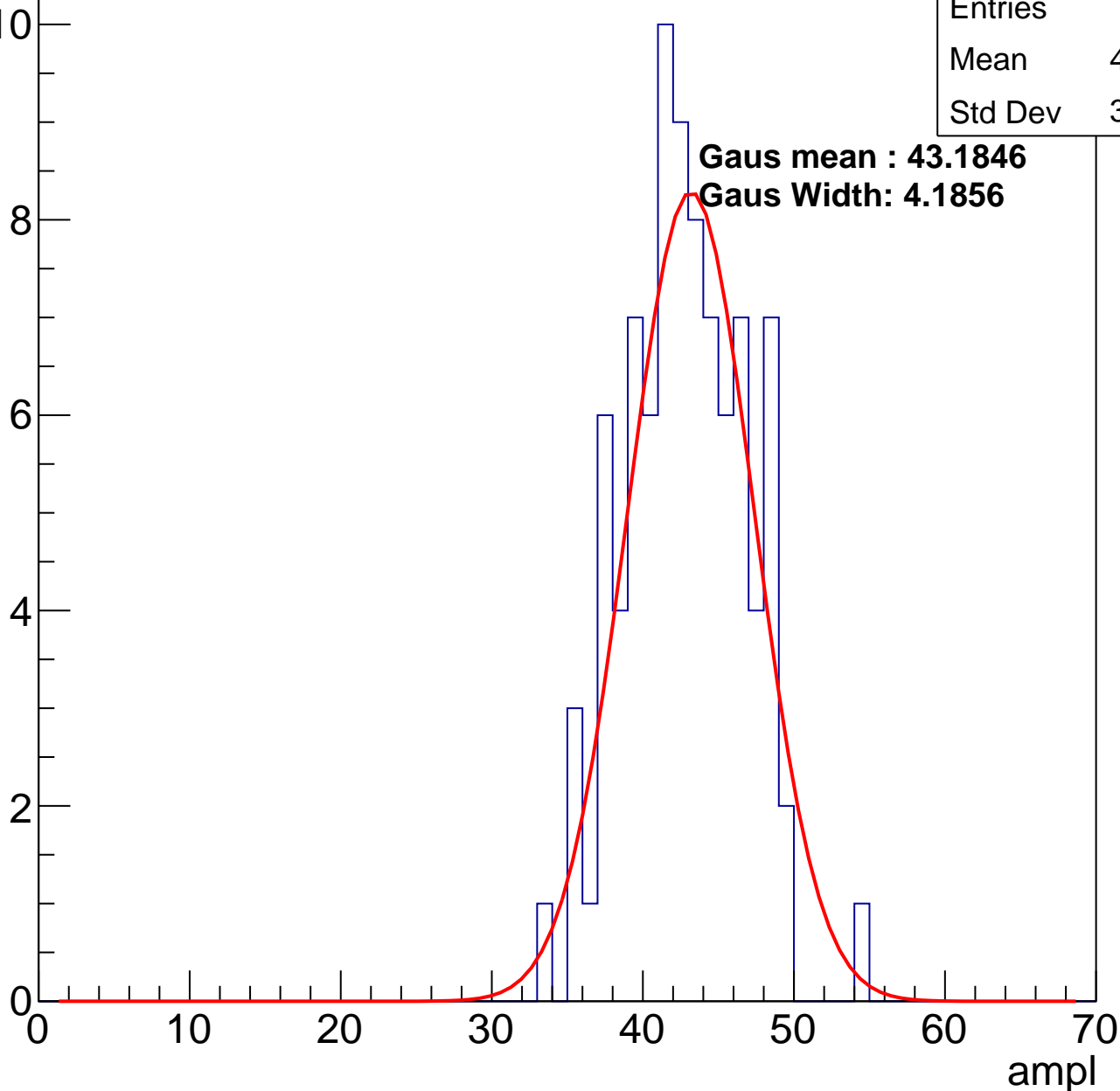
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	42.35
Std Dev	3.909

Gaus mean : 43.1846

Gaus Width: 4.1856

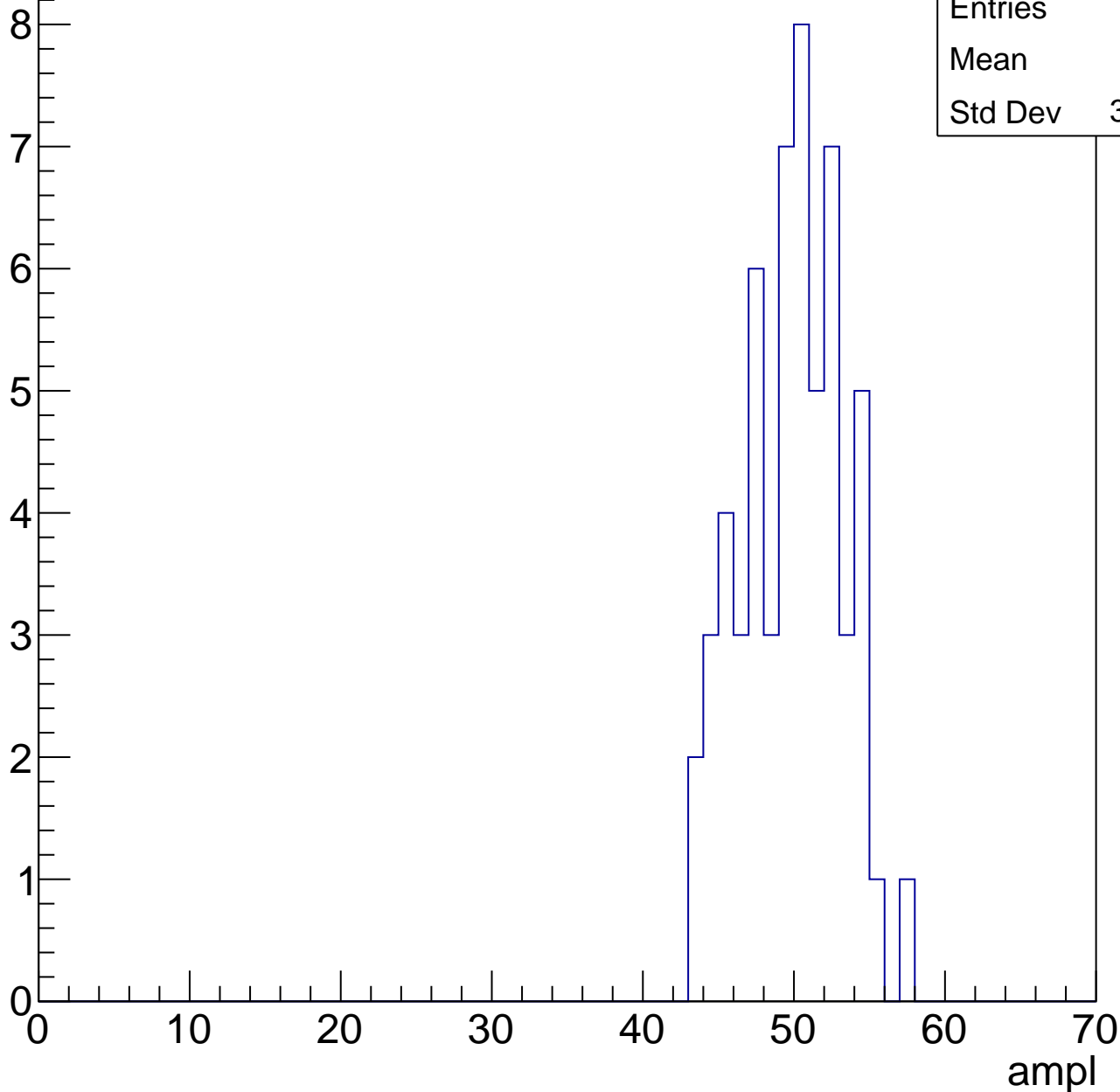


B1L103S, U24-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.4
Std Dev	3.269



B1L103S, U24-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	55.95
Std Dev	3.083

Entry

10

8

6

4

2

0

0

10

20

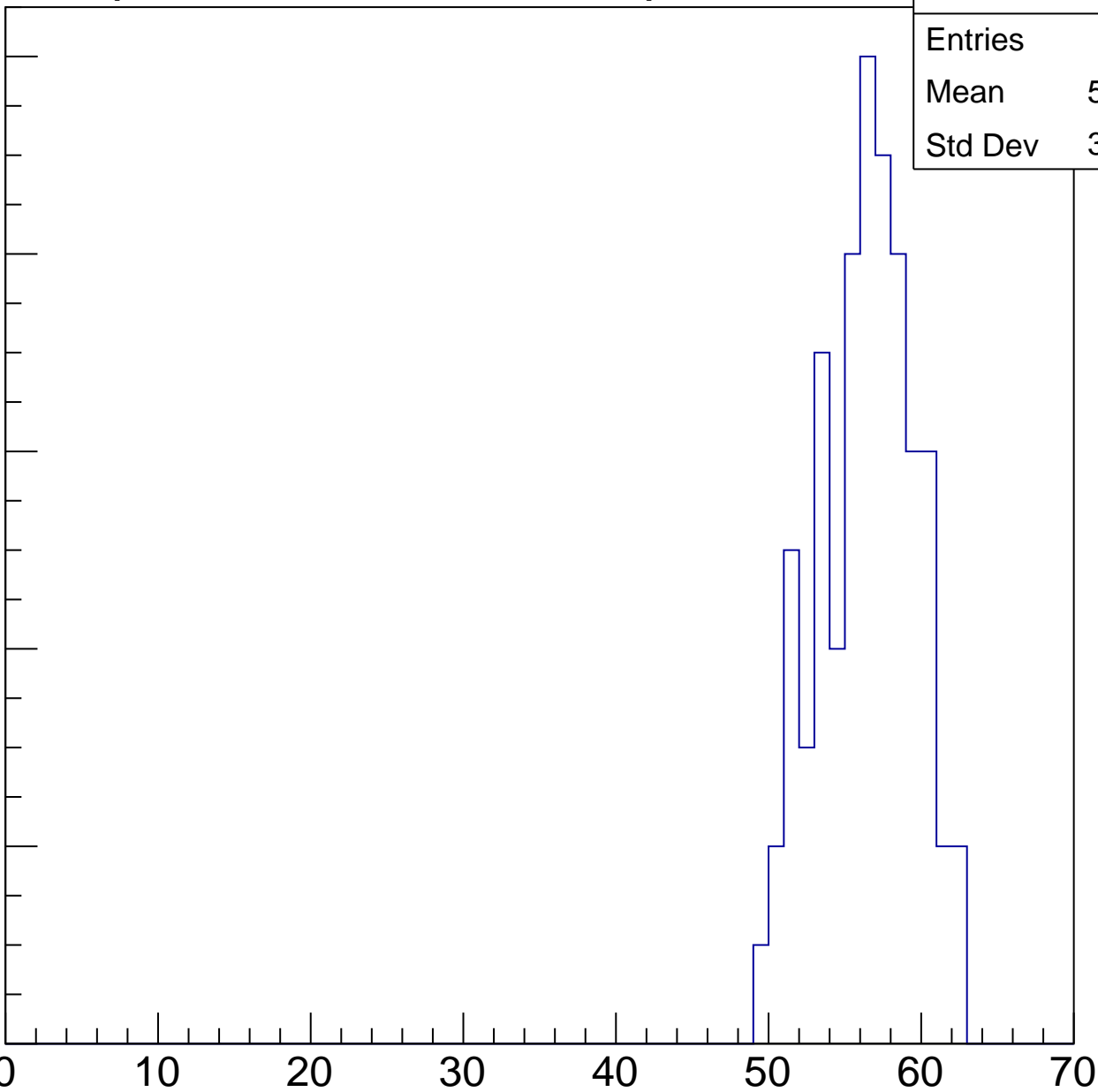
30

40

50

60

ampl

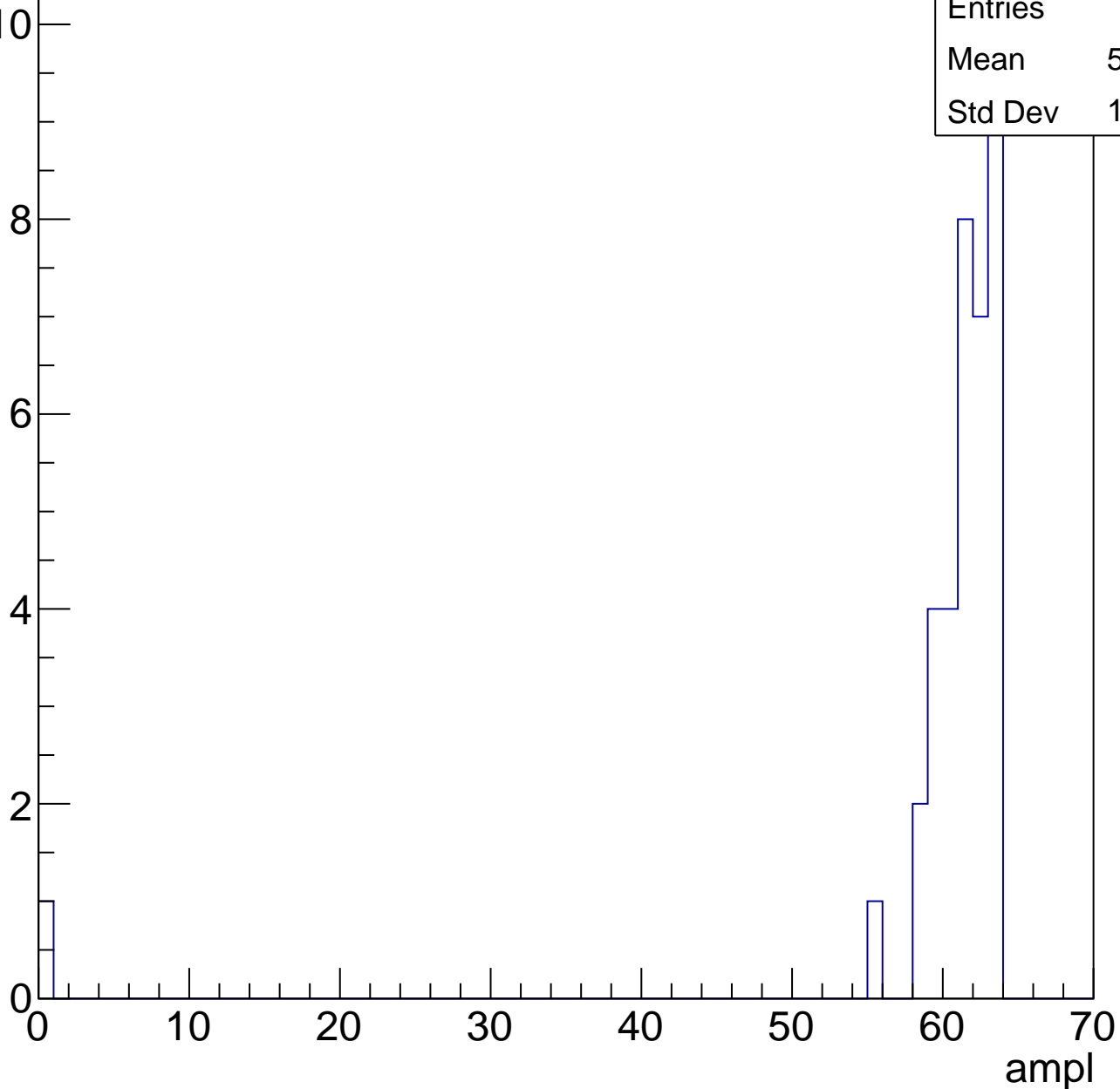


B1L103S, U24-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	59.43
Std Dev	10.07



B1L103S, U24-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch48, adc0

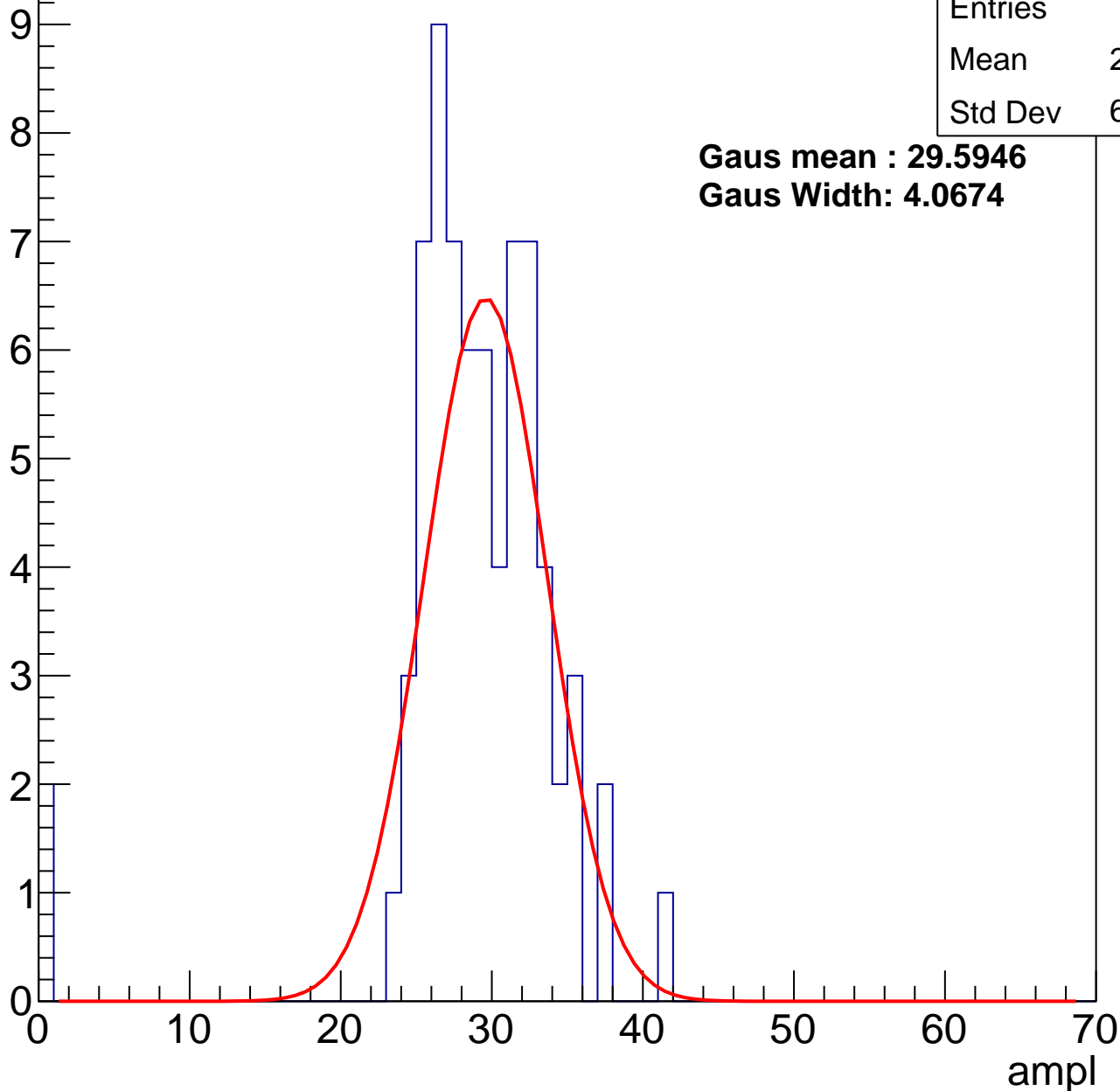
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.39
Std Dev	6.032

Gaus mean : 29.5946

Gaus Width: 4.0674



B1L103S, U24-ch48, adc1

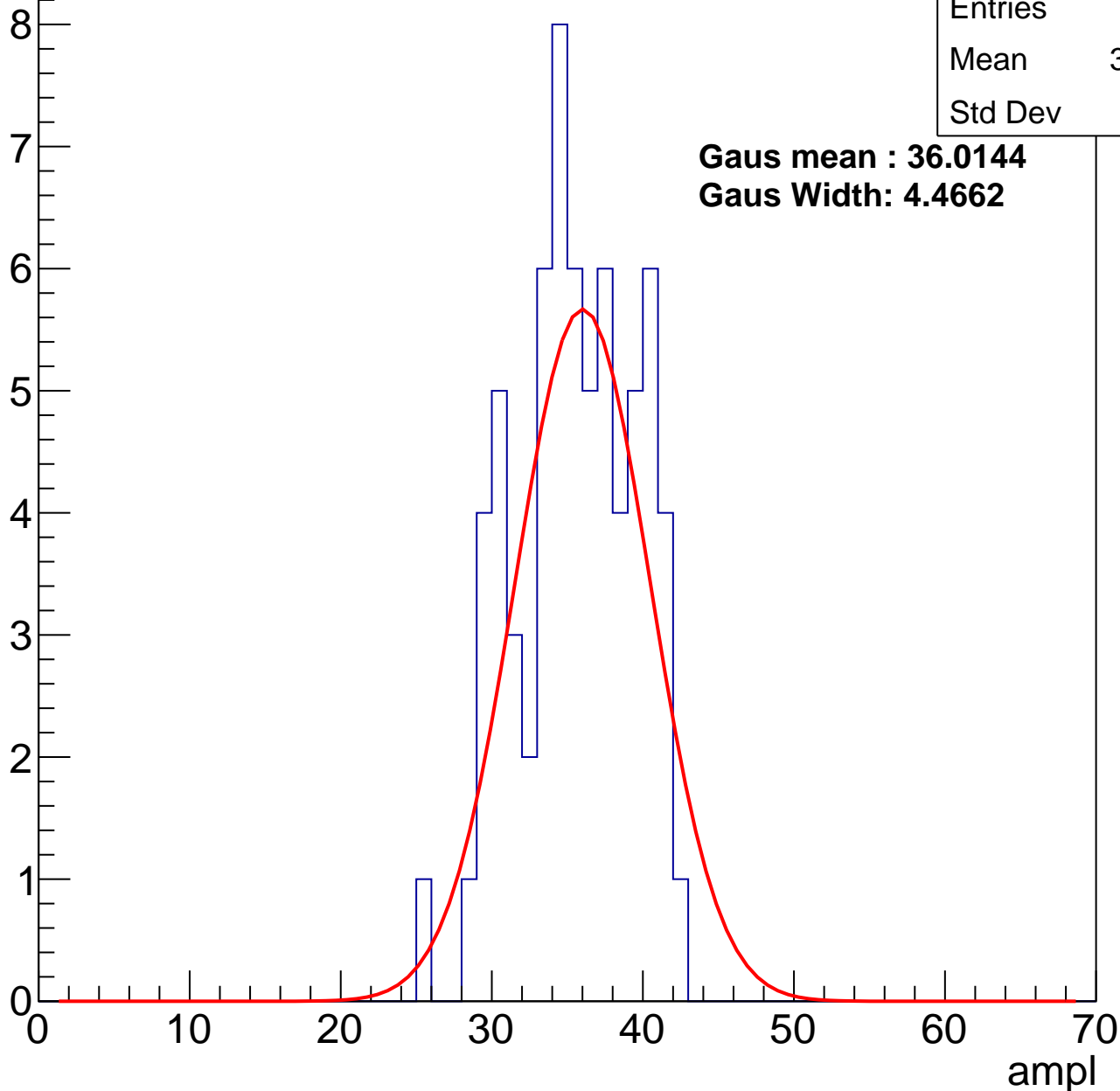
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.09
Std Dev	3.87

Gaus mean : 36.0144

Gaus Width: 4.4662



B1L103S, U24-ch48, adc2

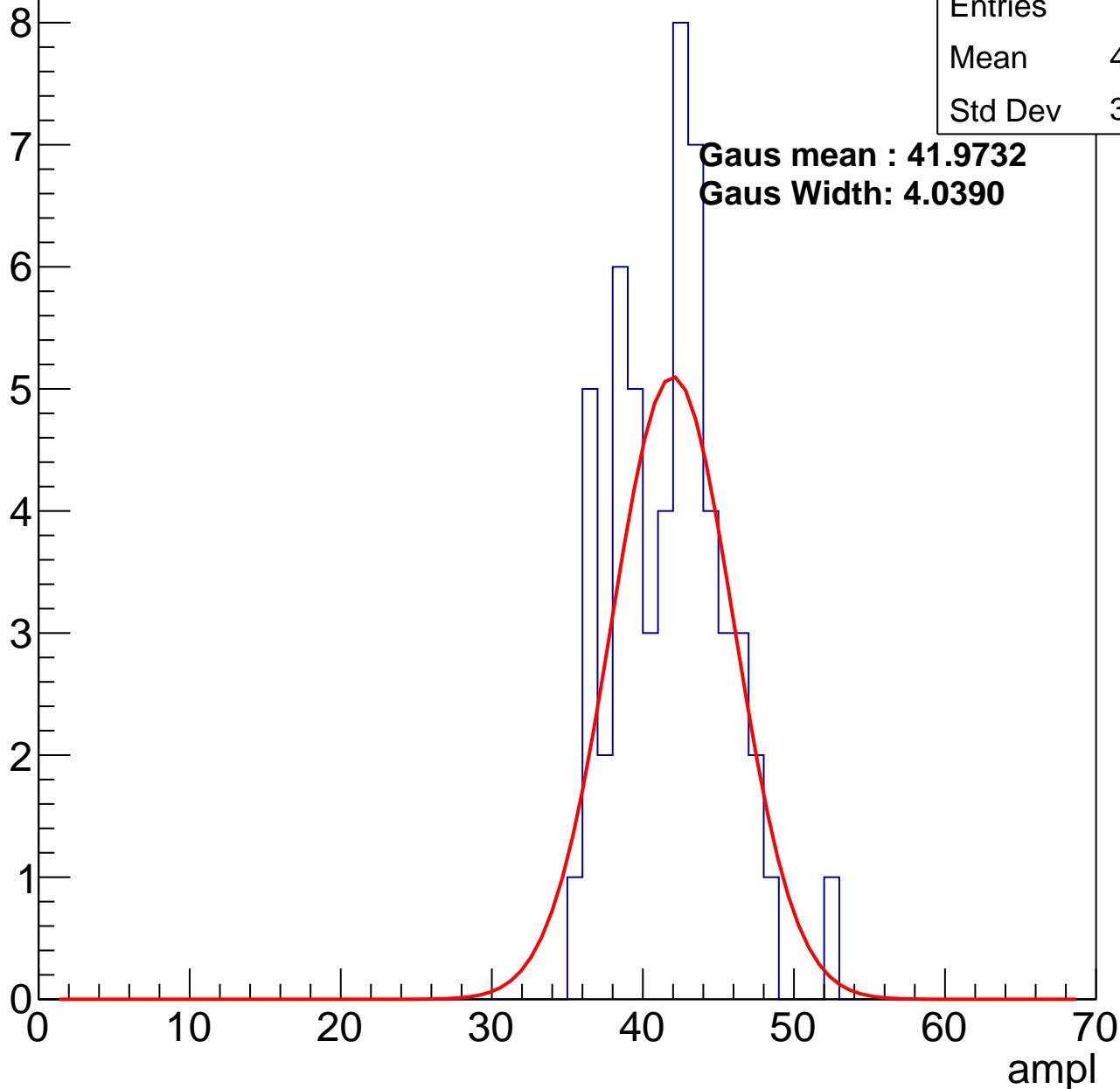
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.38
Std Dev	3.565

Gaus mean : 41.9732

Gaus Width: 4.0390

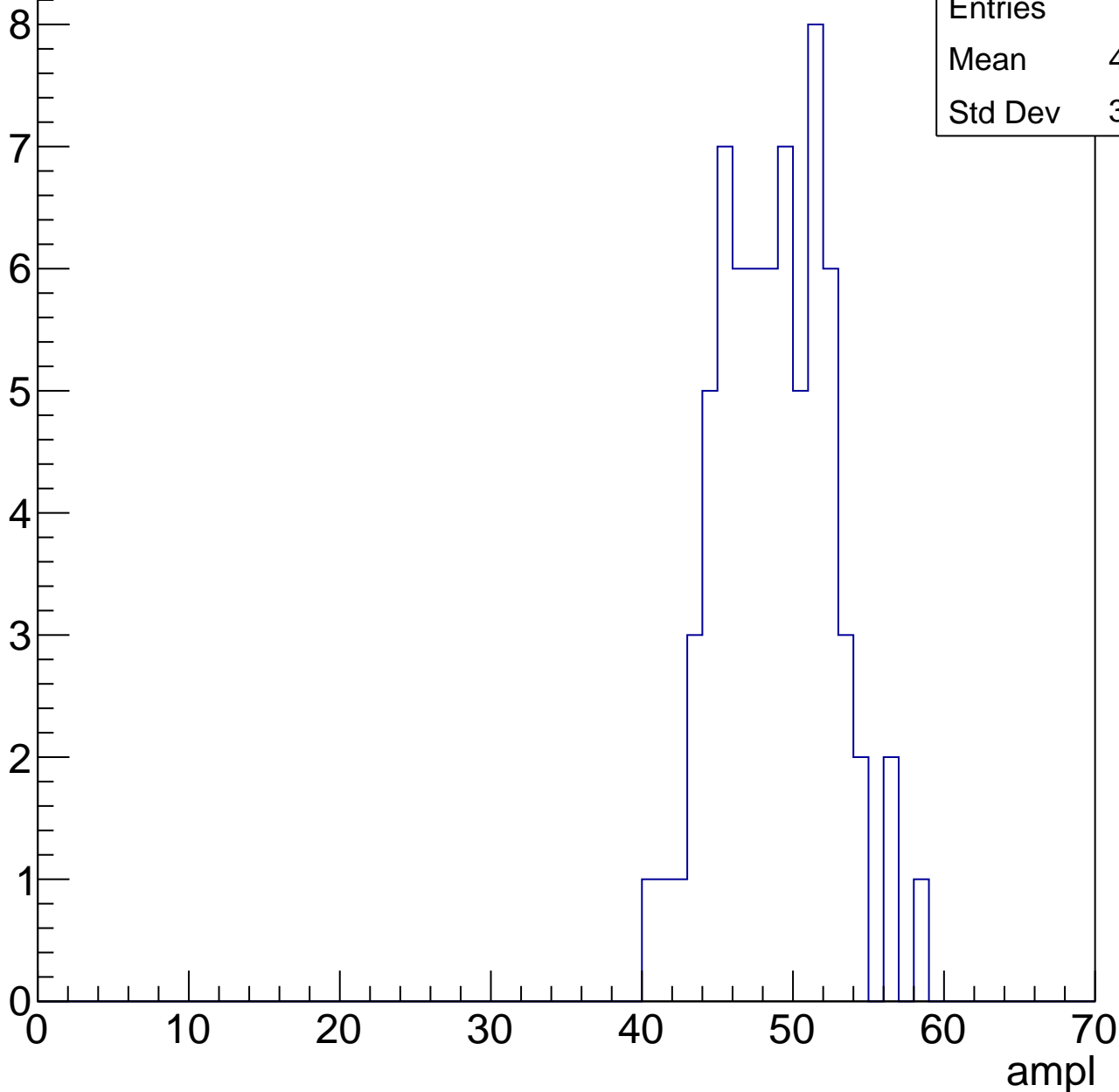


B1L103S, U24-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	48.33
Std Dev	3.702

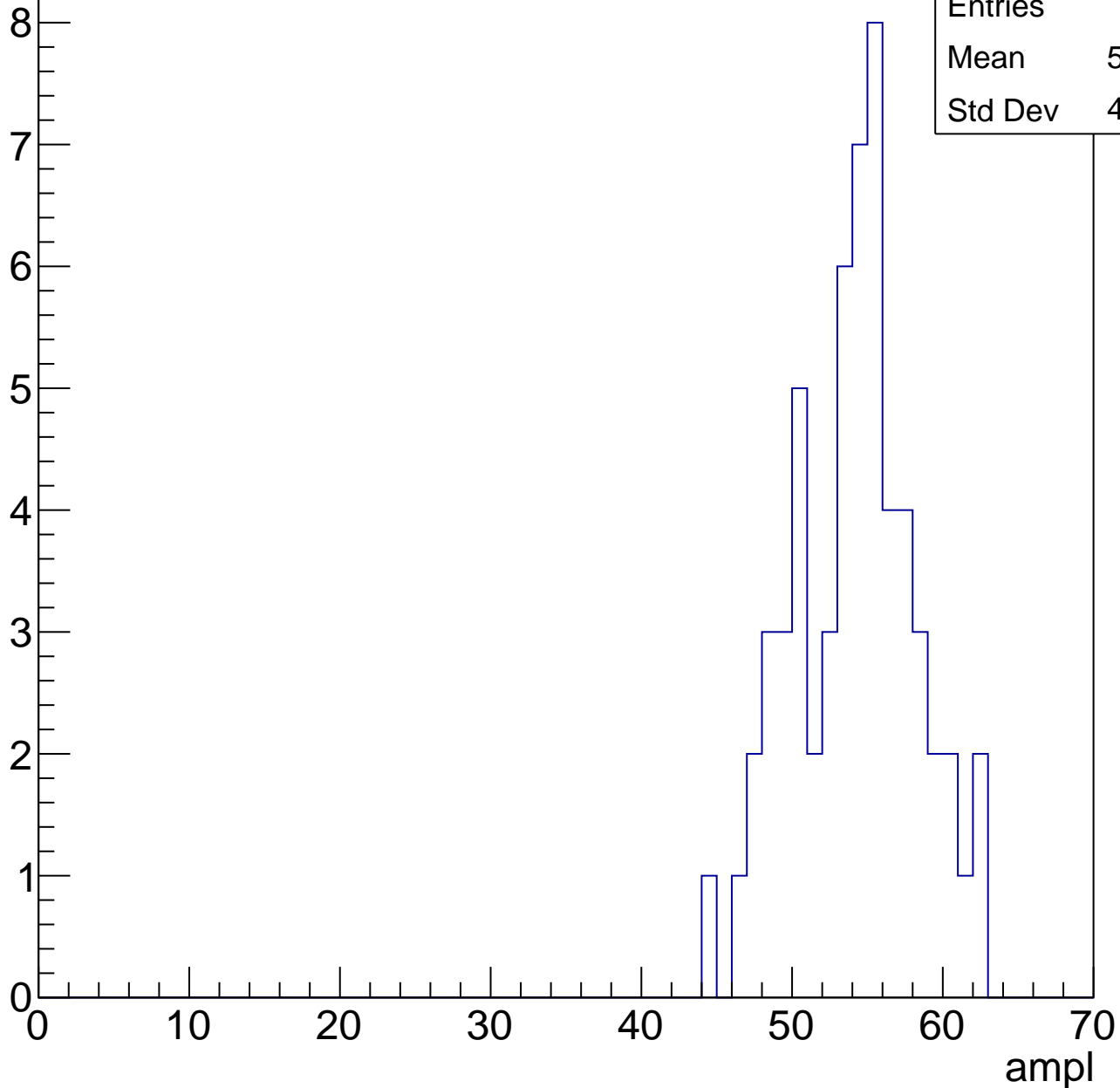


B1L103S, U24-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	53.69
Std Dev	4.039

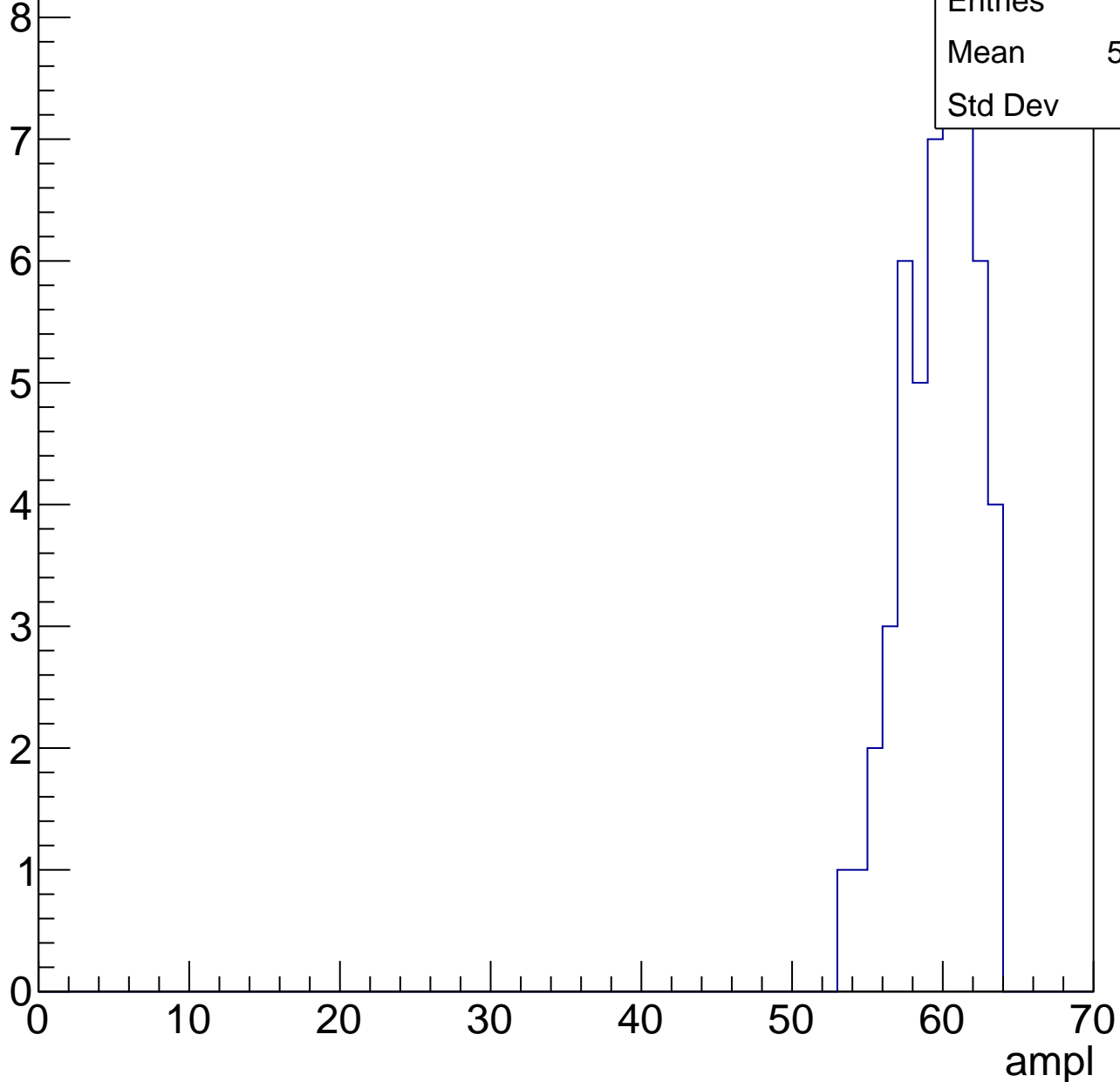


B1L103S, U24-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	59.25
Std Dev	2.44

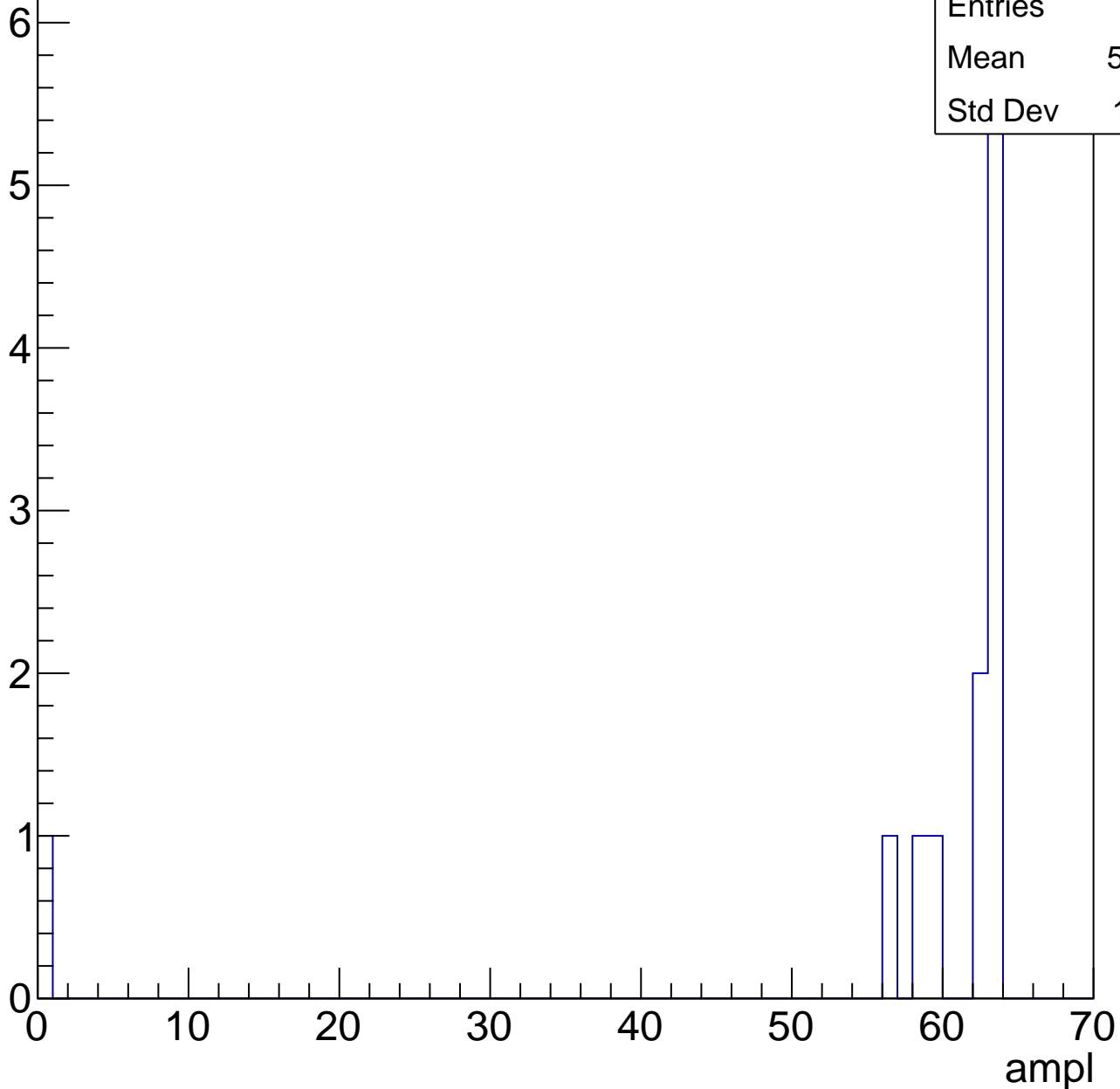


B1L103S, U24-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.25
Std Dev	17.11



B1L103S, U24-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch49, adc0

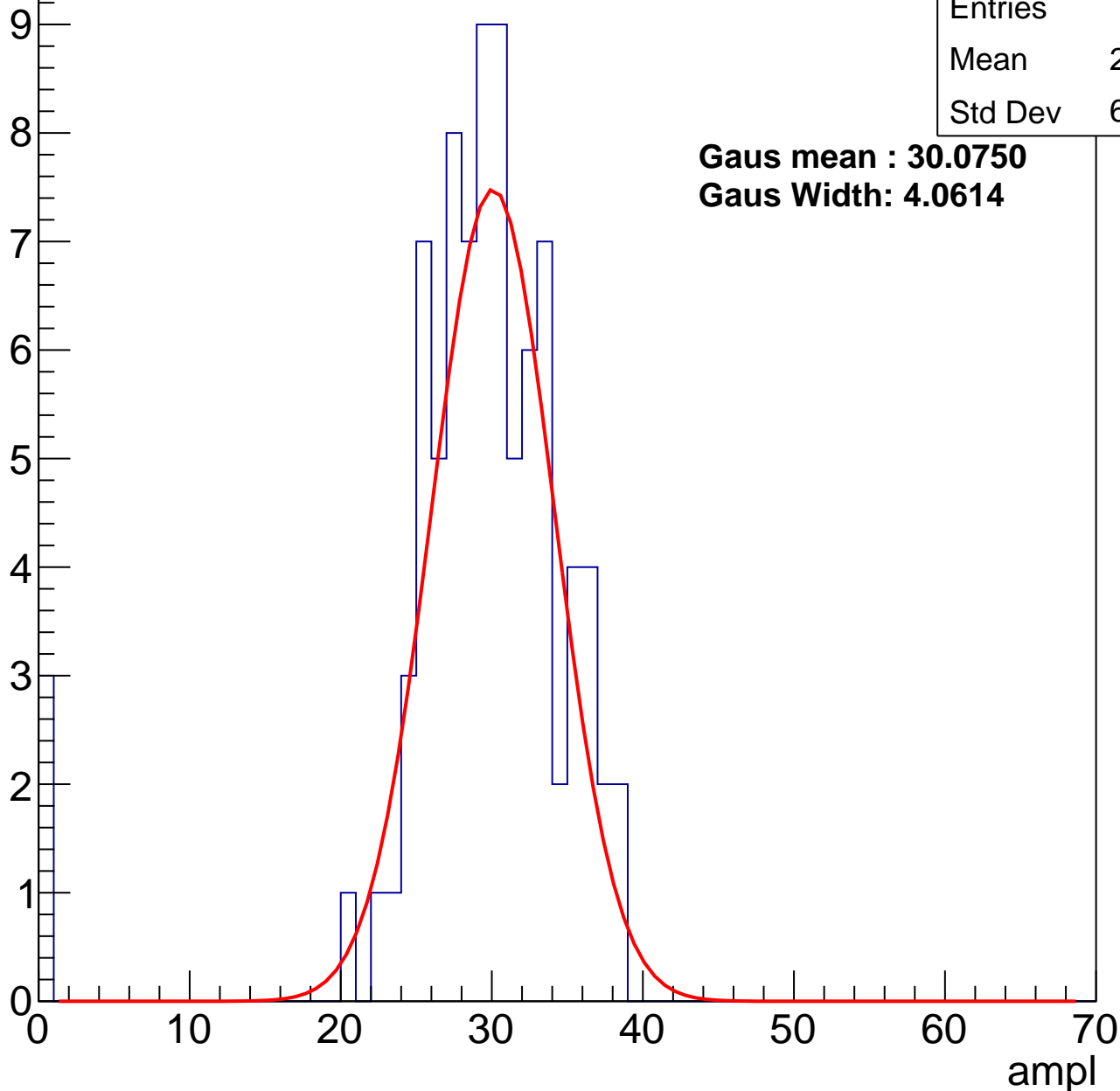
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	28.66
Std Dev	6.669

Gaus mean : 30.0750

Gaus Width: 4.0614



B1L103S, U24-ch49, adc1

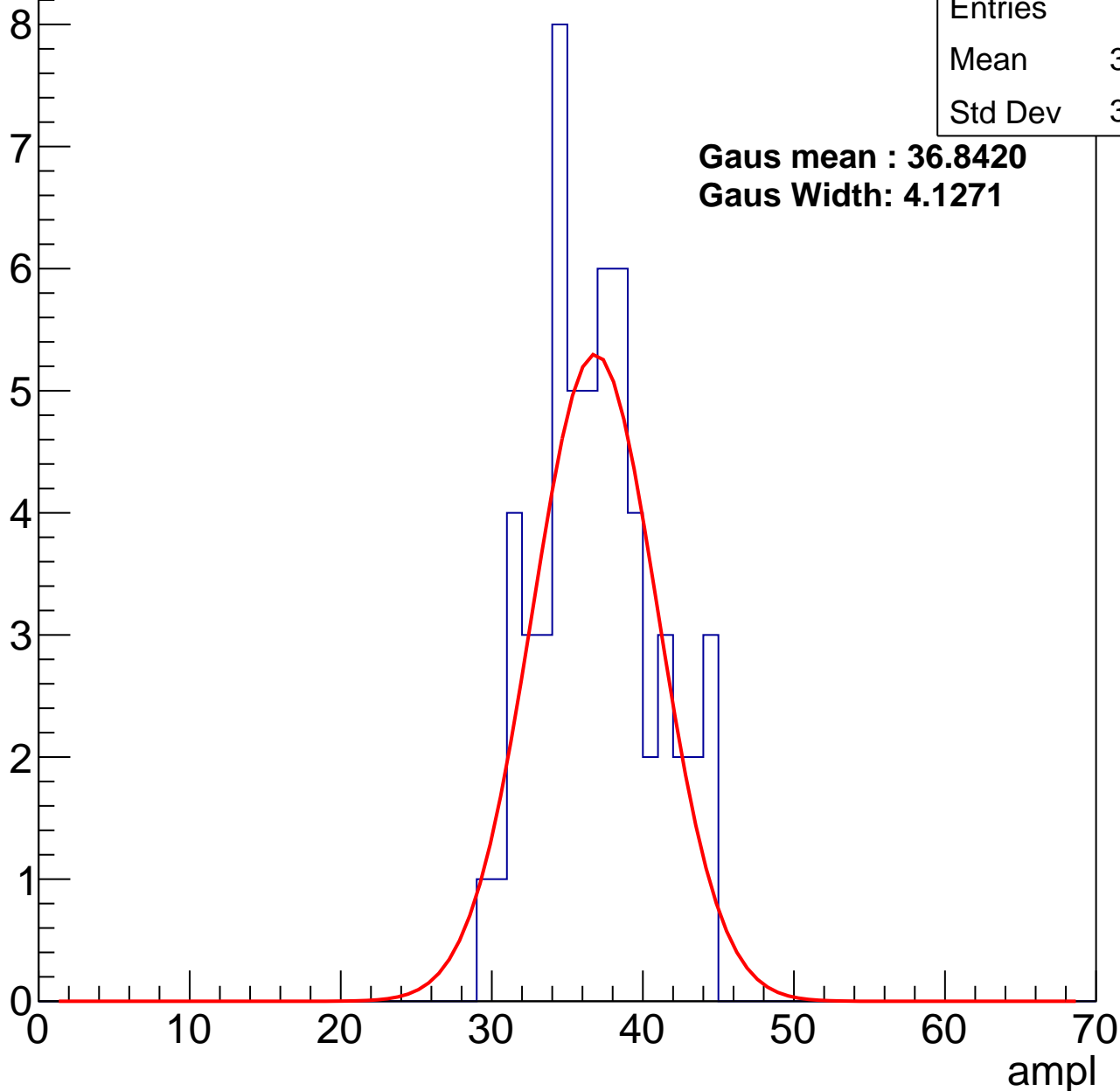
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.48
Std Dev	3.757

Gaus mean : 36.8420

Gaus Width: 4.1271



B1L103S, U24-ch49, adc2

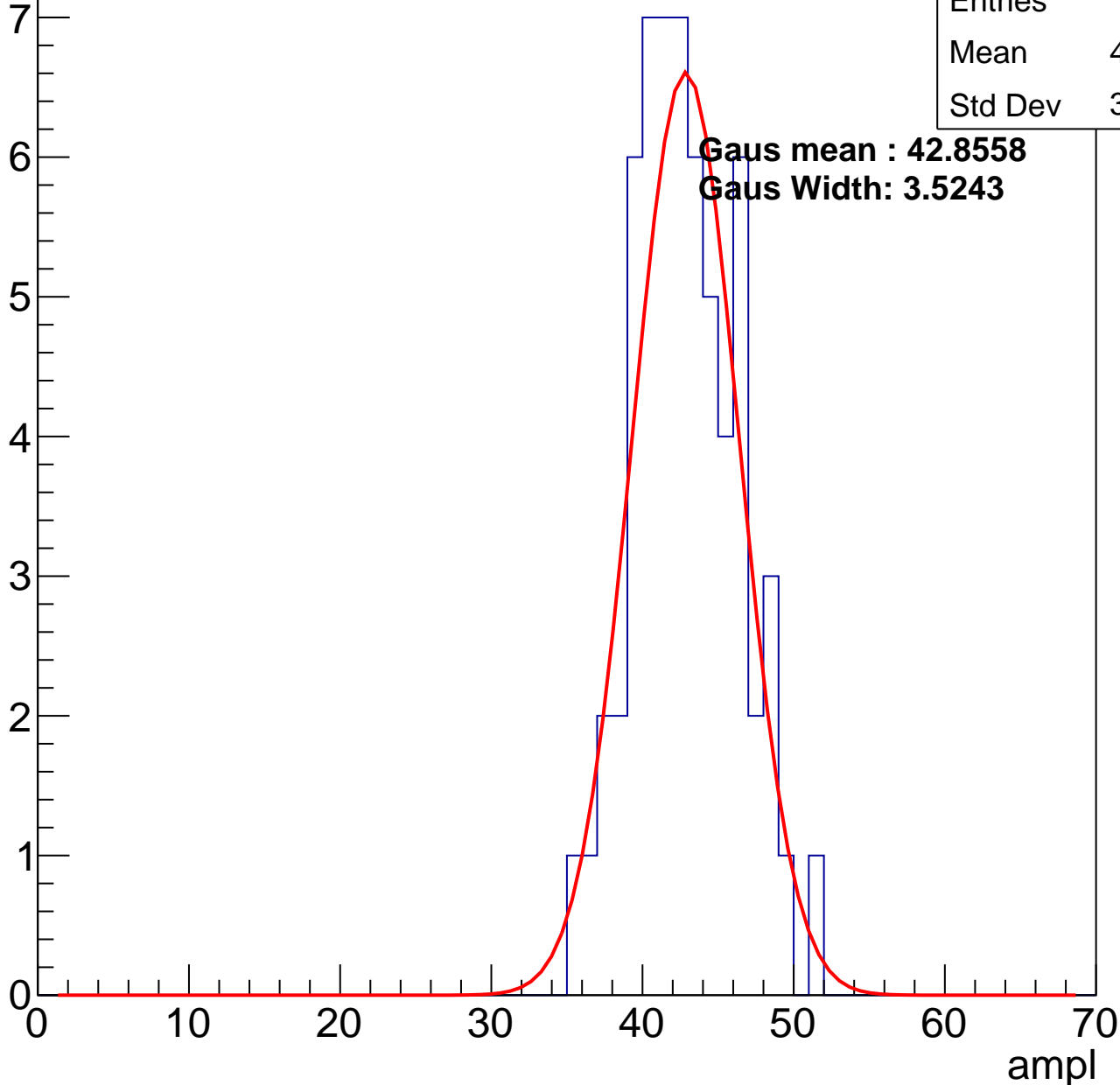
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.43
Std Dev	3.365

Gaus mean : 42.8558

Gaus Width: 3.5243

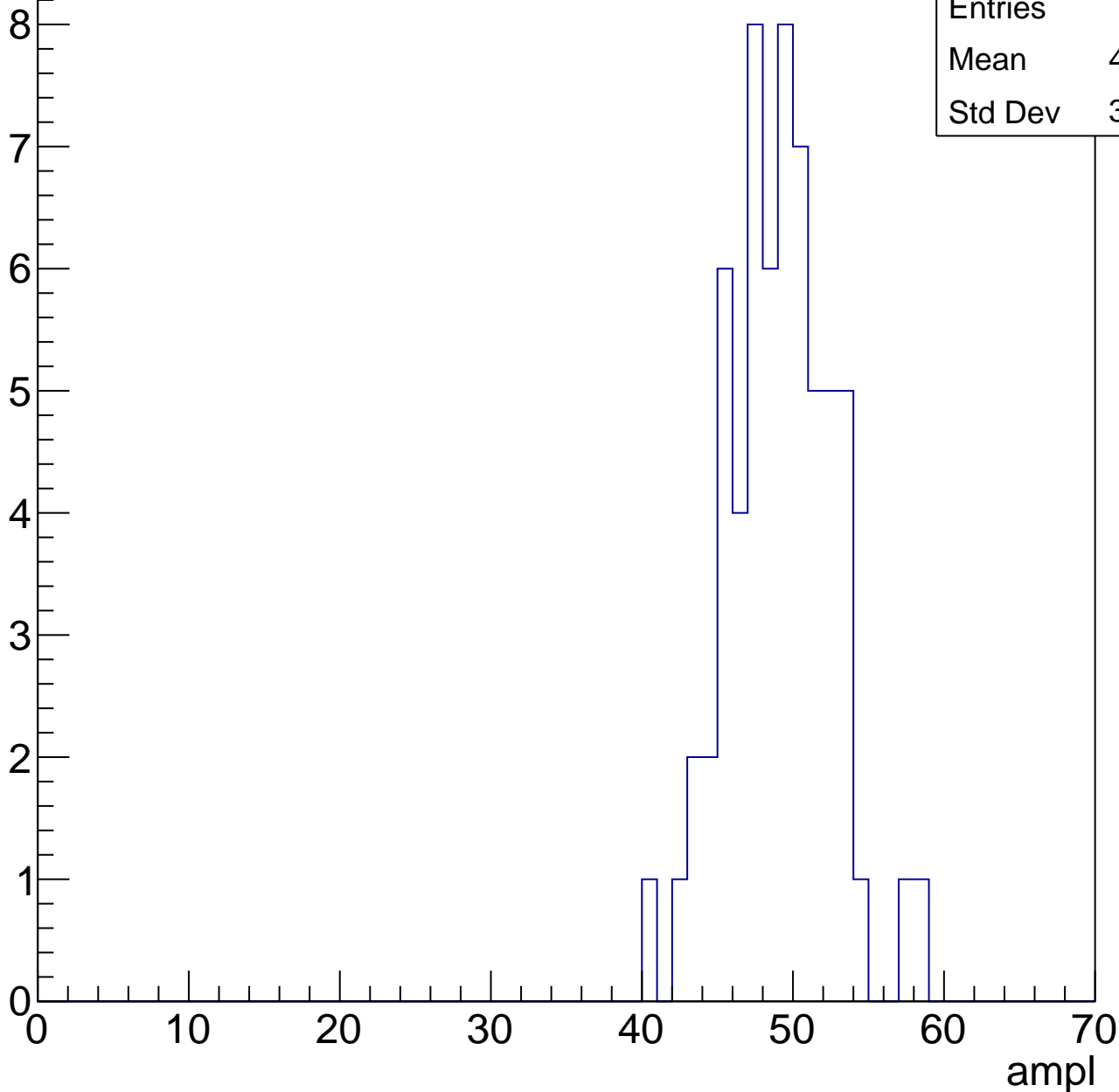


B1L103S, U24-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.65
Std Dev	3.419

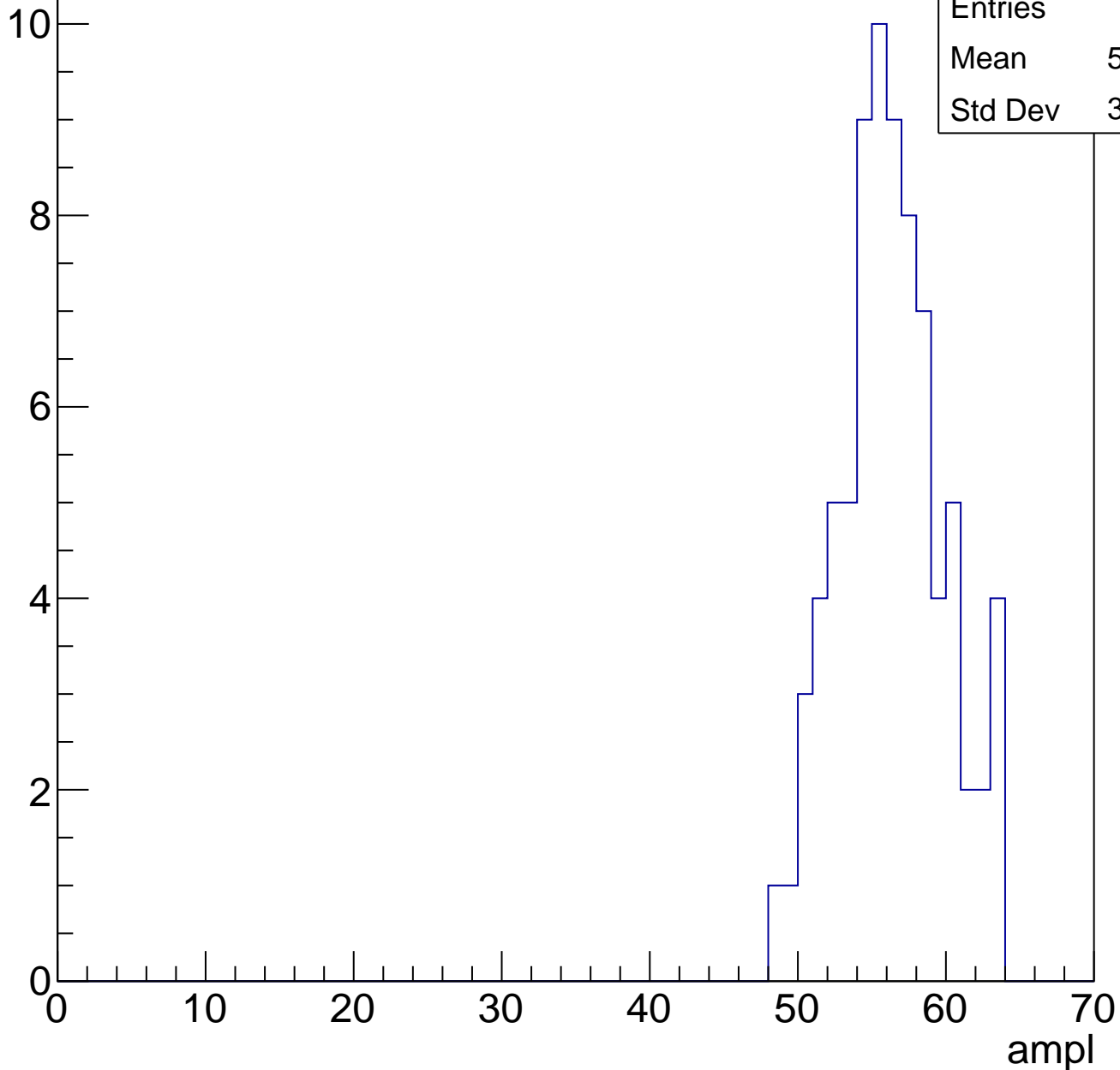


B1L103S, U24-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	55.85
Std Dev	3.486

Entry

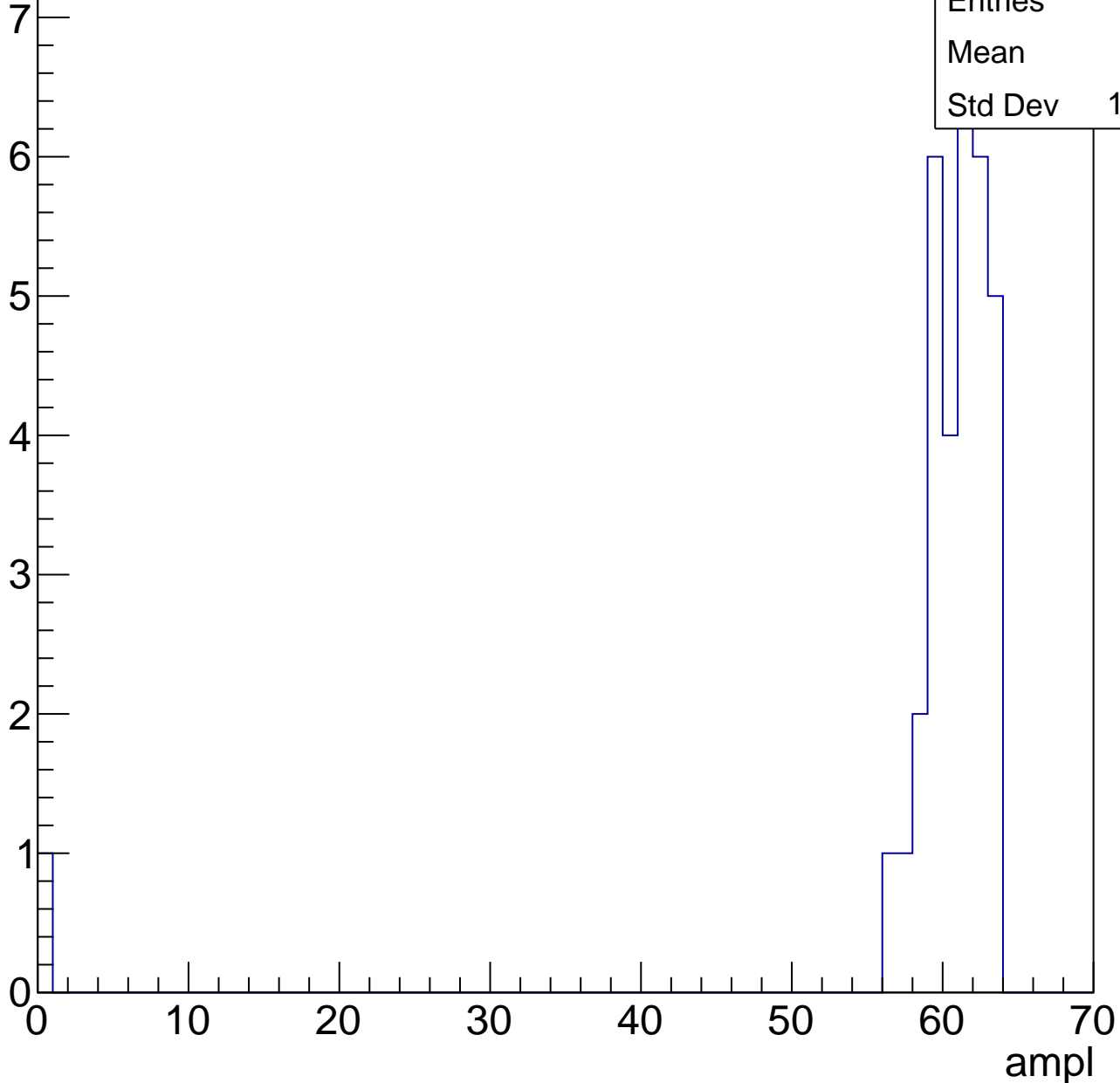


B1L103S, U24-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.7
Std Dev	10.53



B1L103S, U24-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch50, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	27.4
Std Dev	6.446

Gaus mean : 28.7242

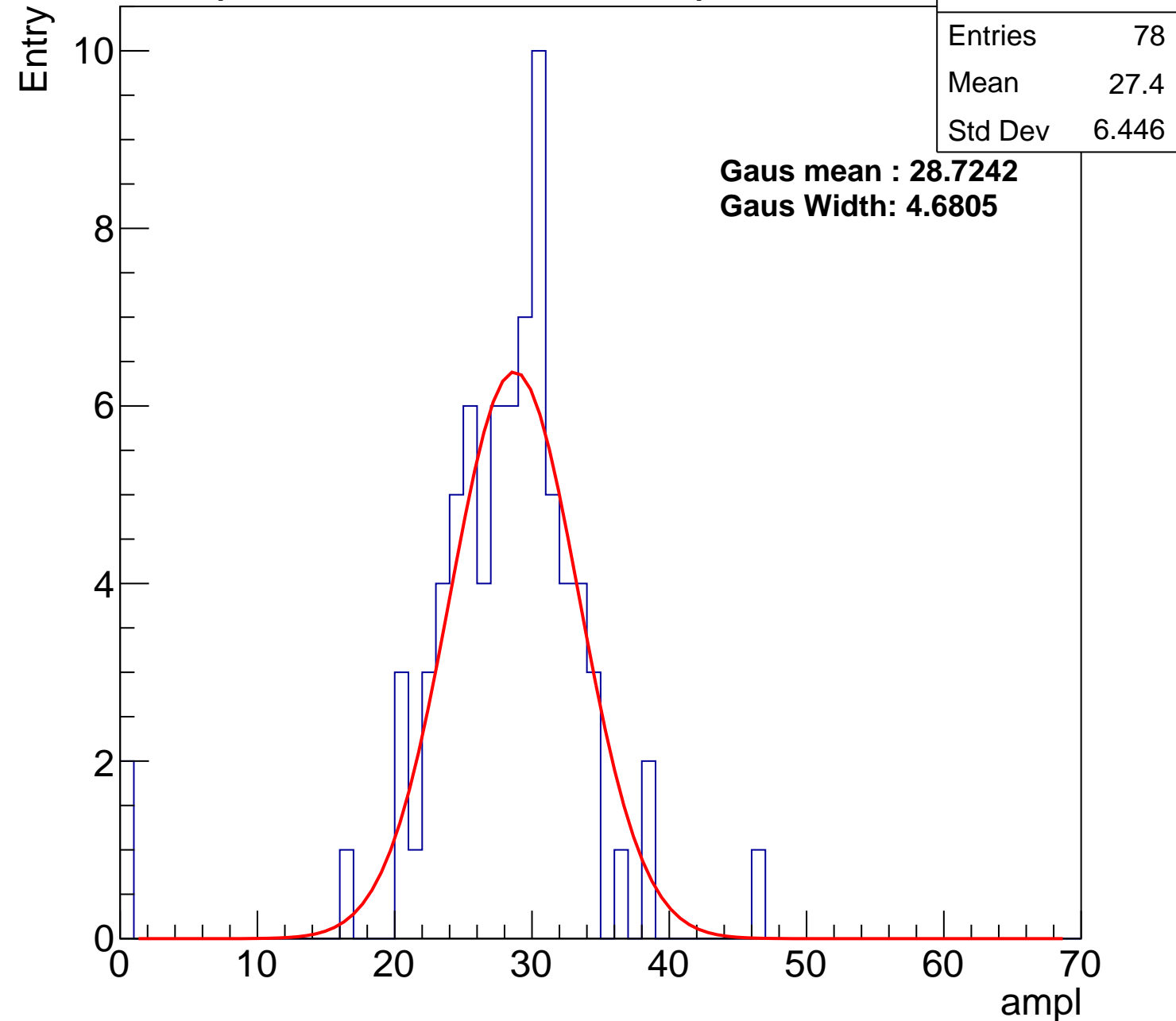
Gaus Width: 4.6805

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch50, adc1

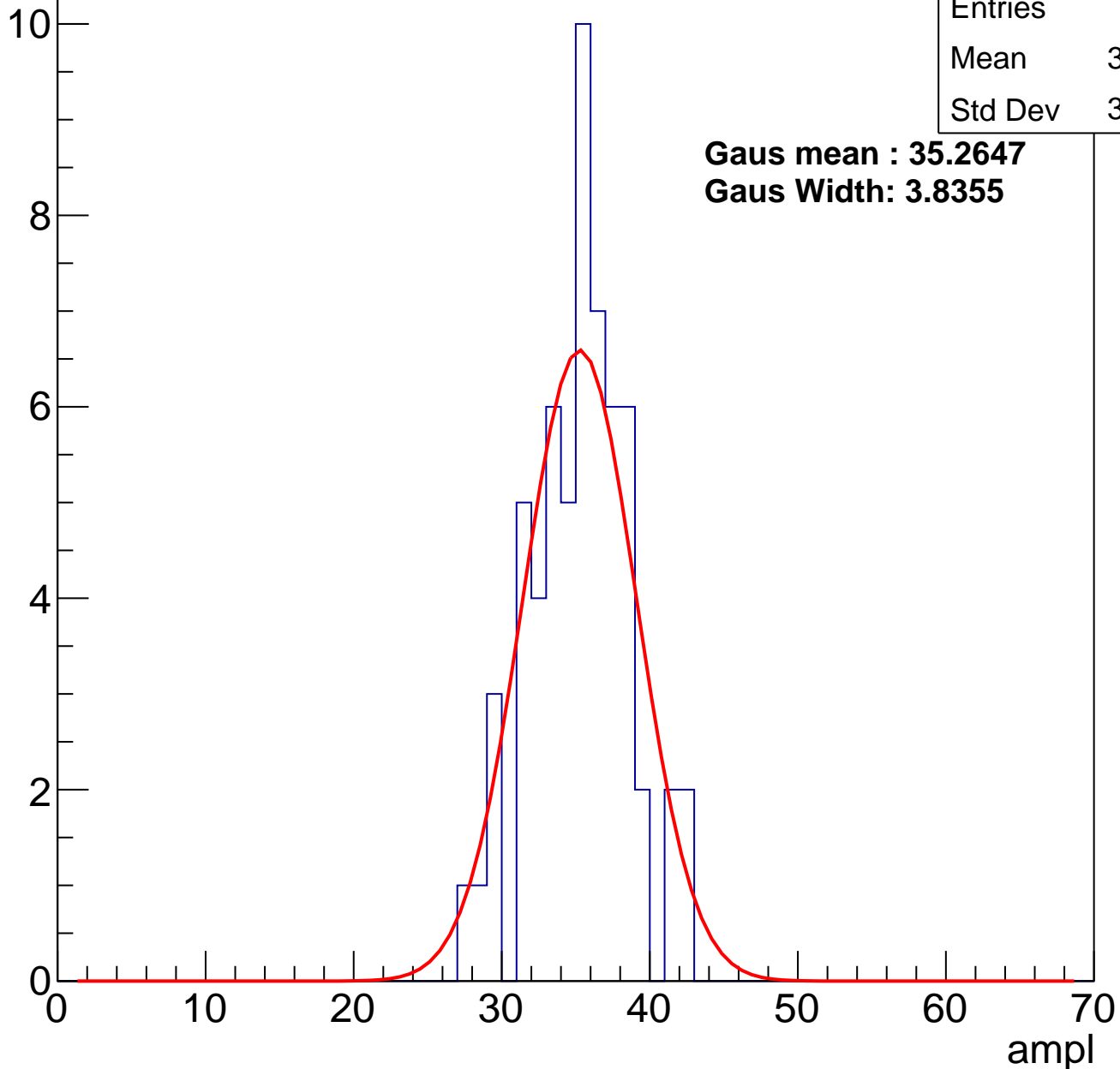
calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	34.82
Std Dev	3.294

Gaus mean : 35.2647

Gaus Width: 3.8355

Entry



B1L103S, U24-ch50, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	42.21
Std Dev	4.293

Gaus mean : 42.7843

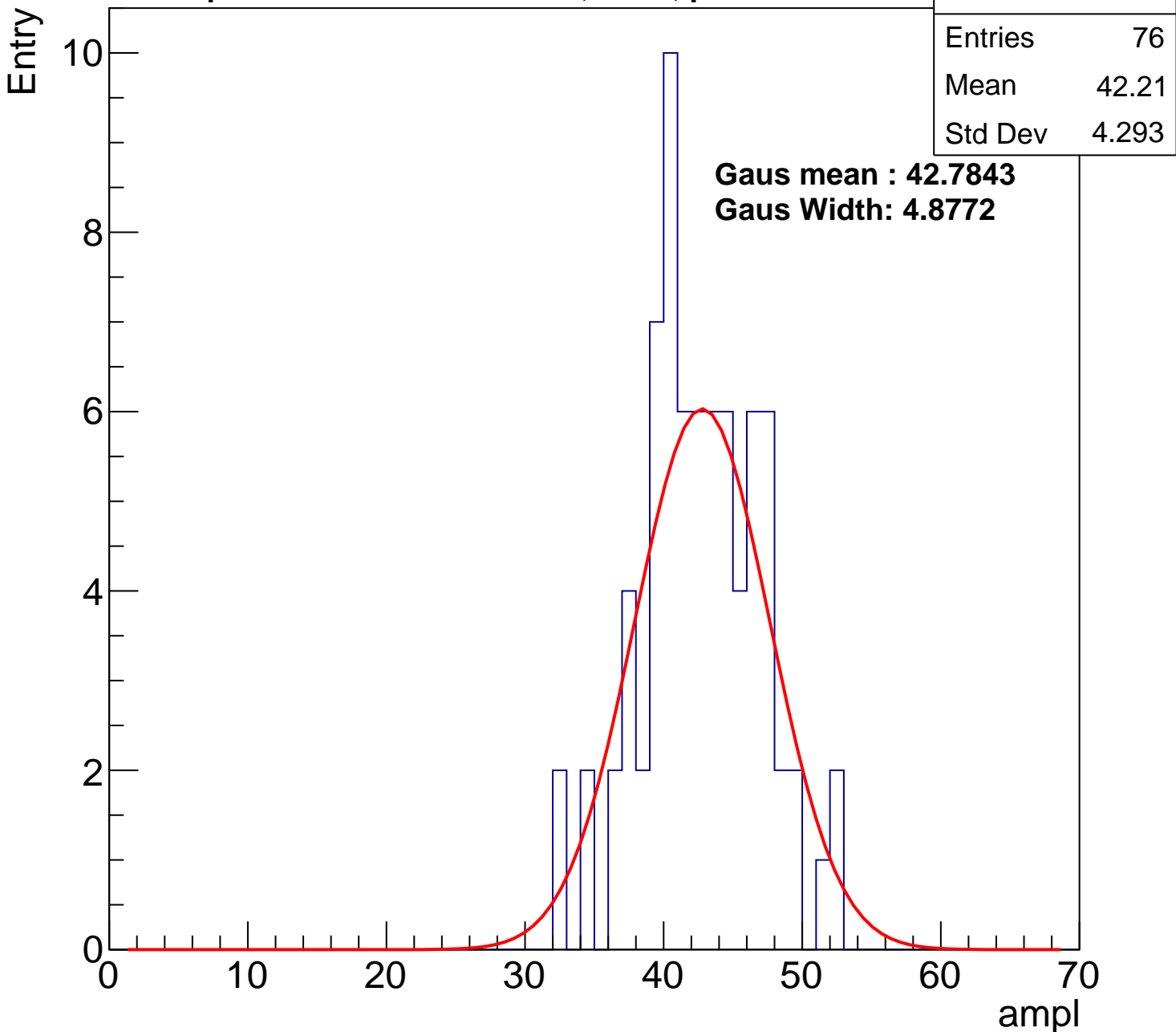
Gaus Width: 4.8772

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

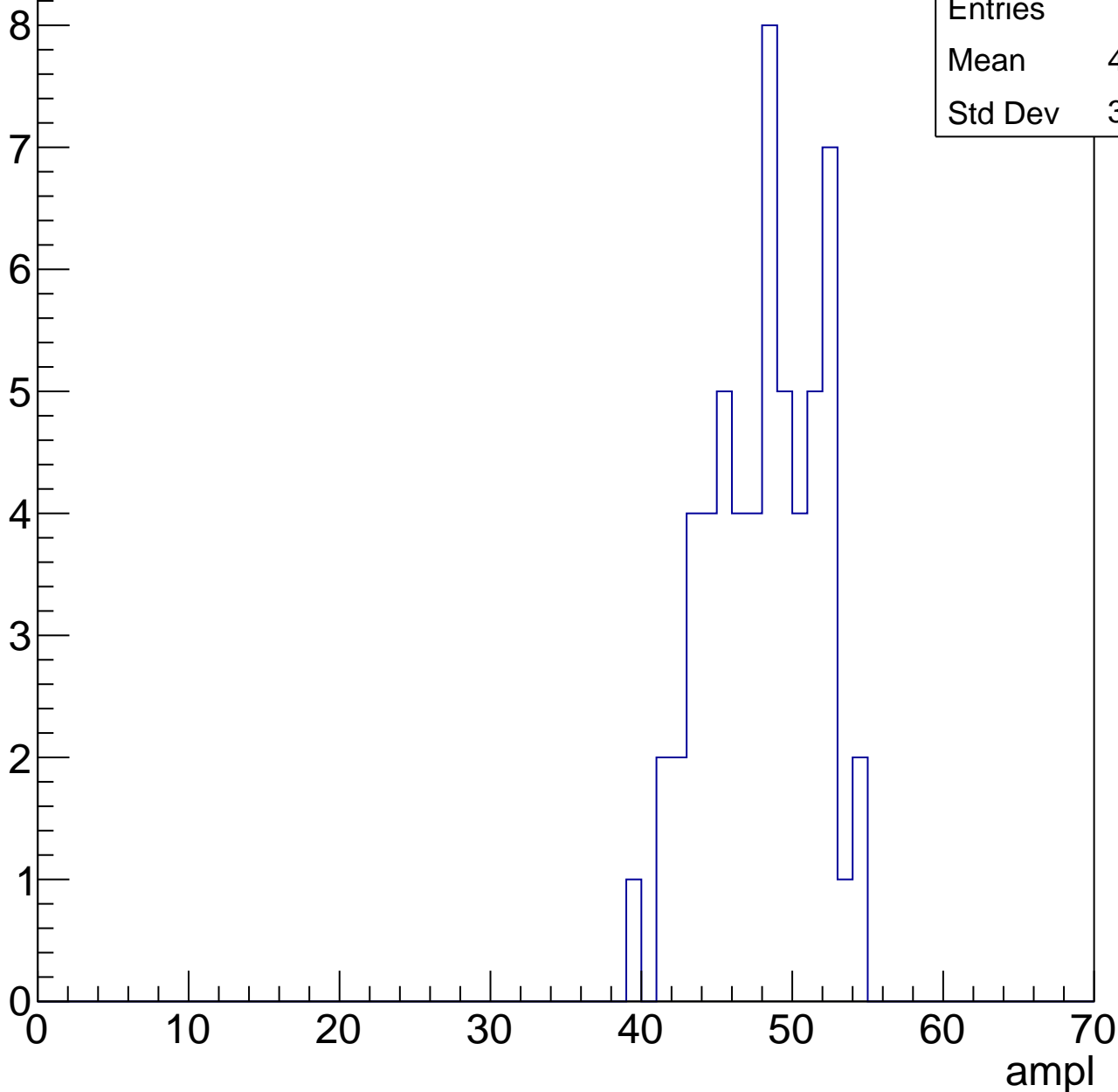


B1L103S, U24-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	47.57
Std Dev	3.582

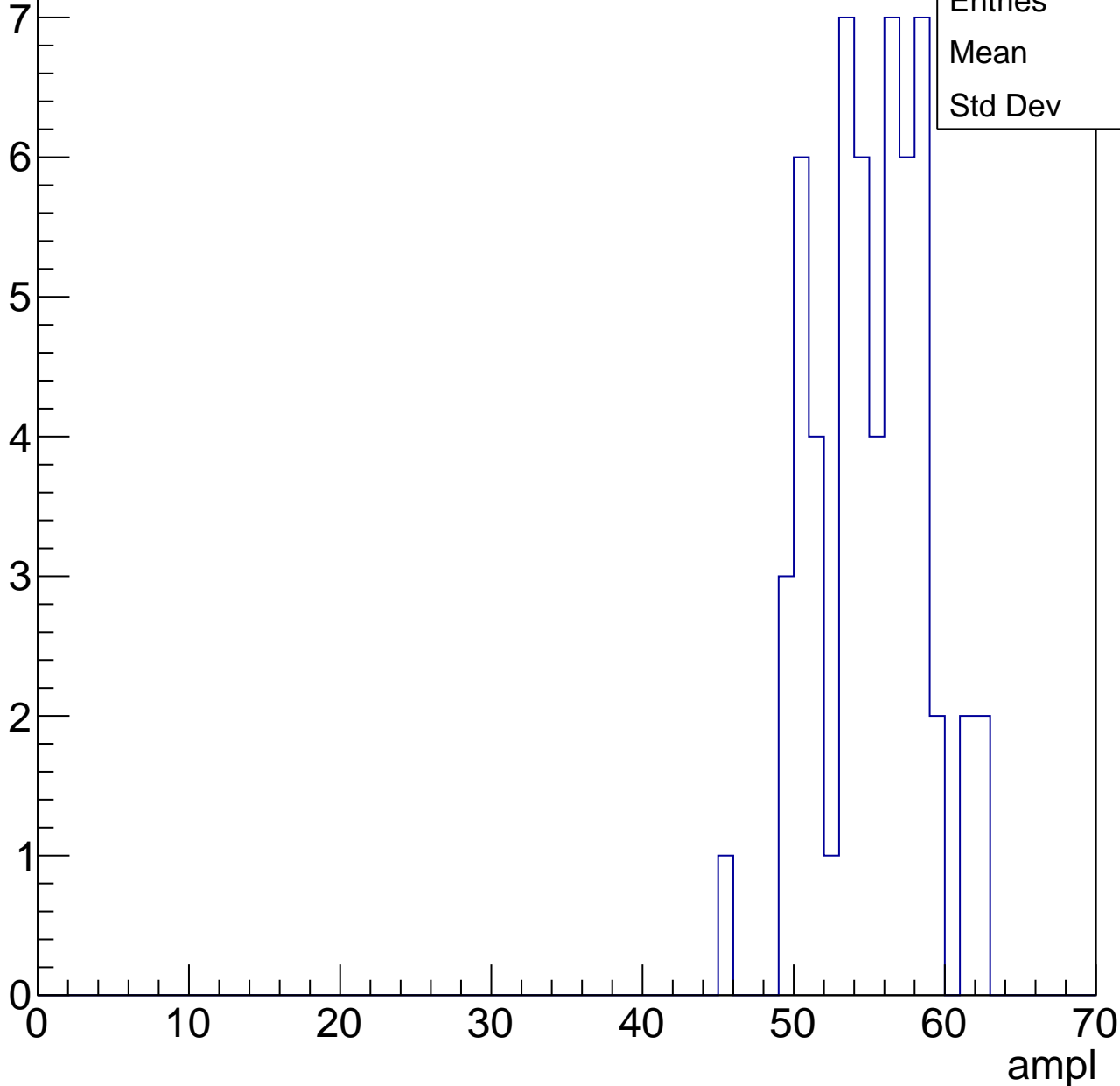


B1L103S, U24-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.6
Std Dev	3.6

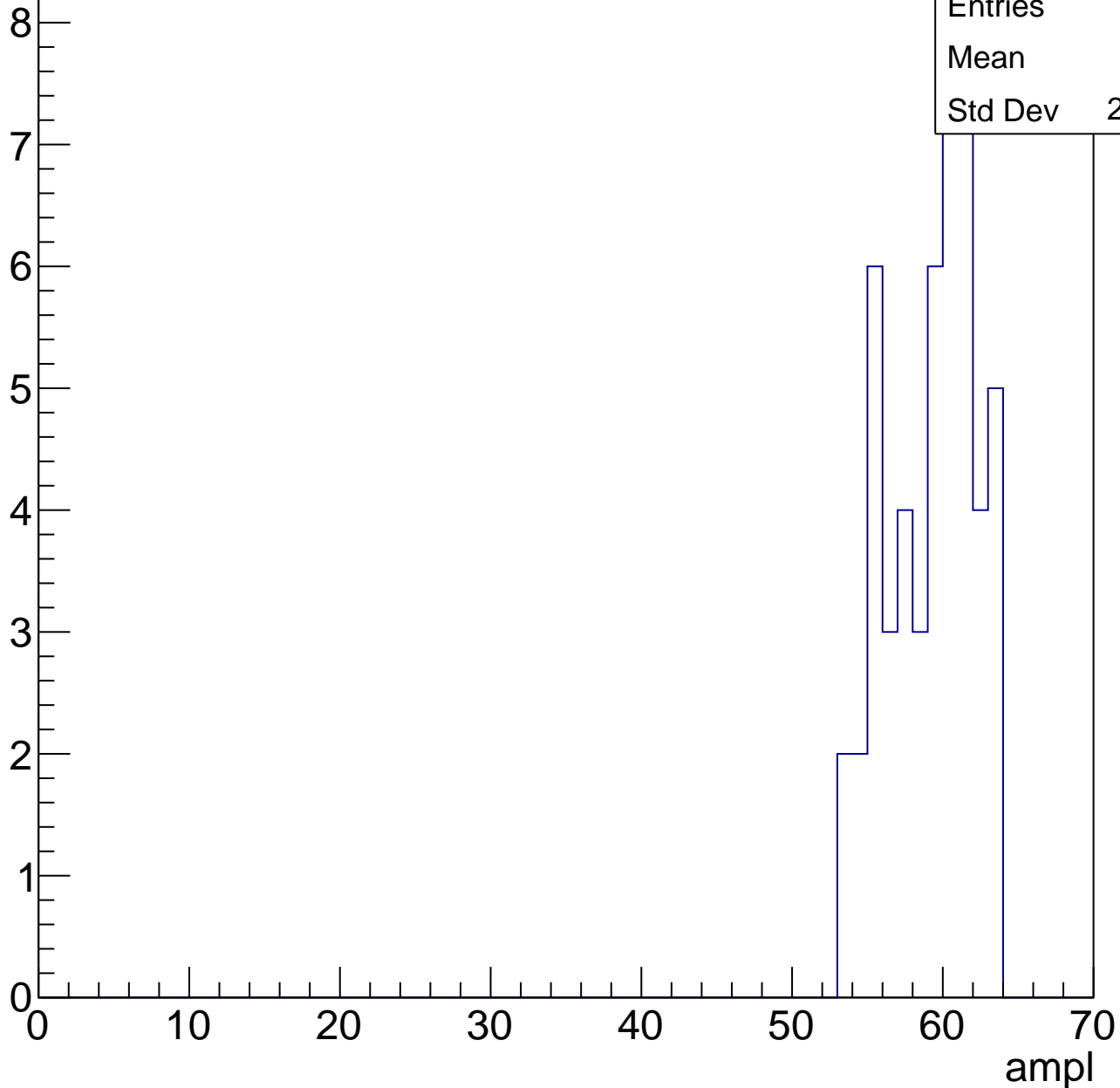


B1L103S, U24-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.8
Std Dev	2.863

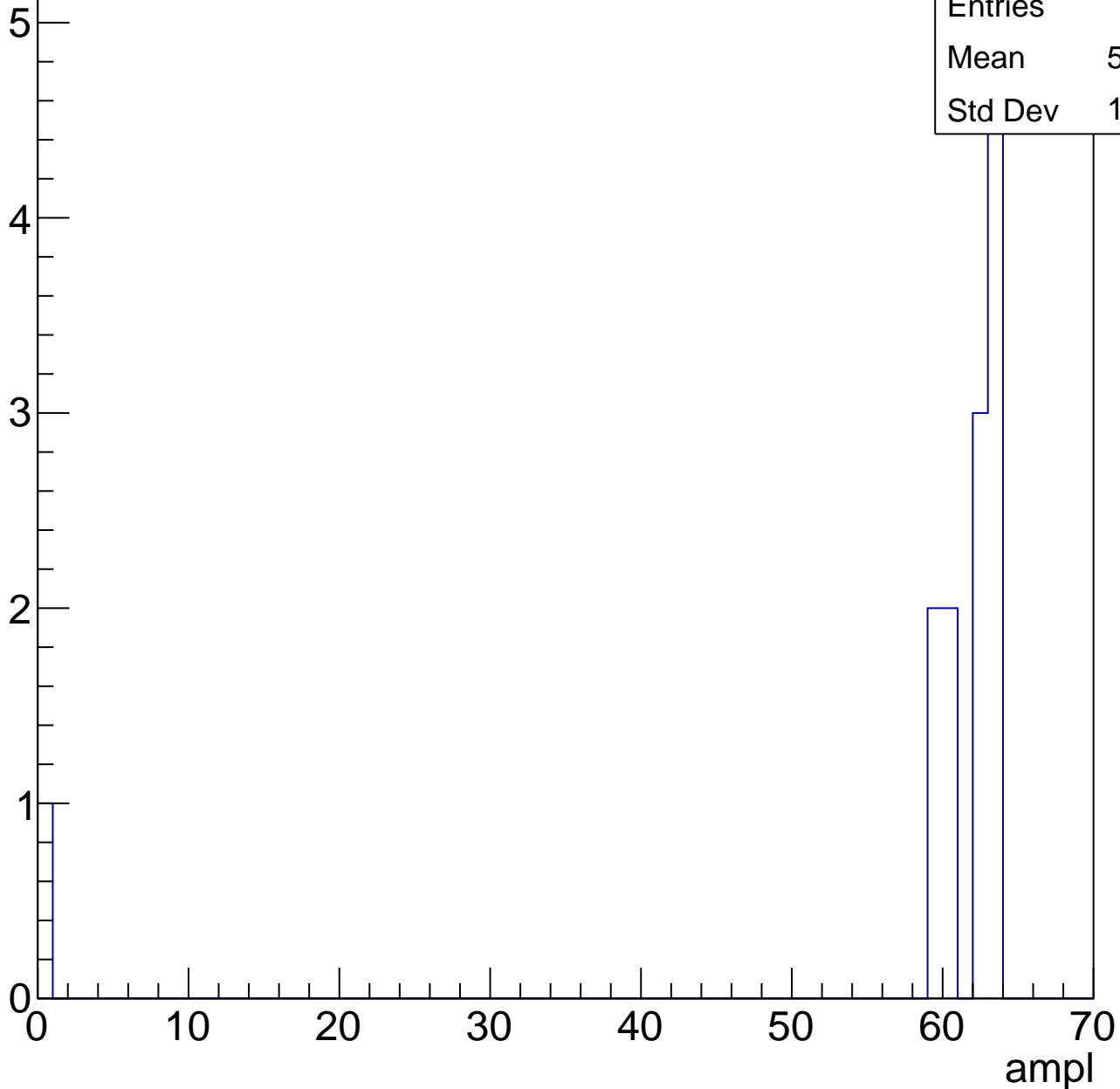


B1L103S, U24-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

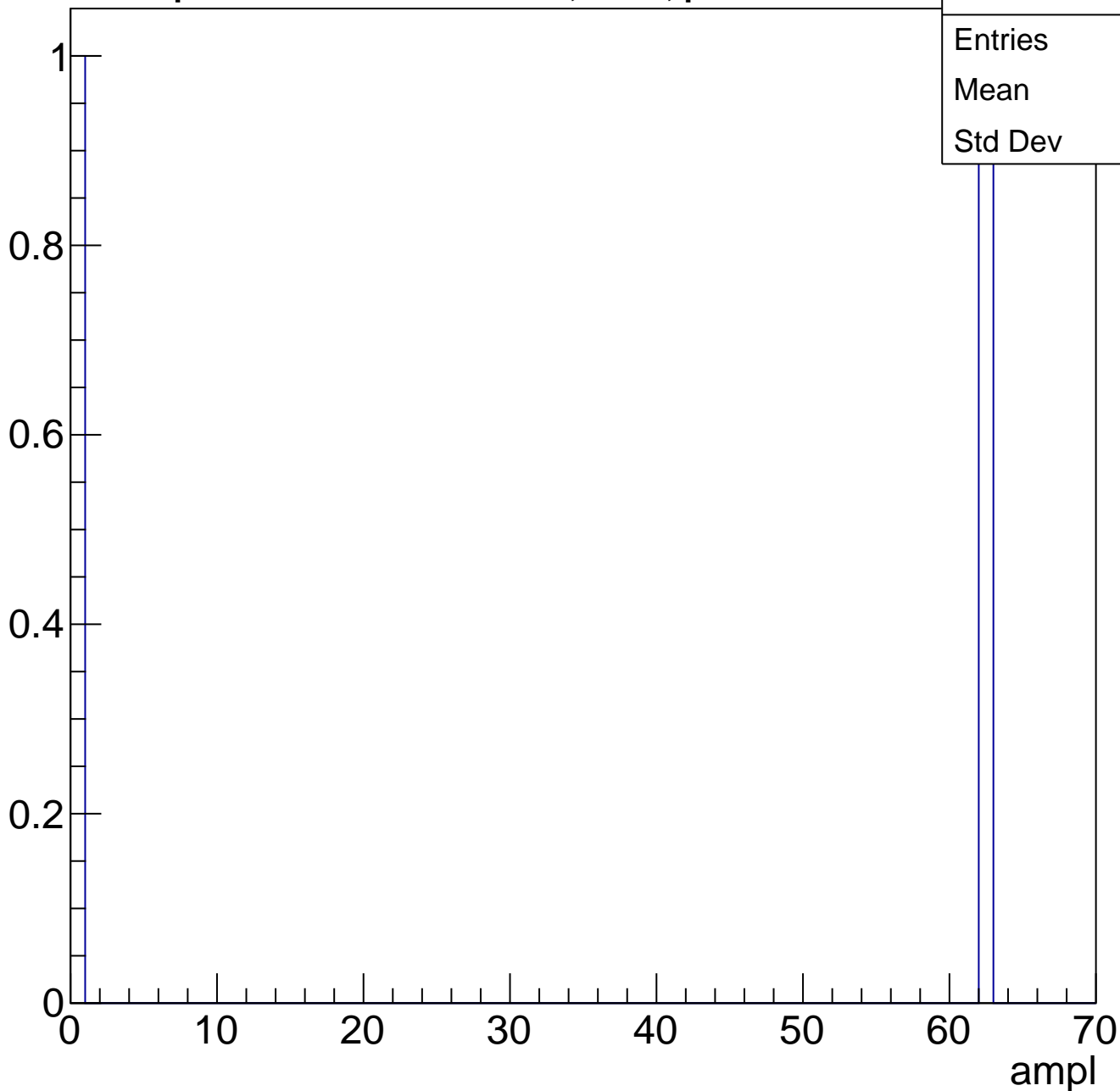
Entries	13
Mean	56.85
Std Dev	16.48



B1L103S, U24-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch51, adc0

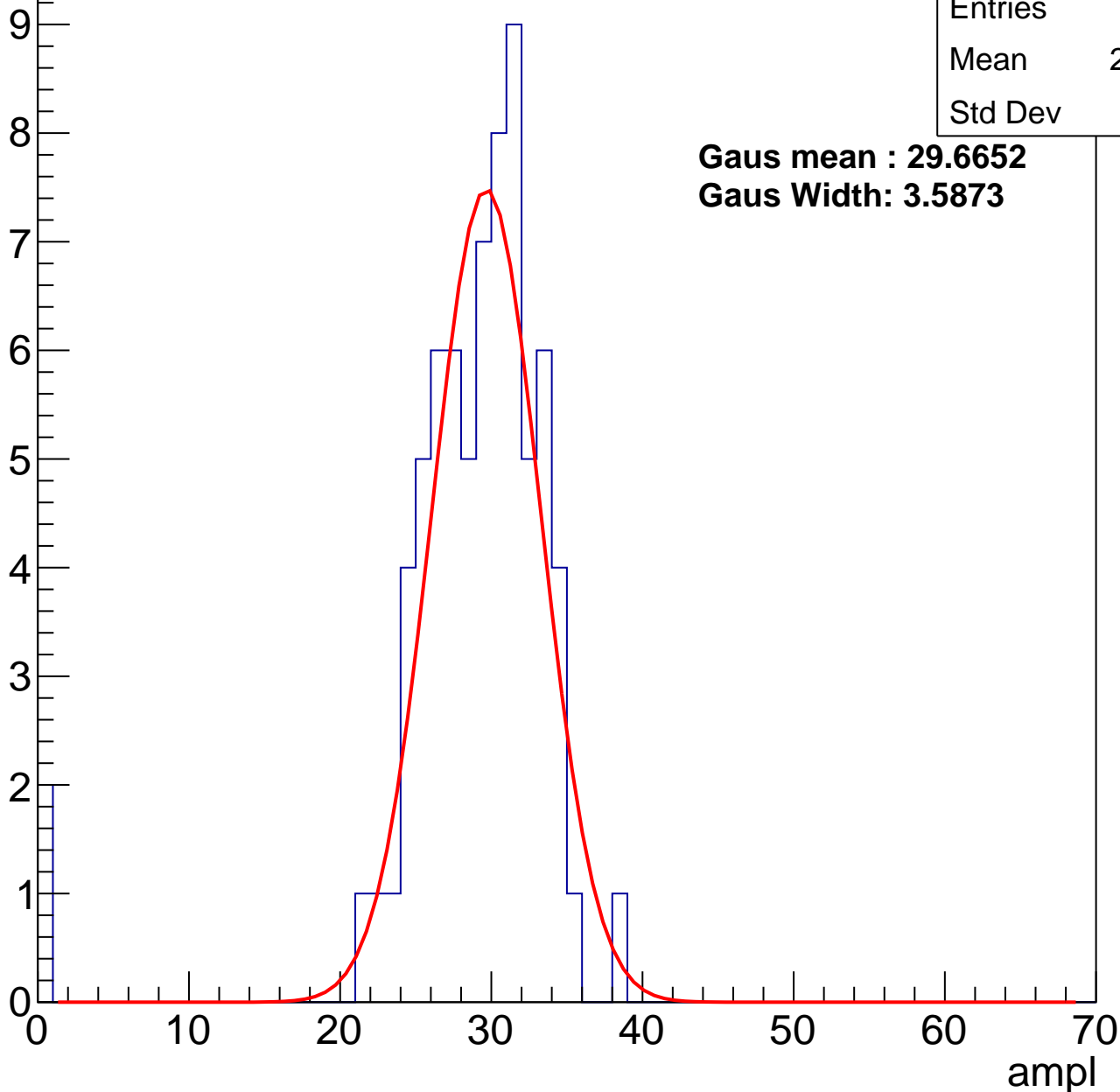
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.25
Std Dev	5.84

Gaus mean : 29.6652

Gaus Width: 3.5873



B1L103S, U24-ch51, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	36.62
Std Dev	3.444

Gaus mean : 37.3194

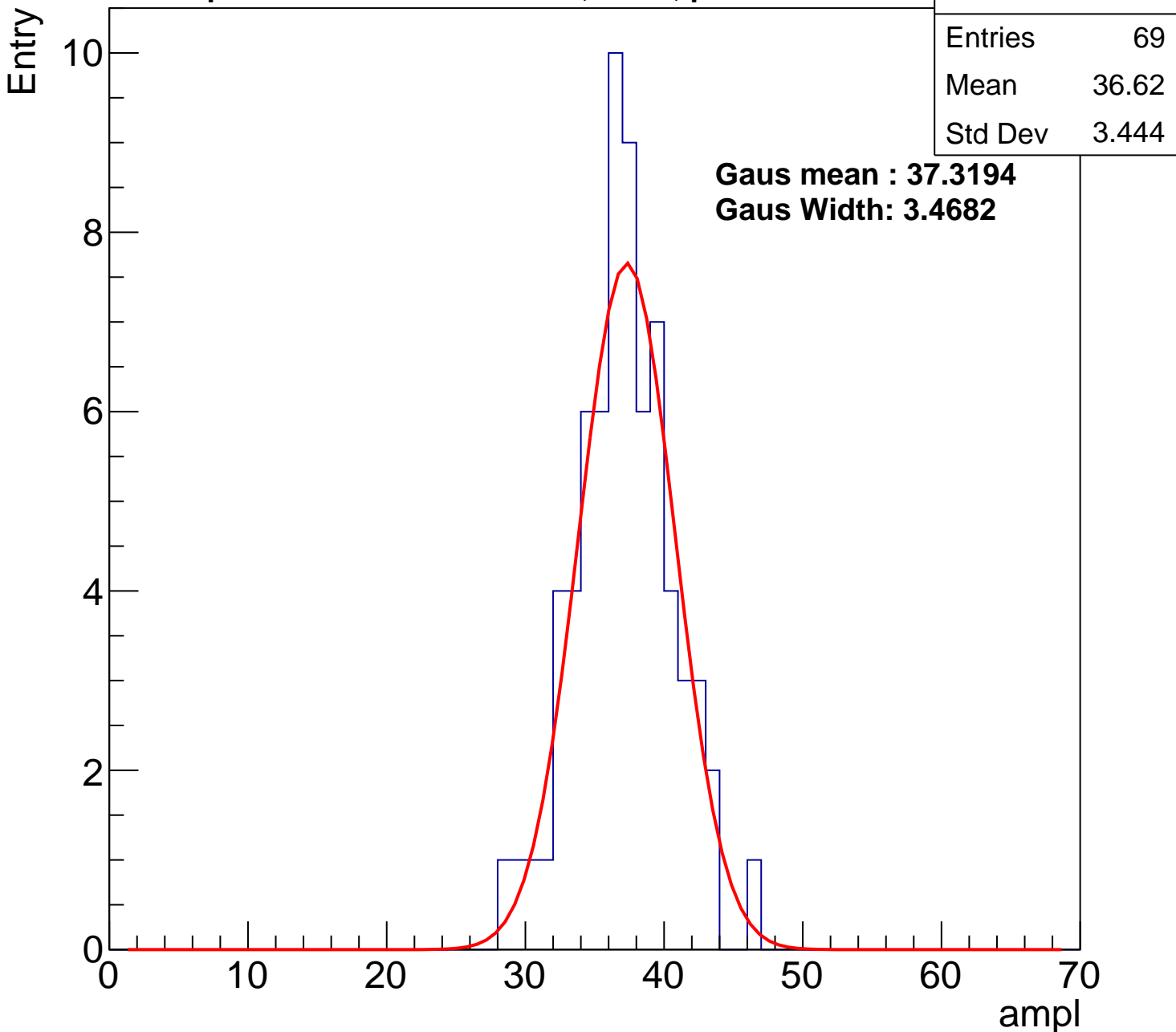
Gaus Width: 3.4682

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch51, adc2

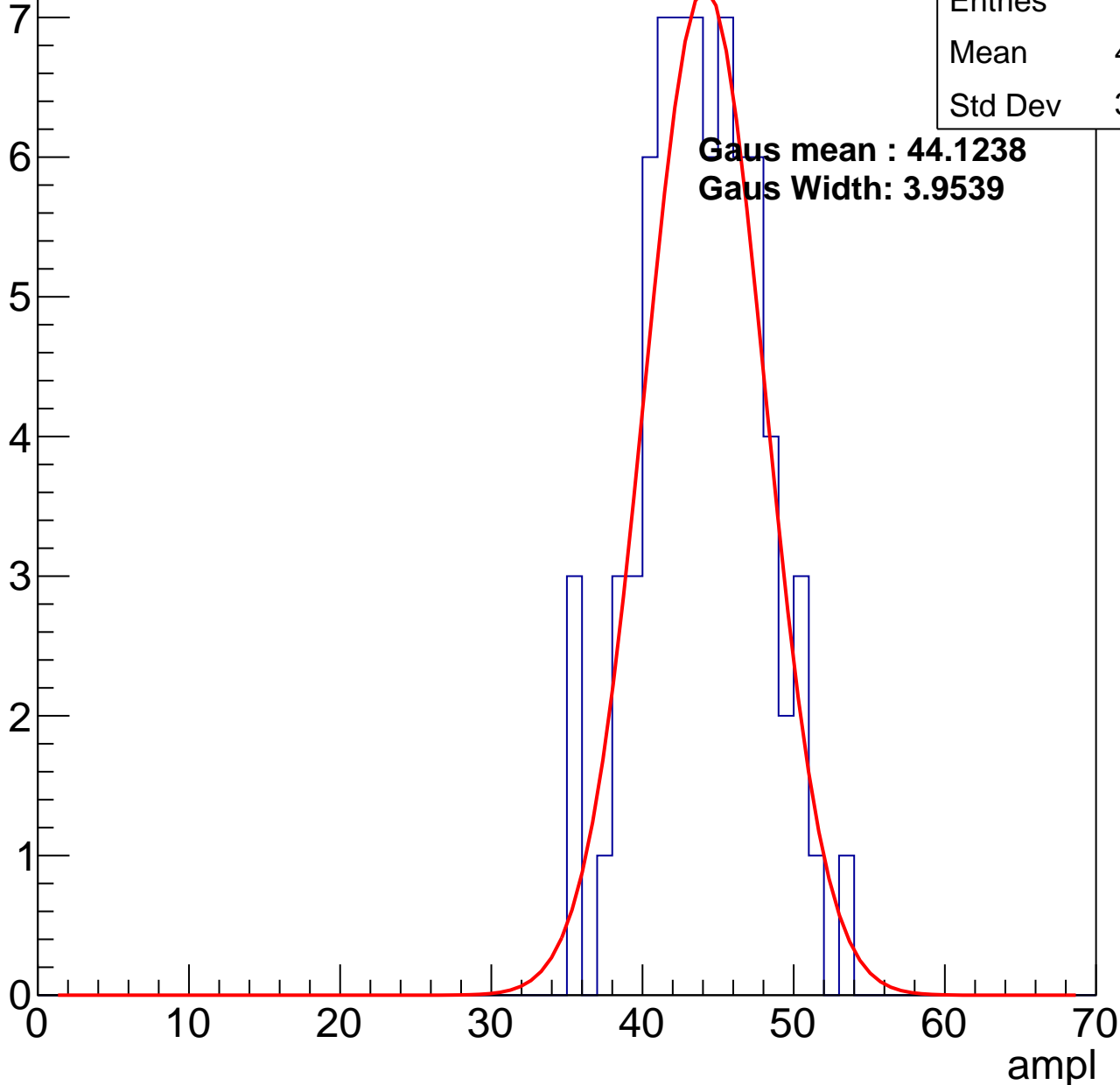
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.51
Std Dev	3.861

Gaus mean : 44.1238

Gaus Width: 3.9539

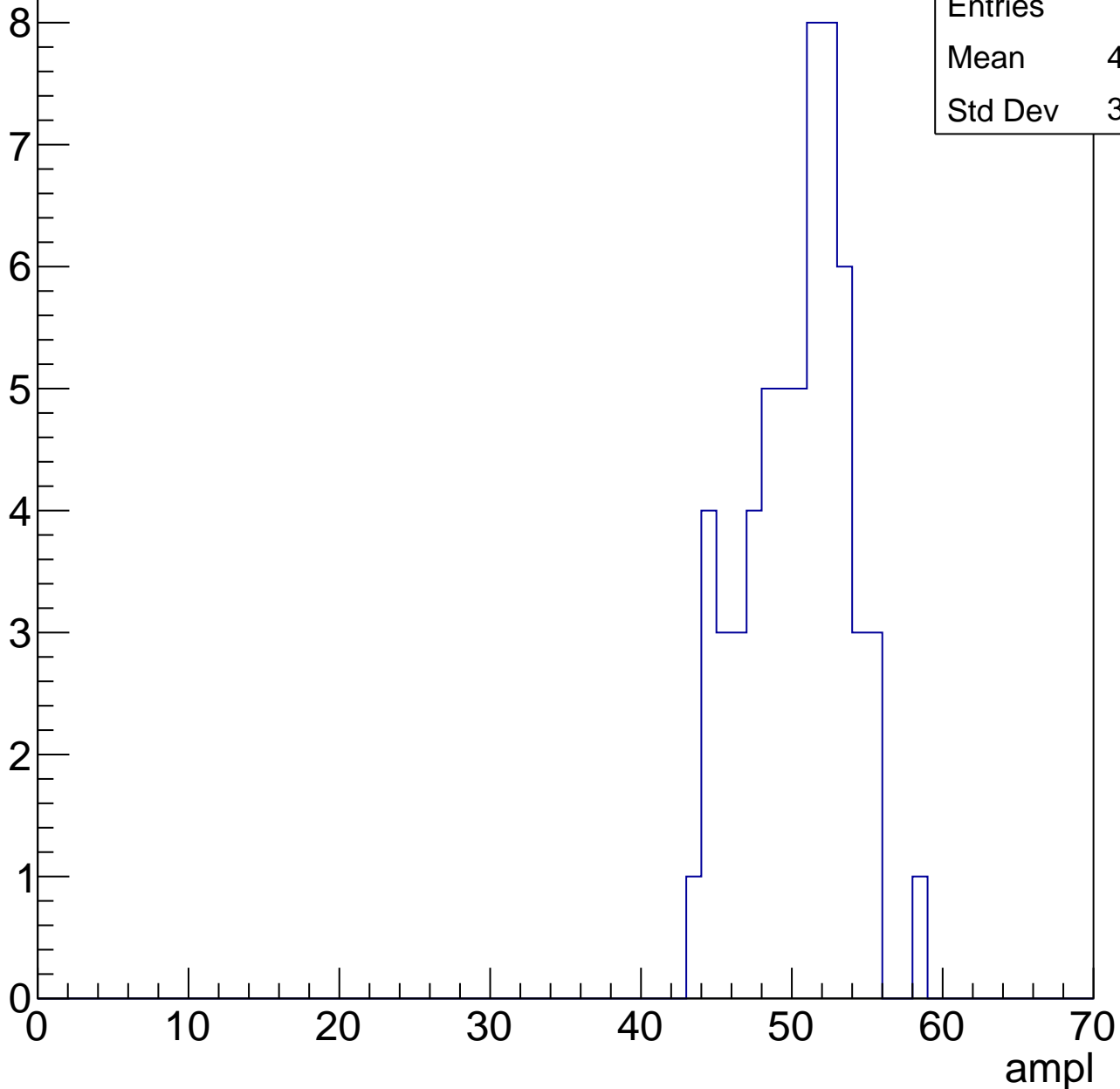


B1L103S, U24-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.86
Std Dev	3.337

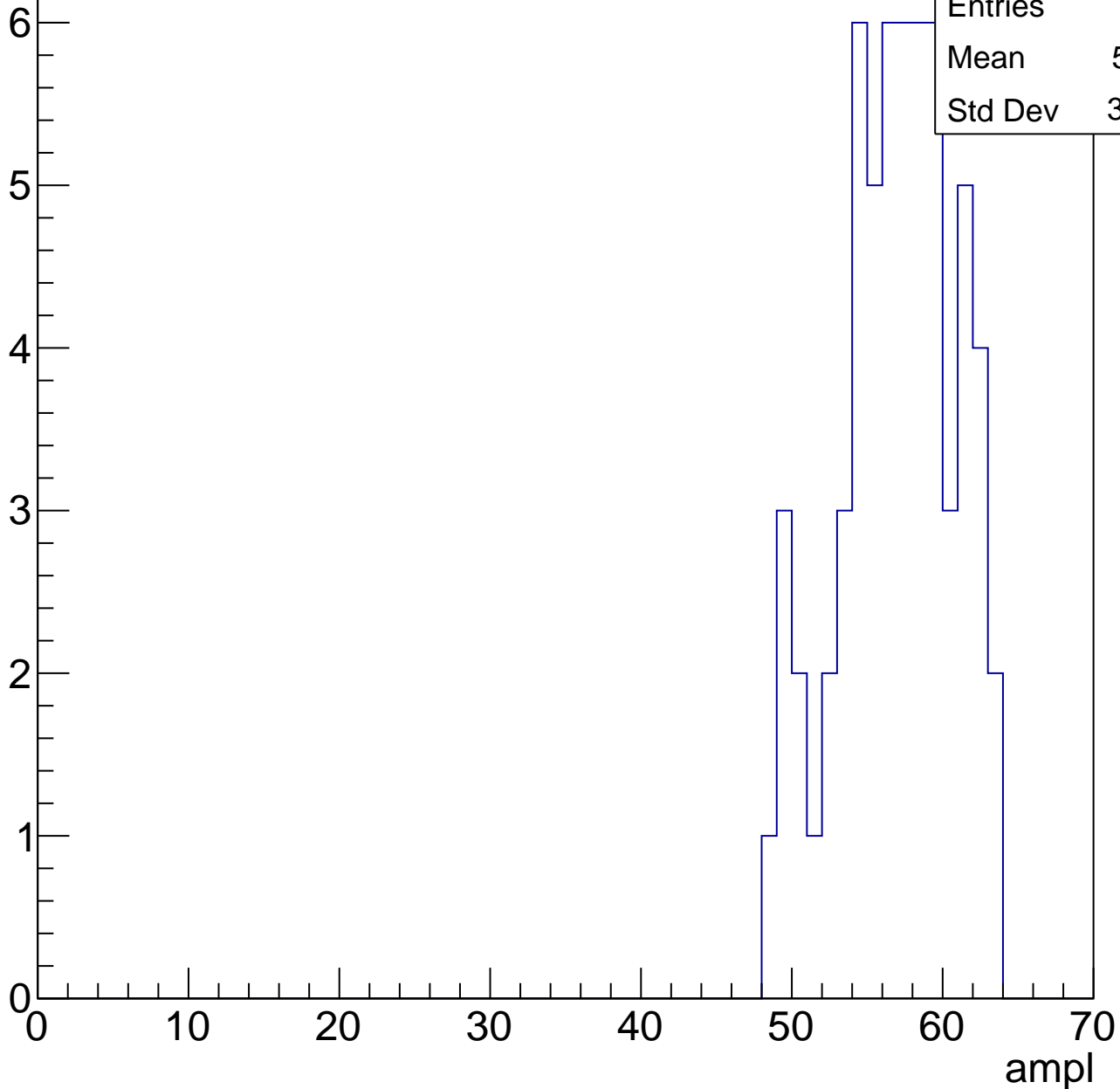


B1L103S, U24-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	56.51
Std Dev	3.797

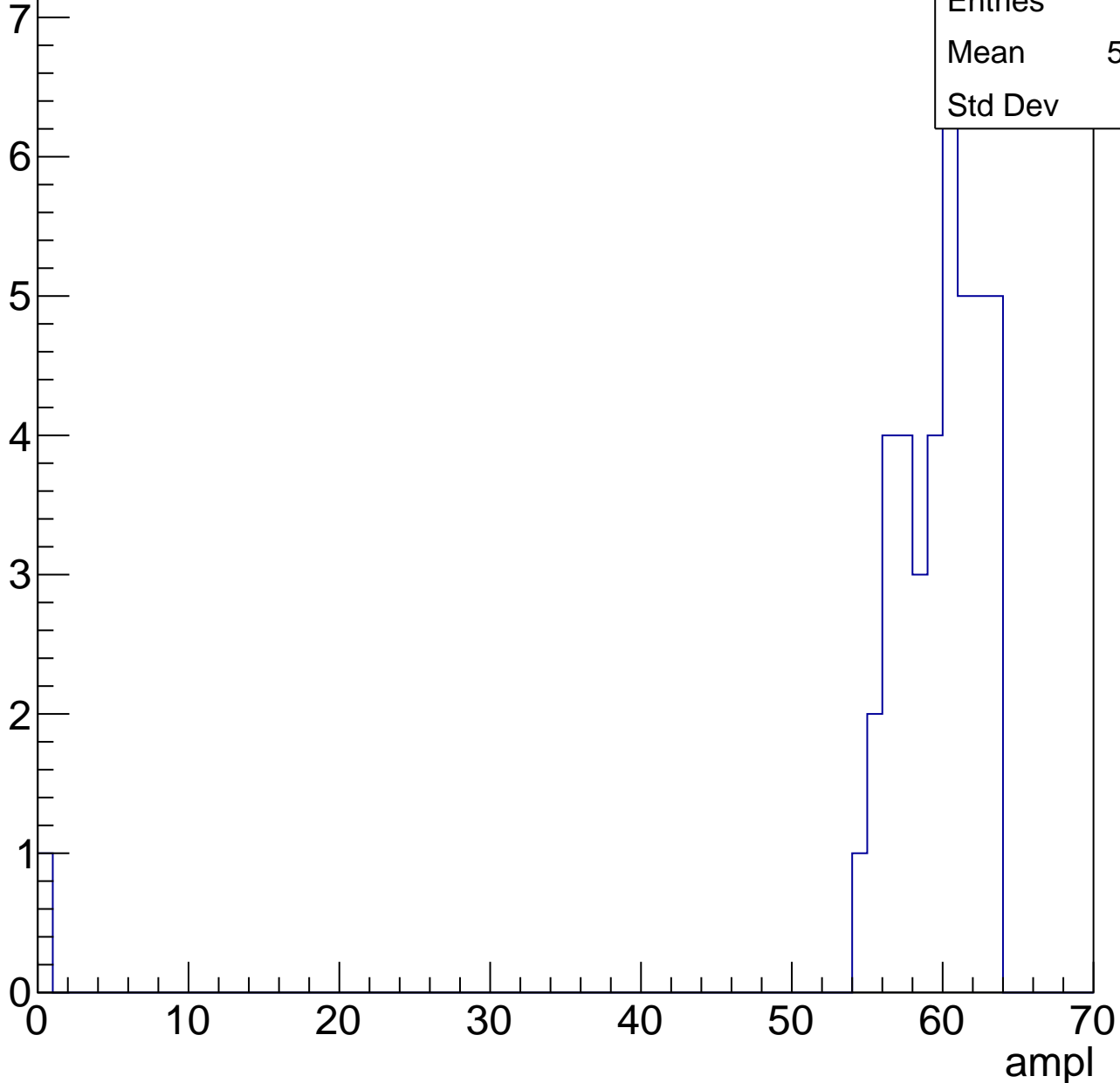


B1L103S, U24-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	57.95
Std Dev	9.5



B1L103S, U24-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch52, adc0

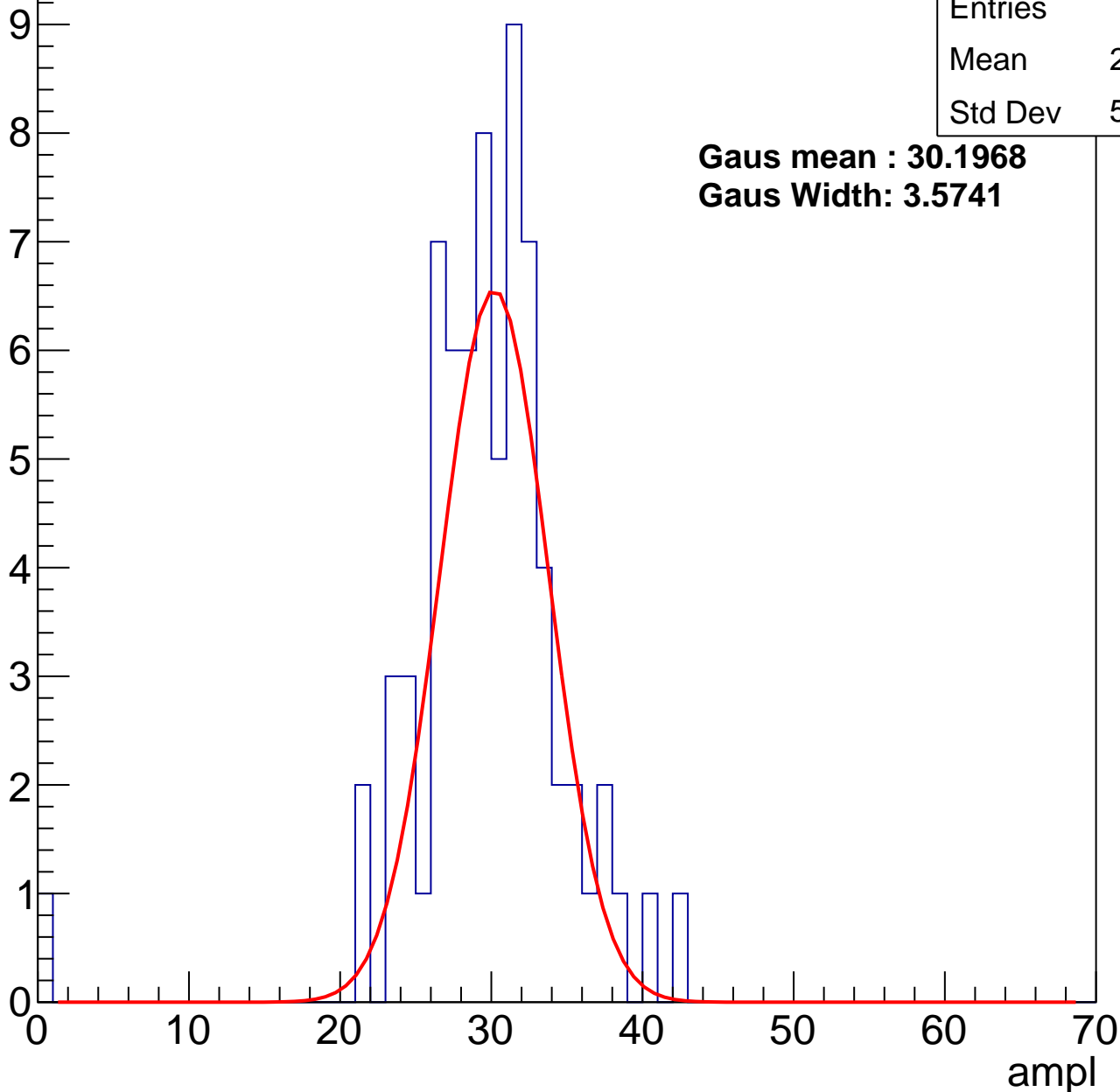
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.24
Std Dev	5.394

Gaus mean : 30.1668

Gaus Width: 3.5741



B1L103S, U24-ch52, adc1

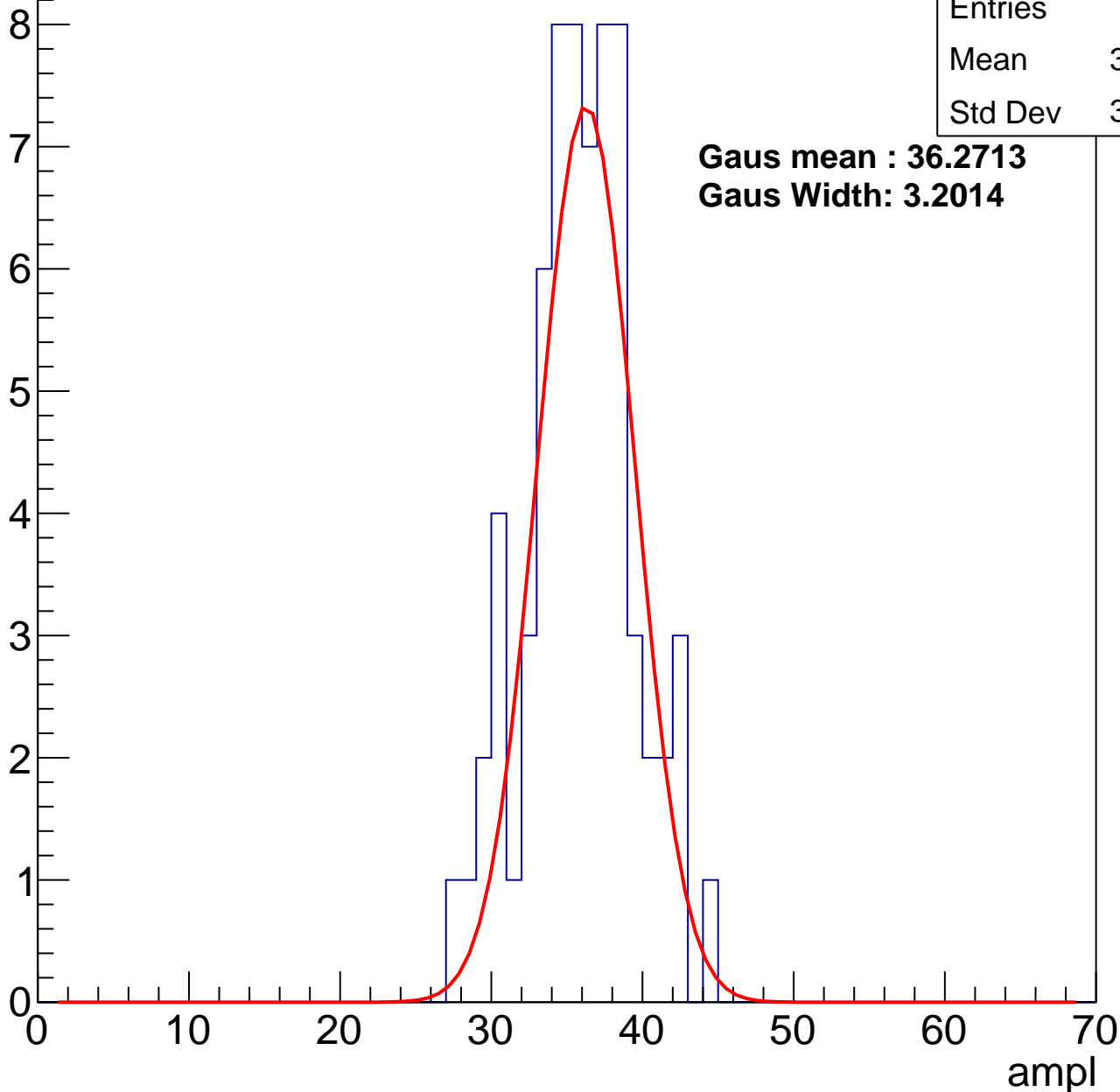
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.46
Std Dev	3.546

Gaus mean : 36.2713

Gaus Width: 3.2014



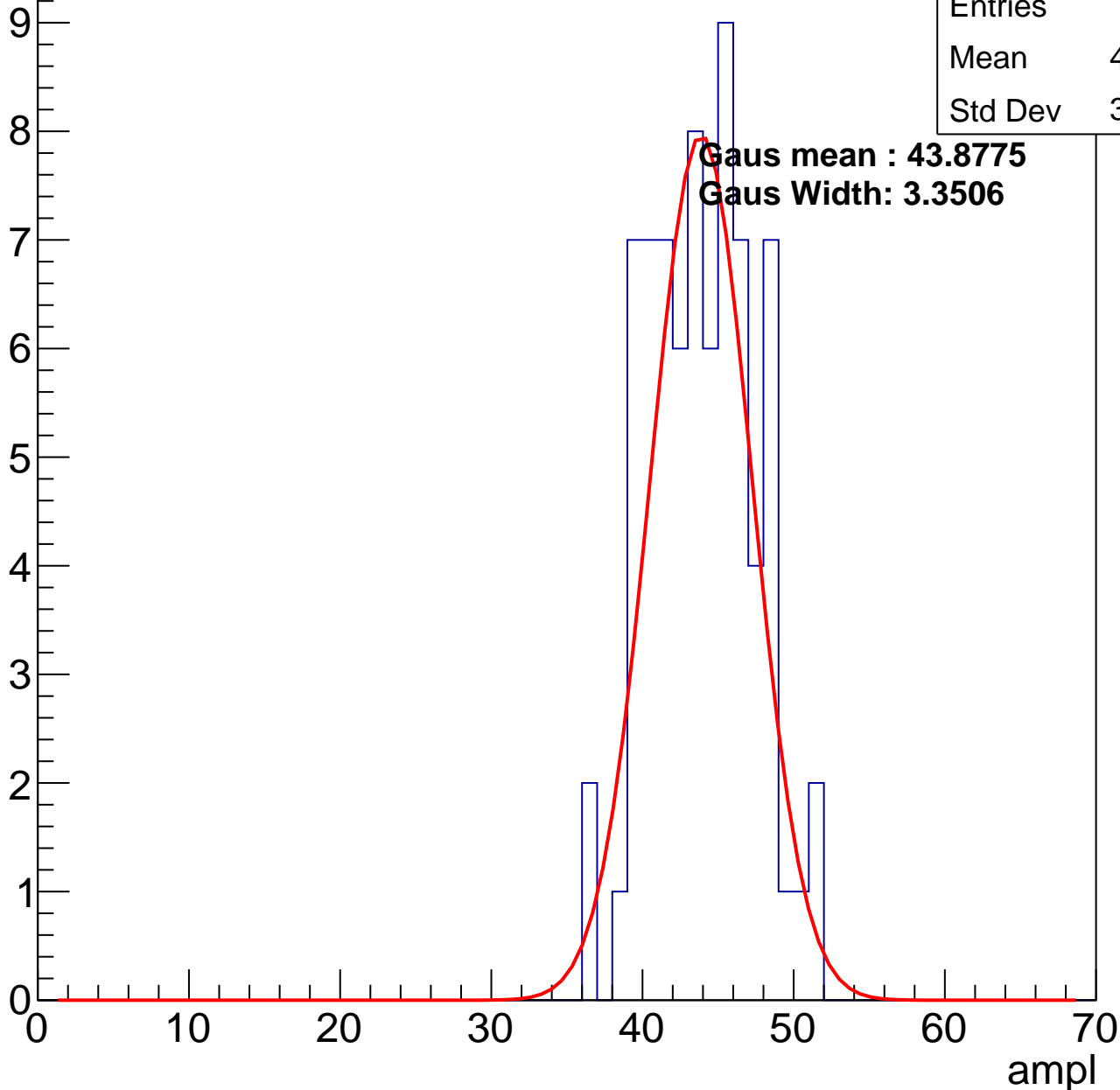
B1L103S, U24-ch52, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	43.49
Std Dev	3.407

Gaus mean : 43.8775
Gaus Width: 3.3506

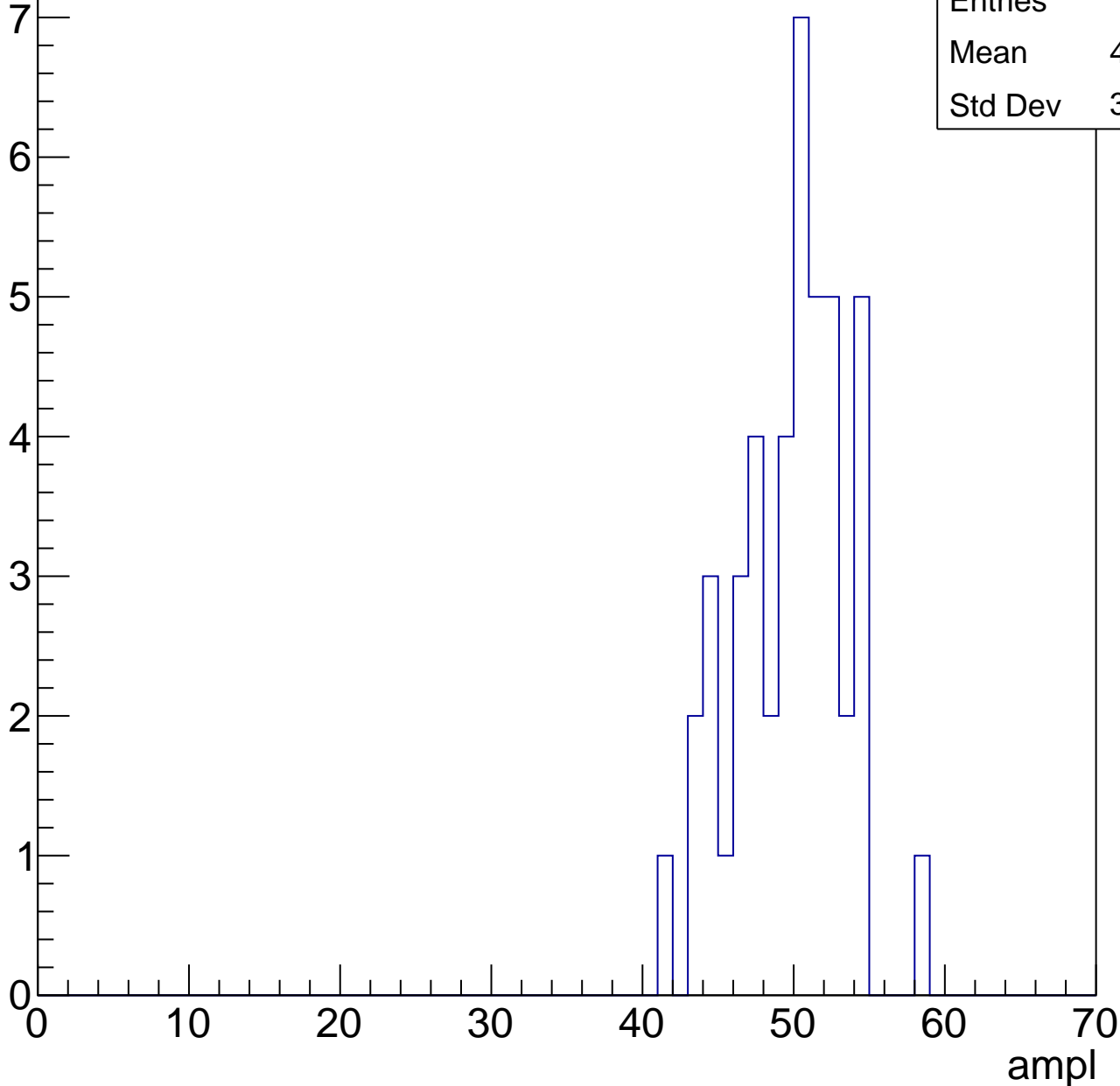


B1L103S, U24-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	49.36
Std Dev	3.579

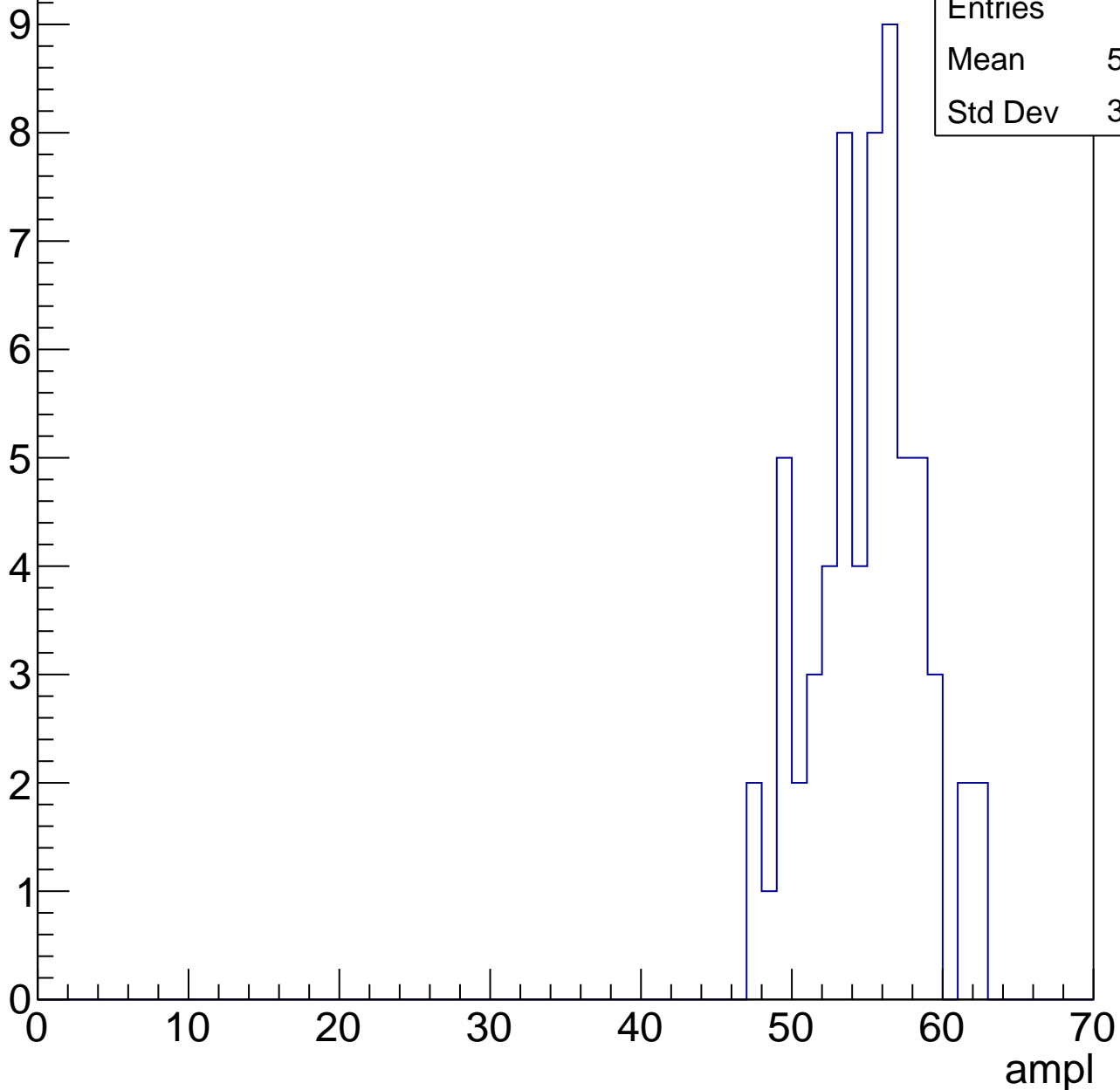


B1L103S, U24-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

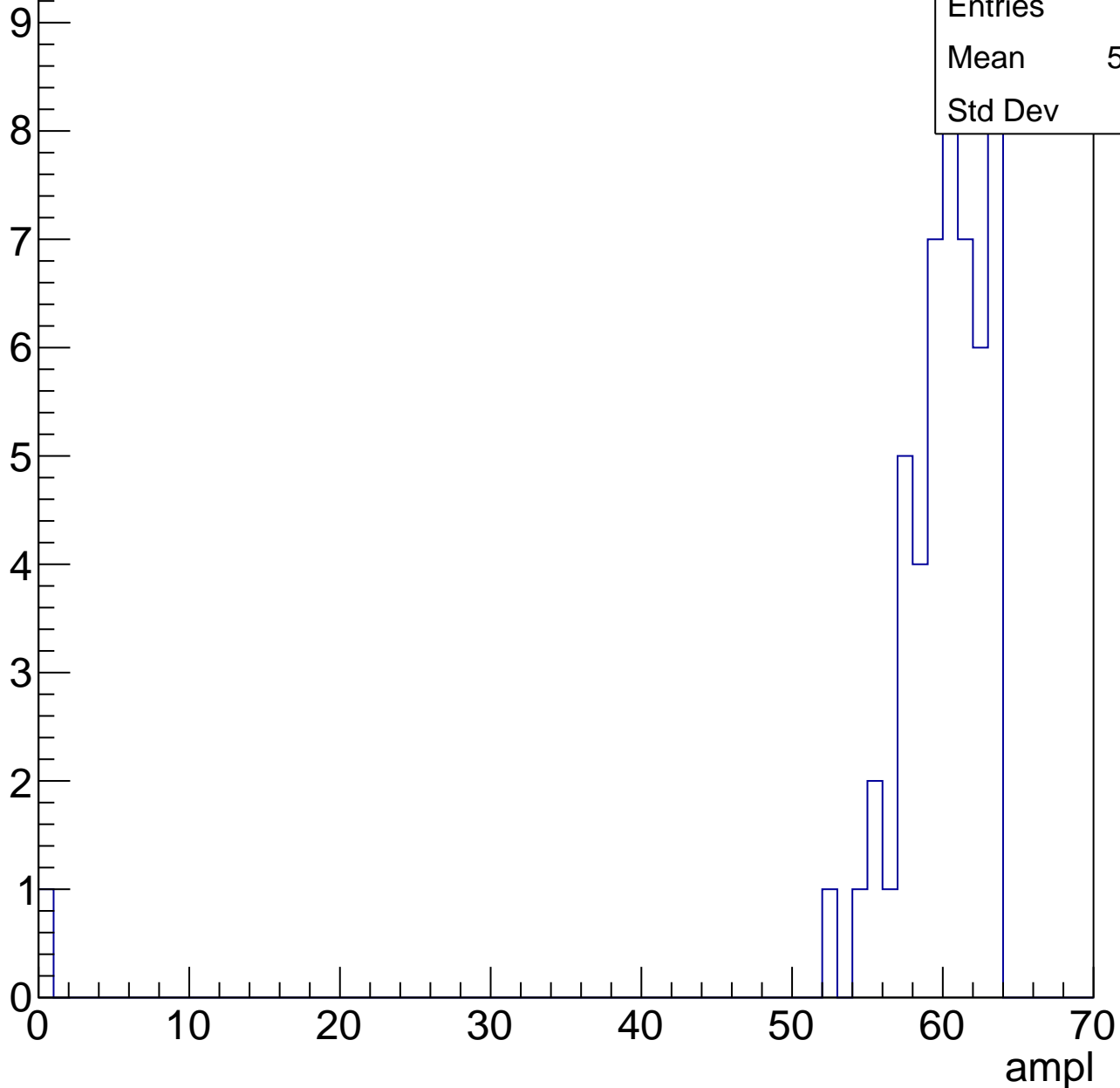
Entries	63
Mean	54.44
Std Dev	3.562



B1L103S, U24-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

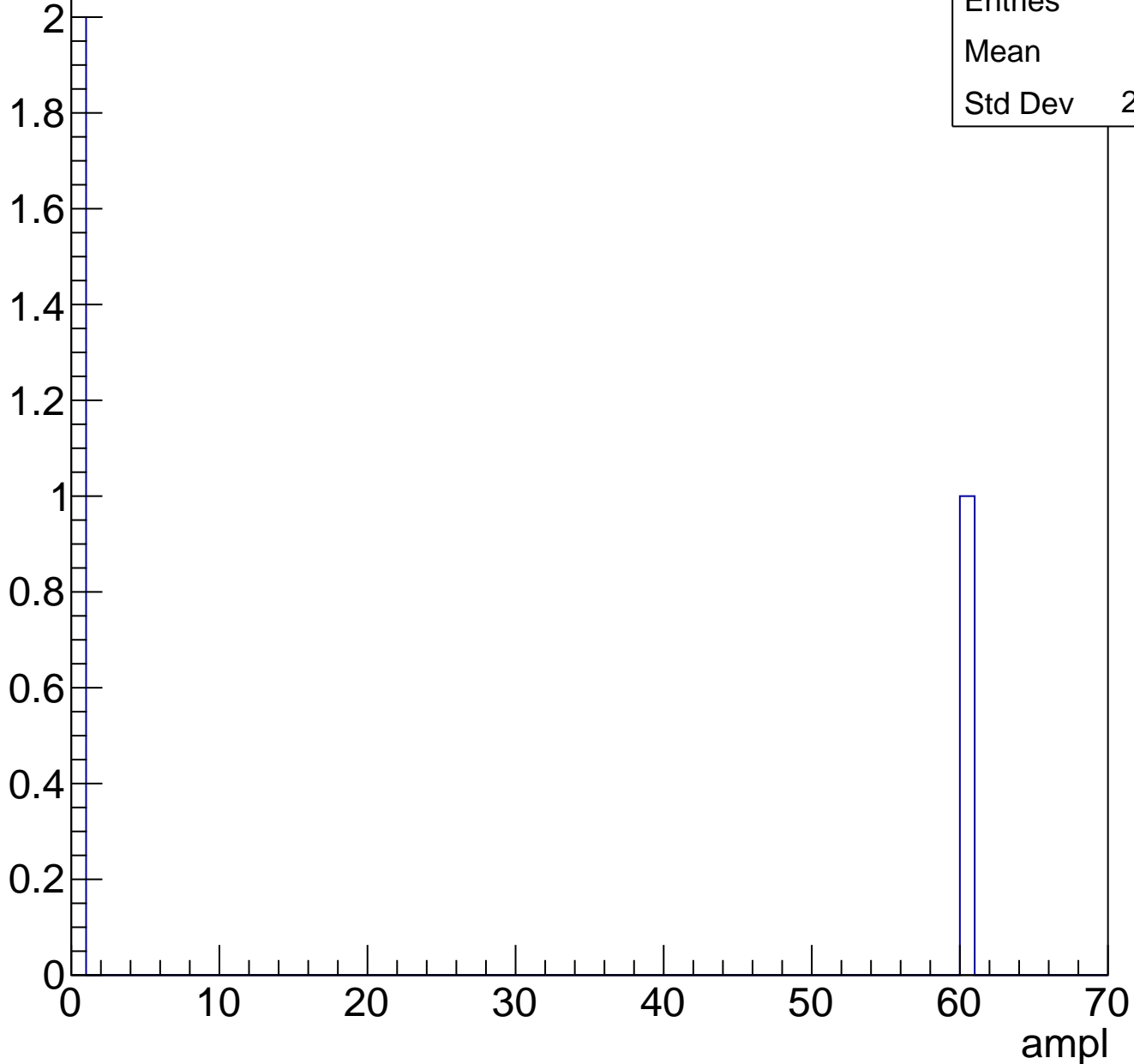


Entries	5
Mean	61.6
Std Dev	1.02

B1L103S, U24-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch53, adc0

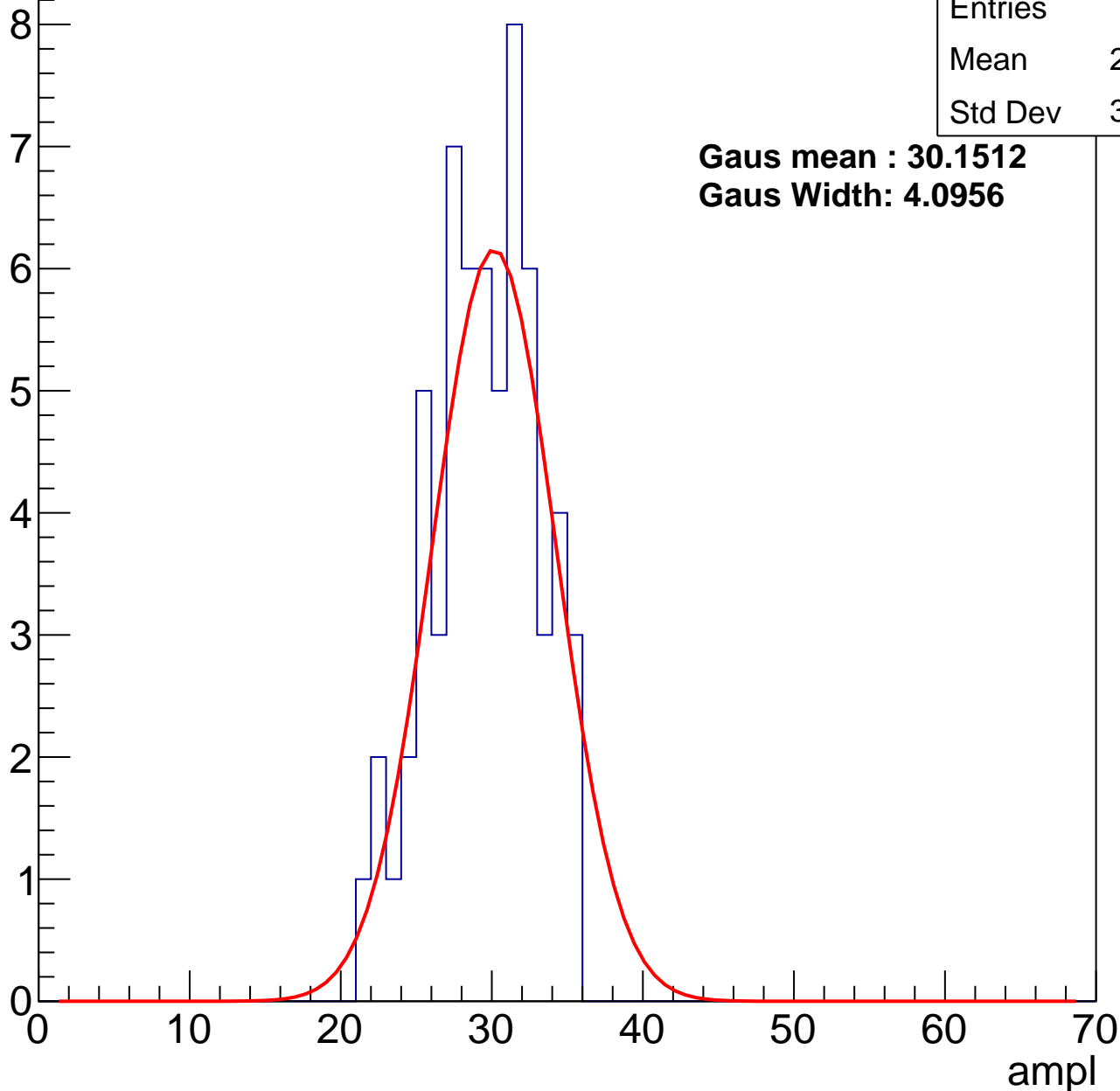
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.03
Std Dev	3.445

Gaus mean : 30.1512

Gaus Width: 4.0956



B1L103S, U24-ch53, adc1

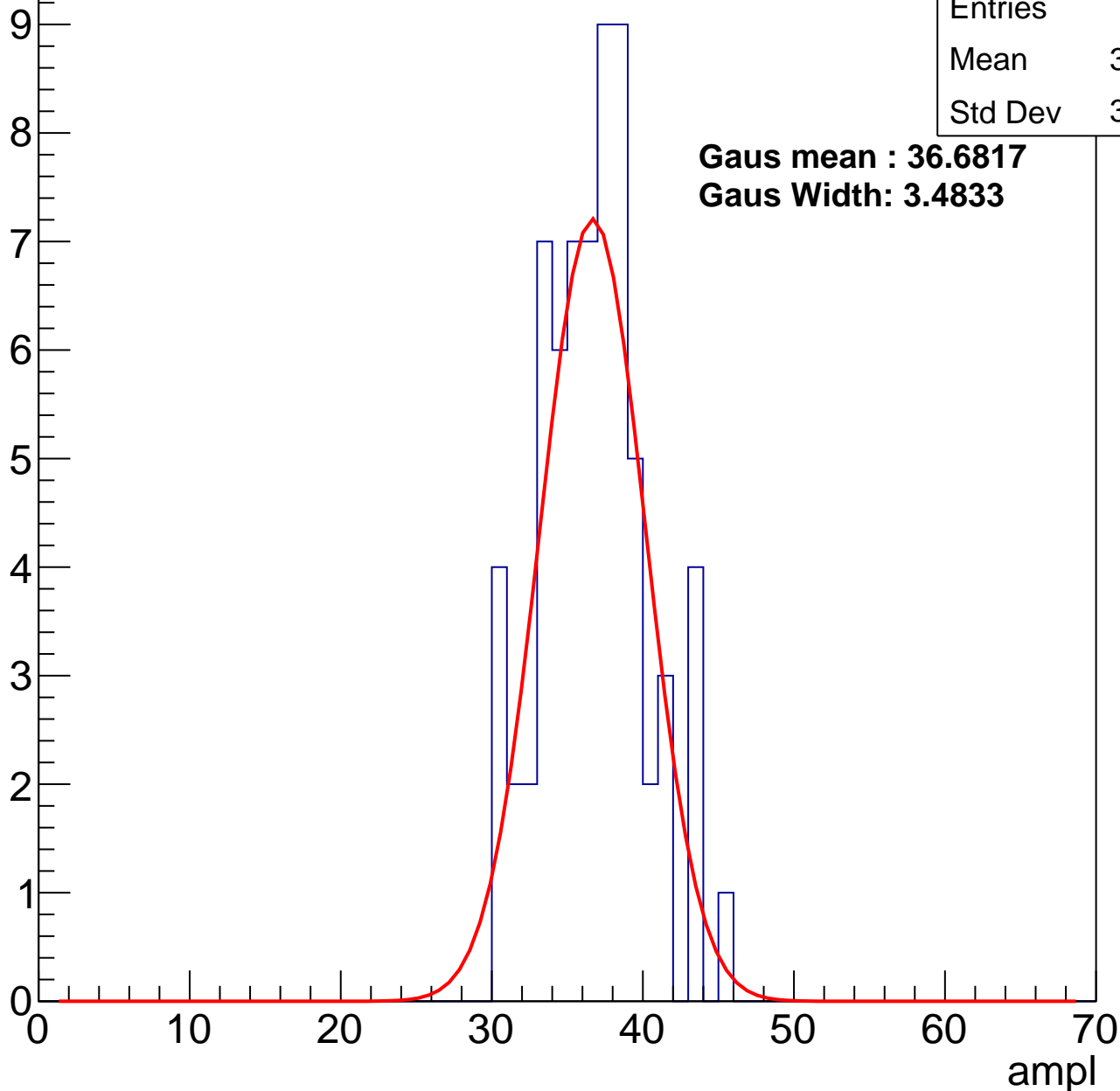
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.29
Std Dev	3.404

Gaus mean : 36.6817

Gaus Width: 3.4833



B1L103S, U24-ch53, adc2

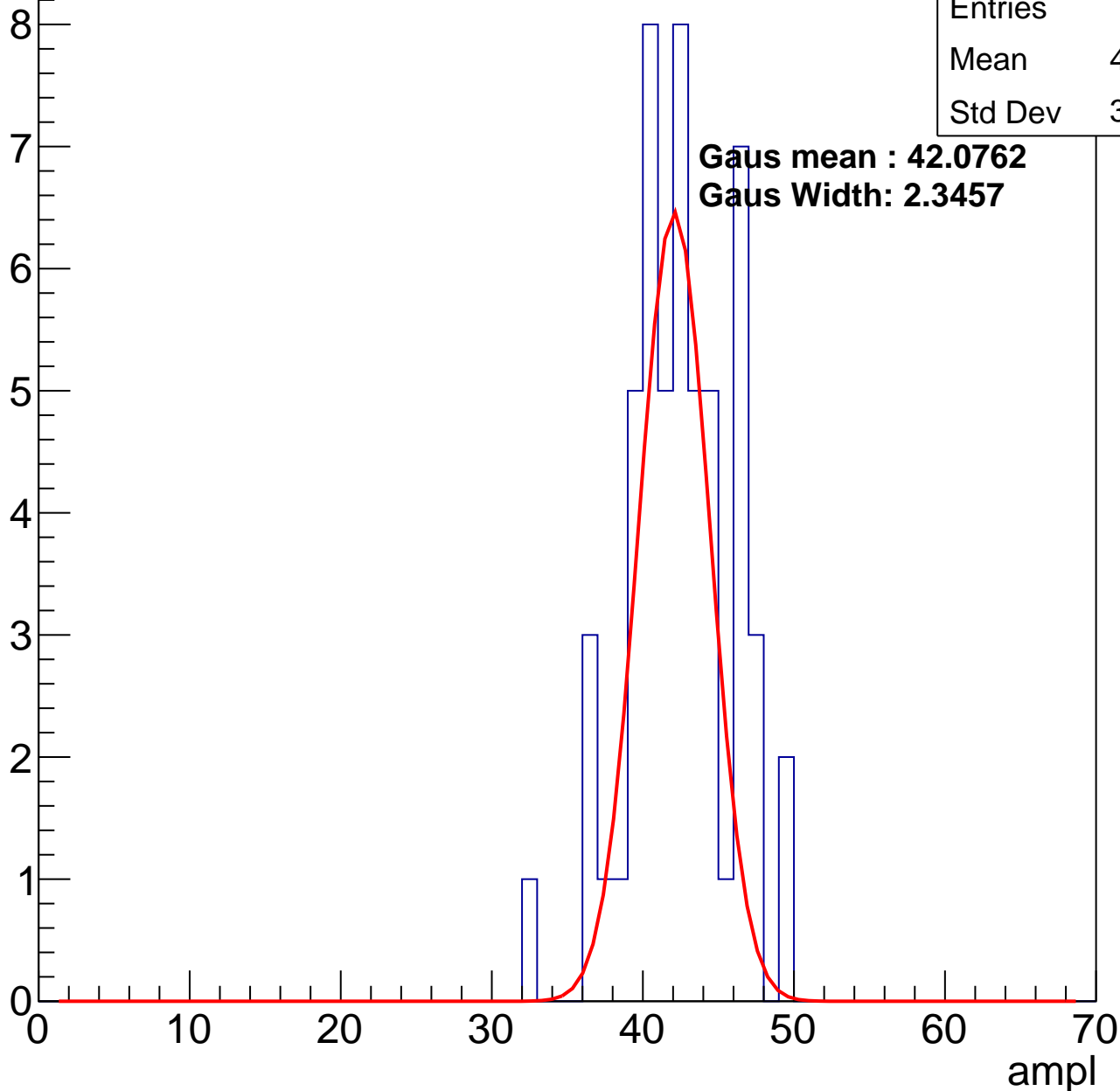
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.04
Std Dev	3.438

Gaus mean : 42.0762

Gaus Width: 2.3457

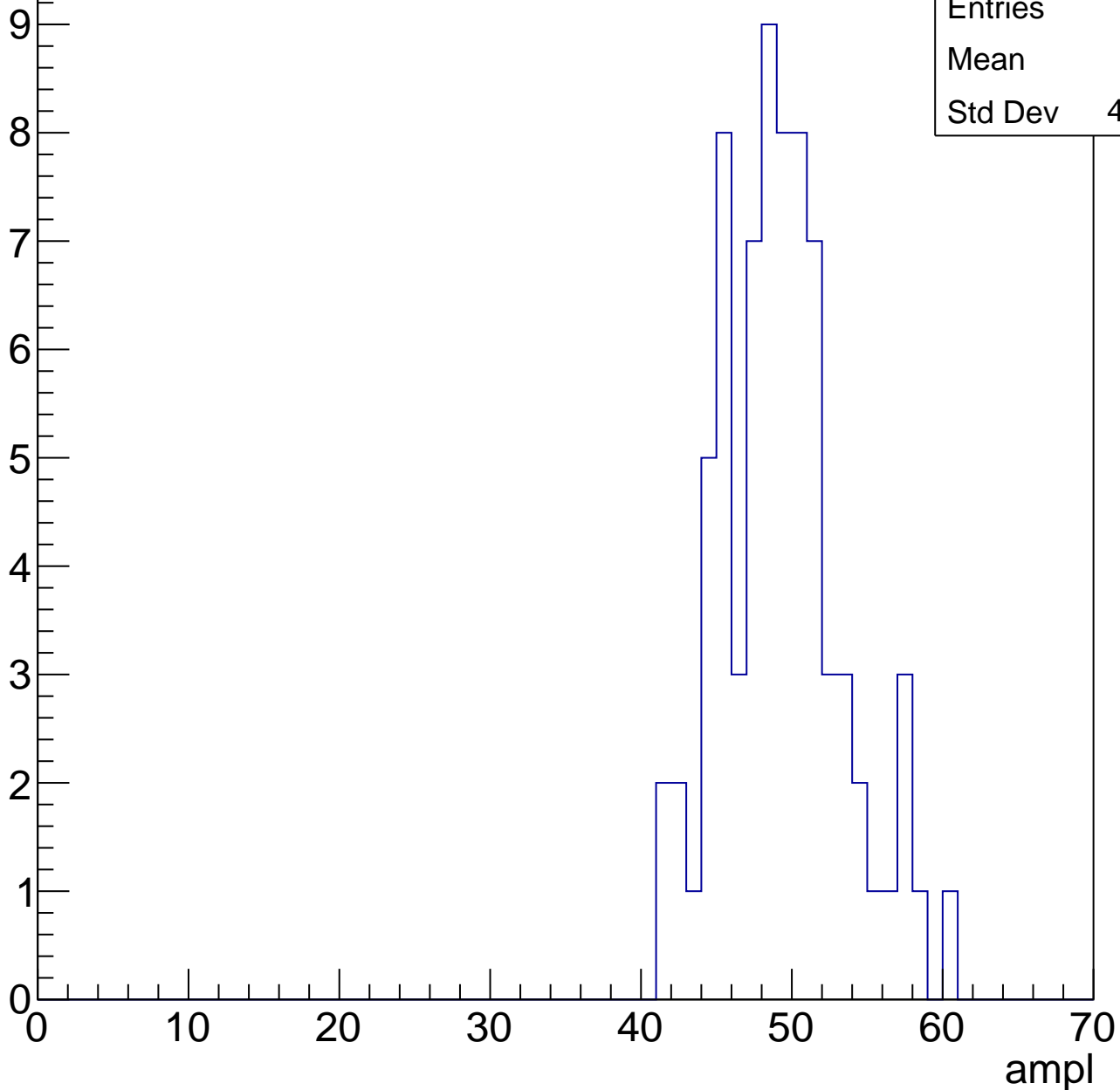


B1L103S, U24-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

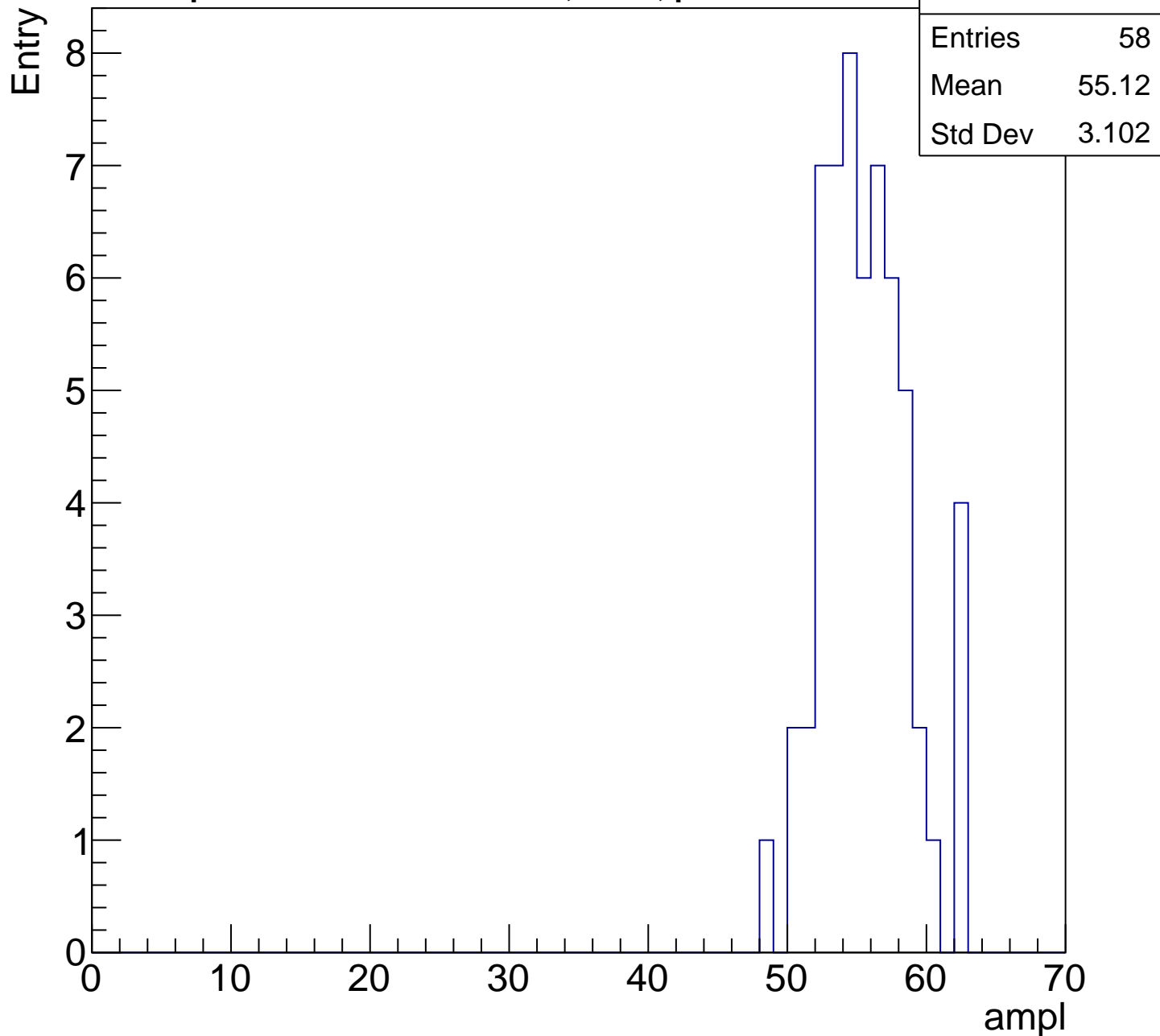
Entry

Entries	75
Mean	48.8
Std Dev	4.027



B1L103S, U24-ch53, adc4

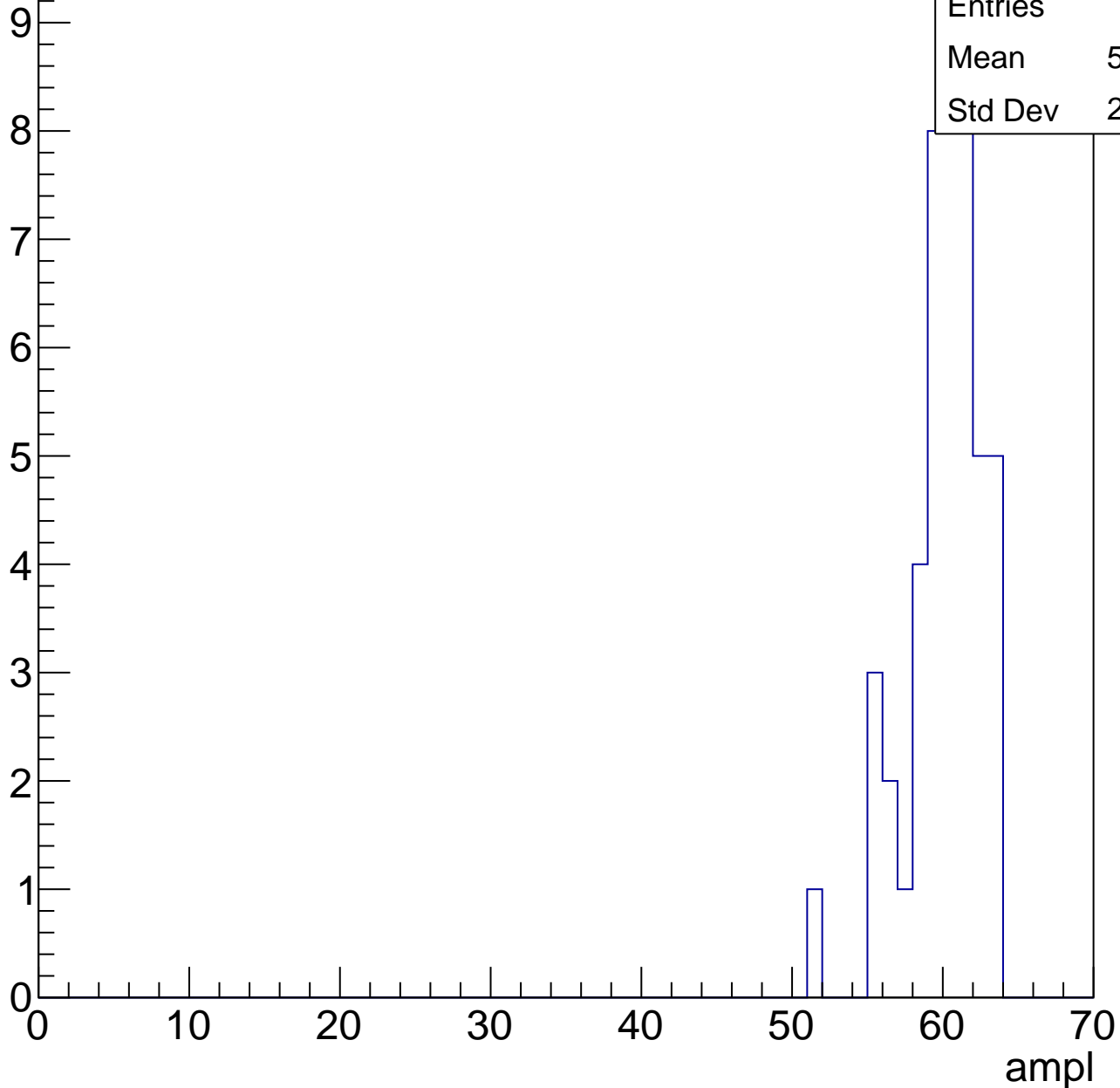
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

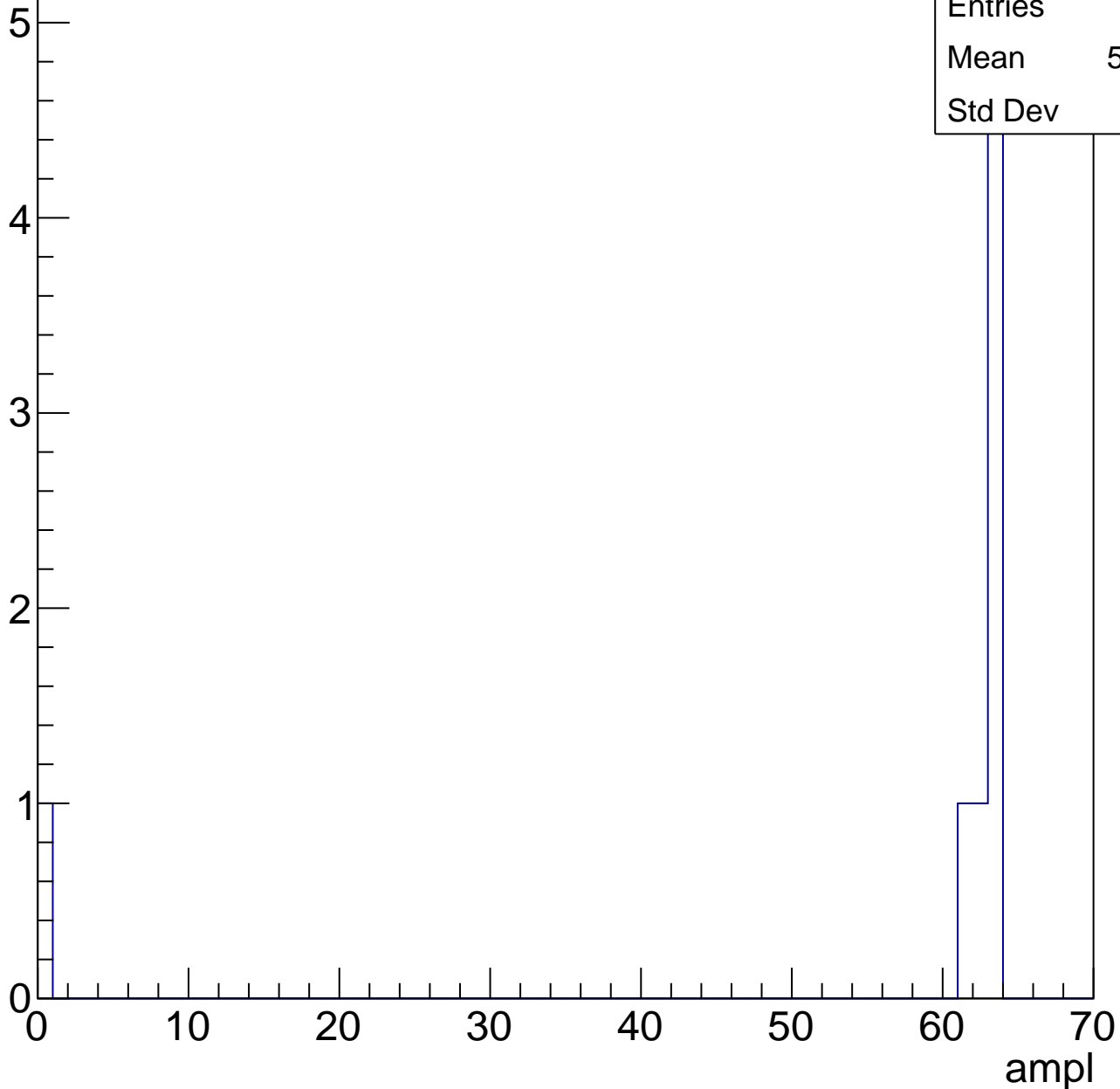


B1L103S, U24-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B1L103S, U24-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch54, adc0

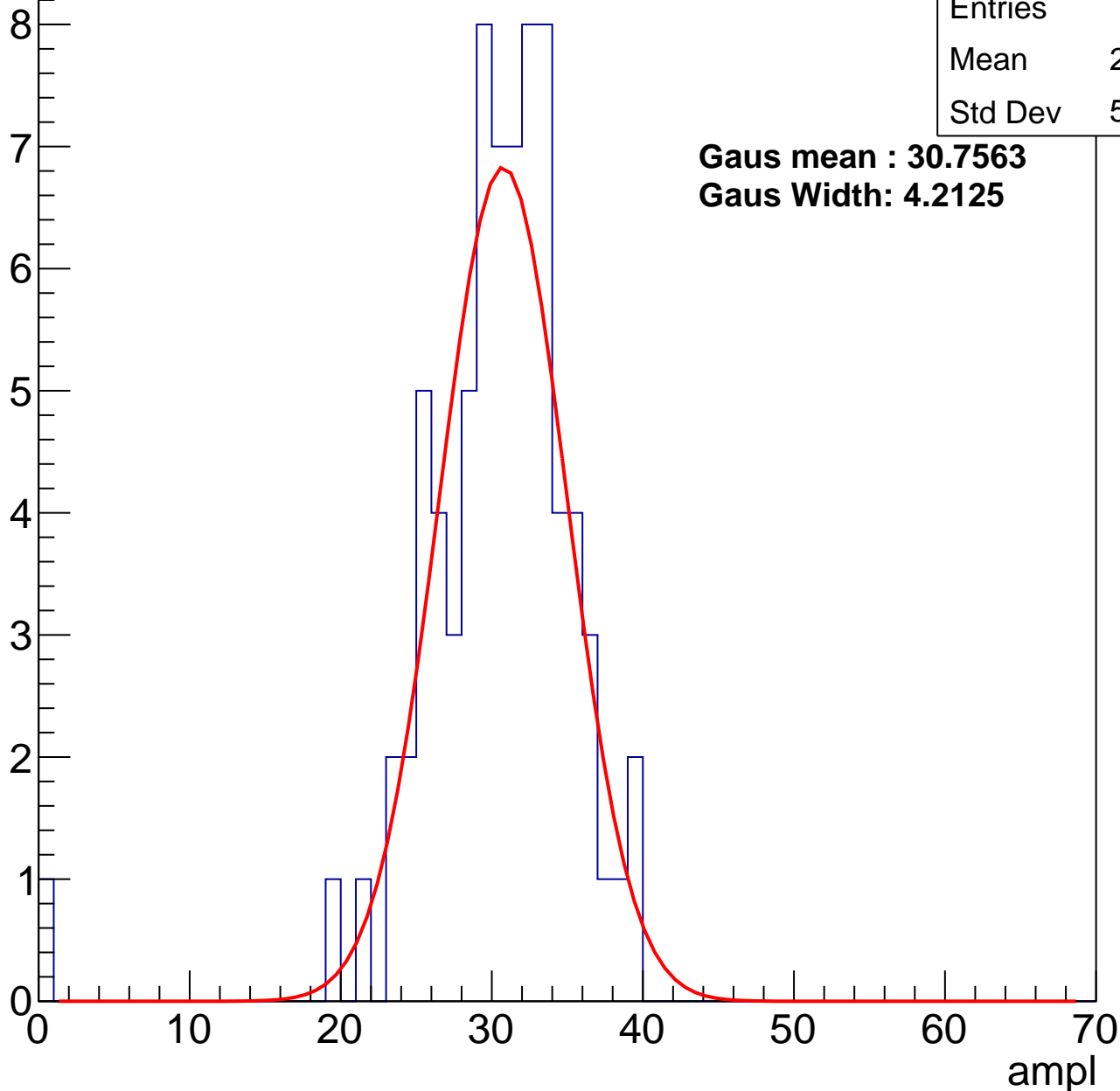
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	29.87
Std Dev	5.312

Gaus mean : 30.7563

Gaus Width: 4.2125



B1L103S, U24-ch54, adc1

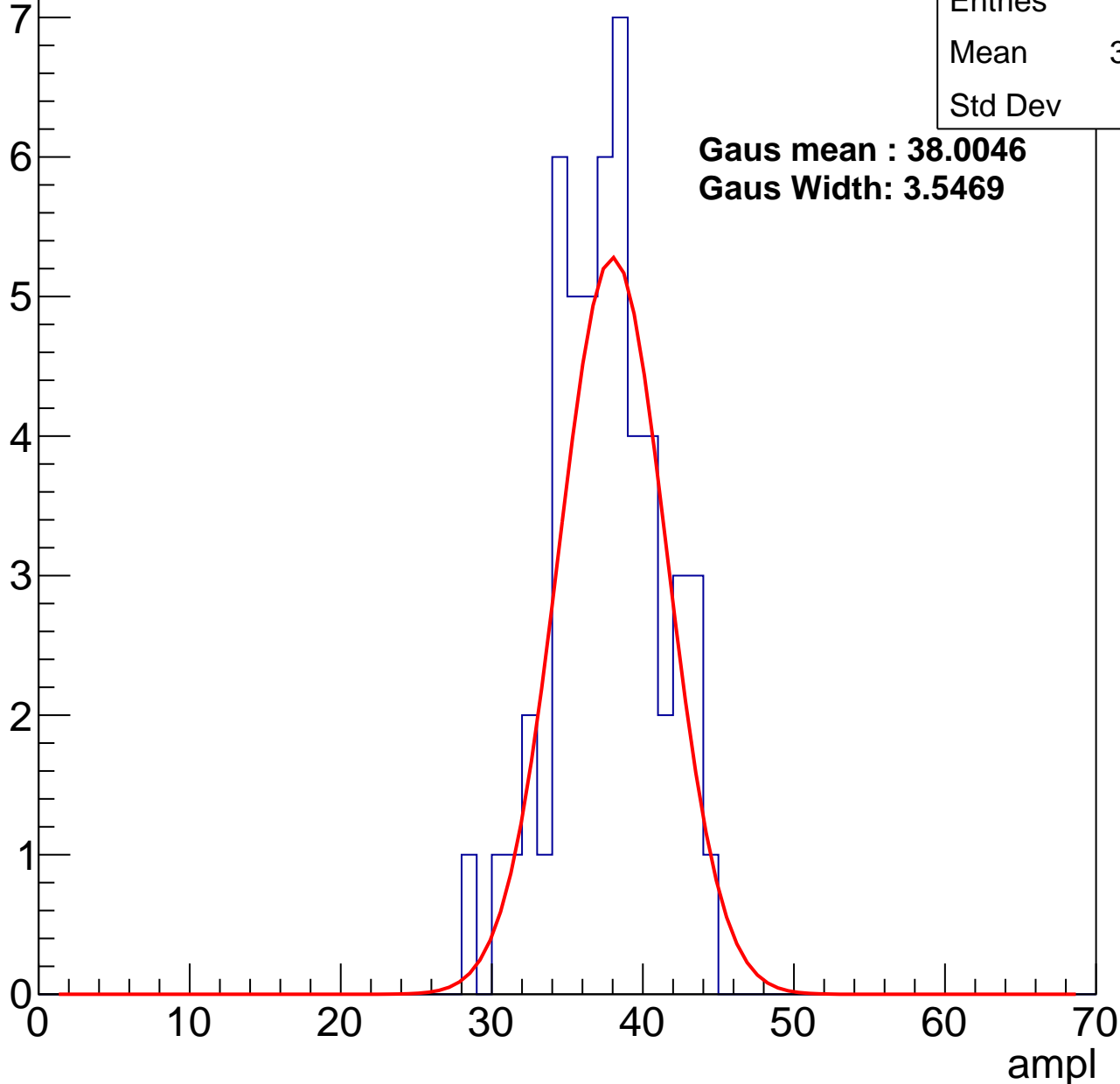
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	37.12
Std Dev	3.49

Gaus mean : 38.0046

Gaus Width: 3.5469



B1L103S, U24-ch54, adc2

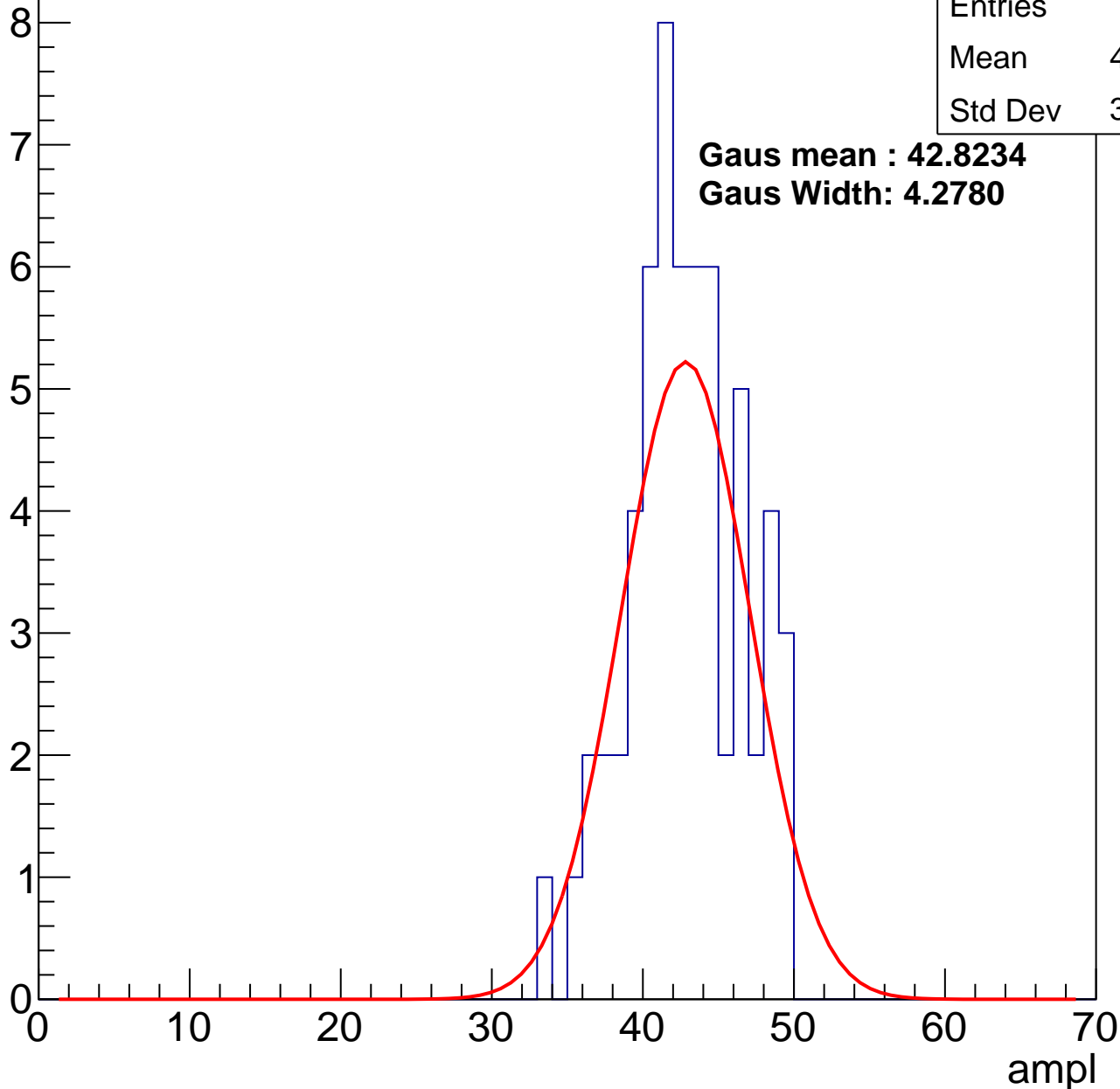
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.35
Std Dev	3.683

Gaus mean : 42.8234

Gaus Width: 4.2780

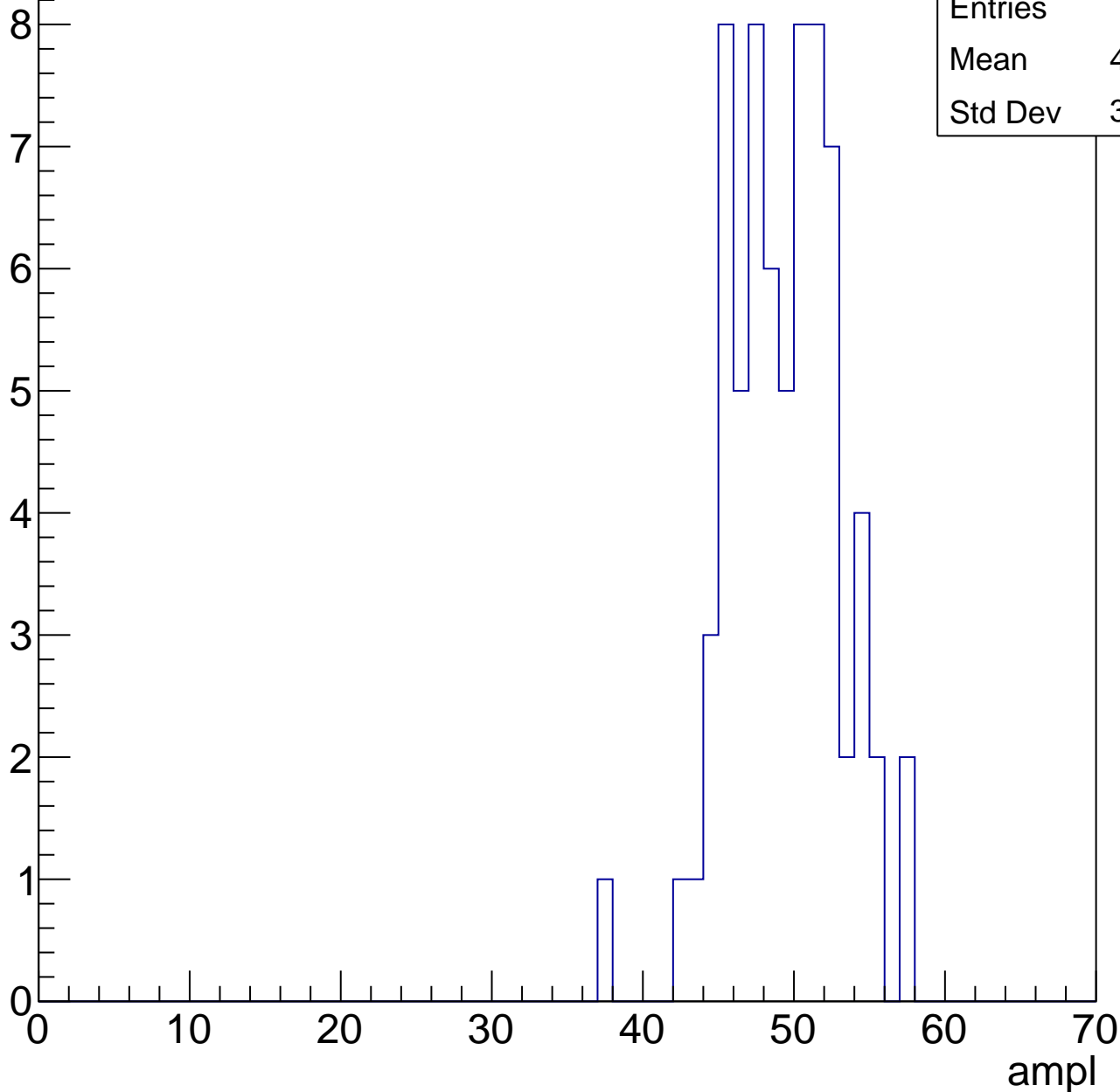


B1L103S, U24-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.89
Std Dev	3.656

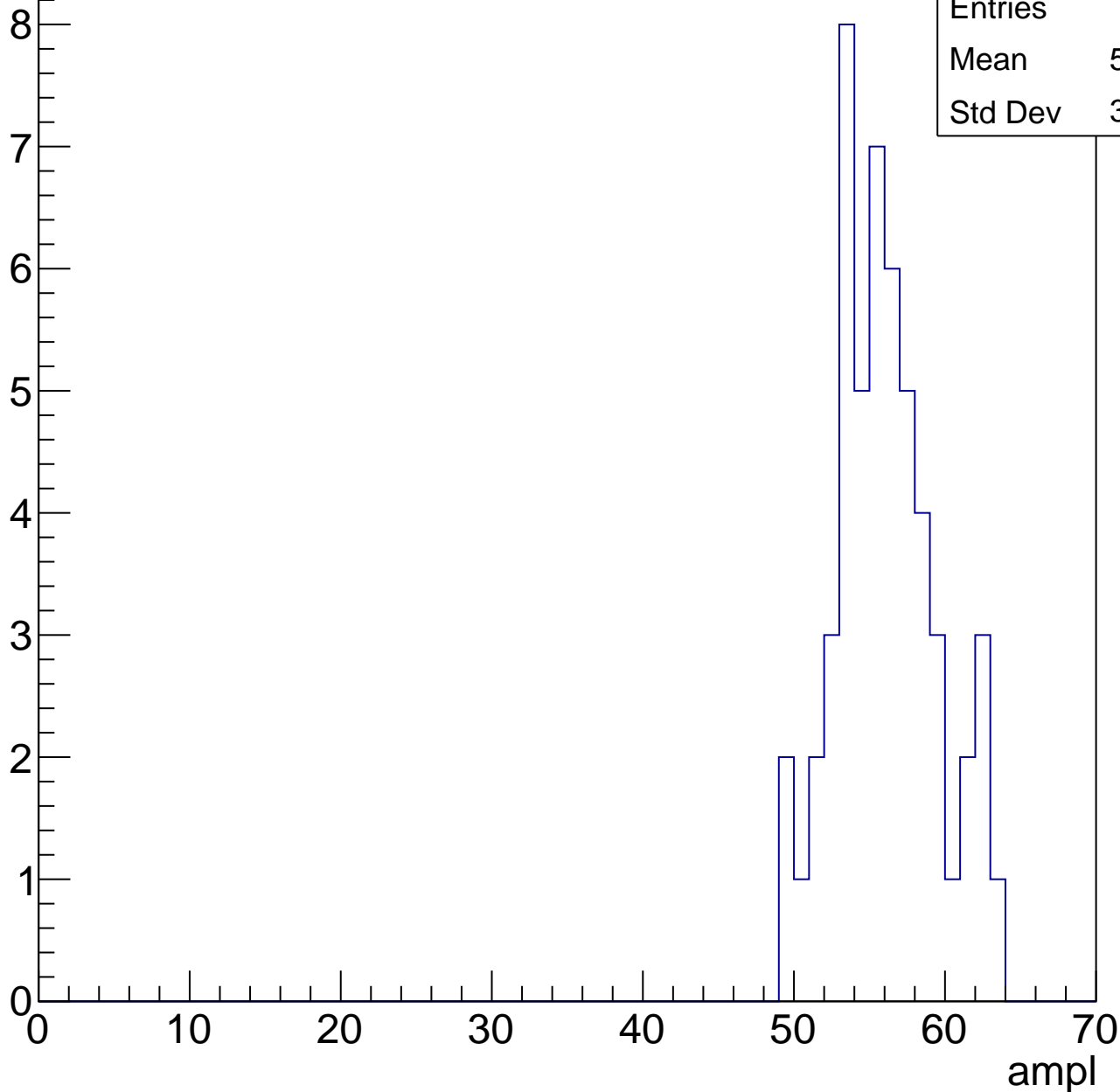


B1L103S, U24-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.58
Std Dev	3.345

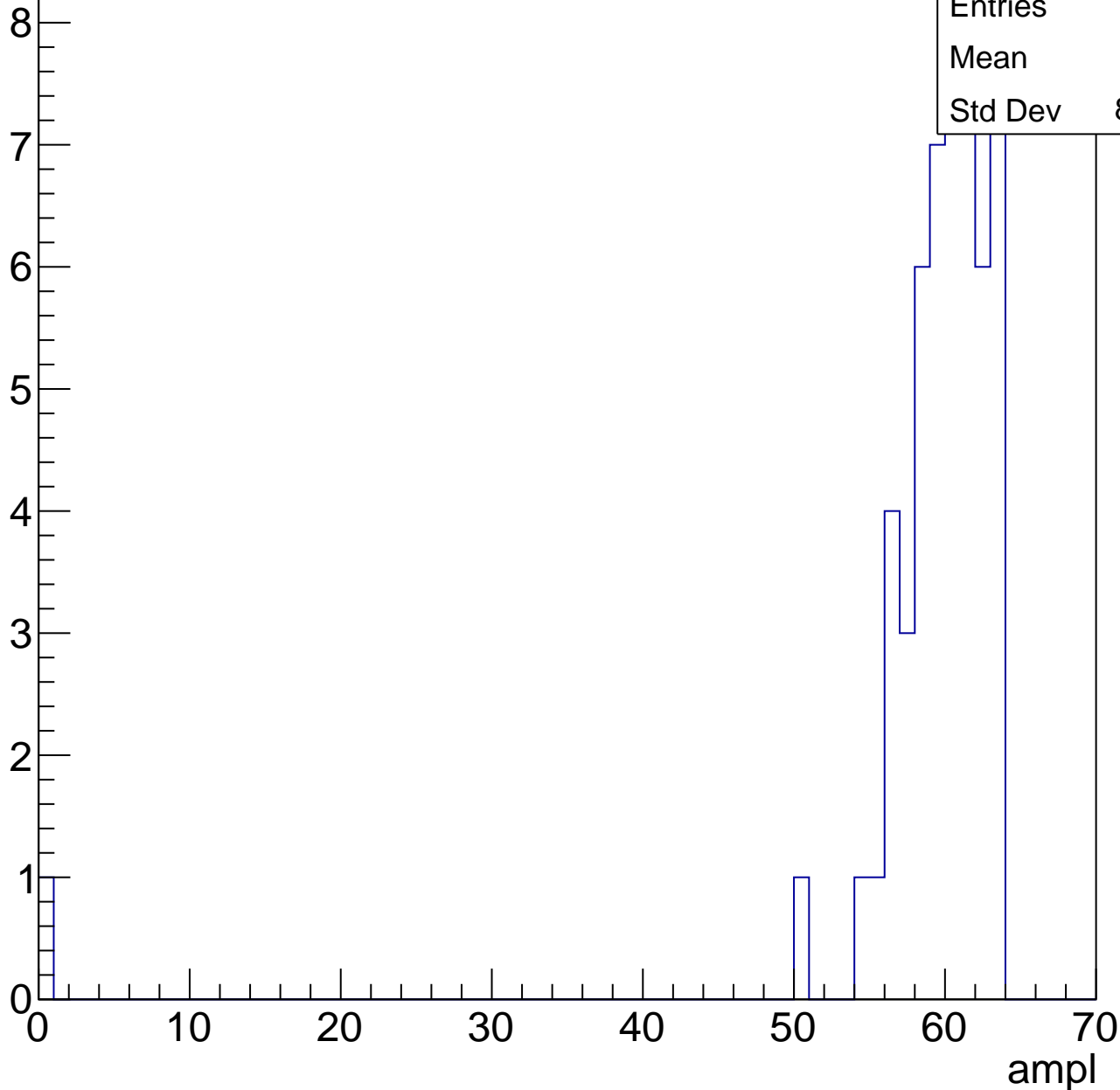


B1L103S, U24-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58.5
Std Dev	8.461



B1L103S, U24-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch55, adc0

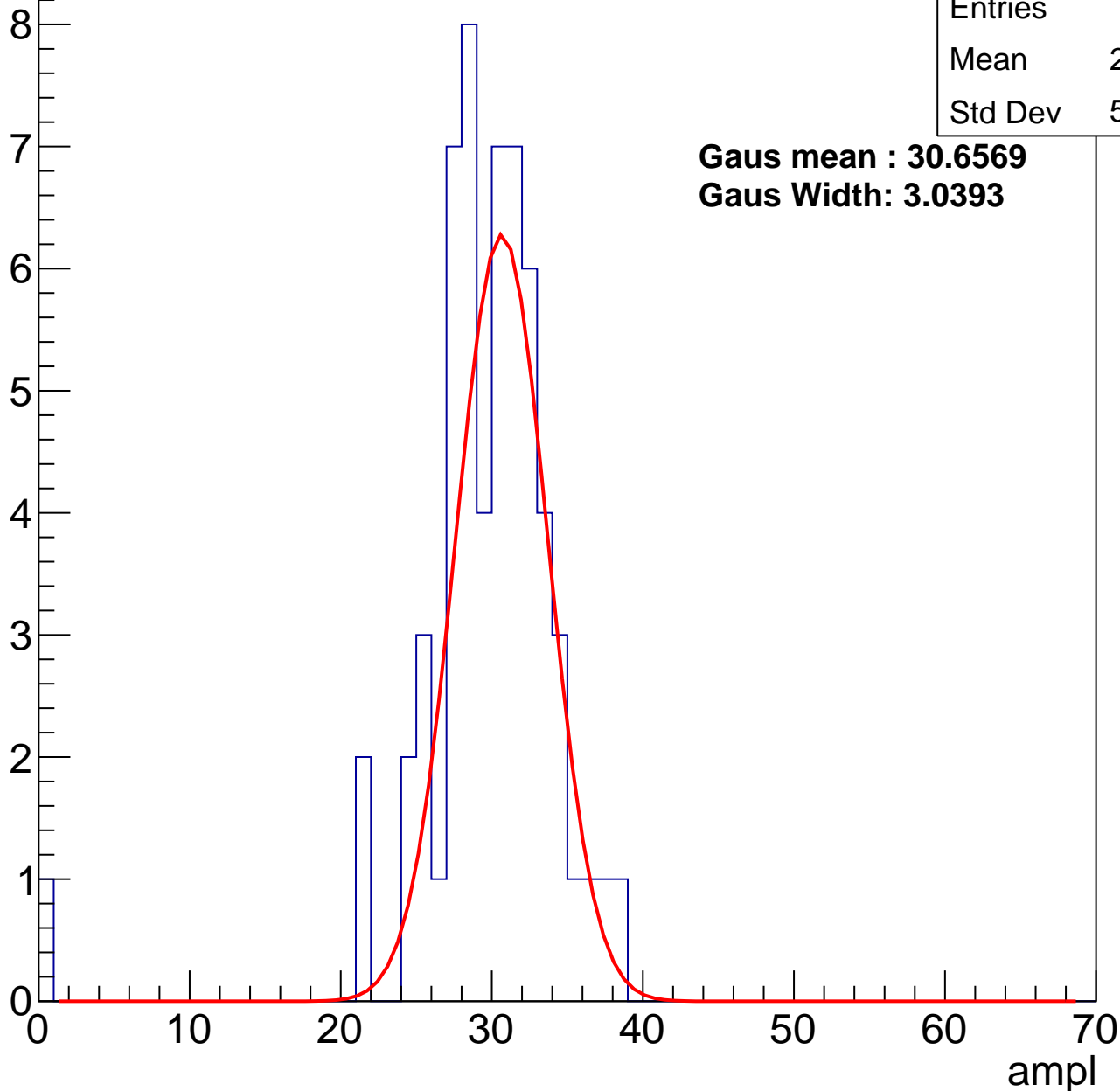
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	29.14
Std Dev	5.157

Gaus mean : 30.6569

Gaus Width: 3.0393



B1L103S, U24-ch55, adc1

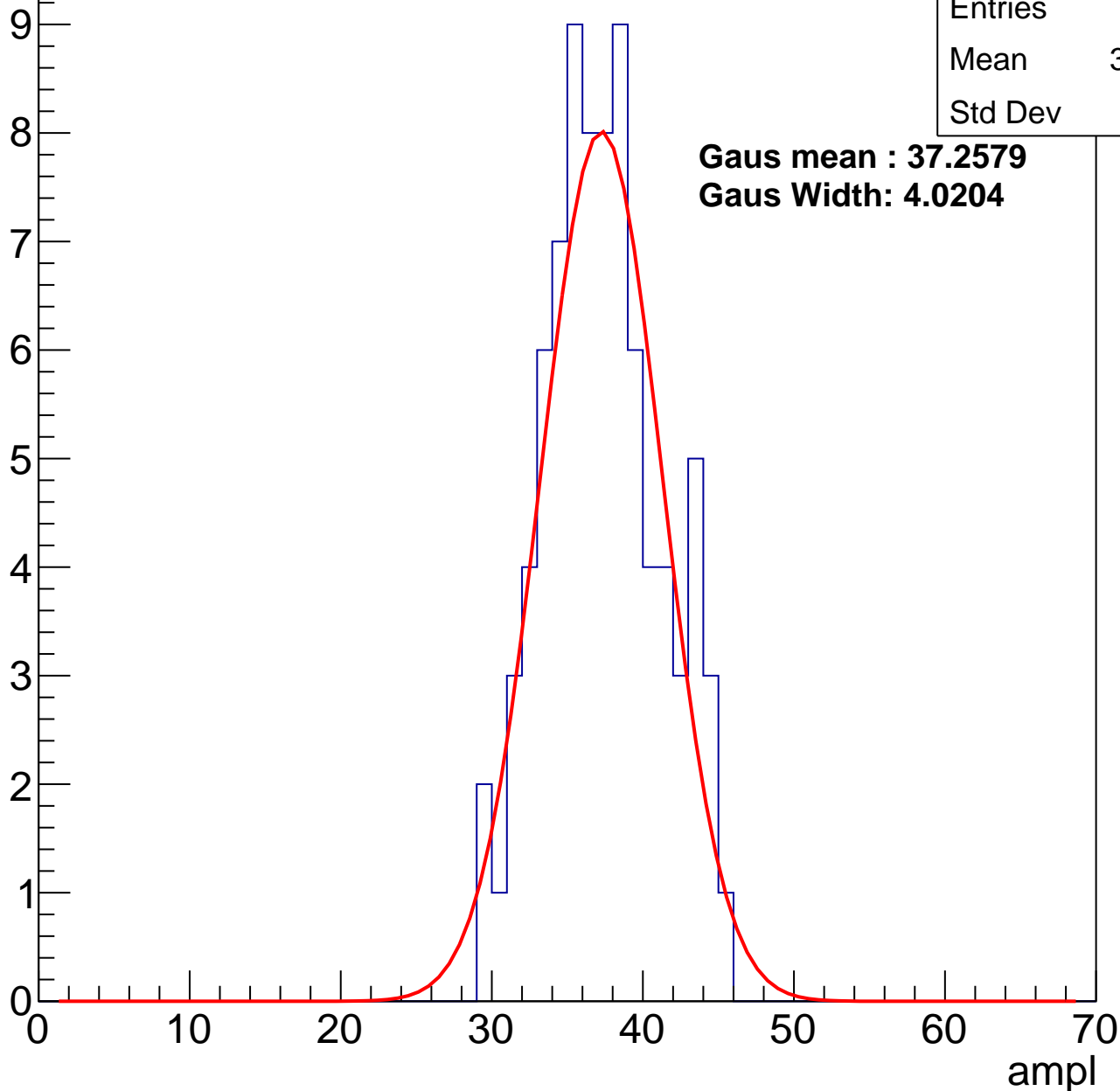
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	36.89
Std Dev	3.78

Gaus mean : 37.2579

Gaus Width: 4.0204



B1L103S, U24-ch55, adc2

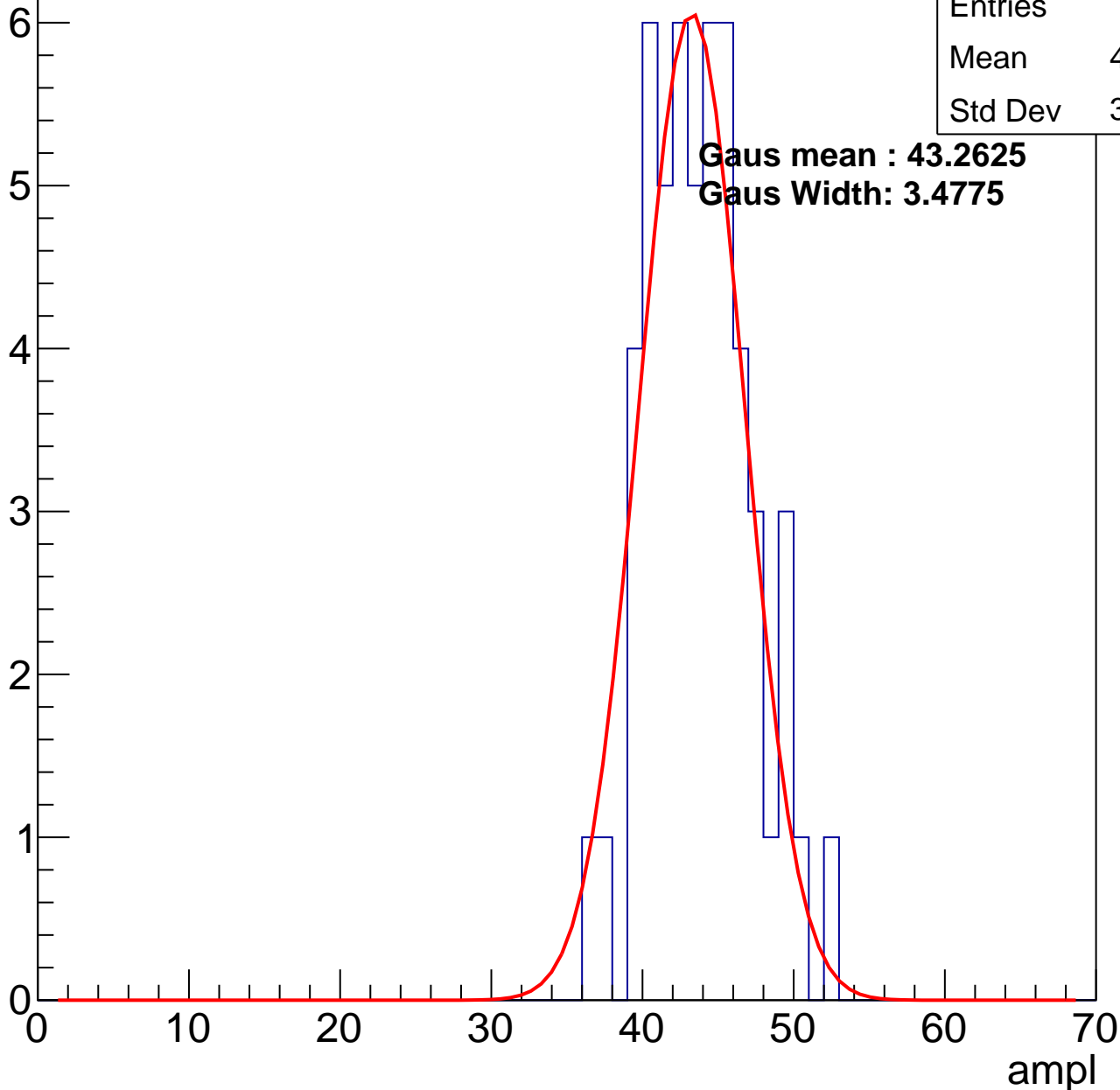
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	43.34
Std Dev	3.364

Gaus mean : 43.2625

Gaus Width: 3.4775

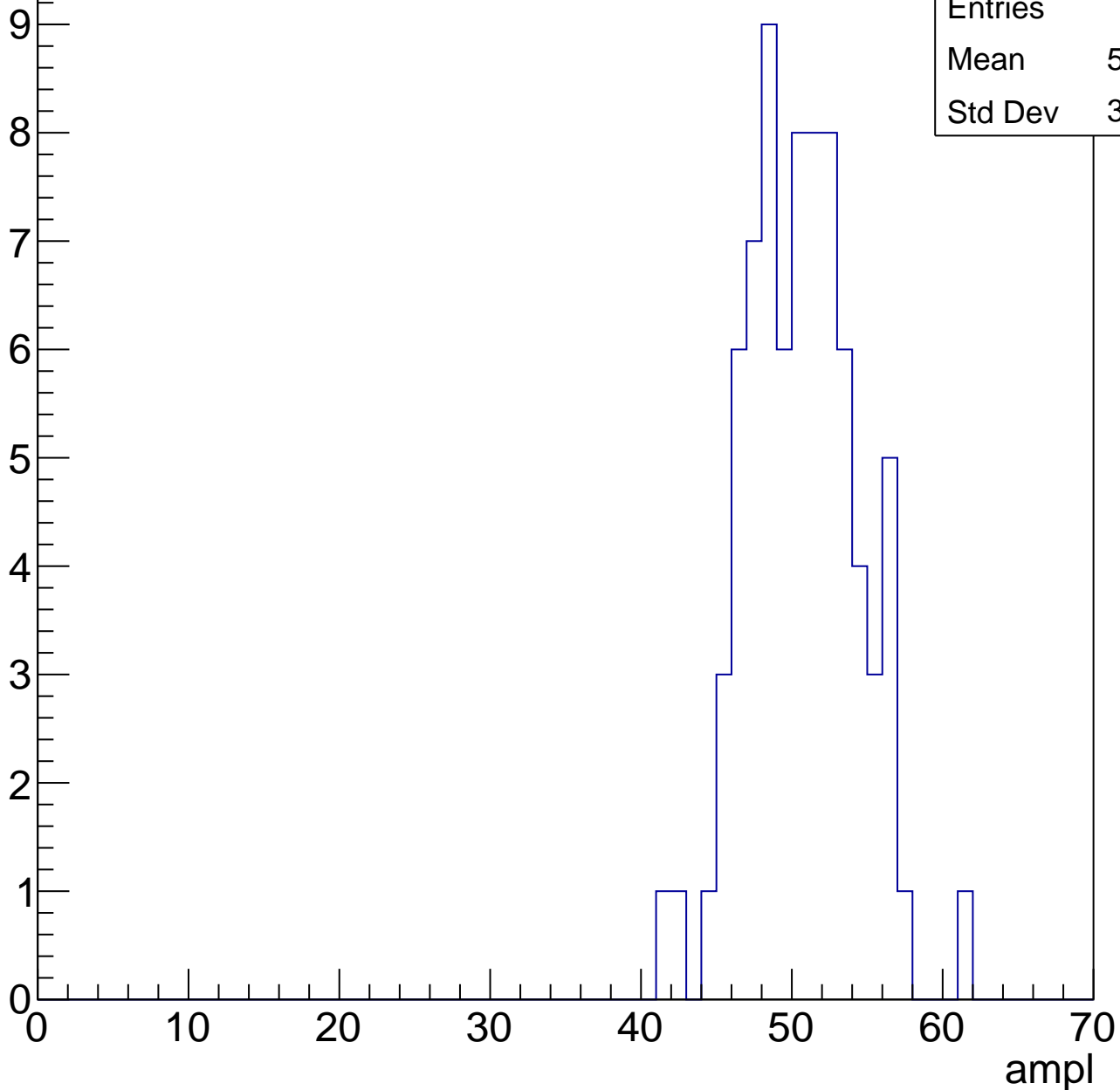


B1L103S, U24-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	50.18
Std Dev	3.647

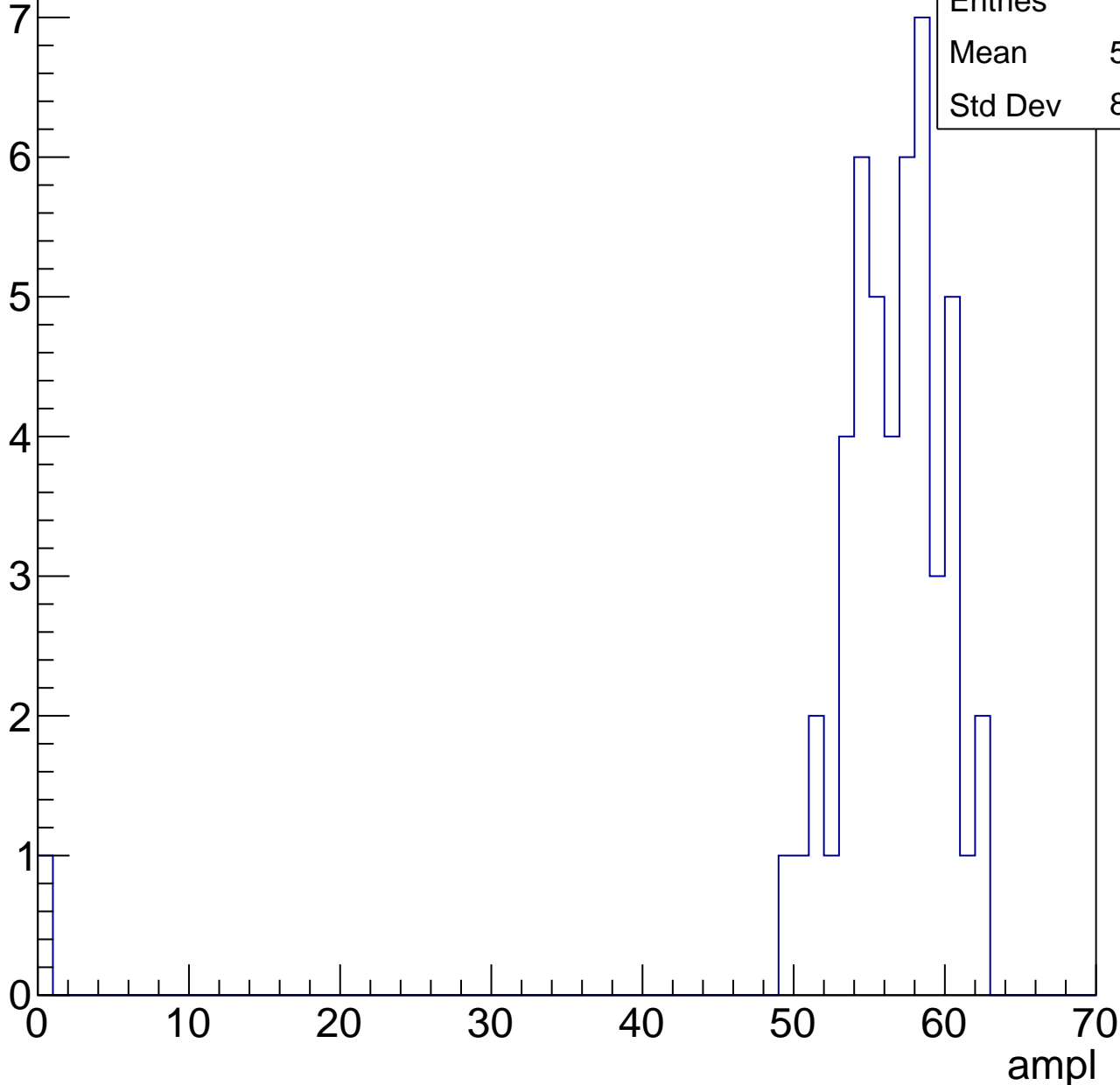


B1L103S, U24-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.06
Std Dev	8.508

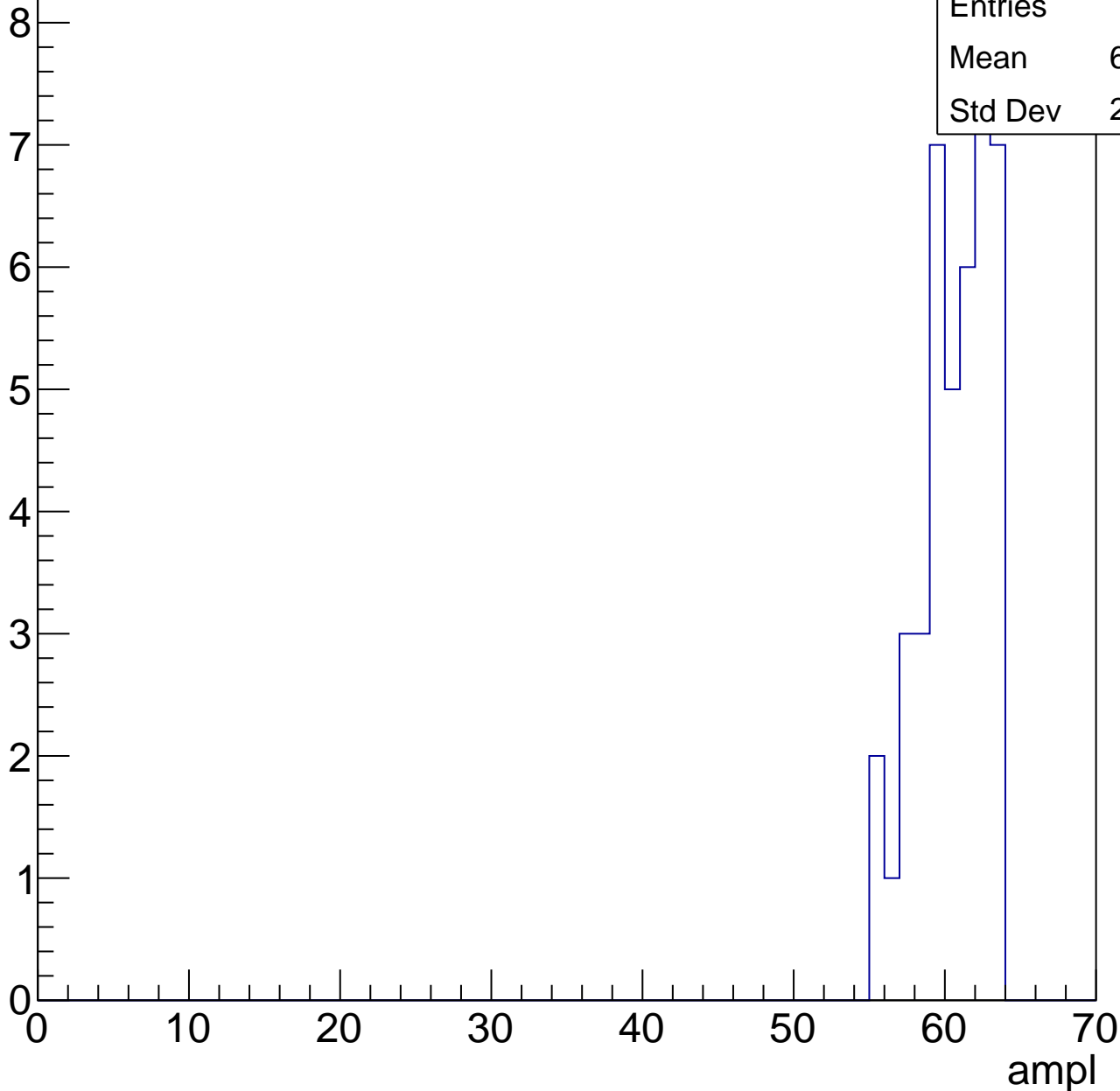


B1L103S, U24-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	60.17
Std Dev	2.246



B1L103S, U24-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch56, adc0

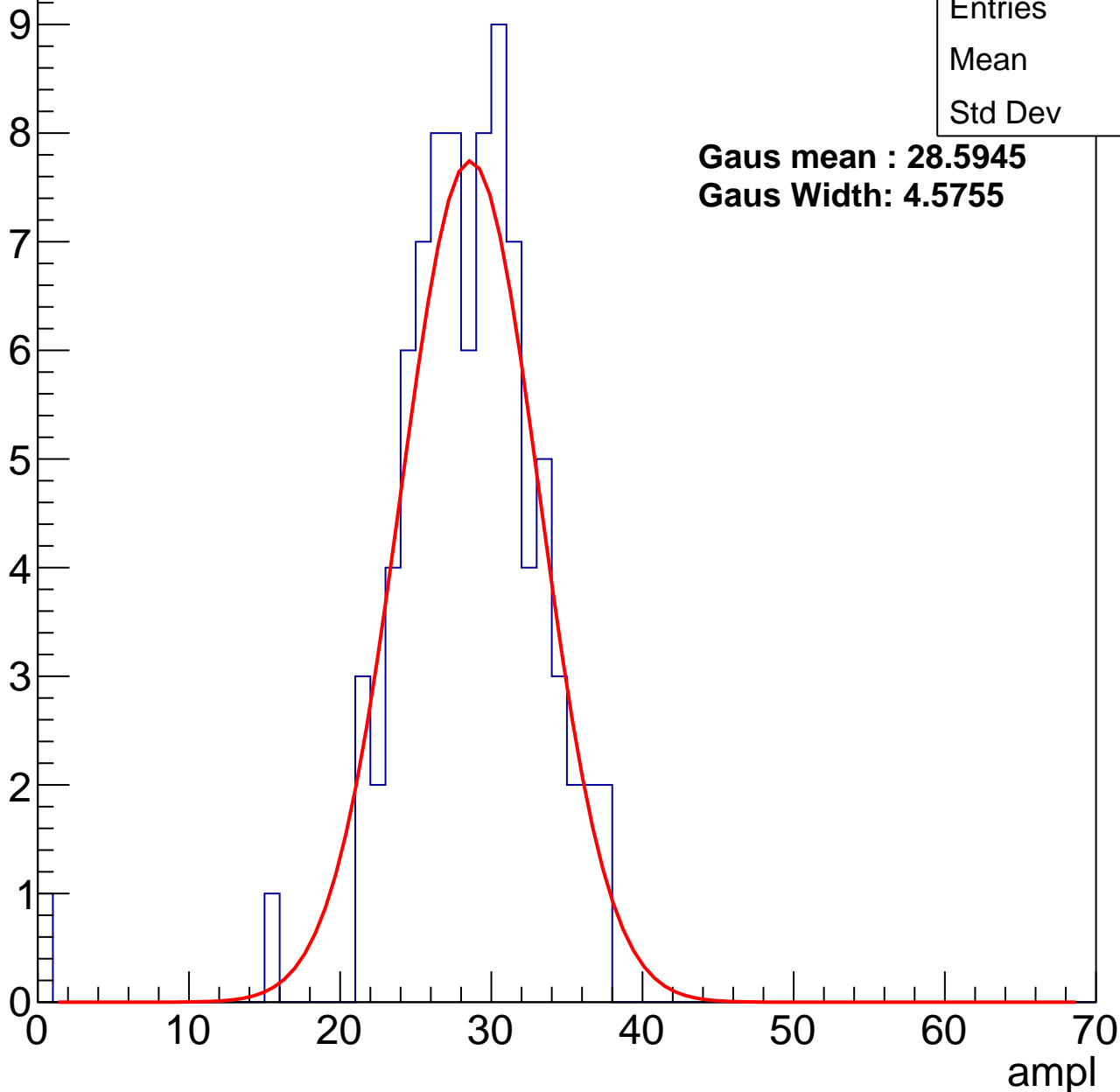
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	88
Mean	27.9
Std Dev	5.07

Gaus mean : 28.5945

Gaus Width: 4.5755



B1L103S, U24-ch56, adc1

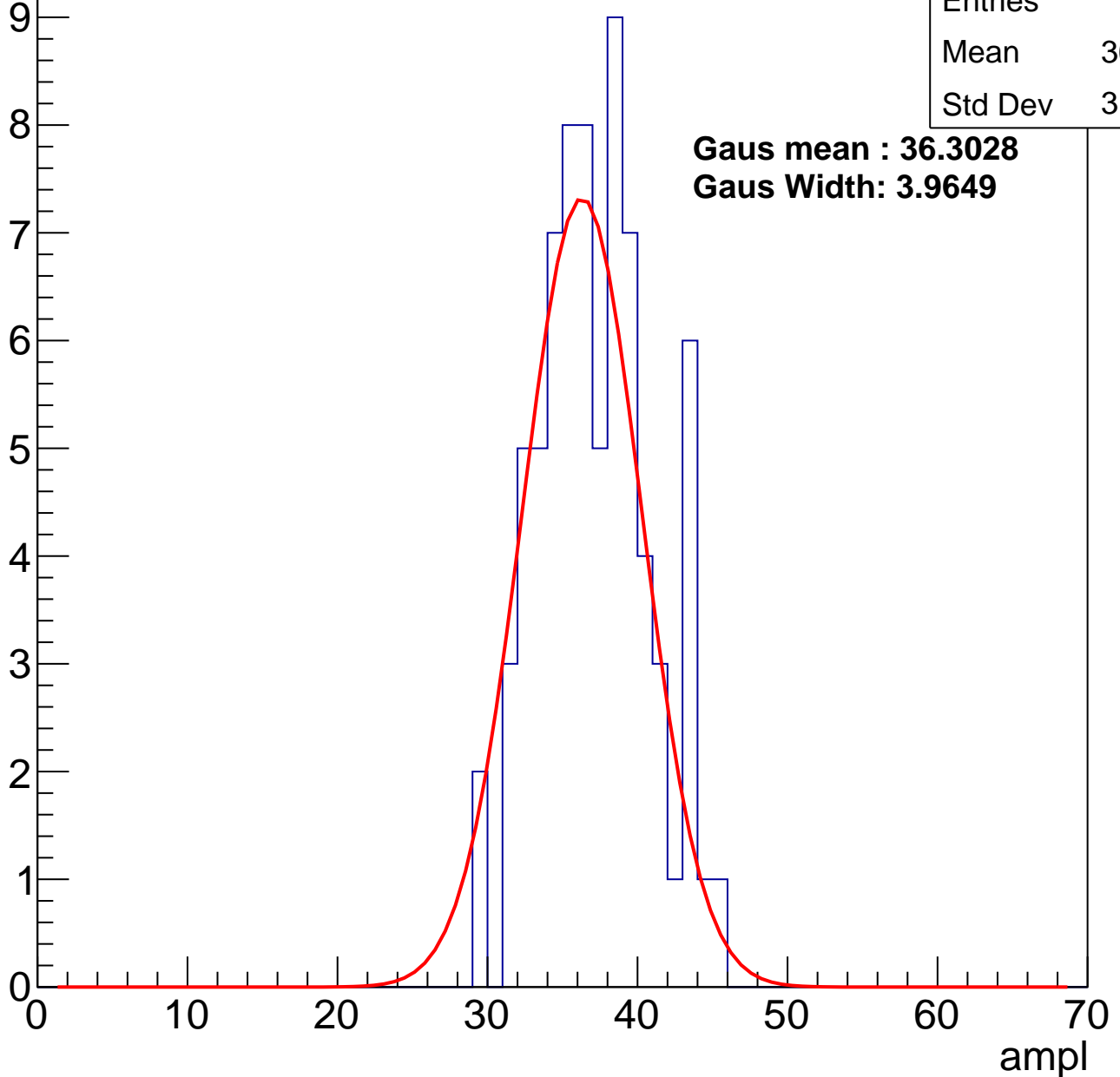
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.72
Std Dev	3.675

Gaus mean : 36.3028

Gaus Width: 3.9649



B1L103S, U24-ch56, adc2

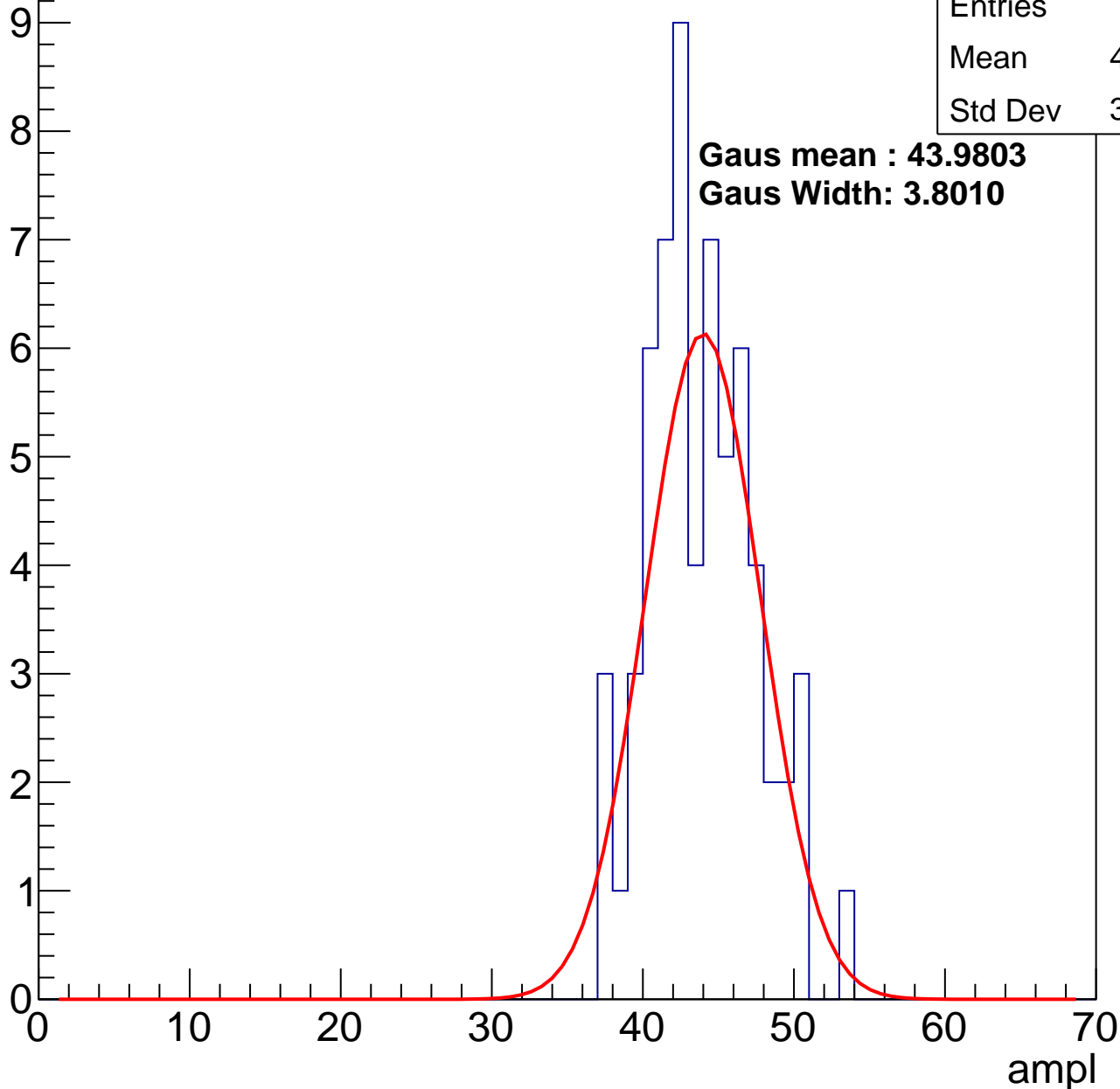
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	43.44
Std Dev	3.504

Gaus mean : 43.9803

Gaus Width: 3.8010

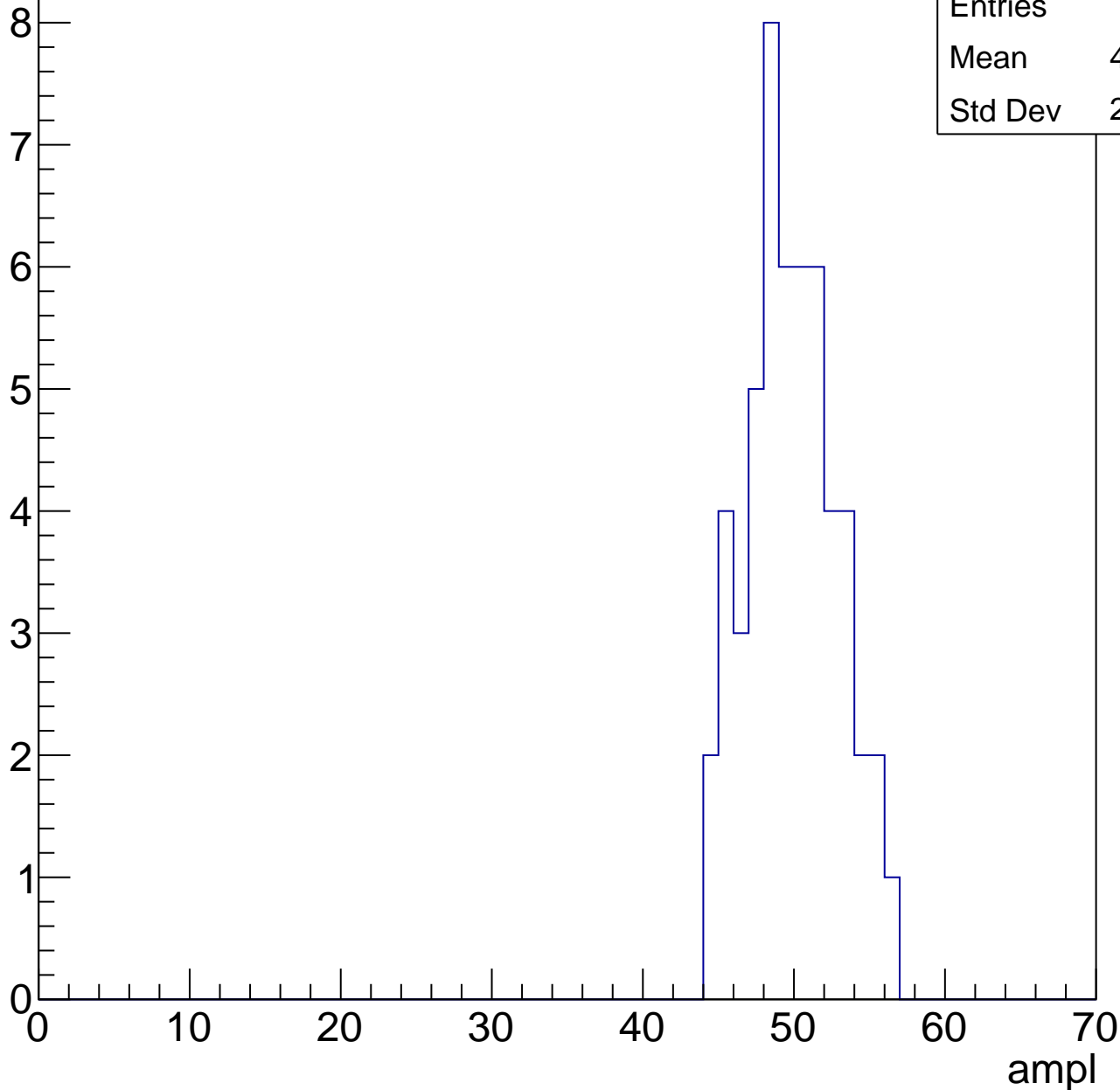


B1L103S, U24-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	49.42
Std Dev	2.949

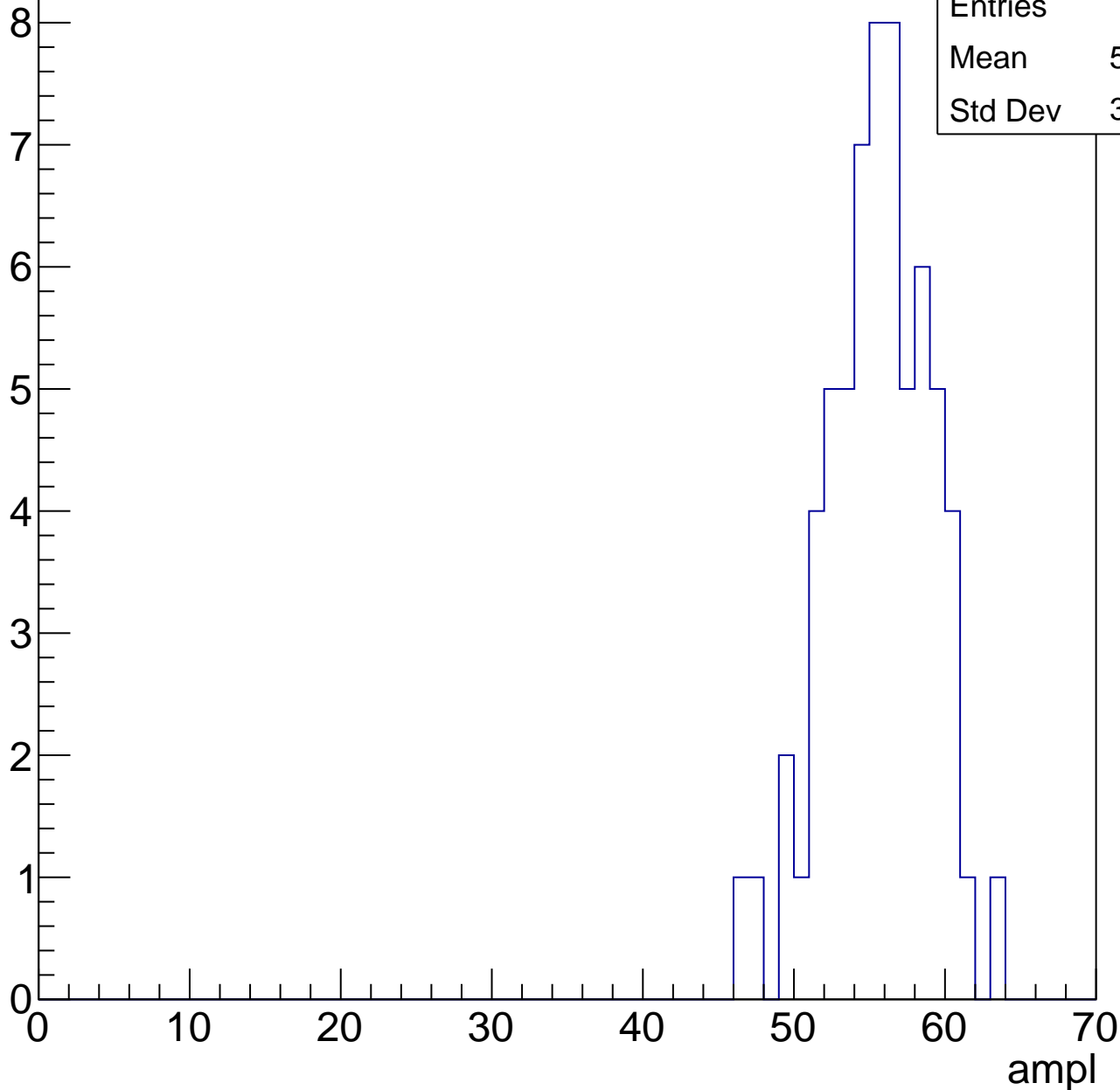


B1L103S, U24-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.12
Std Dev	3.398

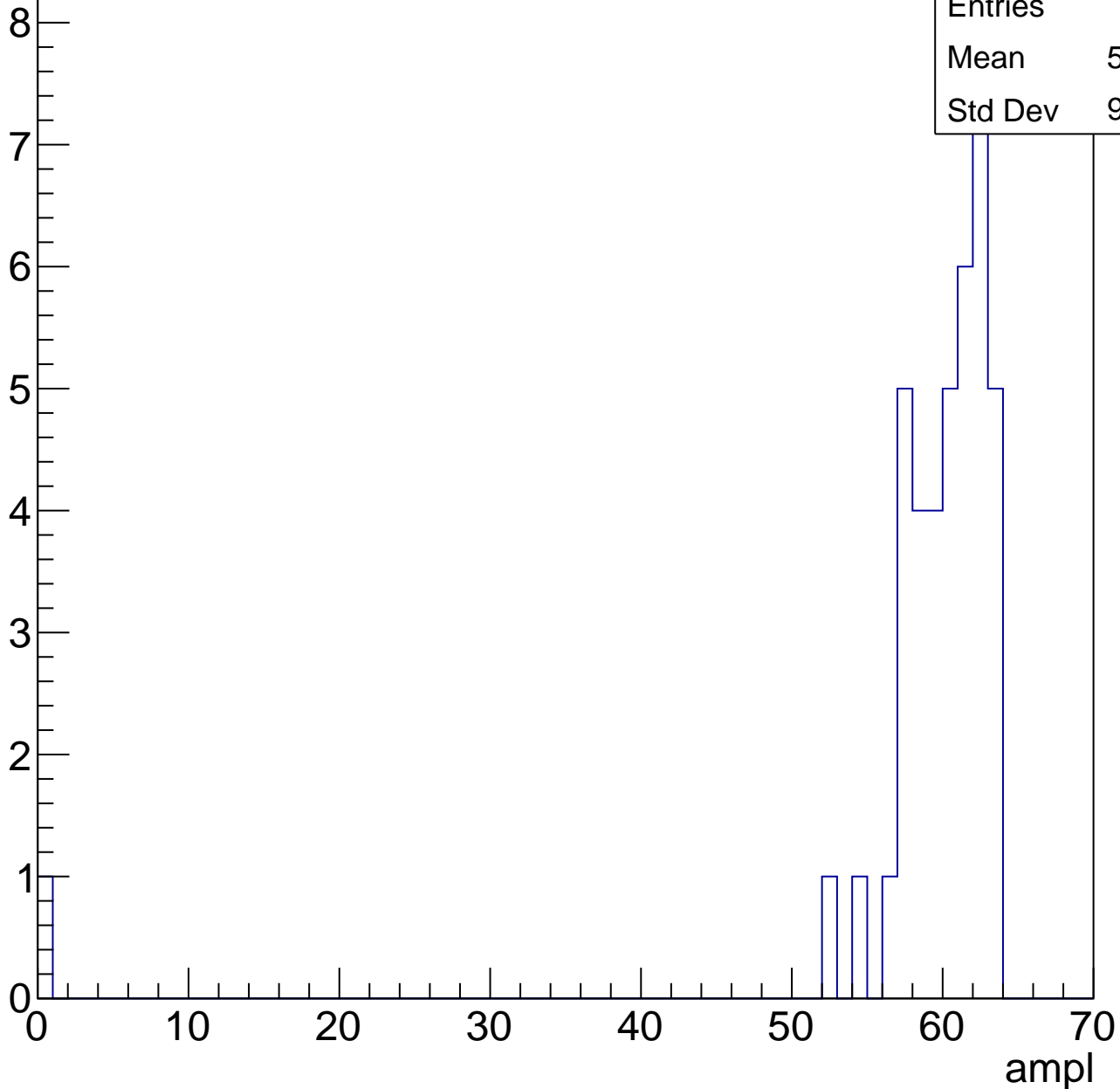


B1L103S, U24-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

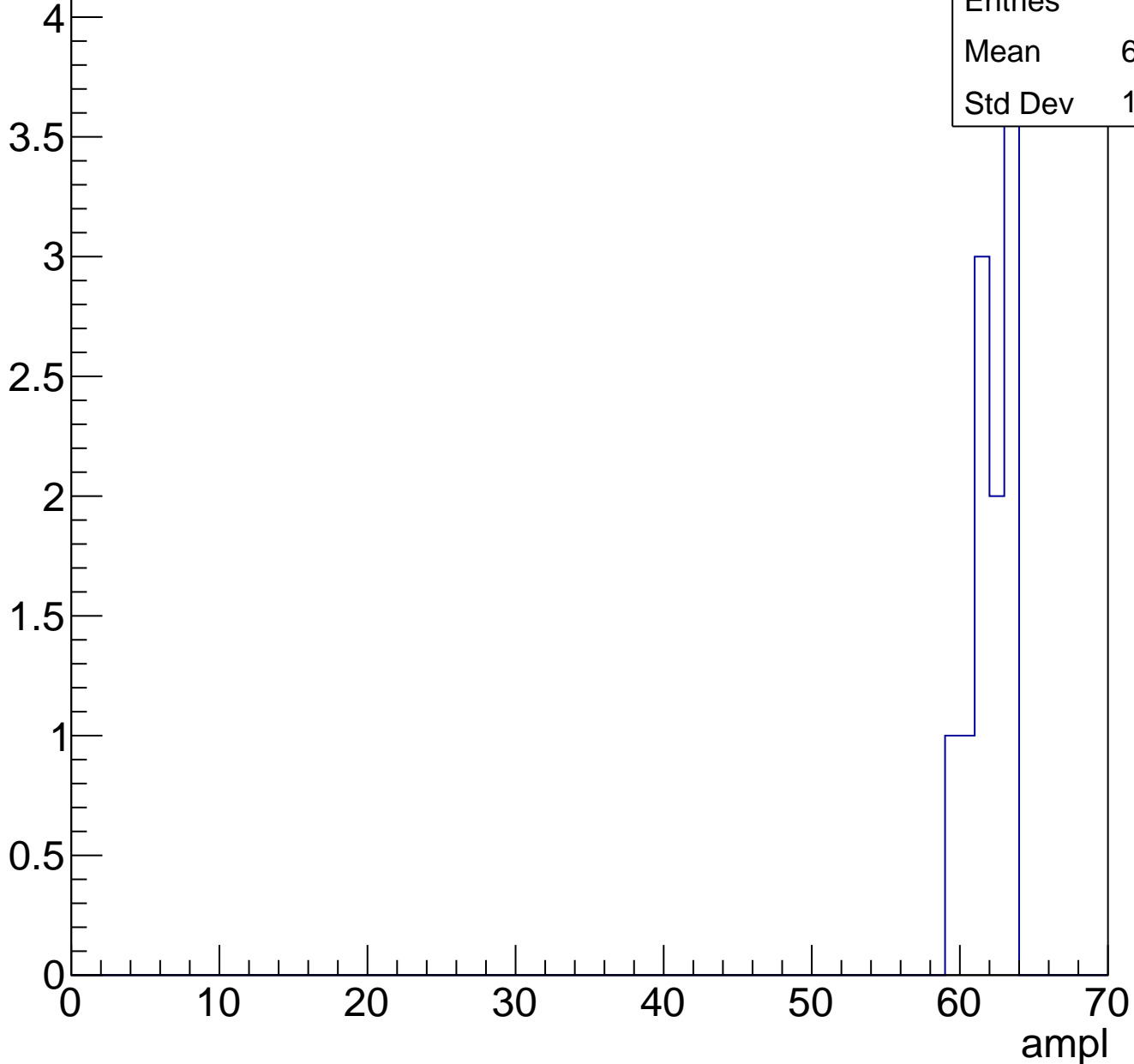
Entries	41
Mean	58.34
Std Dev	9.565



B1L103S, U24-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch57, adc0

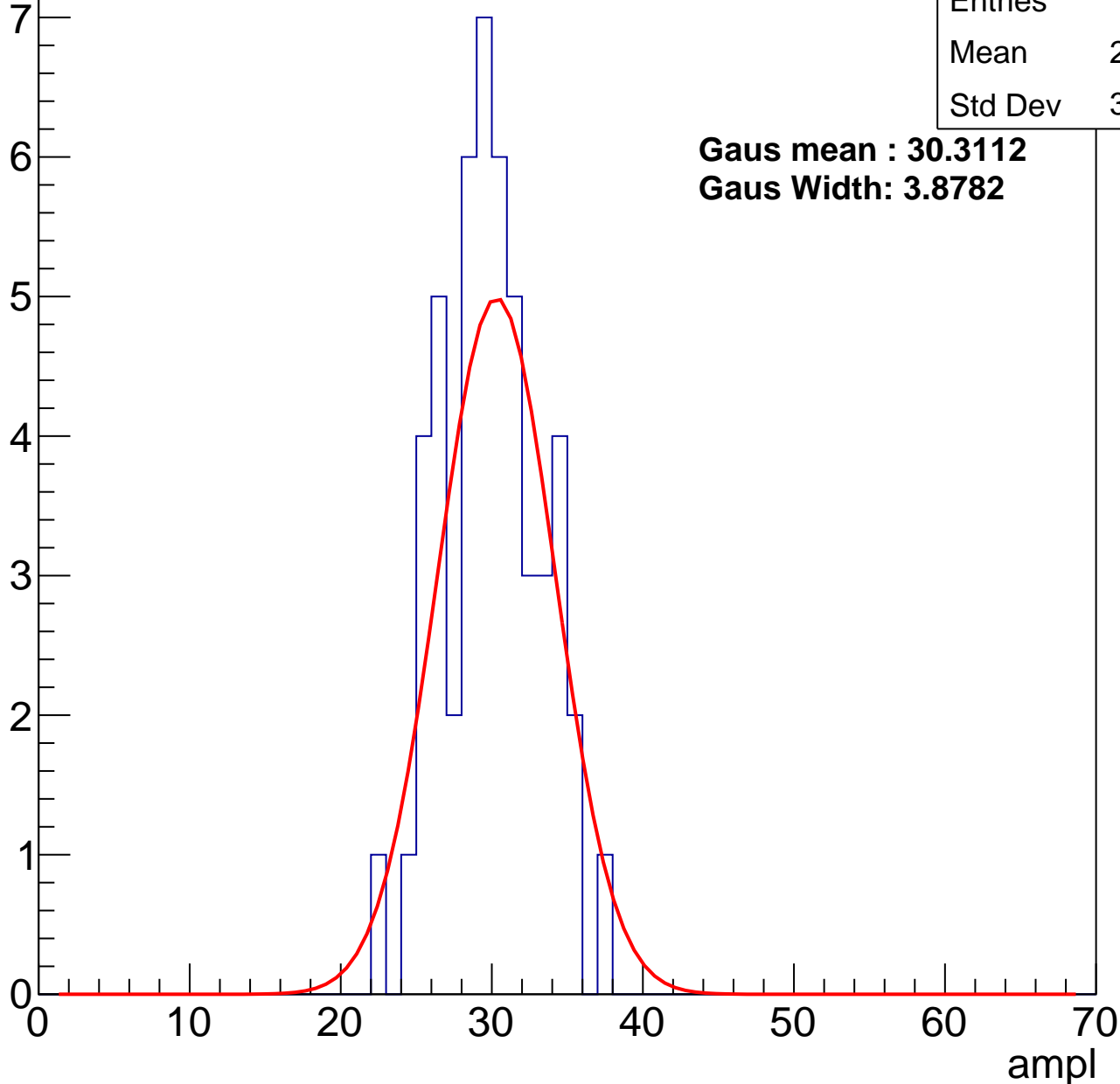
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	29.48
Std Dev	3.233

Gaus mean : 30.3112

Gaus Width: 3.8782



B1L103S, U24-ch57, adc1

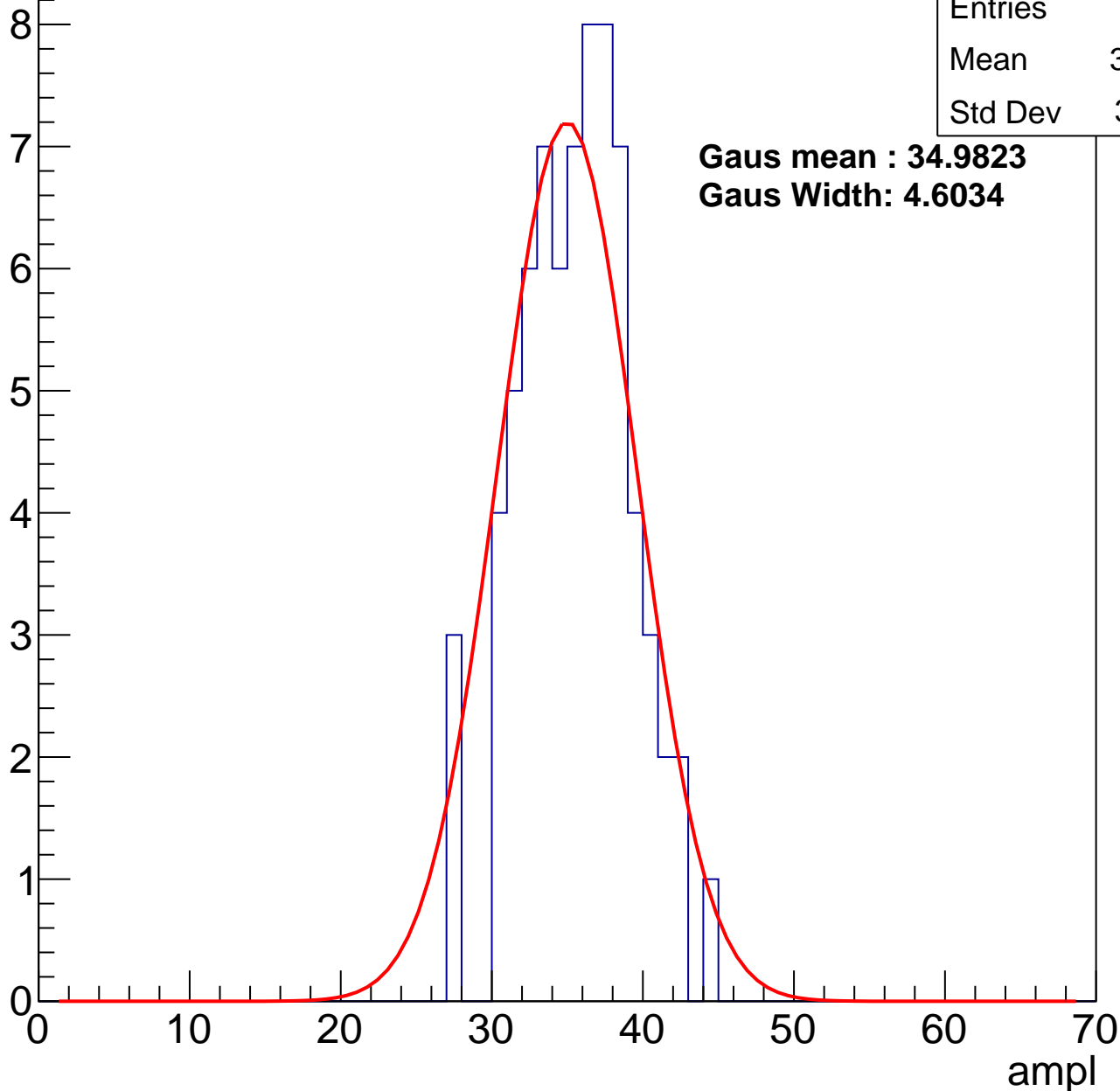
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.12
Std Dev	3.611

Gaus mean : 34.9823

Gaus Width: 4.6034



B1L103S, U24-ch57, adc2

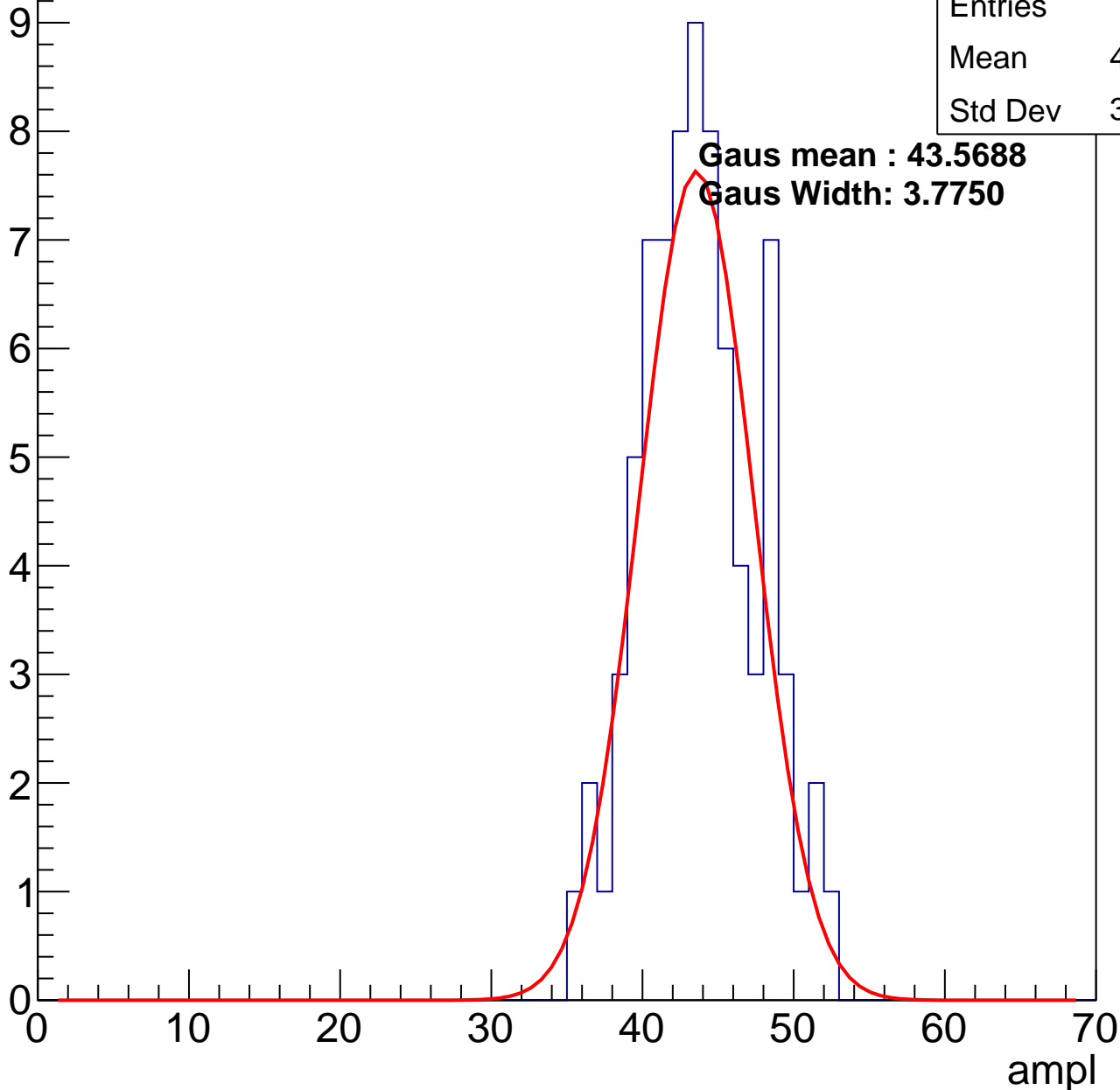
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	43.29
Std Dev	3.759

Gaus mean : 43.5688

Gaus Width: 3.7750

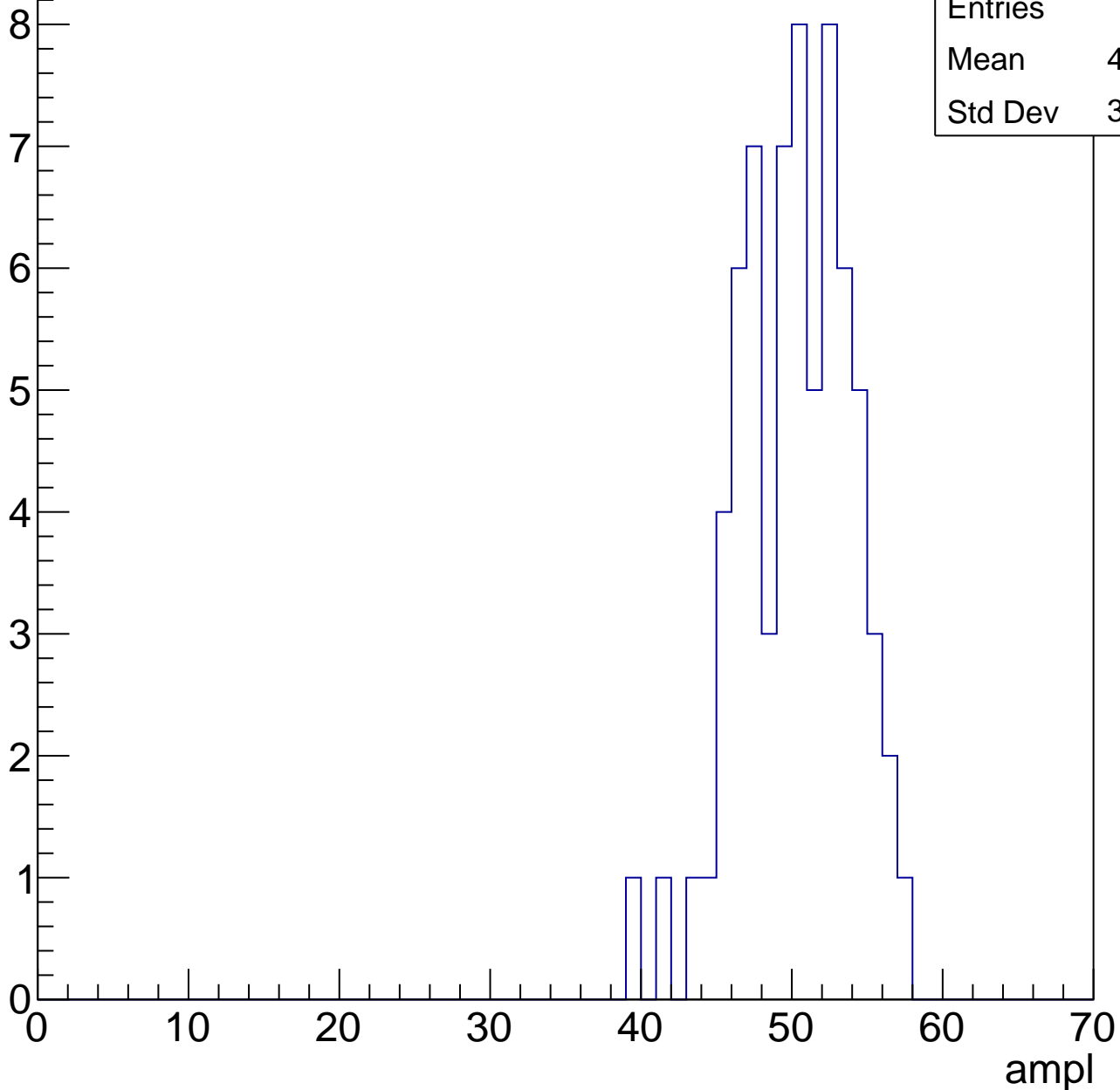


B1L103S, U24-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.74
Std Dev	3.666

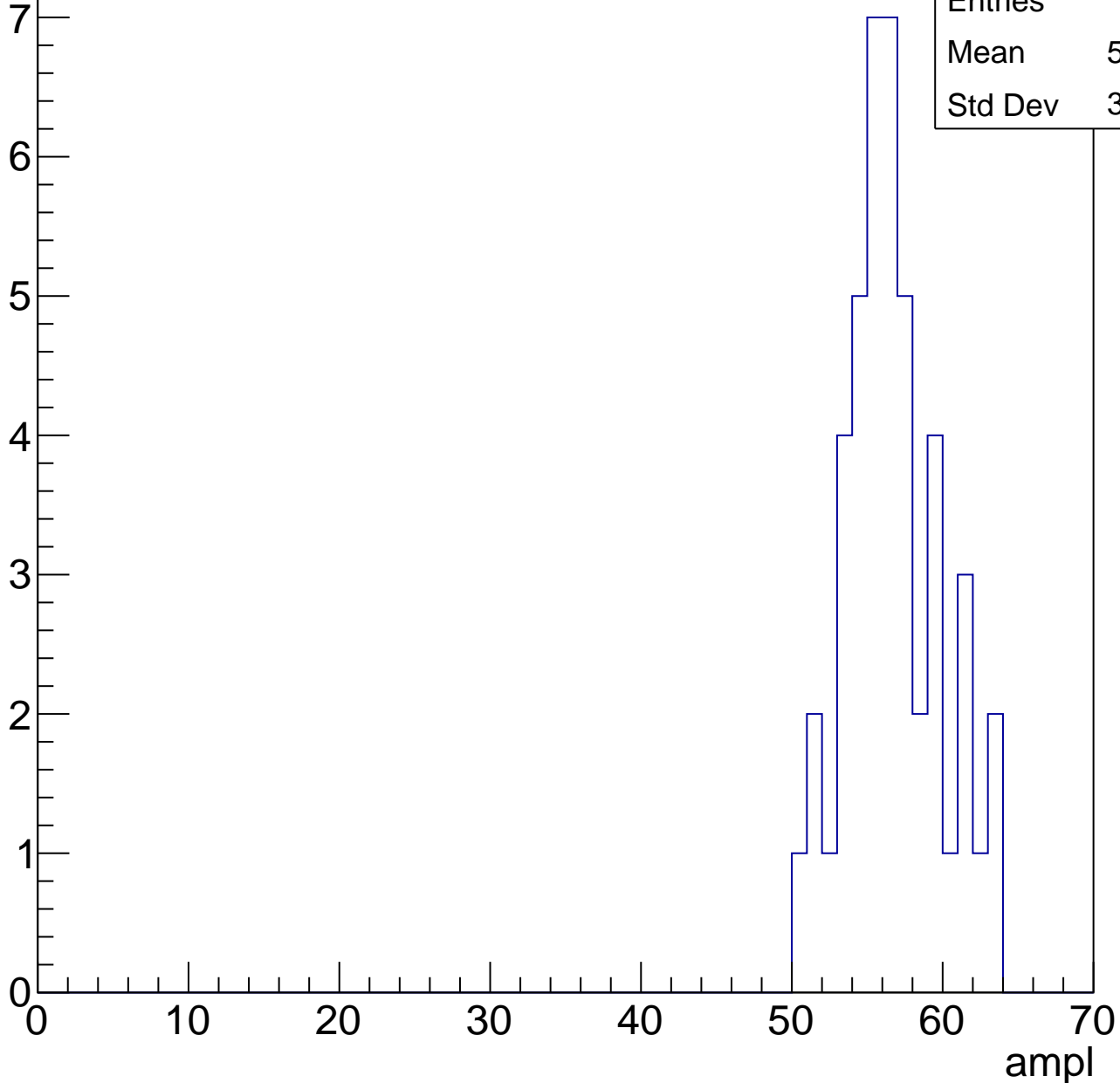


B1L103S, U24-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	56.24
Std Dev	3.114

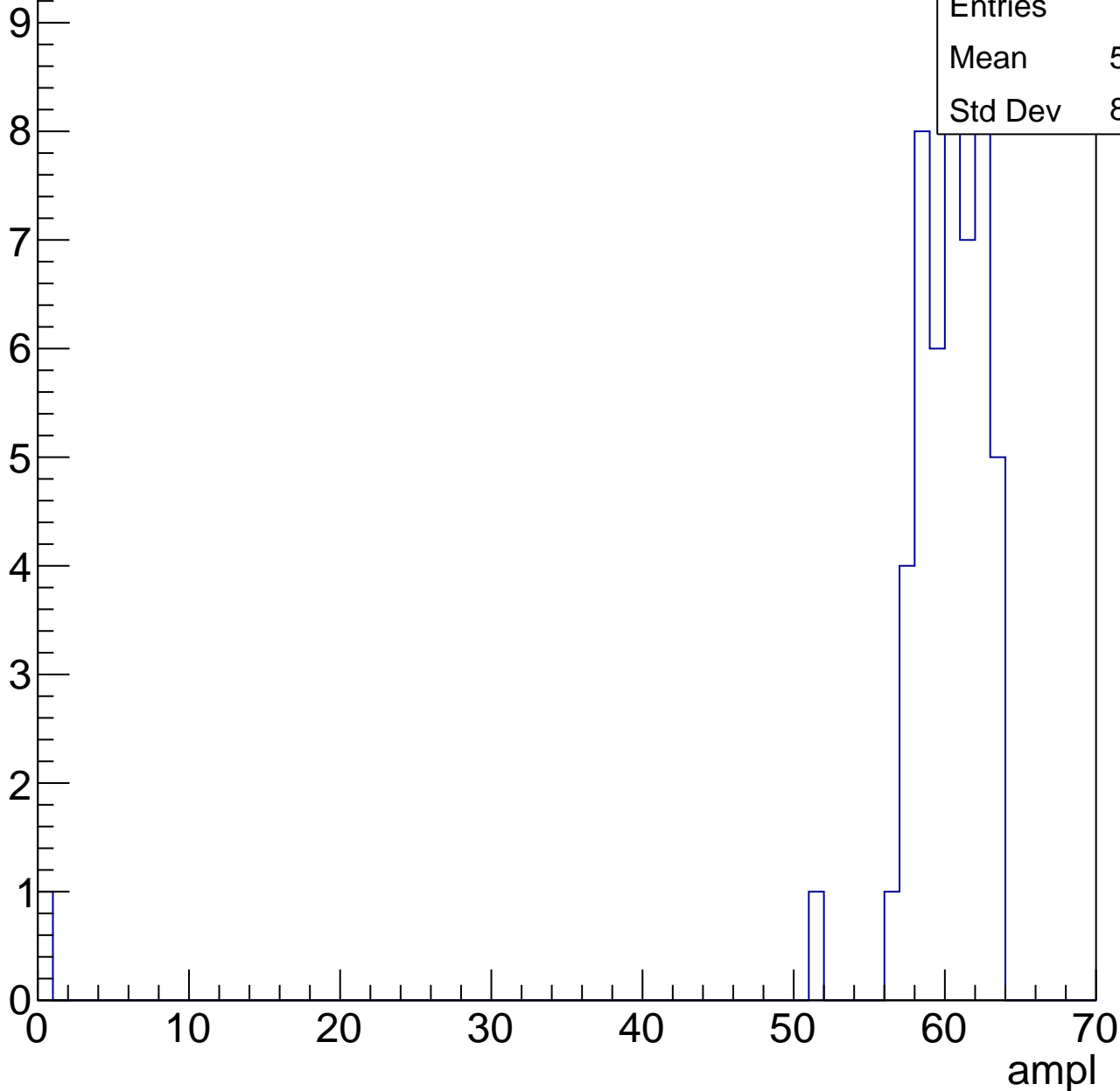


B1L103S, U24-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.69
Std Dev	8.599



B1L103S, U24-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch58, adc0

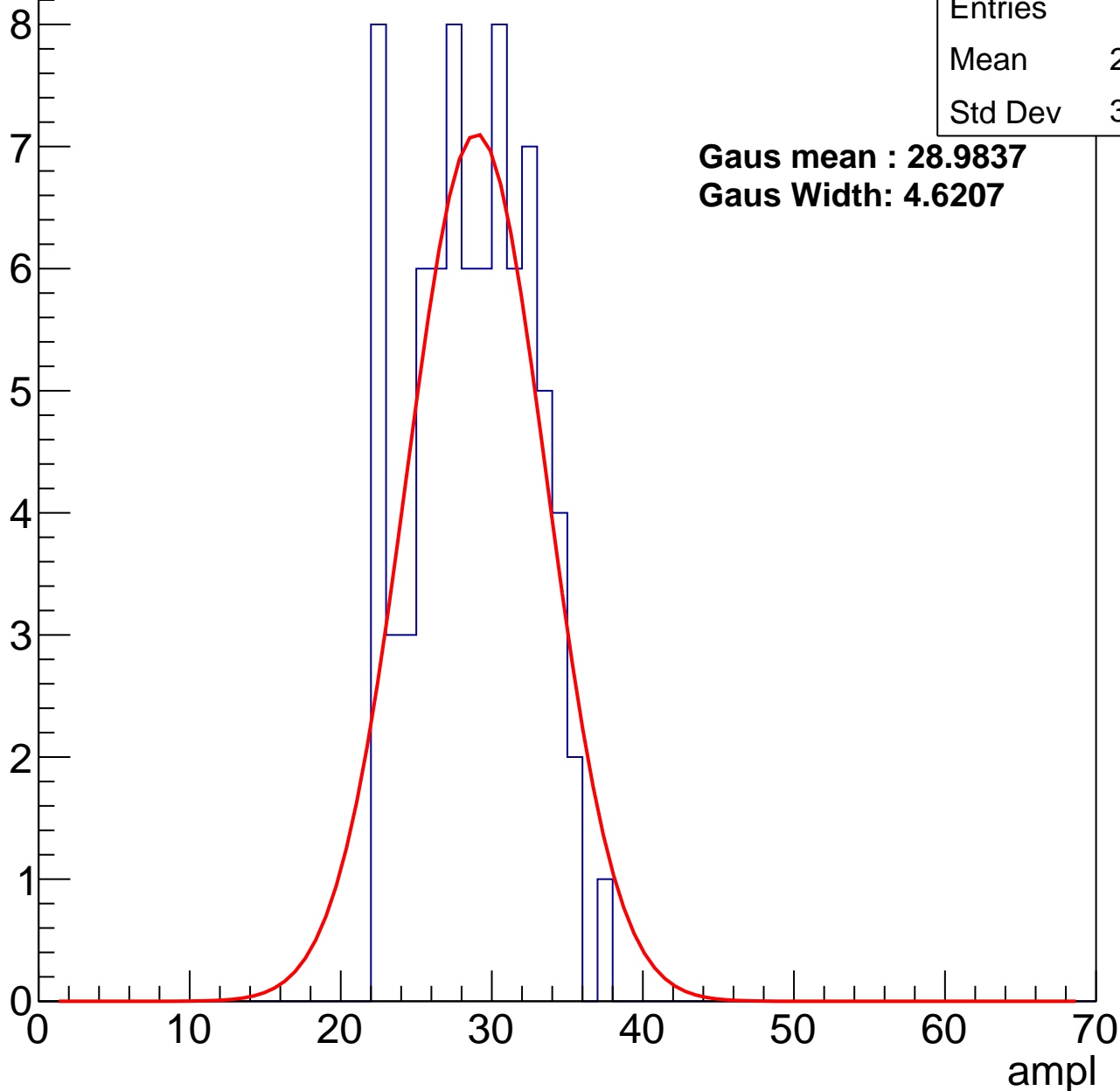
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.34
Std Dev	3.798

Gaus mean : 28.9837

Gaus Width: 4.6207



B1L103S, U24-ch58, adc1

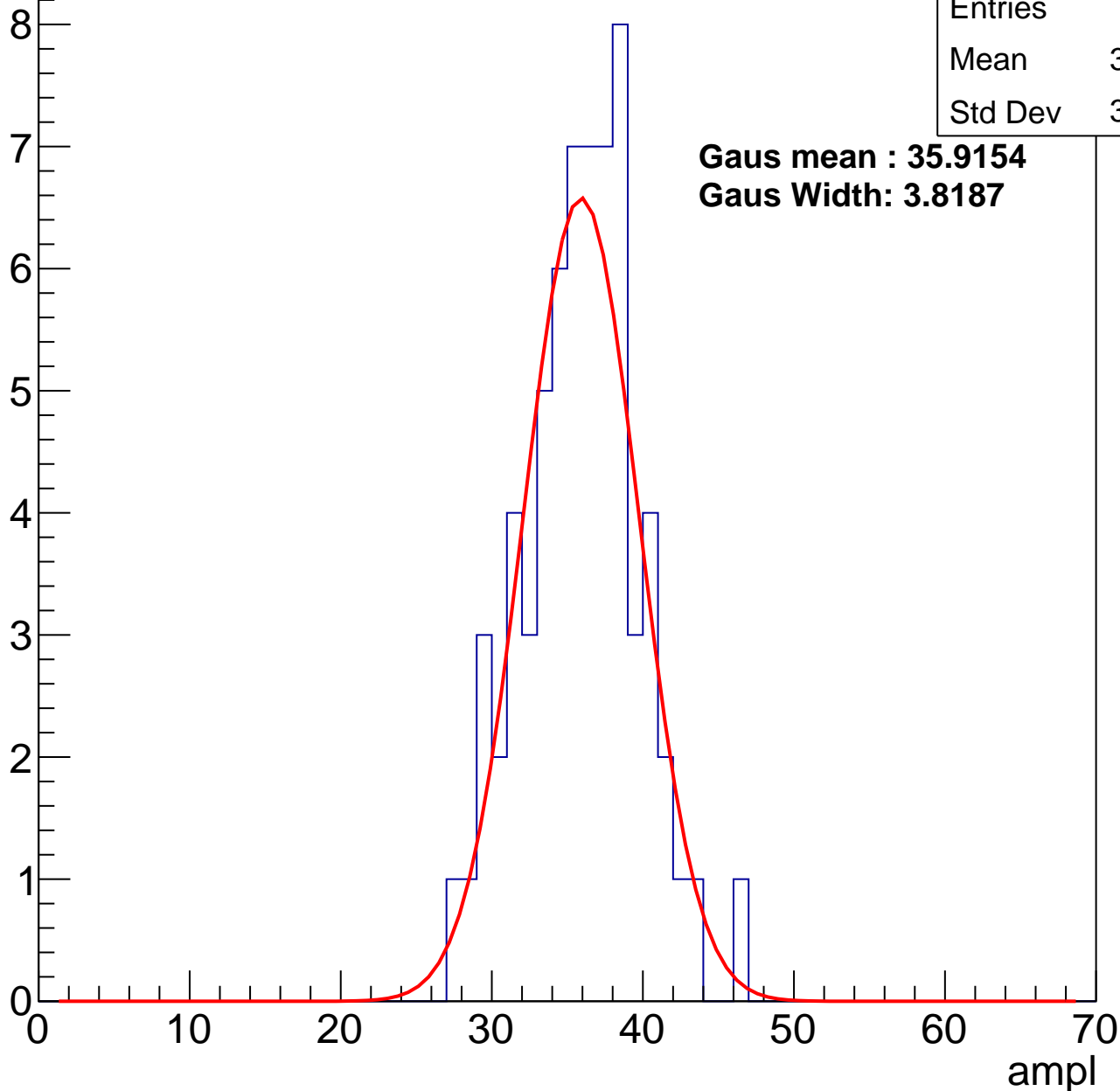
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.47
Std Dev	3.763

Gaus mean : 35.9154

Gaus Width: 3.8187



B1L103S, U24-ch58, adc2

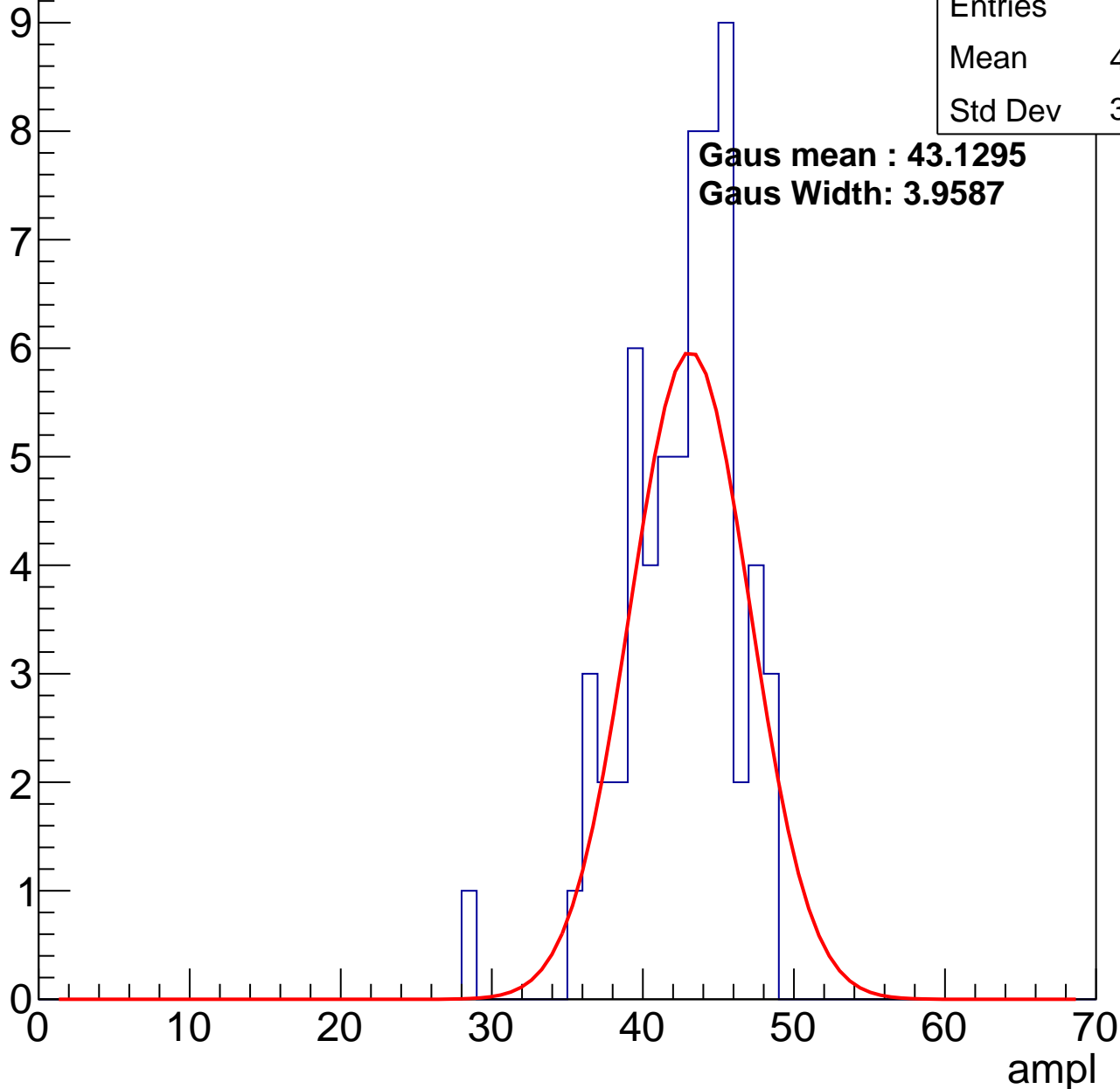
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.14
Std Dev	3.716

Gaus mean : 43.1295

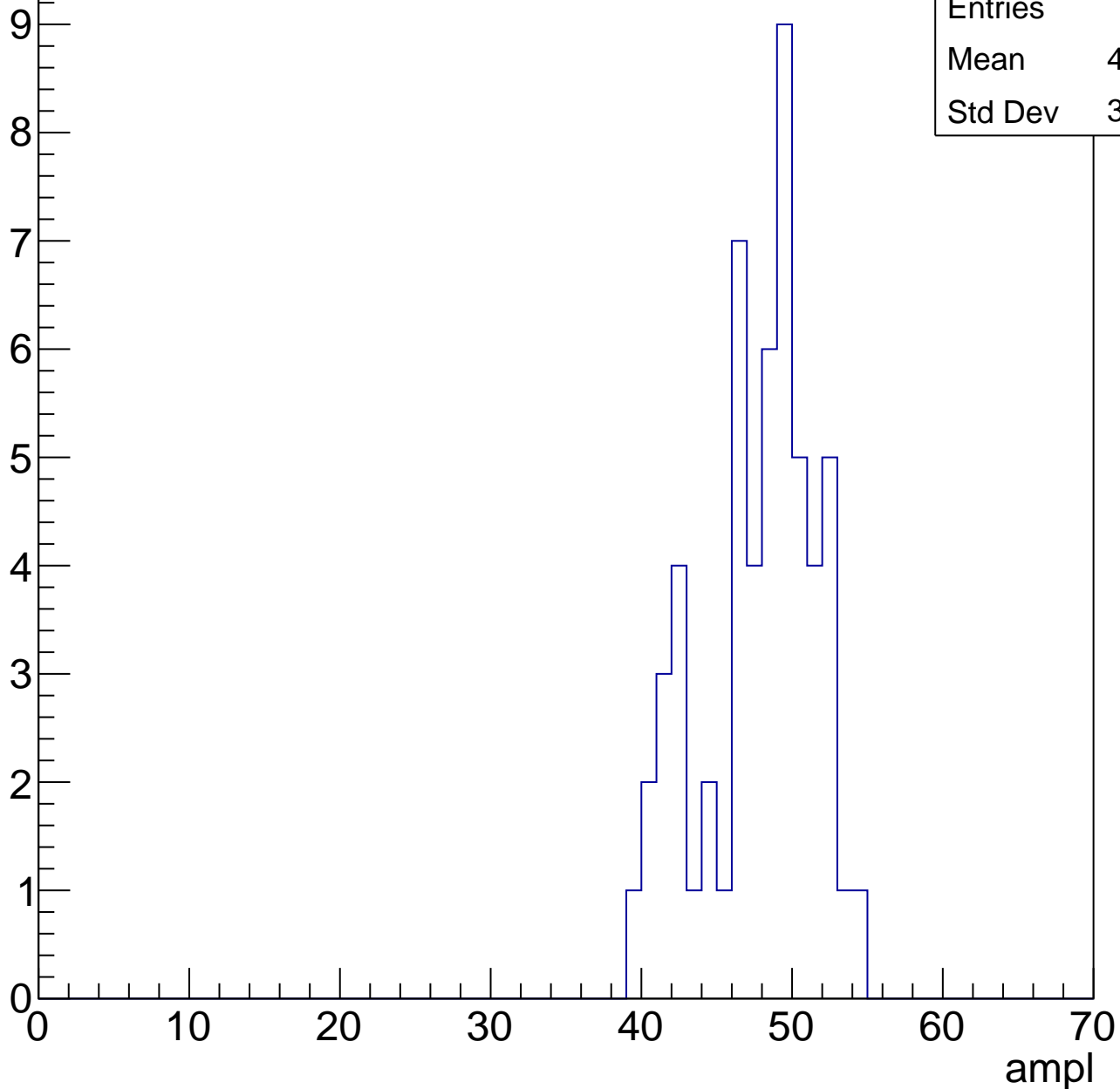
Gaus Width: 3.9587



B1L103S, U24-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

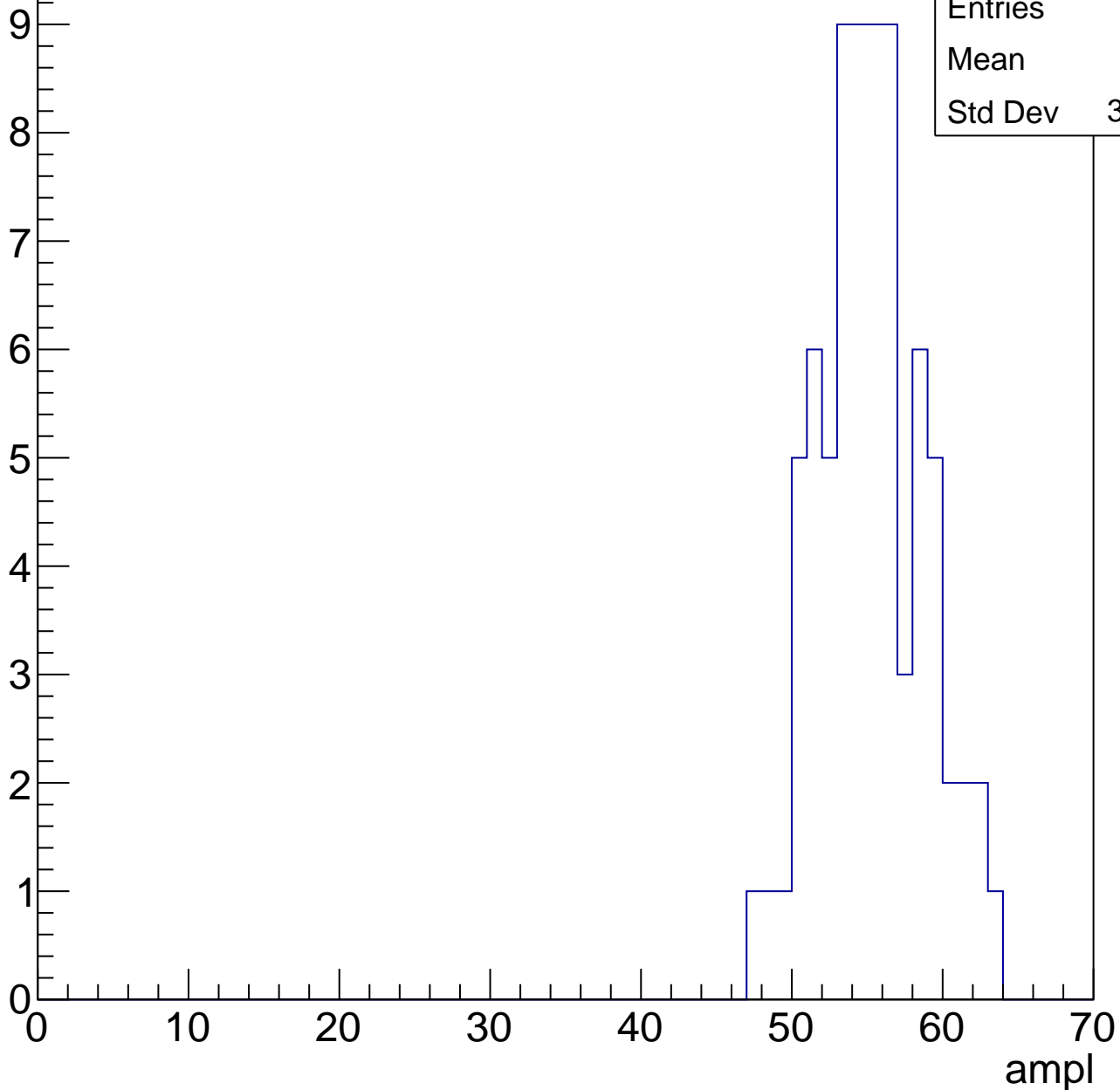


B1L103S, U24-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	54.8
Std Dev	3.434

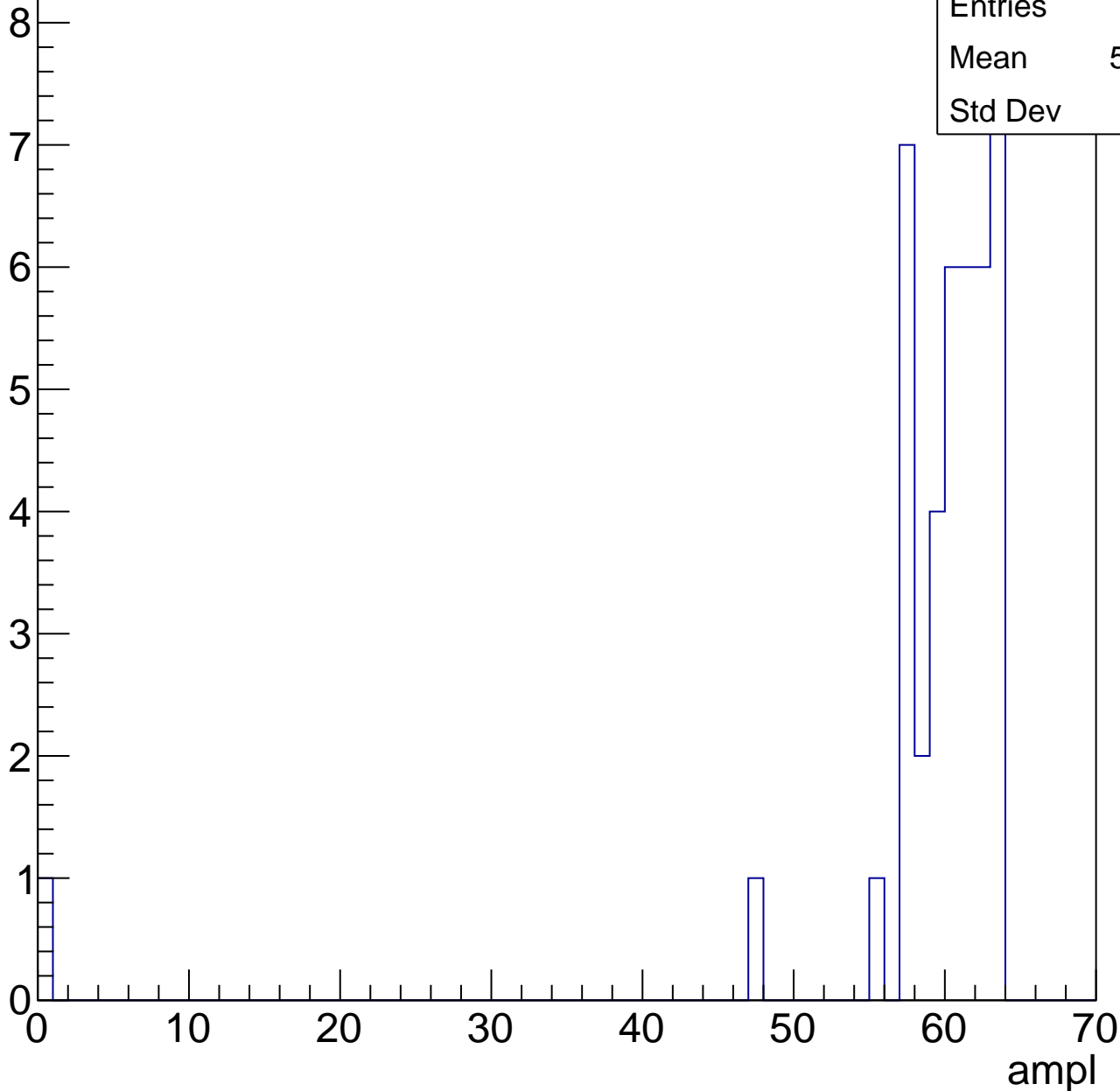


B1L103S, U24-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.45
Std Dev	9.6



B1L103S, U24-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

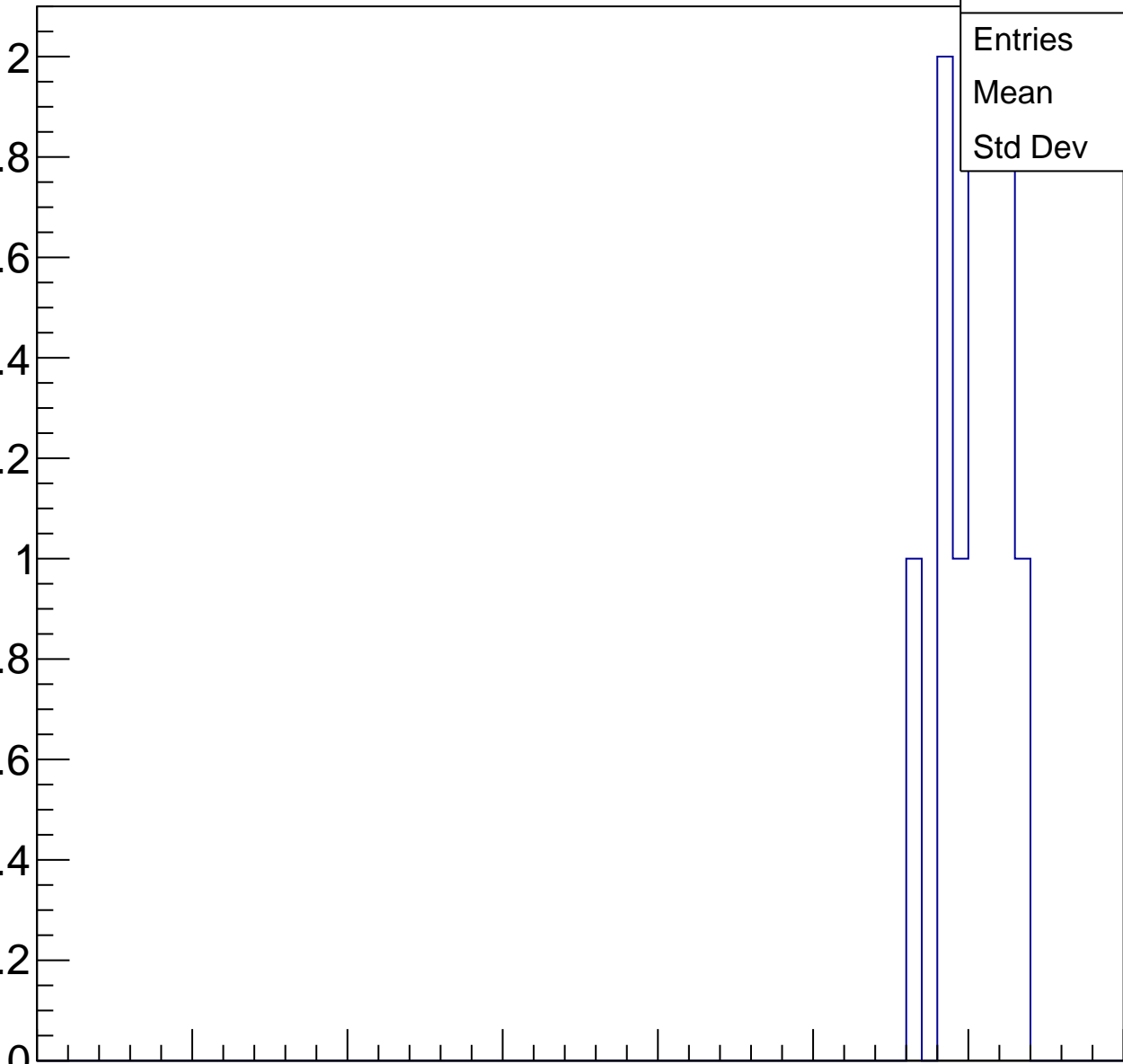
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	11
Mean	60
Std Dev	2

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch59, adc0

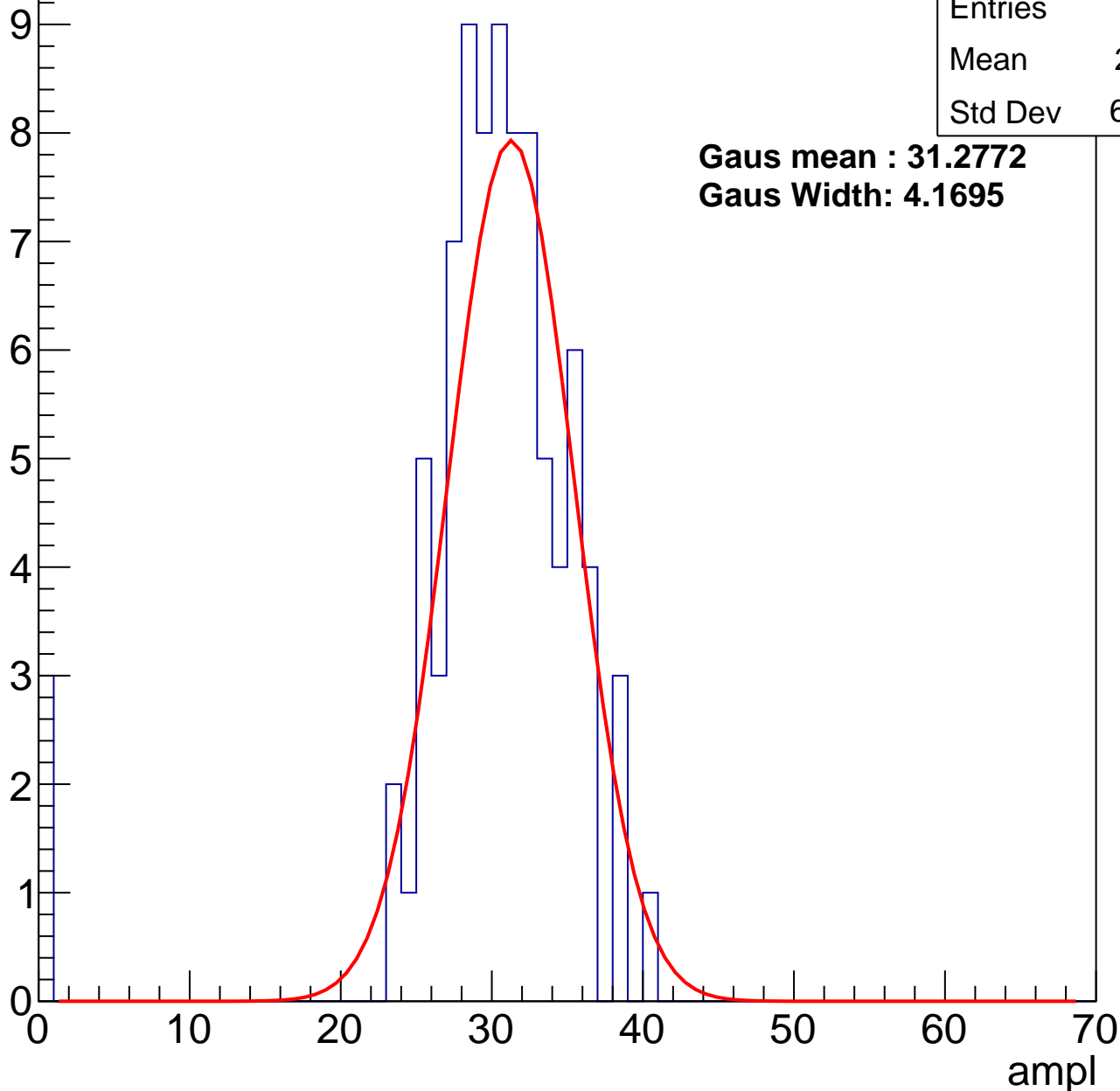
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	29.41
Std Dev	6.664

Gaus mean : 31.2772

Gaus Width: 4.1695



B1L103S, U24-ch59, adc1

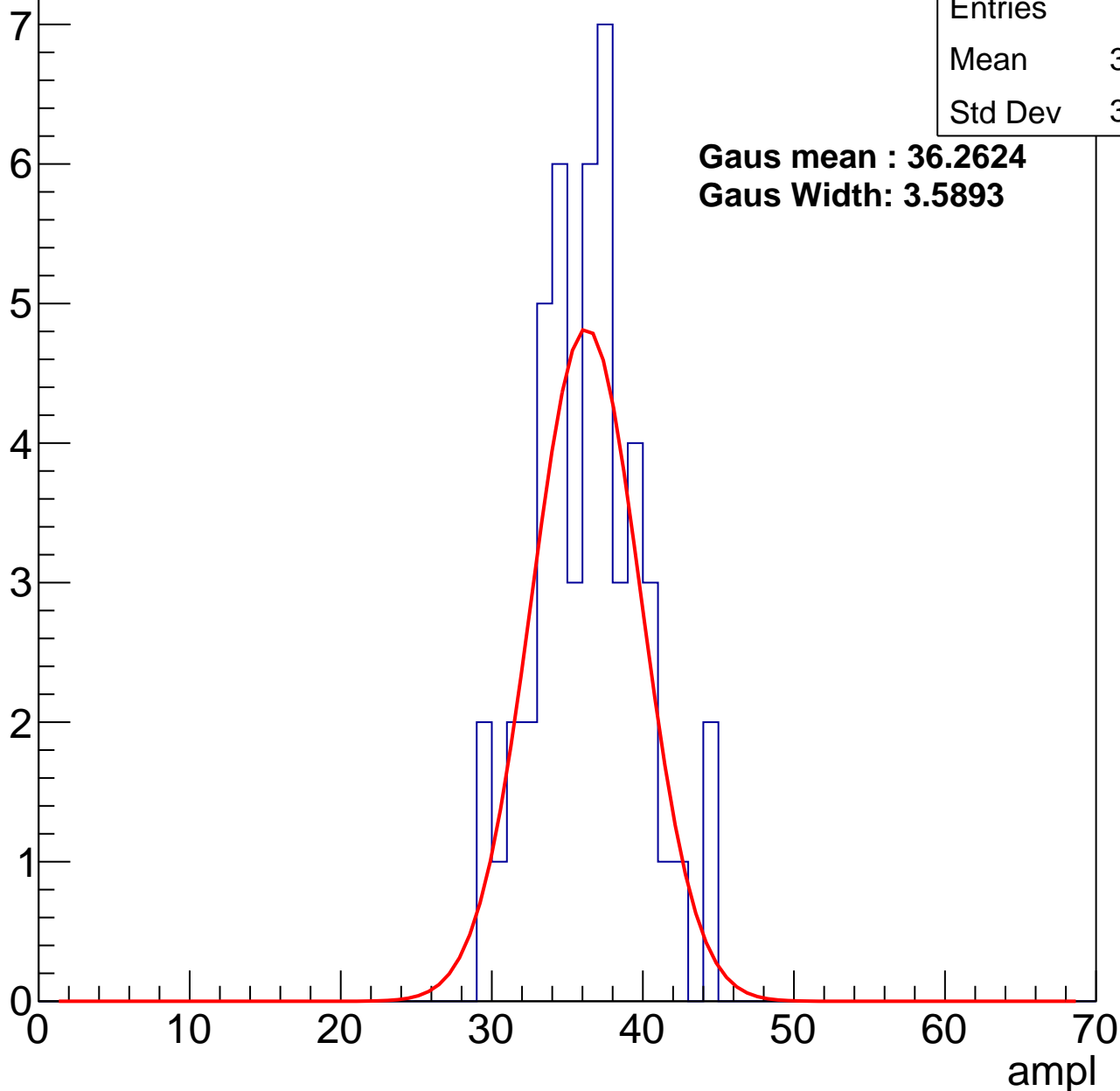
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	35.92
Std Dev	3.475

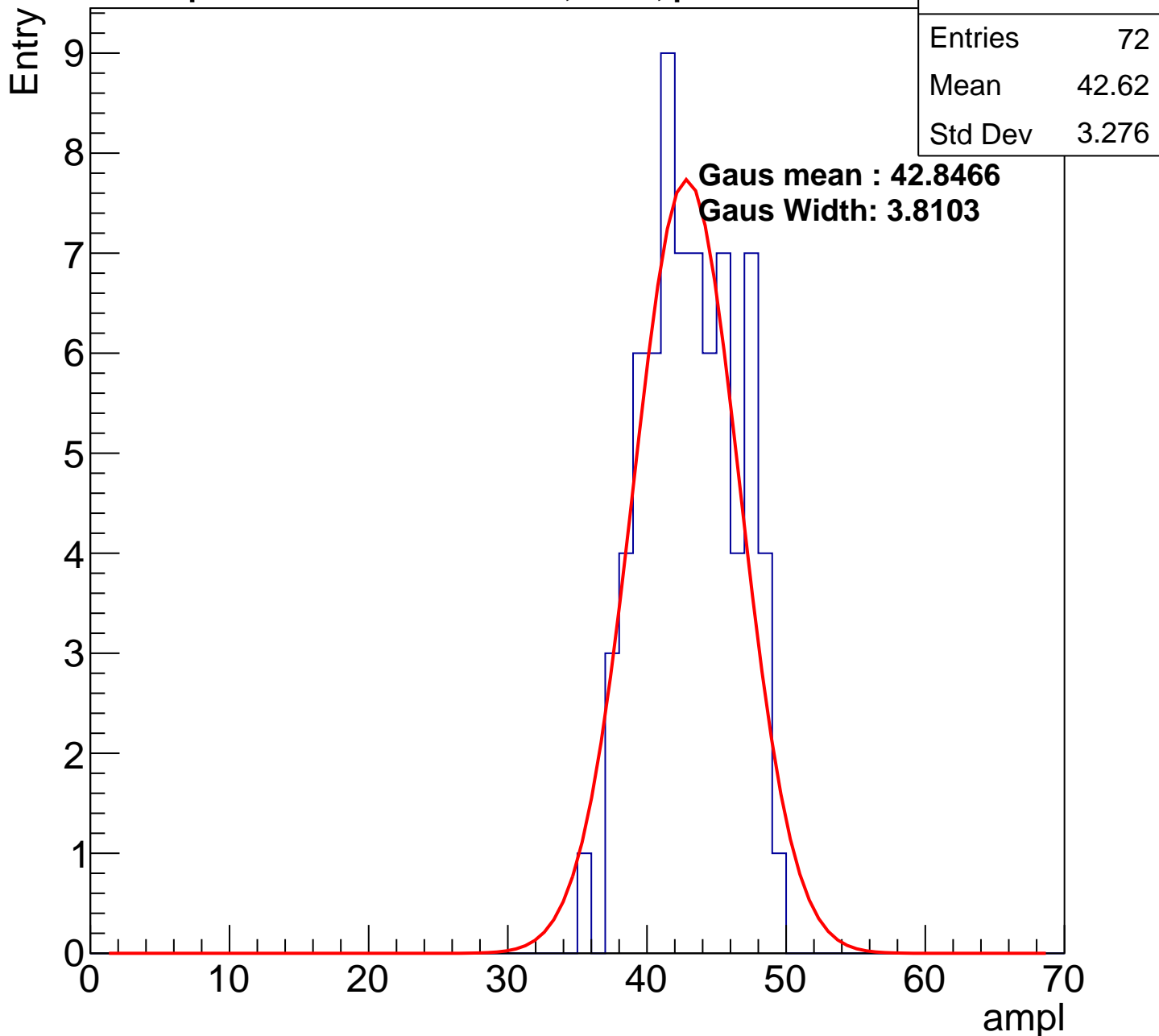
Gaus mean : 36.2624

Gaus Width: 3.5893



B1L103S, U24-ch59, adc2

calib_packv5_042523_0143.root, FC#7, port C2

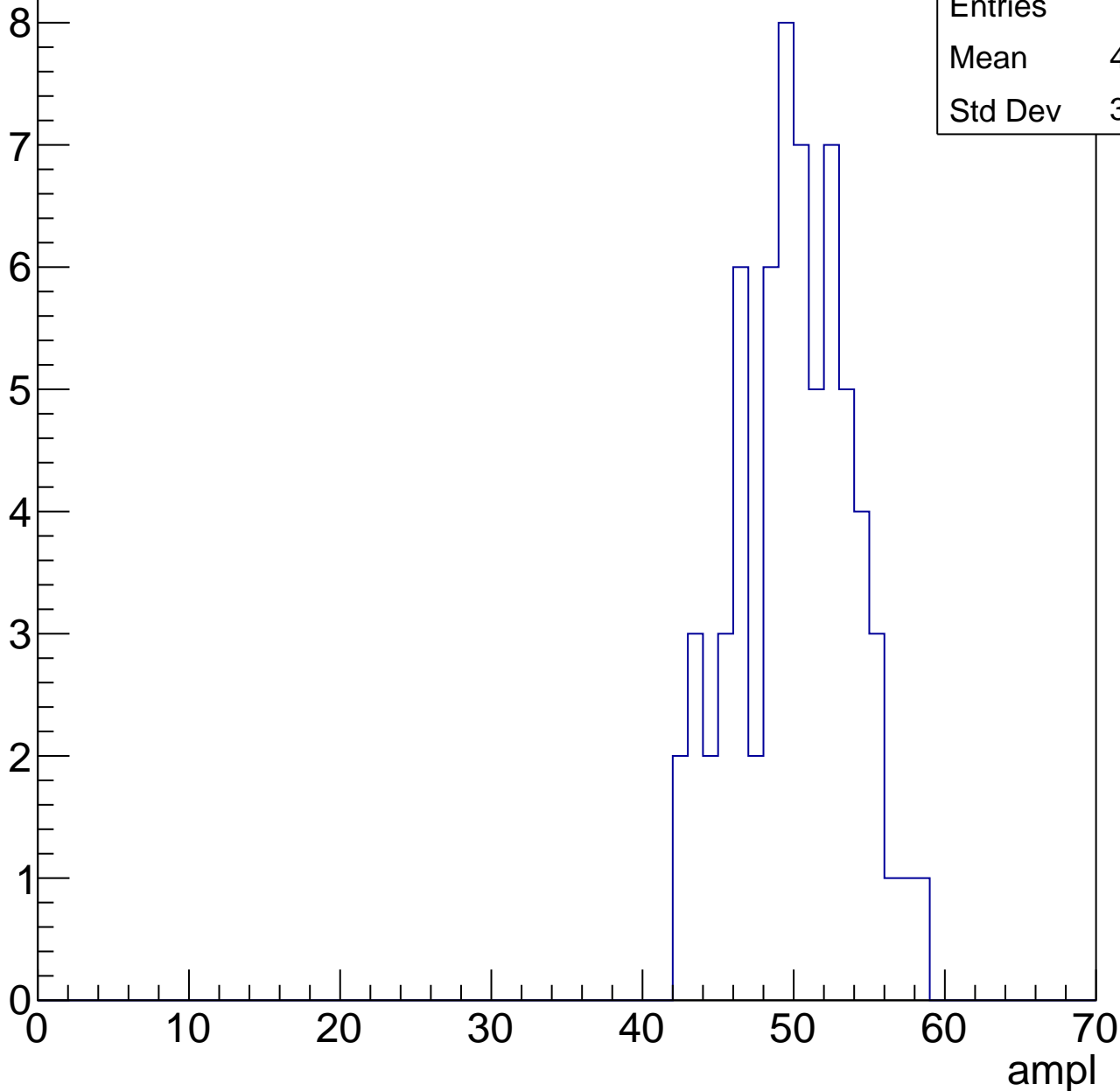


B1L103S, U24-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

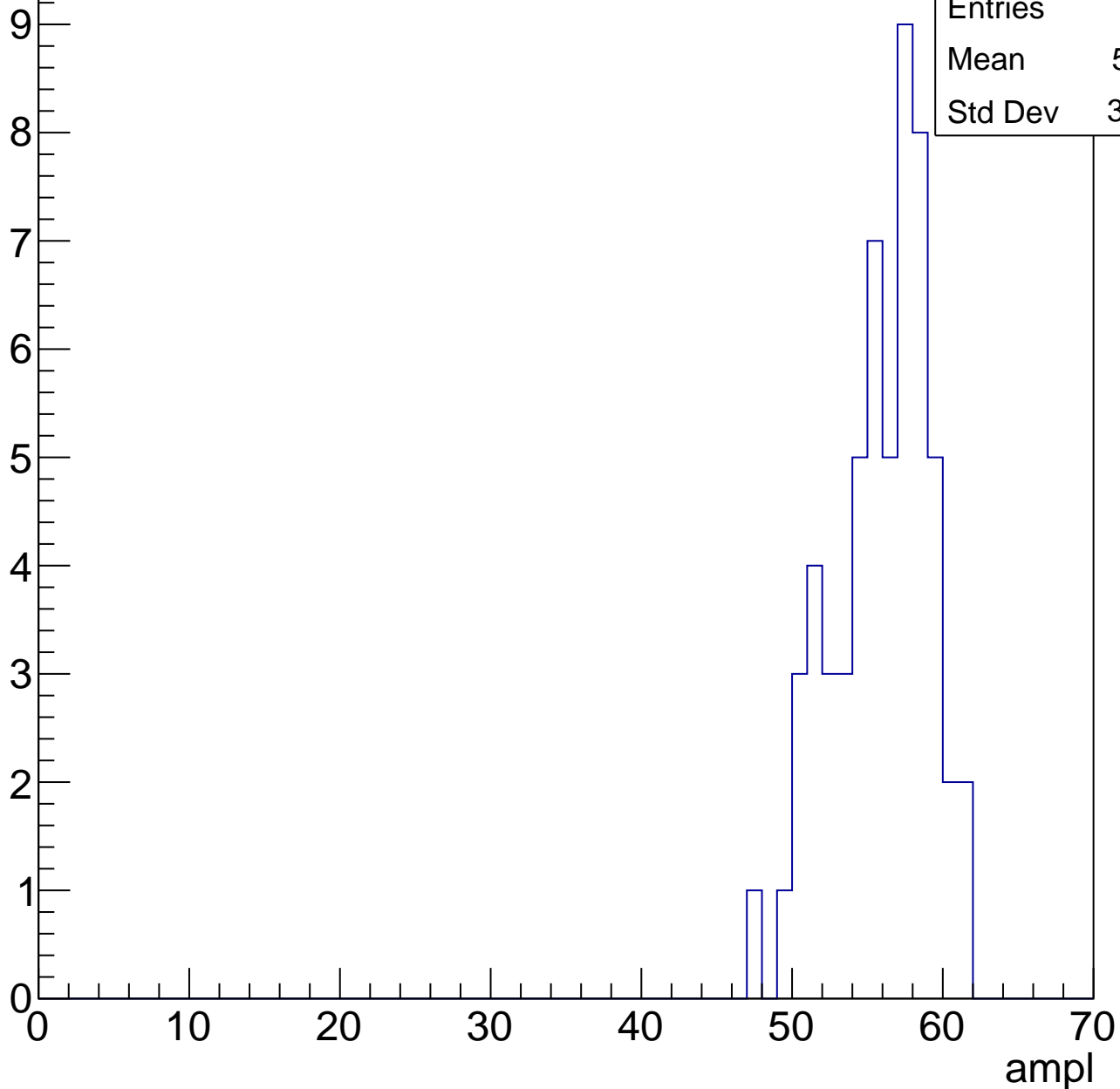
Entries	66
Mean	49.58
Std Dev	3.738



B1L103S, U24-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

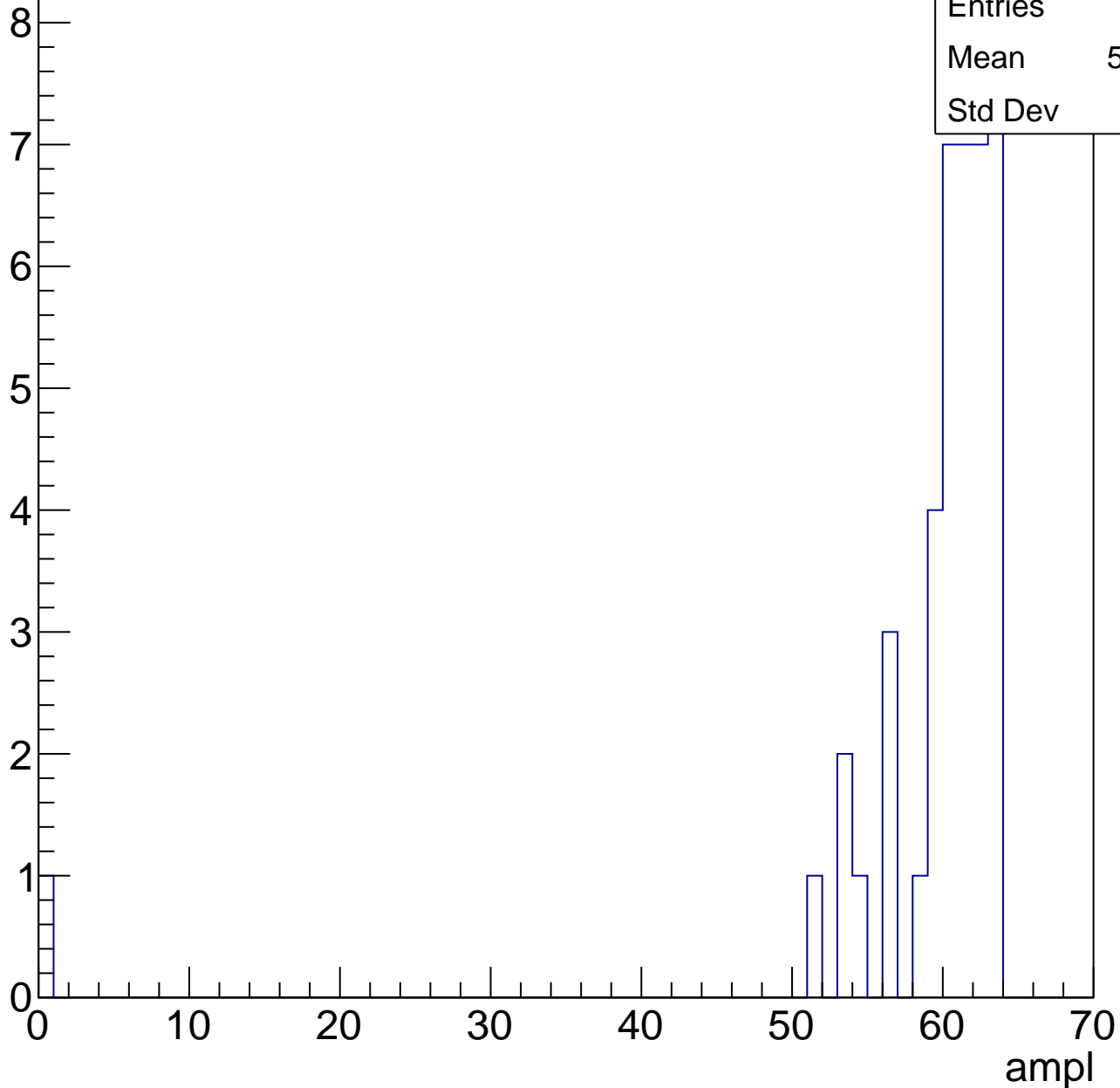


Entries	58
Mean	55.41
Std Dev	3.184

B1L103S, U24-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

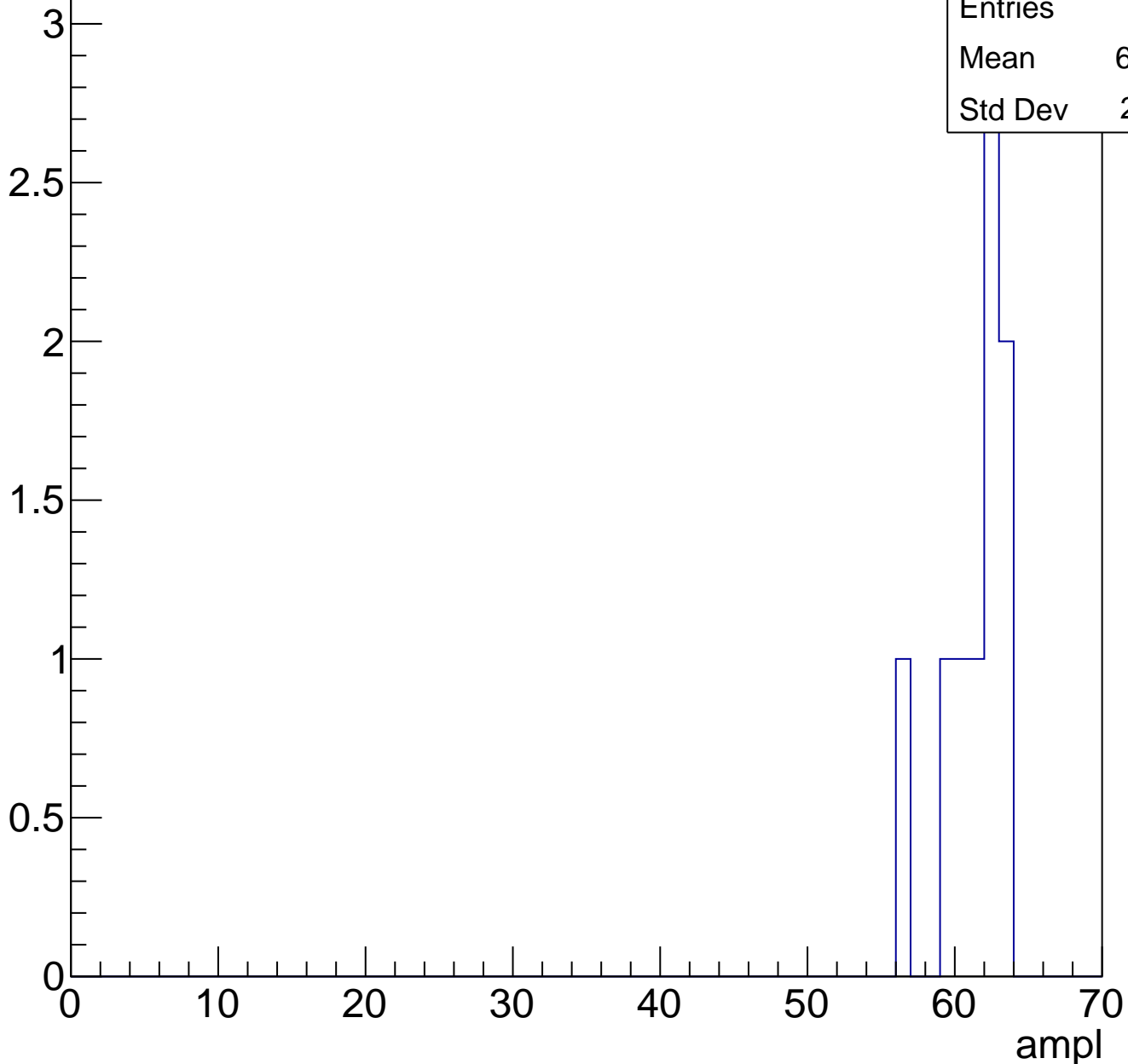
Entry



B1L103S, U24-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

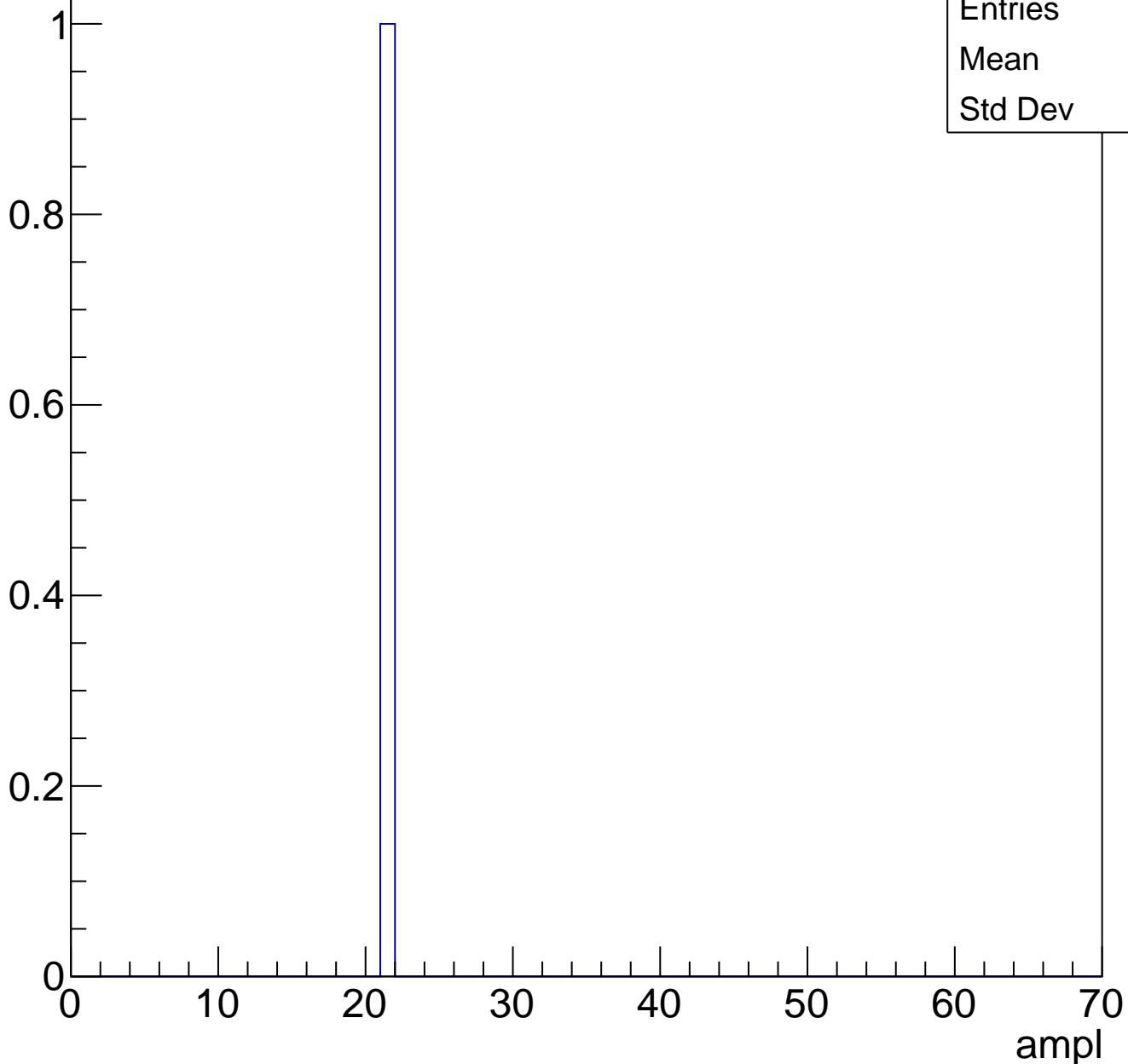


Entries	9
Mean	60.89
Std Dev	2.131

B1L103S, U24-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

B1L103S, U24-ch60, adc0

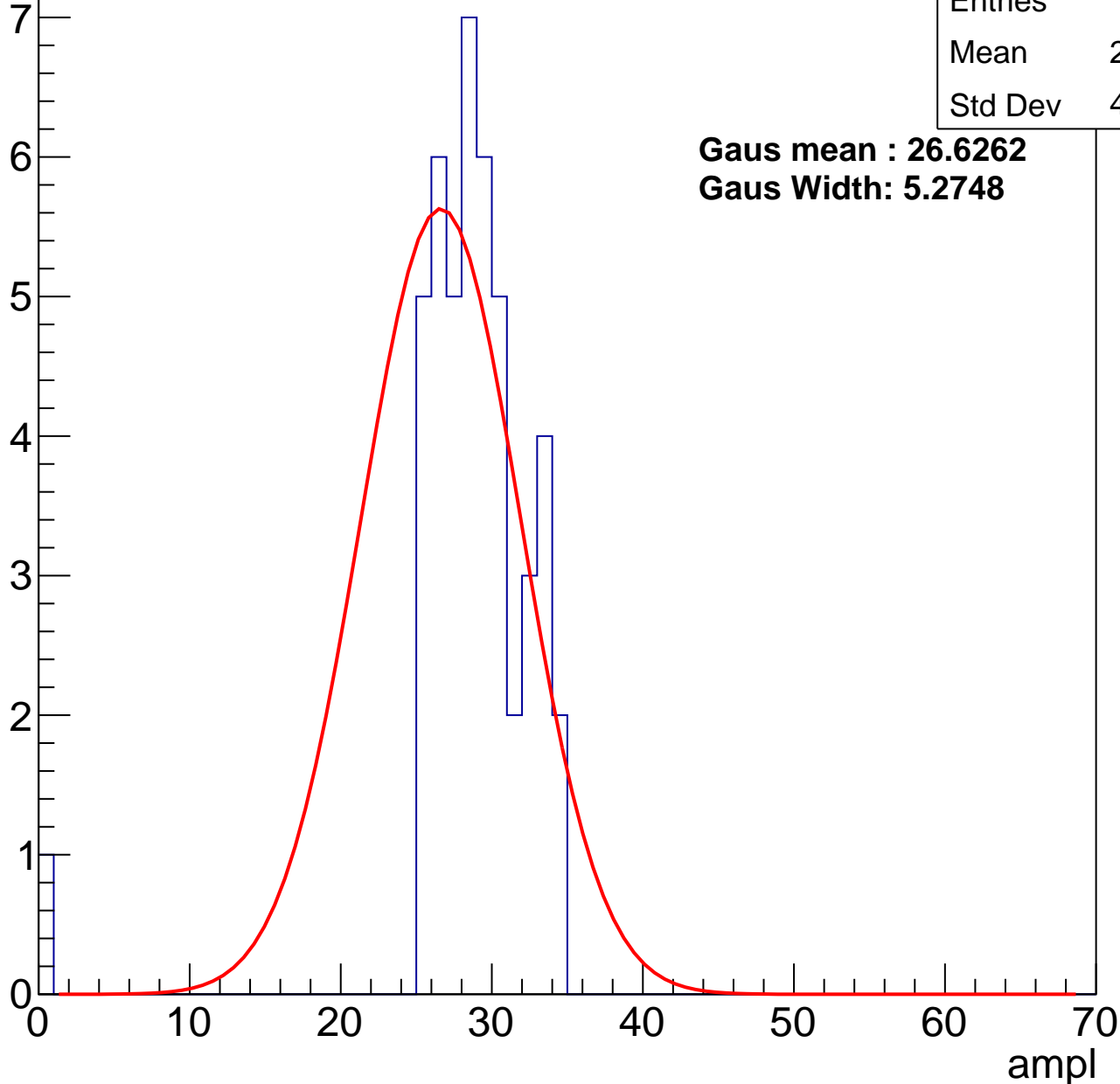
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	28.13
Std Dev	4.937

Gaus mean : 26.6262

Gaus Width: 5.2748



B1L103S, U24-ch60, adc1

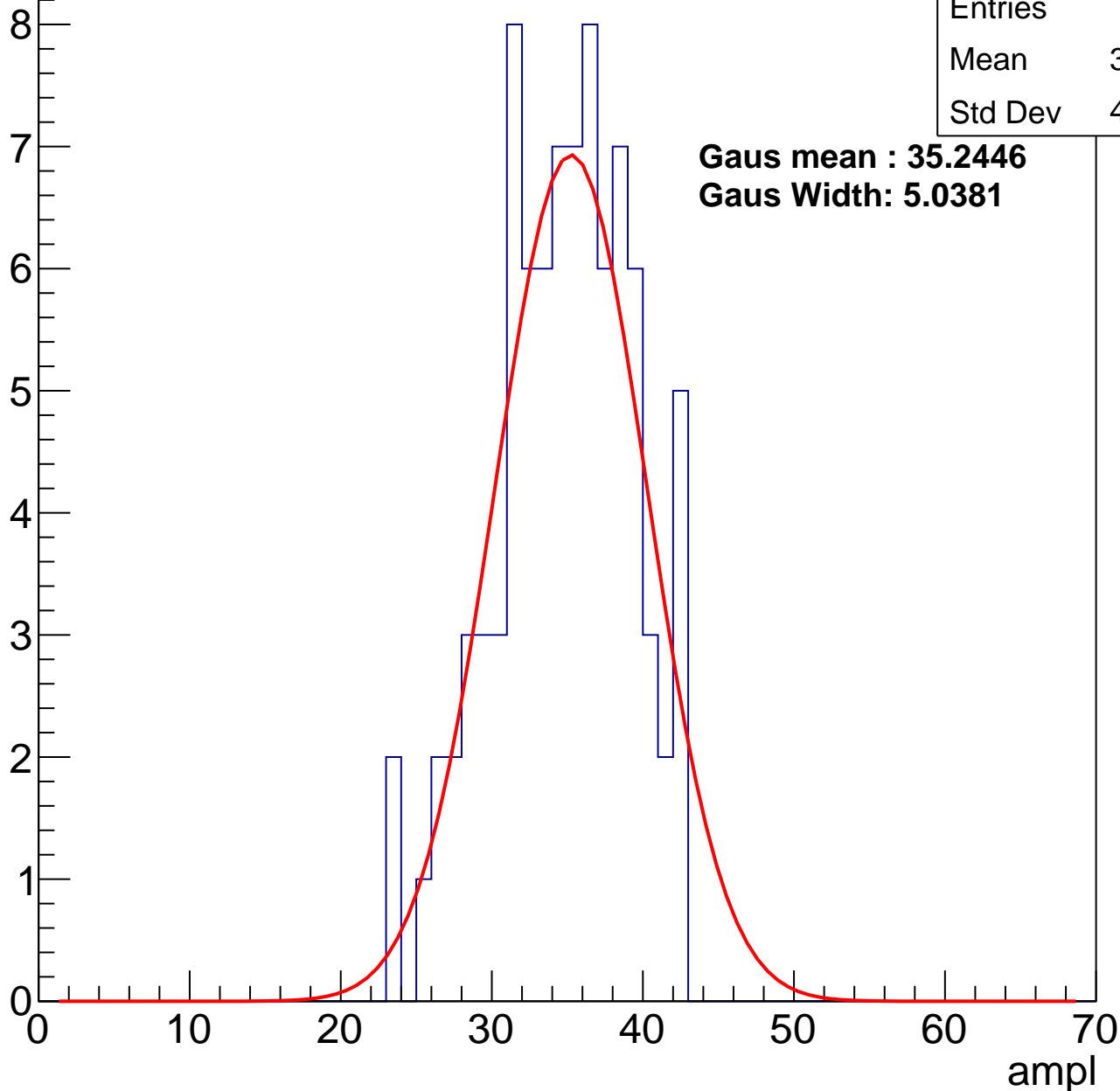
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	34.26
Std Dev	4.494

Gaus mean : 35.2446

Gaus Width: 5.0381



B1L103S, U24-ch60, adc2

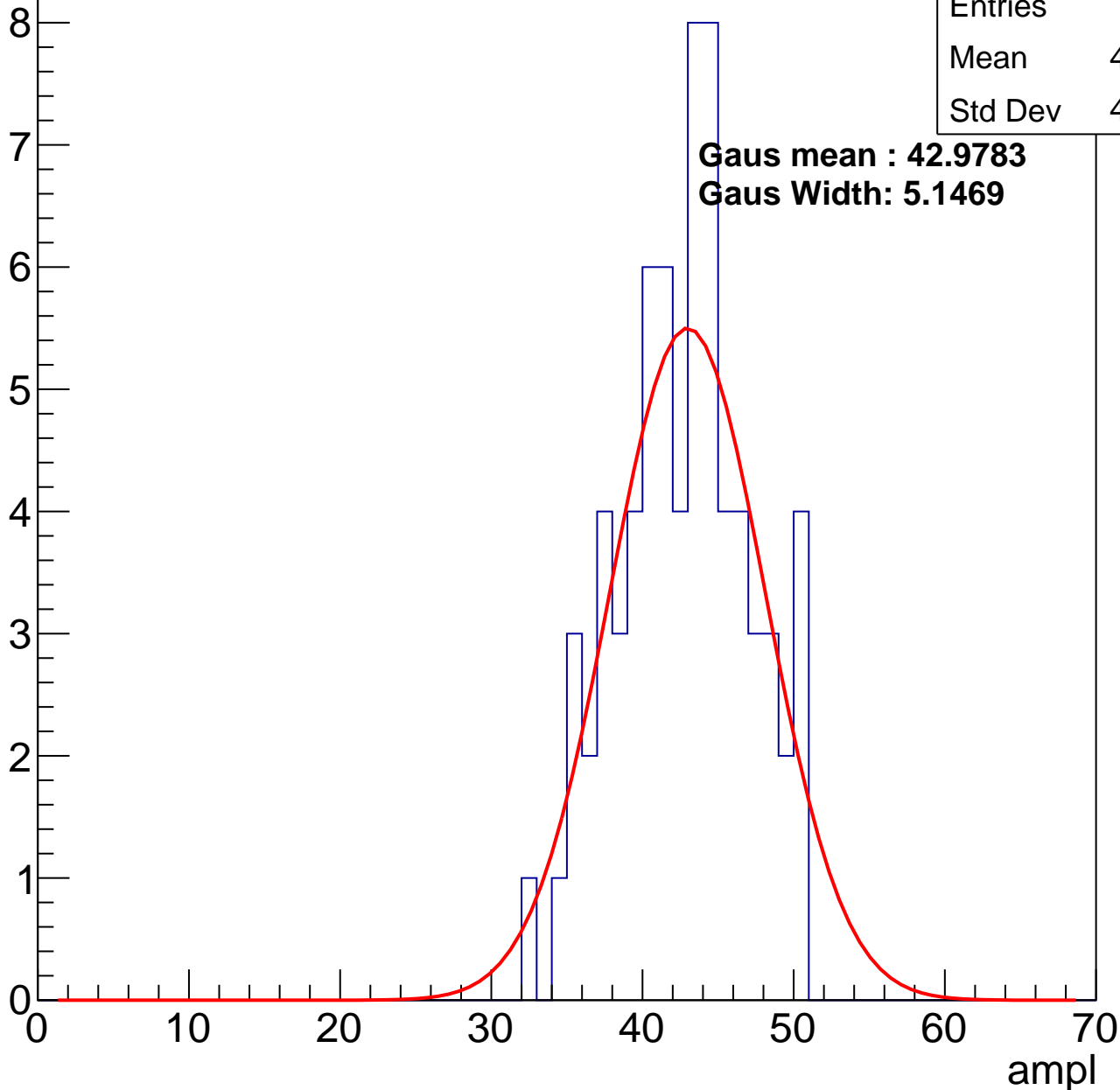
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.26
Std Dev	4.258

Gaus mean : 42.9783

Gaus Width: 5.1469

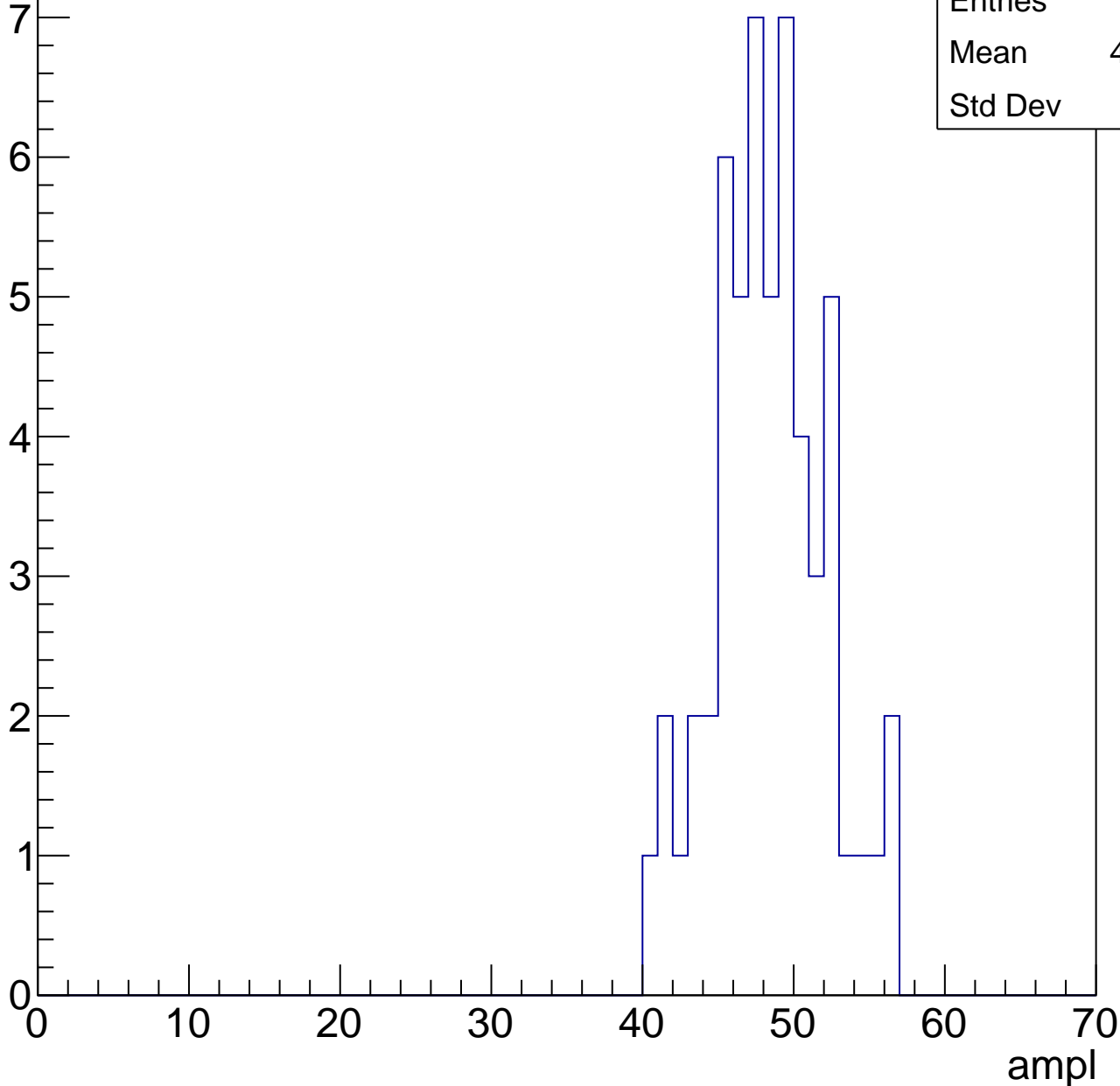


B1L103S, U24-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	47.95
Std Dev	3.64

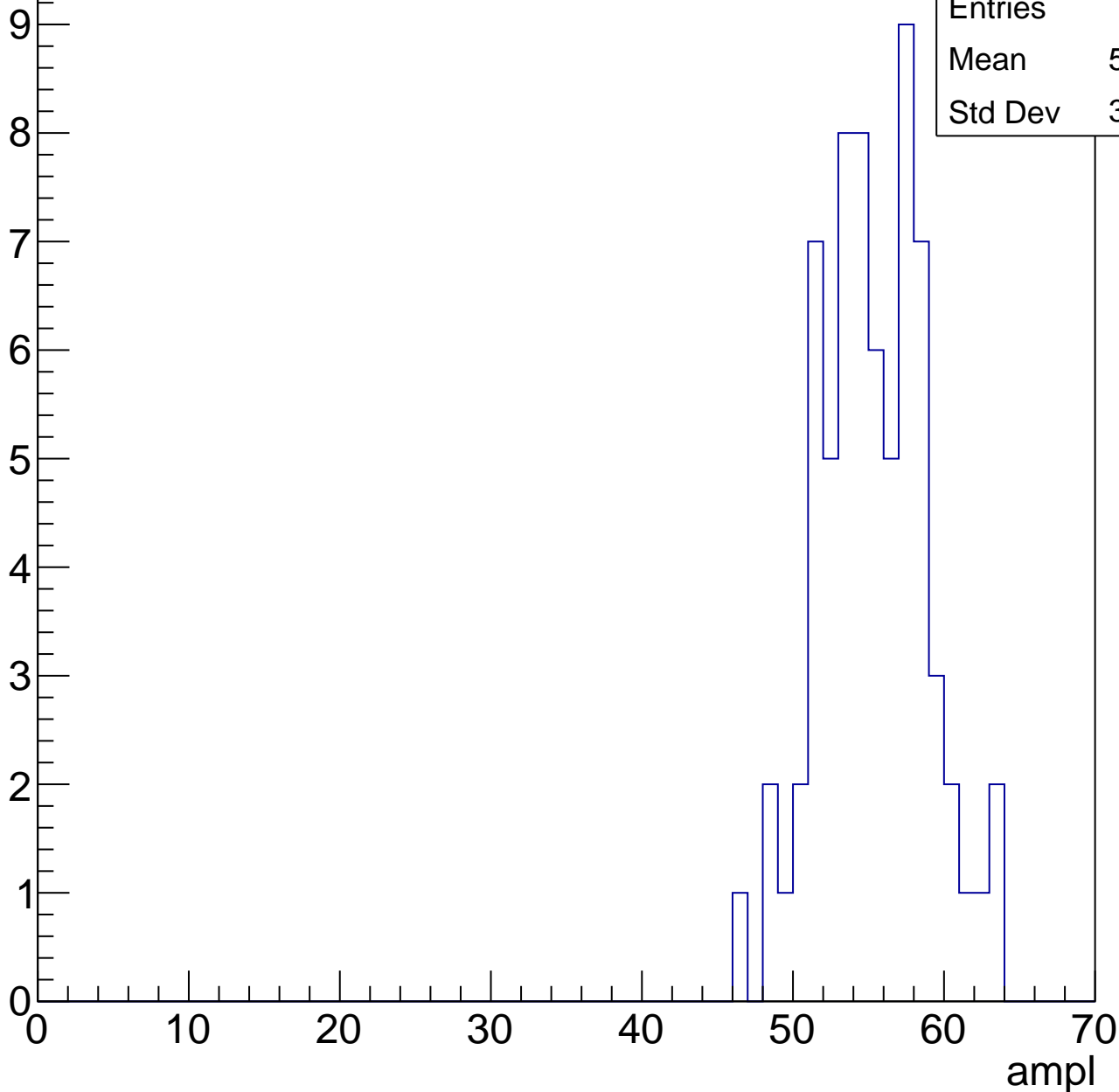


B1L103S, U24-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	54.84
Std Dev	3.536



B1L103S, U24-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 58.3

Std Dev 9.079

ampl

0

10

20

30

40

50

60

70

B1L103S, U24-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.62

Std Dev

0.9922

B1L103S, U24-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U24-ch61, adc0

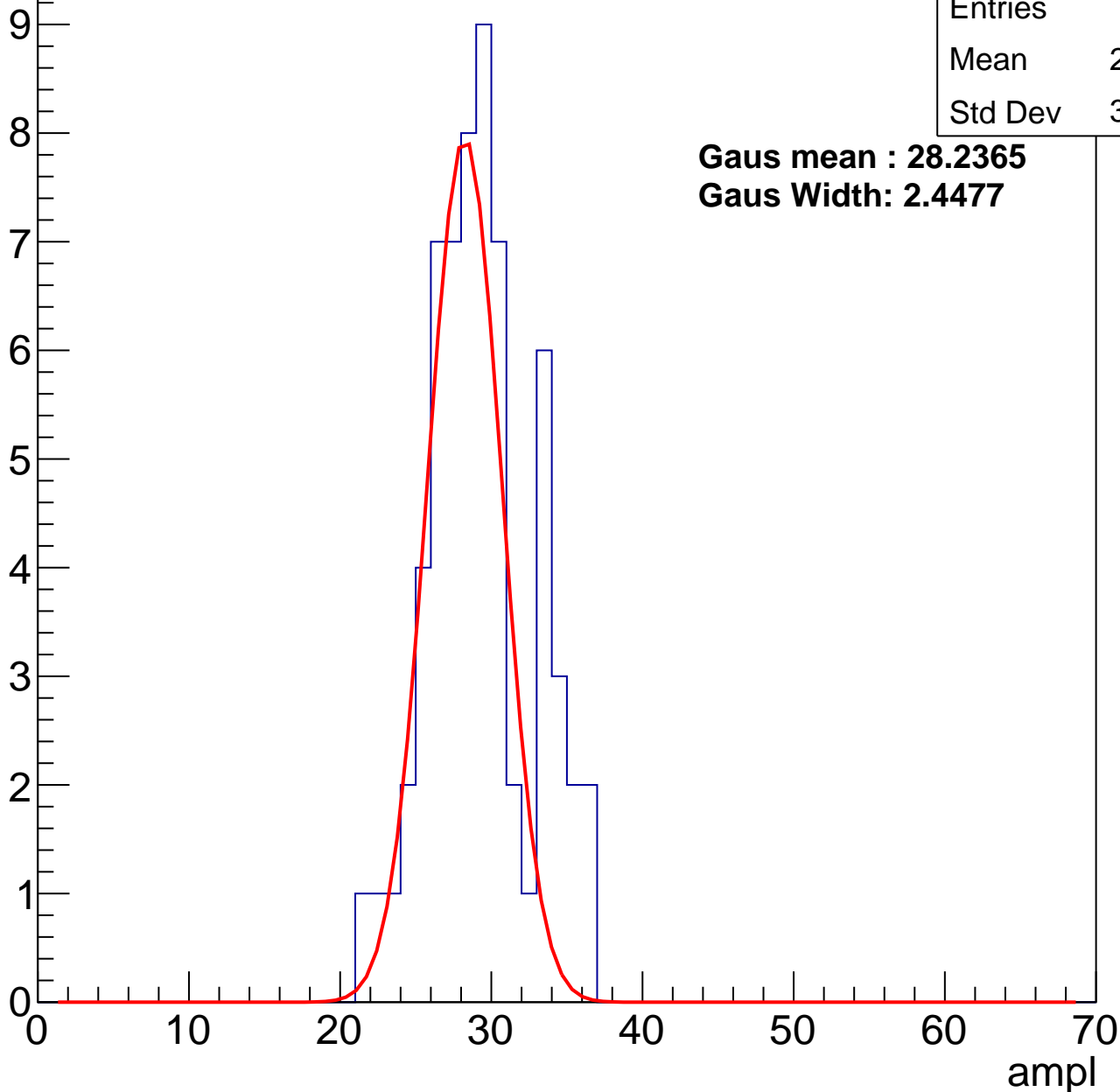
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.83
Std Dev	3.393

Gaus mean : 28.2365

Gaus Width: 2.4477



B1L103S, U24-ch61, adc1

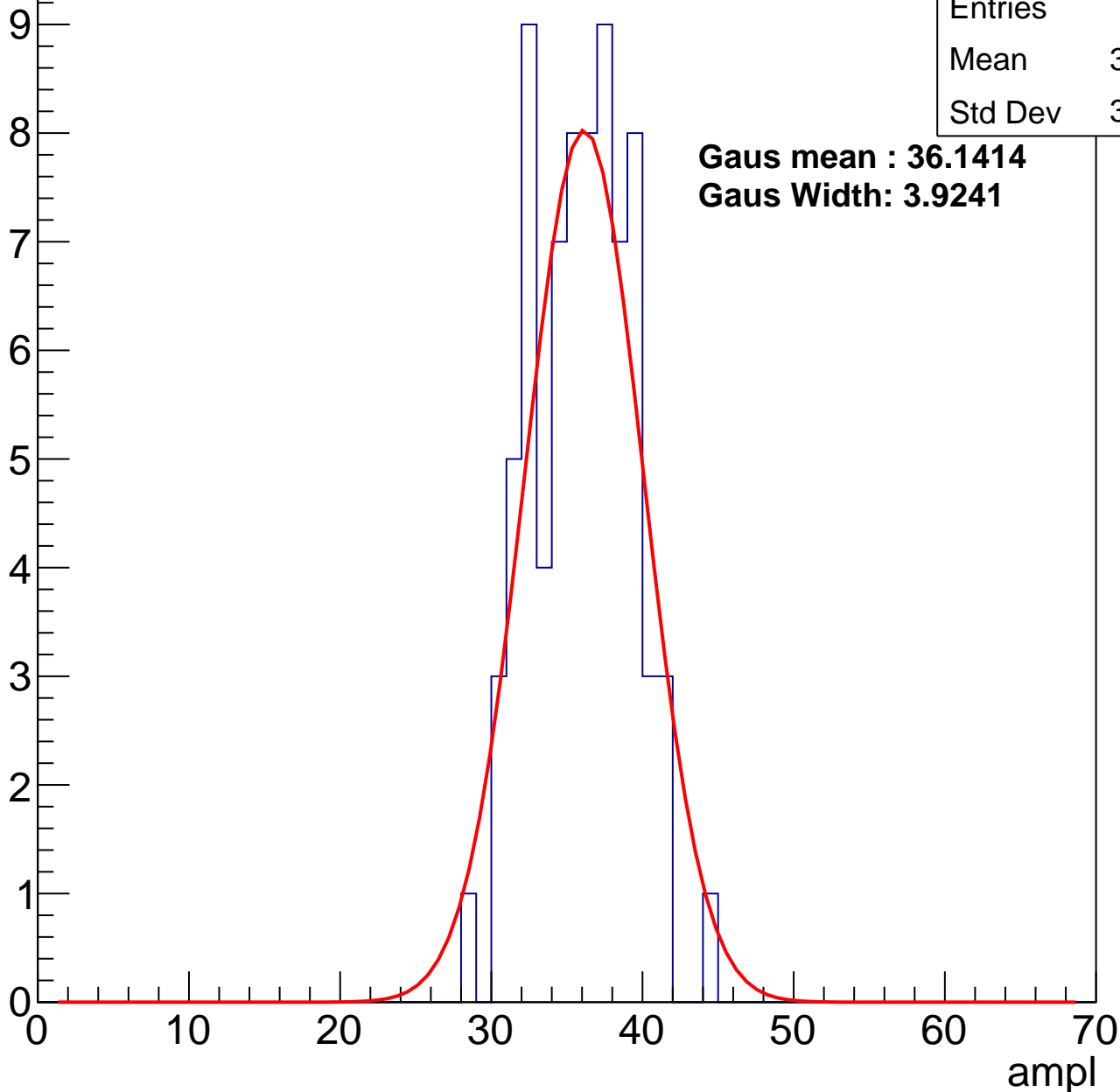
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.49
Std Dev	3.222

Gaus mean : 36.1414

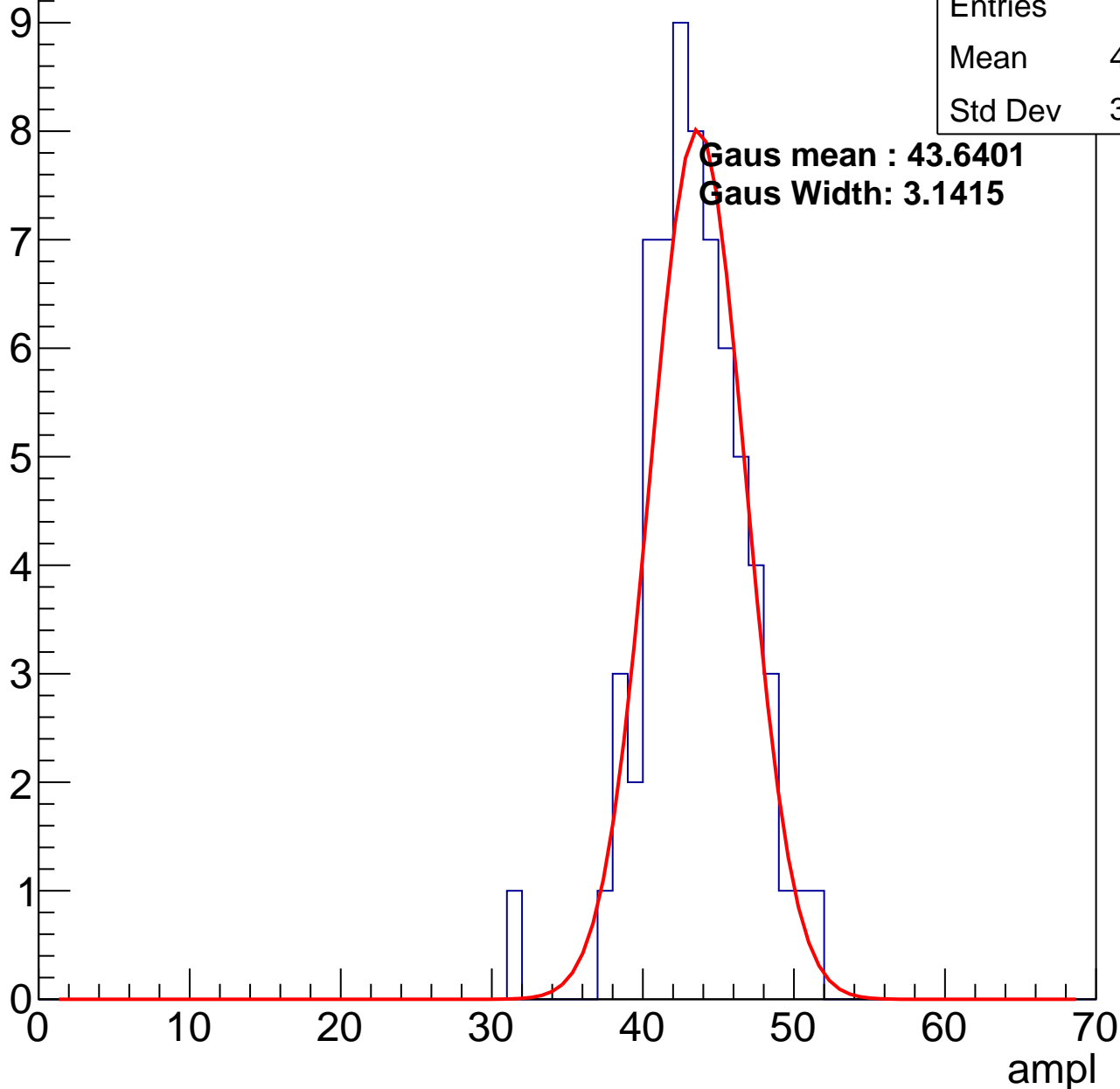
Gaus Width: 3.9241



B1L103S, U24-ch61, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

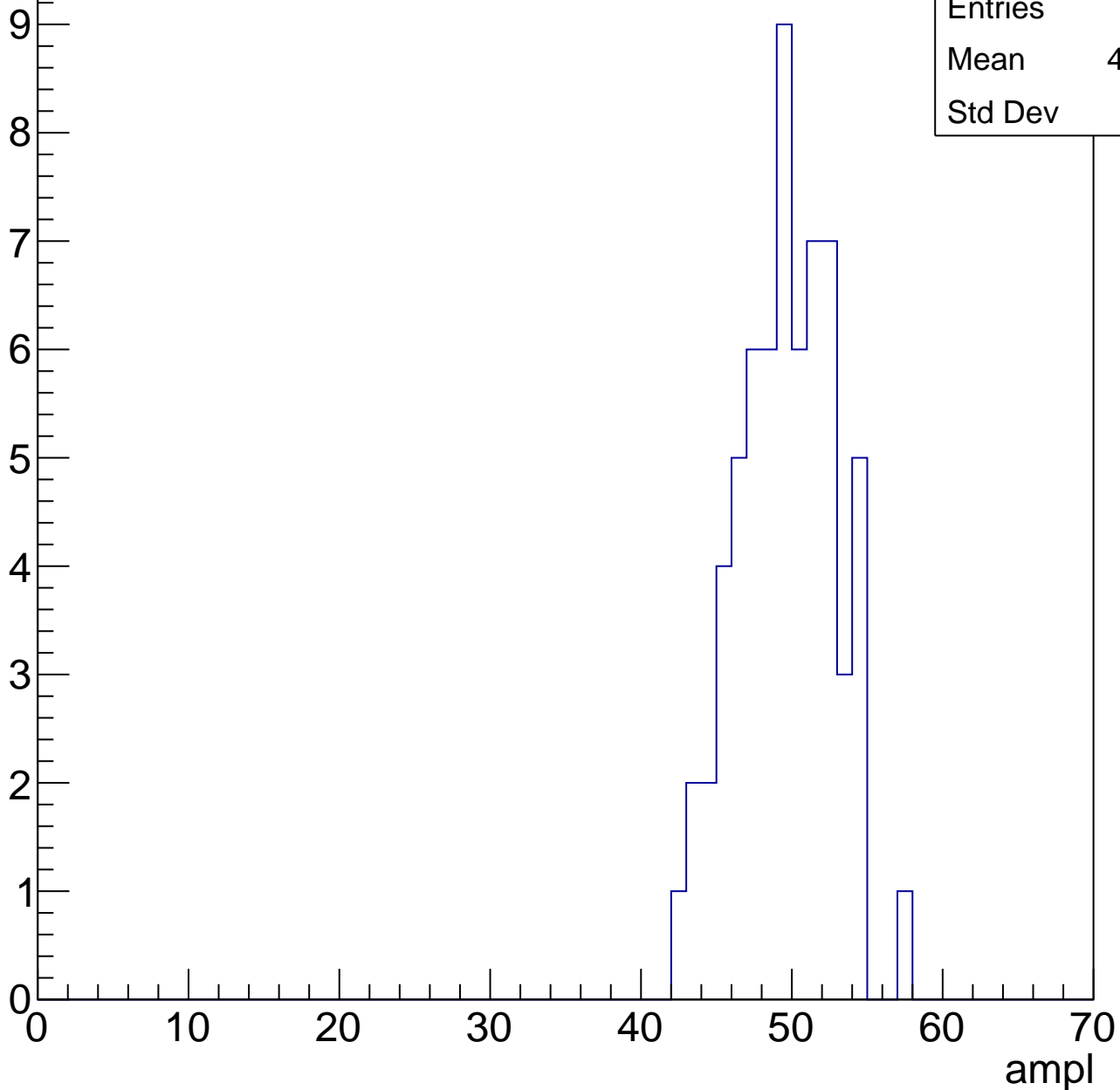


B1L103S, U24-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.12
Std Dev	3.17

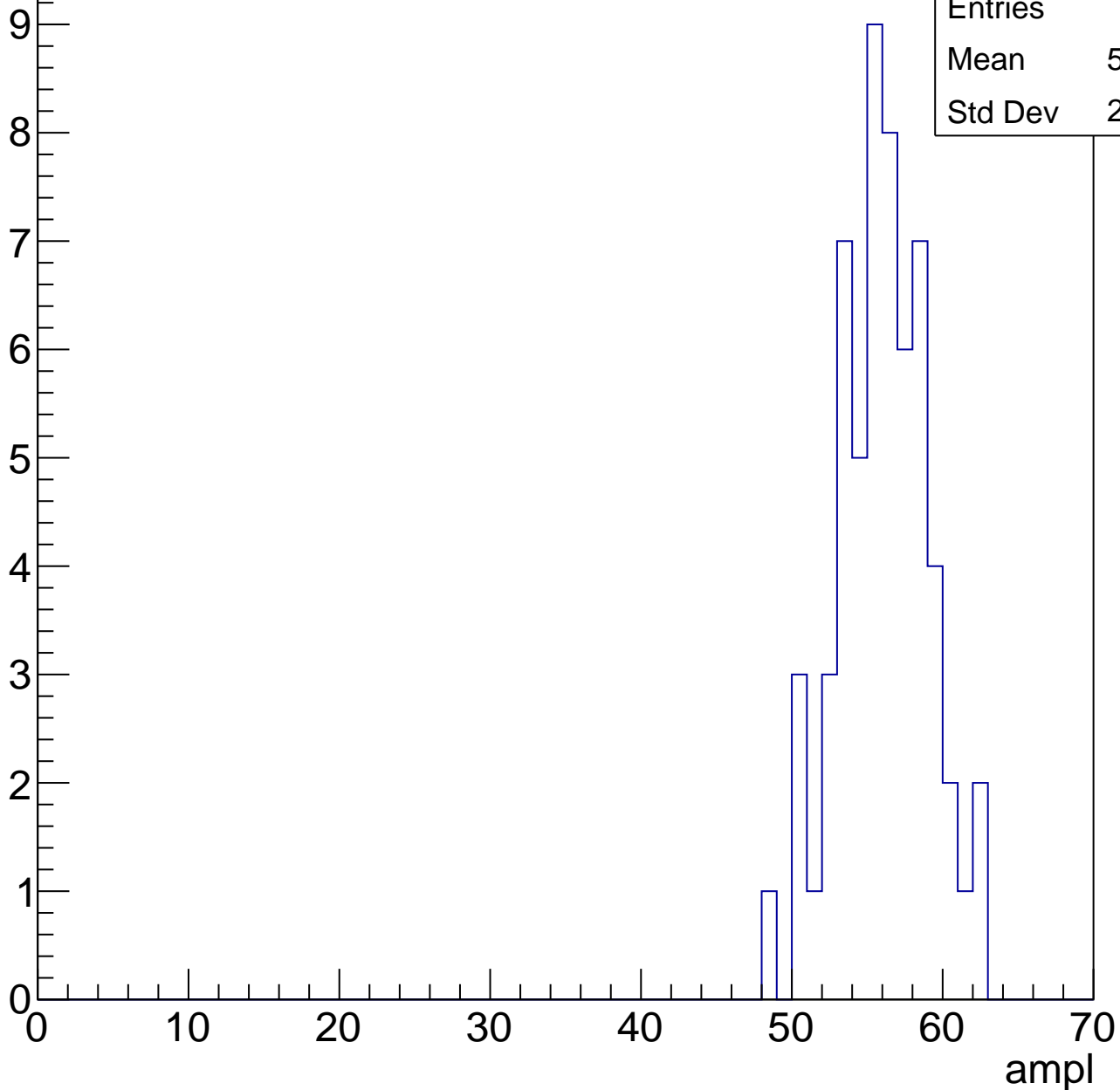


B1L103S, U24-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.56
Std Dev	2.982

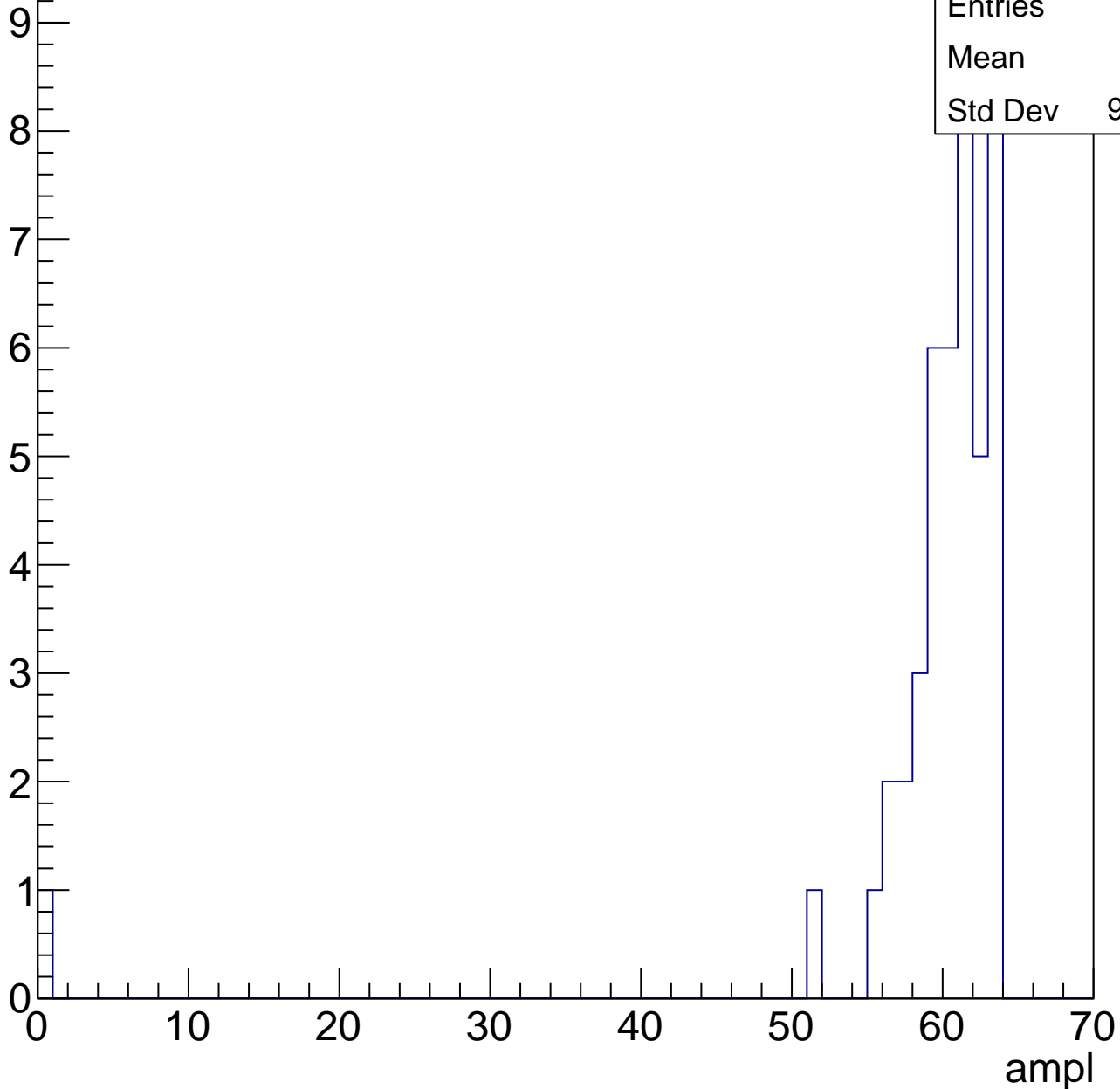


B1L103S, U24-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

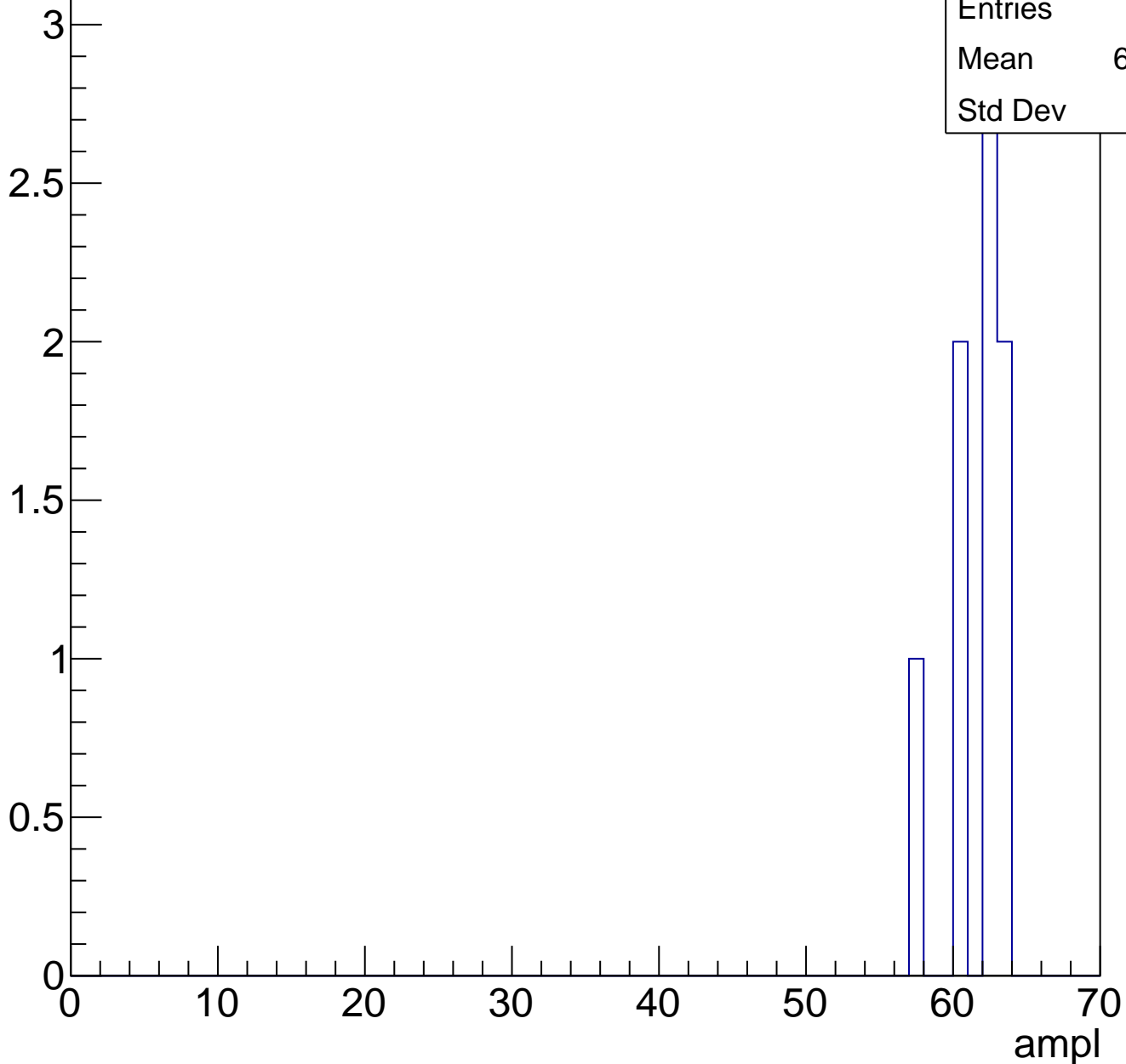
Entries	44
Mean	58.7
Std Dev	9.295



B1L103S, U24-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch62, adc0

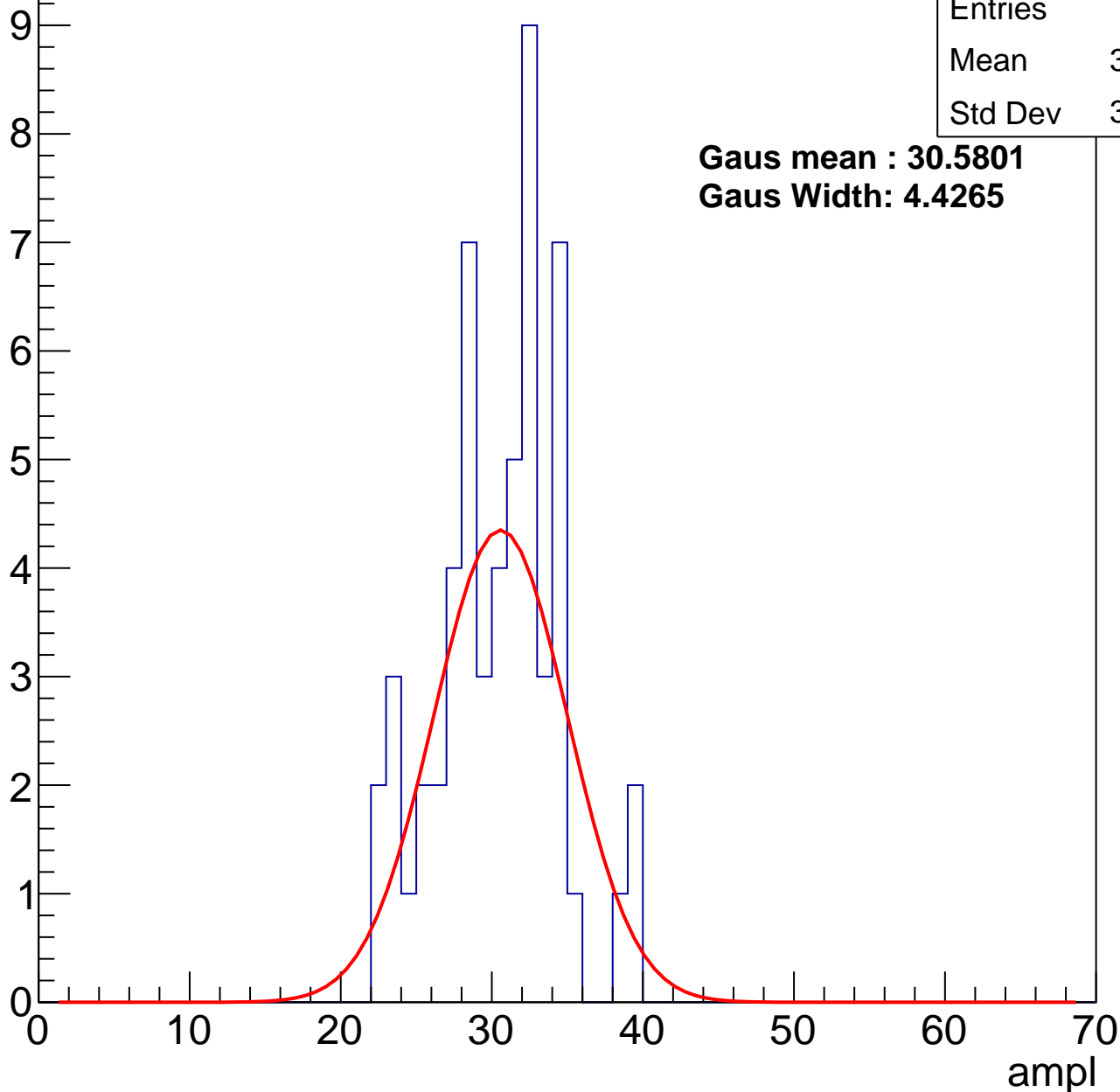
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	30.02
Std Dev	3.989

Gaus mean : 30.5801

Gaus Width: 4.4265



B1L103S, U24-ch62, adc1

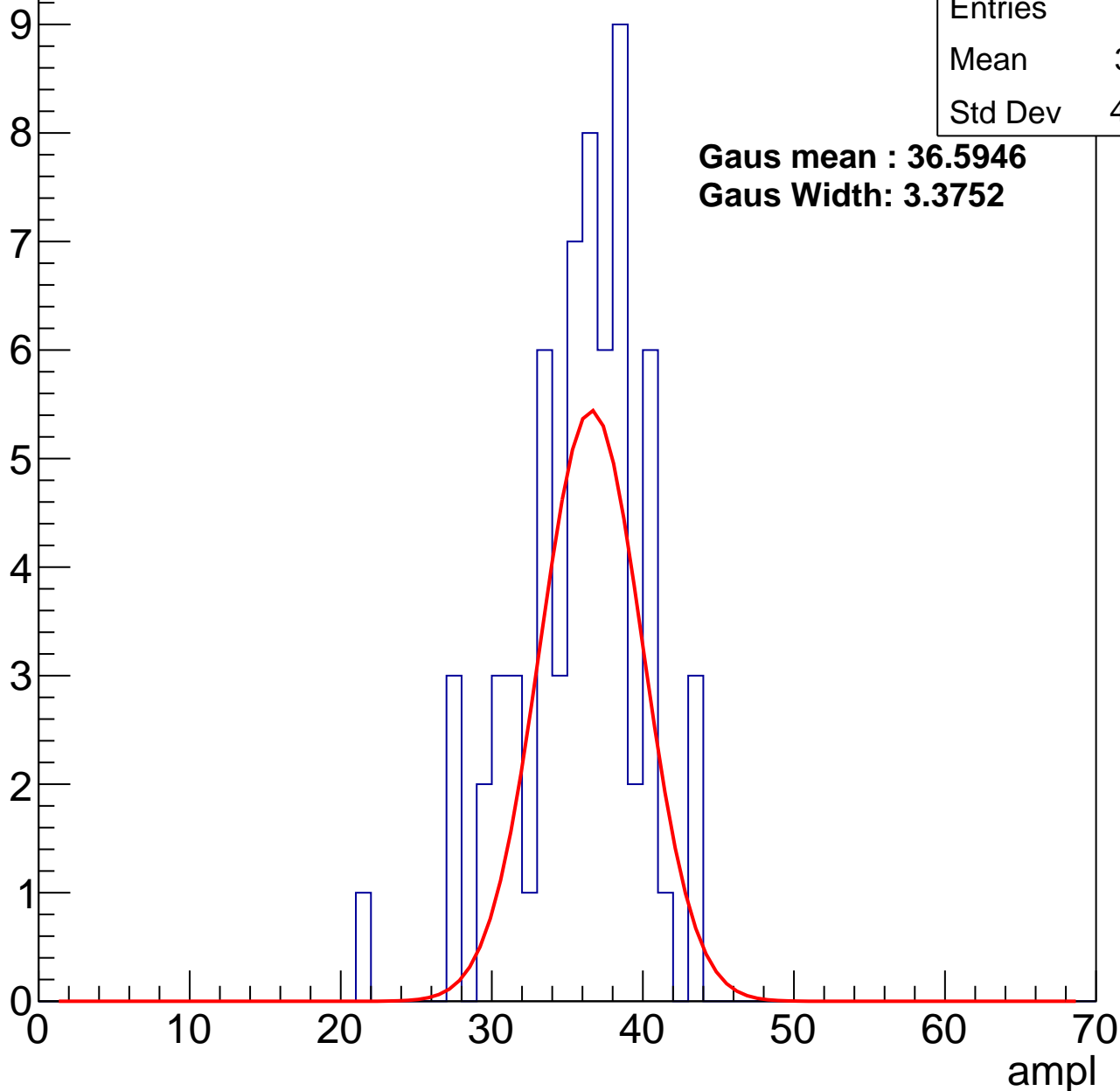
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.31
Std Dev	4.213

Gaus mean : 36.5946

Gaus Width: 3.3752



B1L103S, U24-ch62, adc2

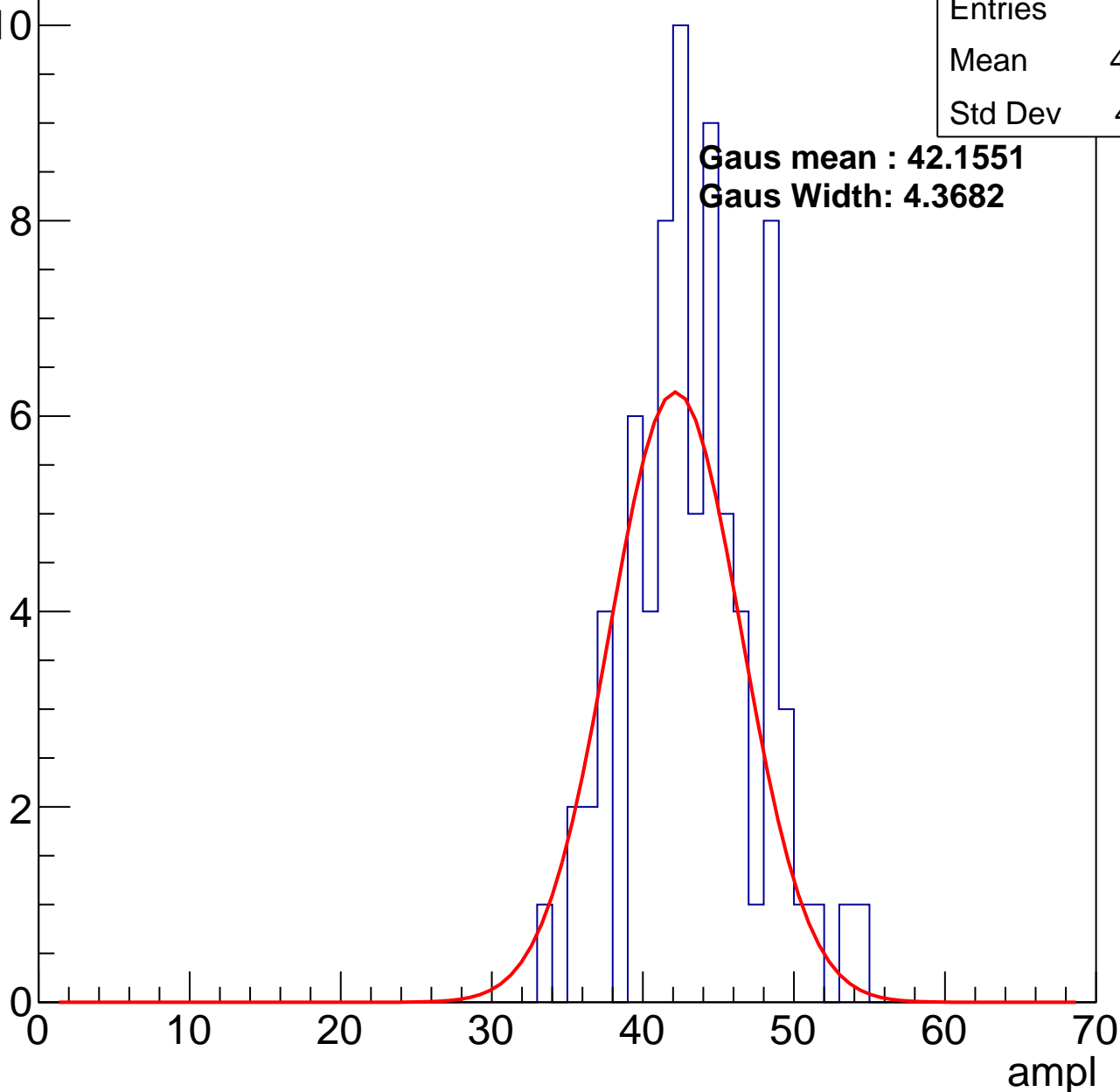
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.04
Std Dev	4.241

Gaus mean : 42.1551

Gaus Width: 4.3682

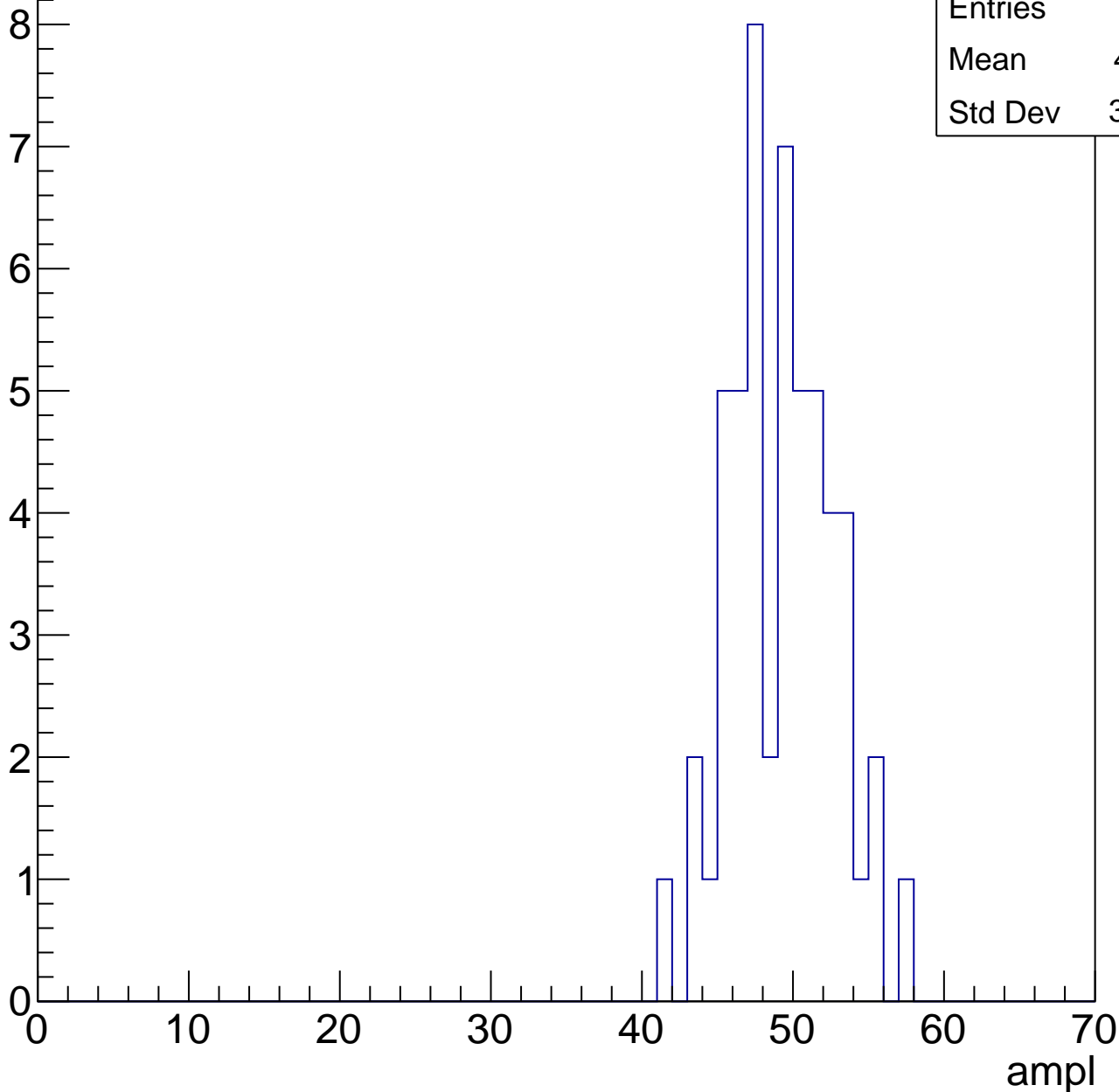


B1L103S, U24-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.81
Std Dev	3.376

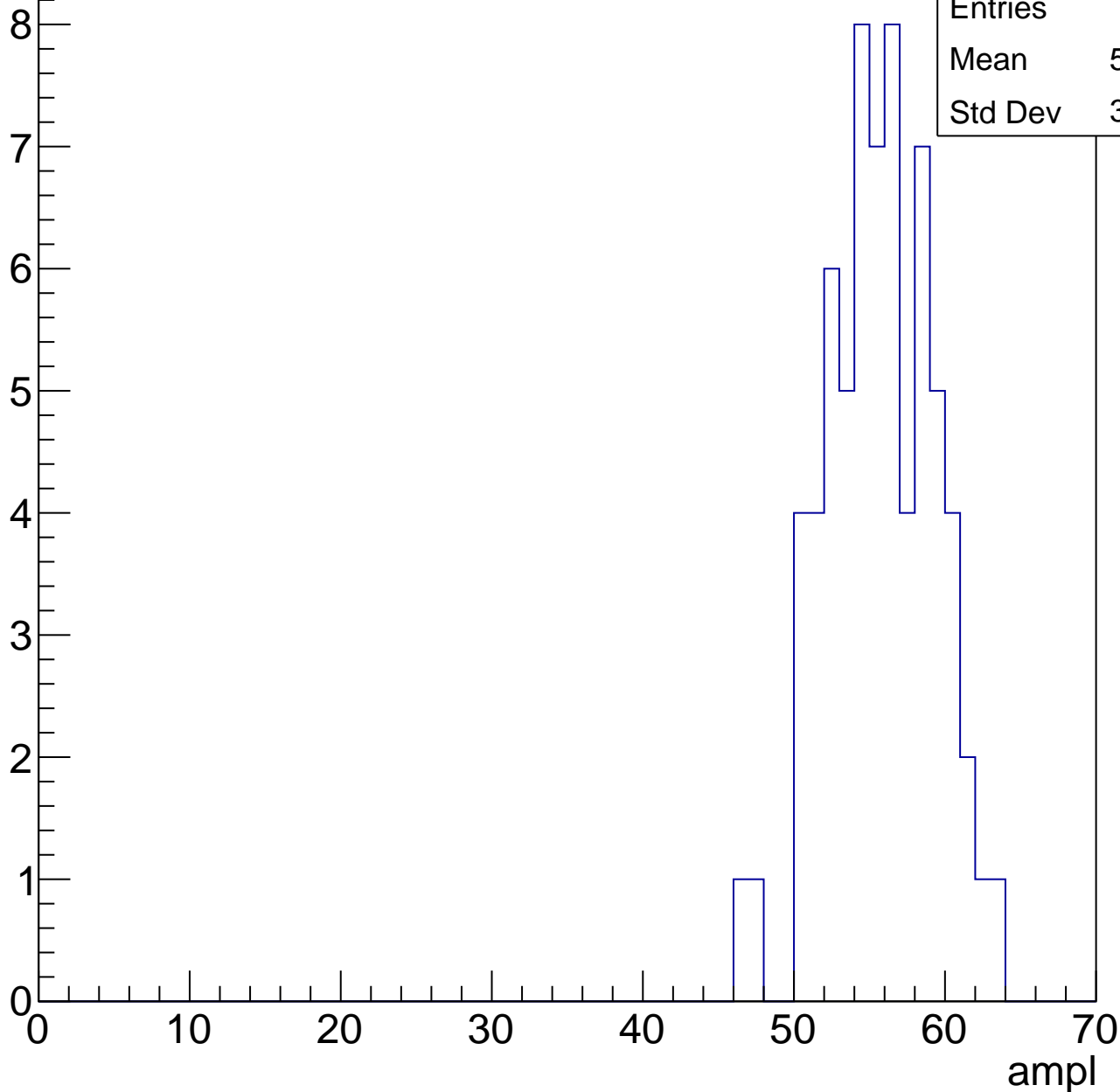


B1L103S, U24-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.22
Std Dev	3.514

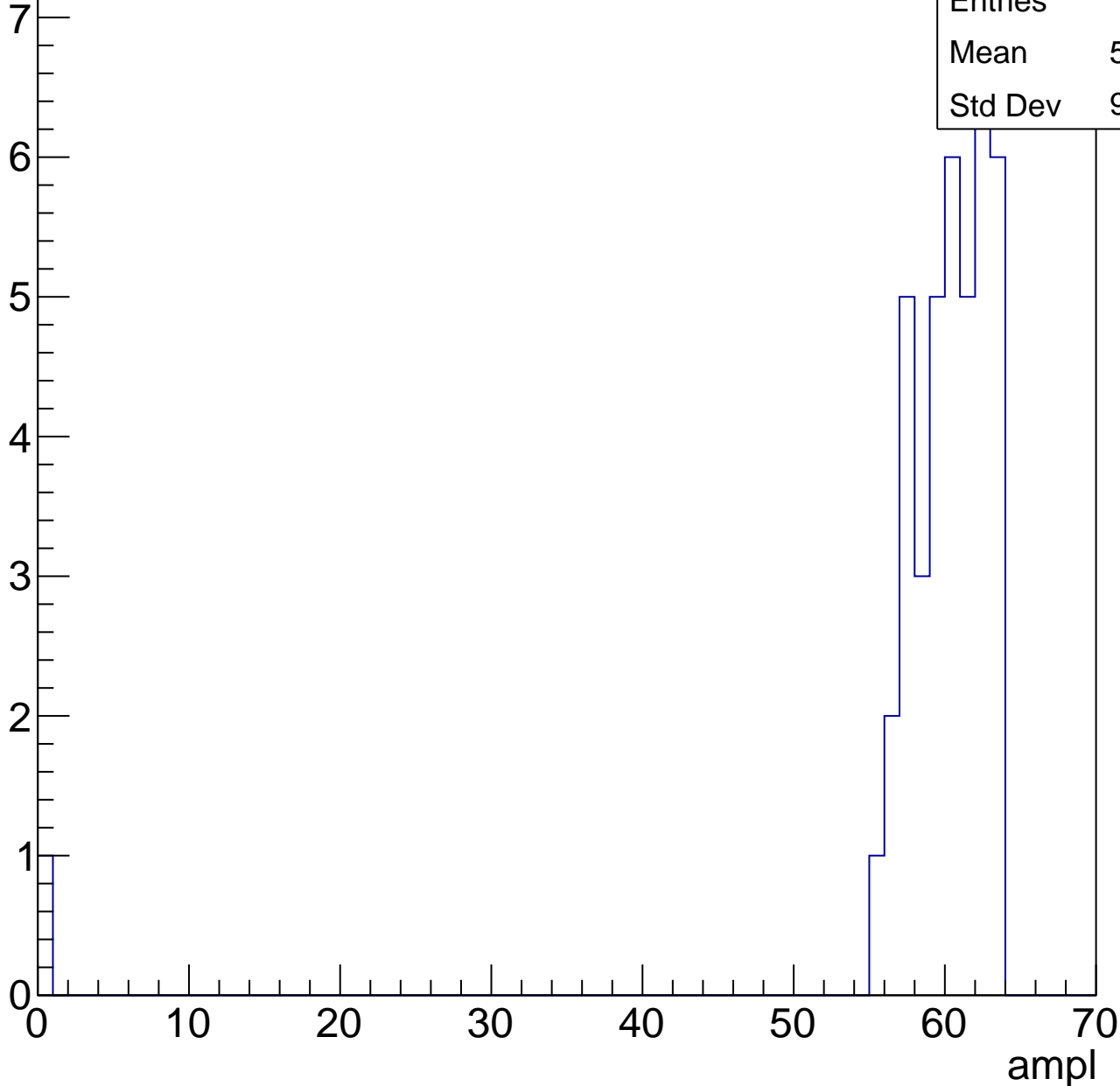


B1L103S, U24-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

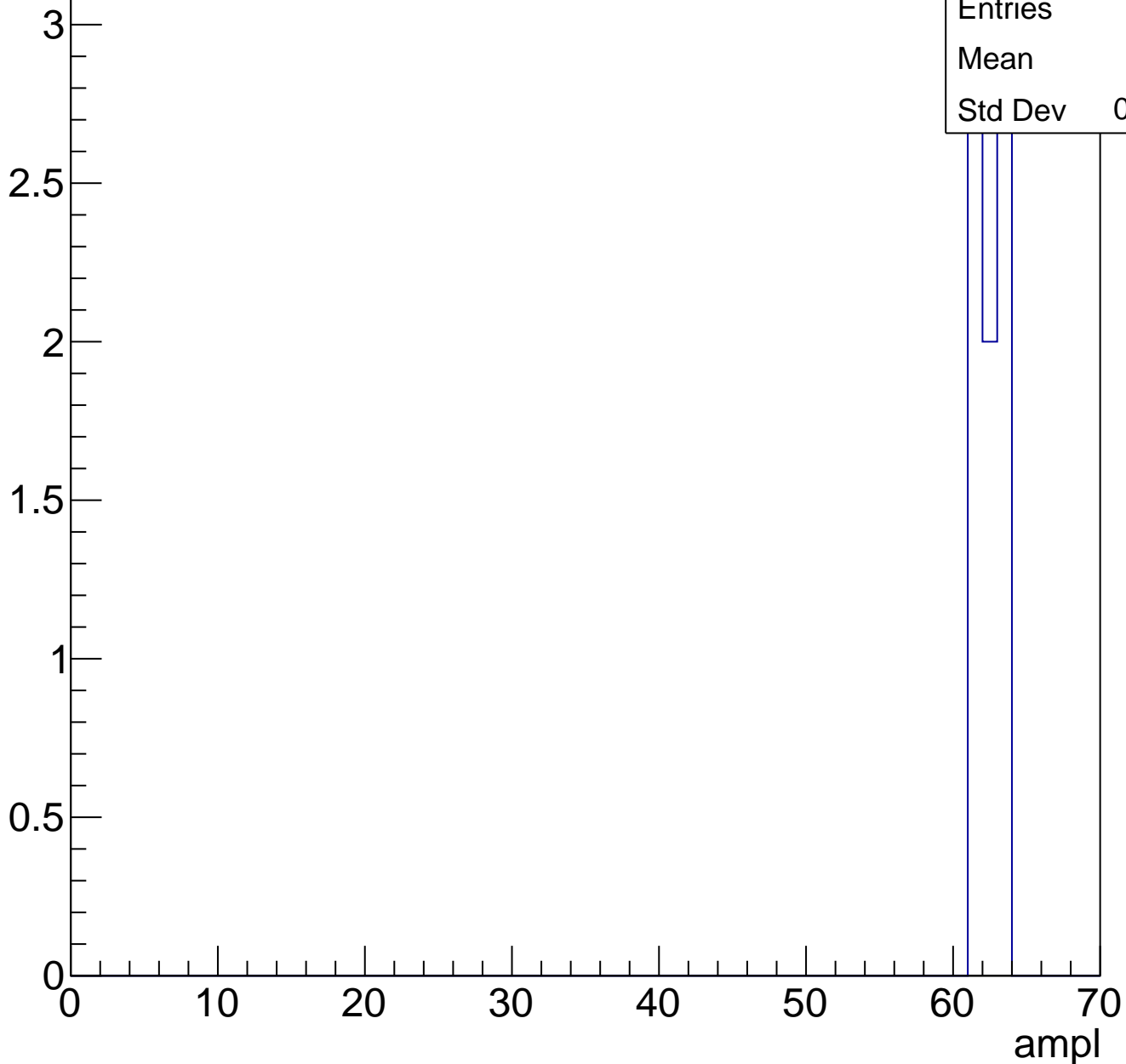
Entries	41
Mean	58.49
Std Dev	9.515



B1L103S, U24-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch63, adc0

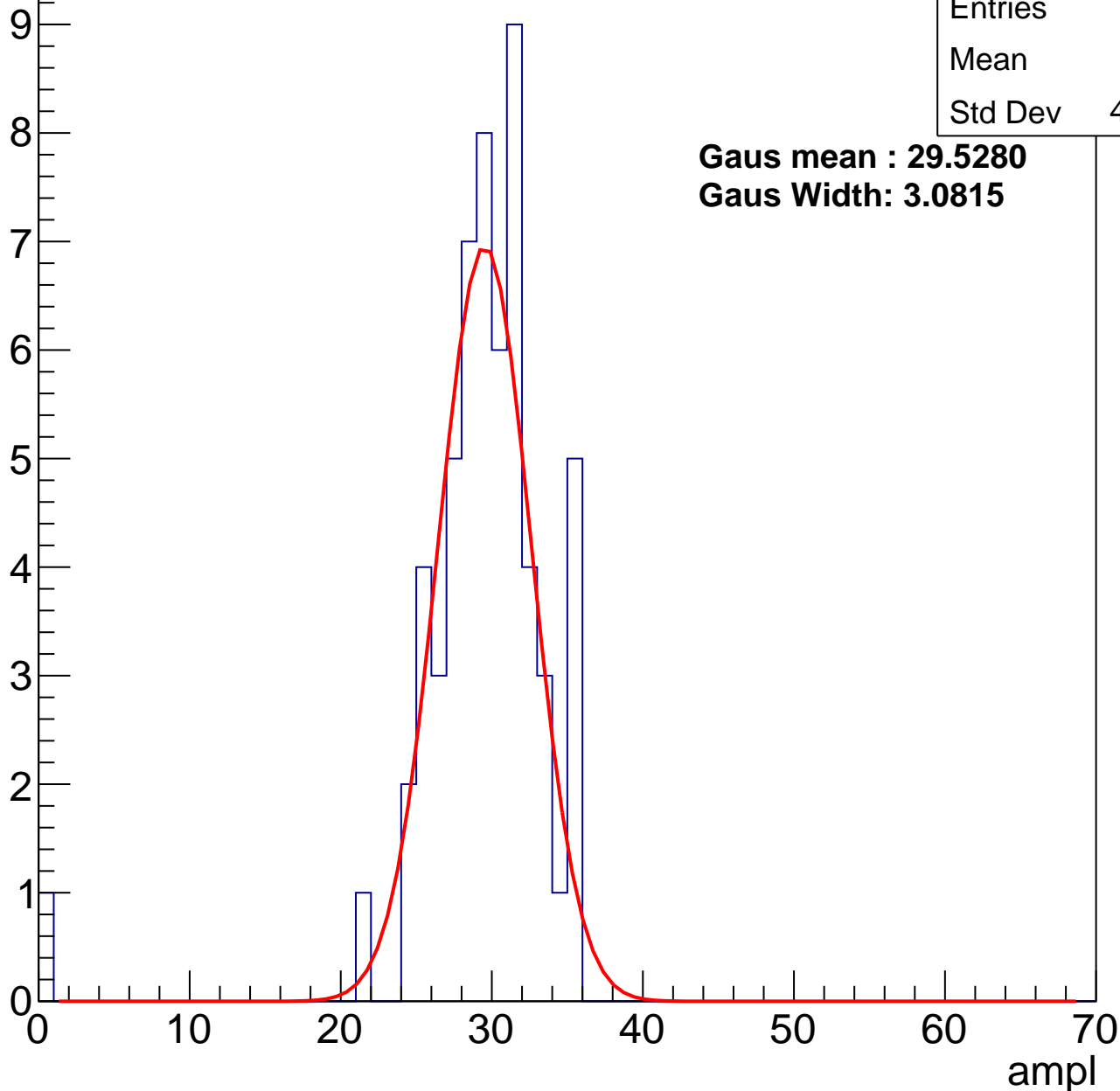
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	28.9
Std Dev	4.877

Gaus mean : 29.5280

Gaus Width: 3.0815



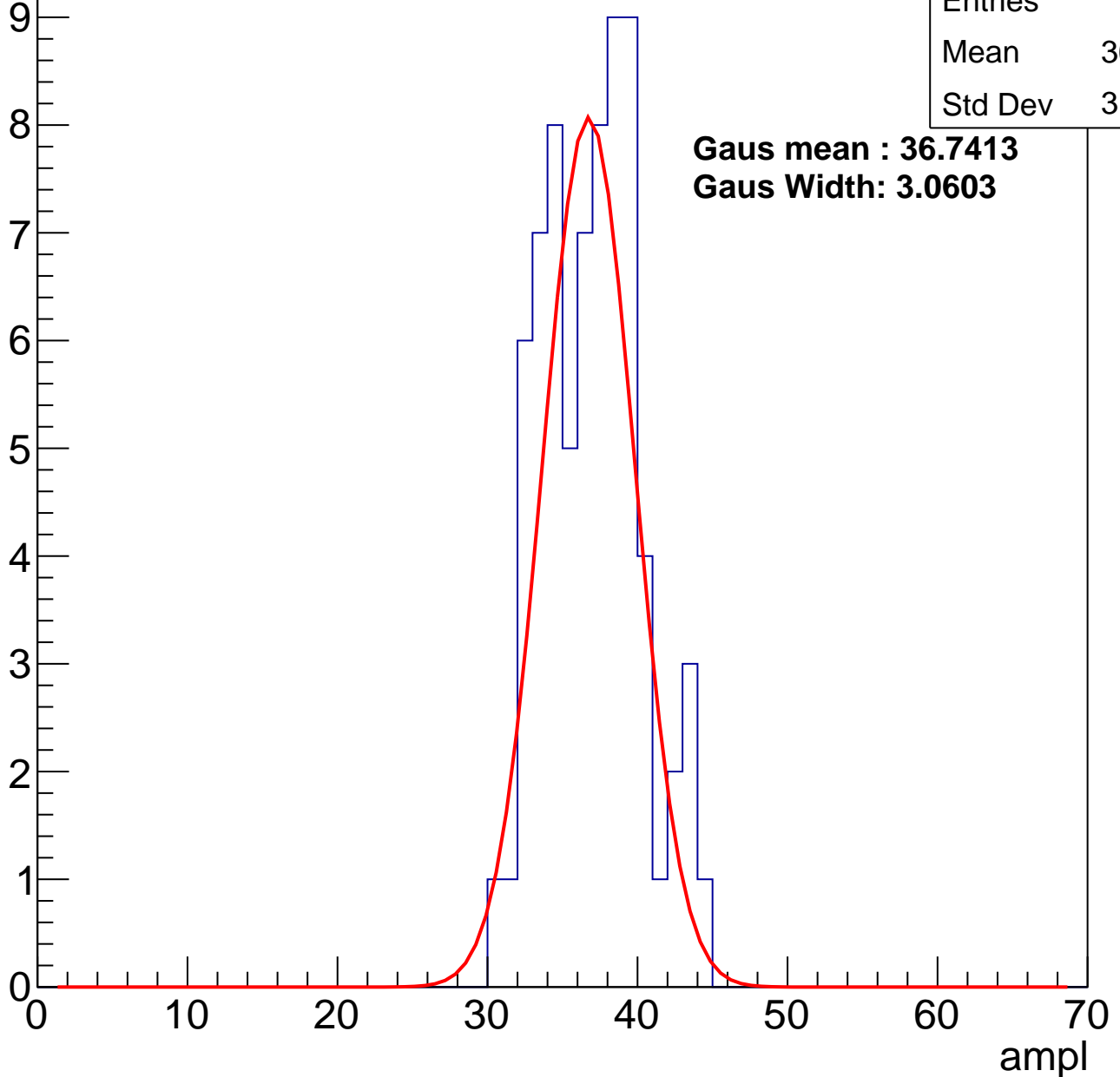
B1L103S, U24-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	36.53
Std Dev	3.193

Gaus mean : 36.7413
Gaus Width: 3.0603



B1L103S, U24-ch63, adc2

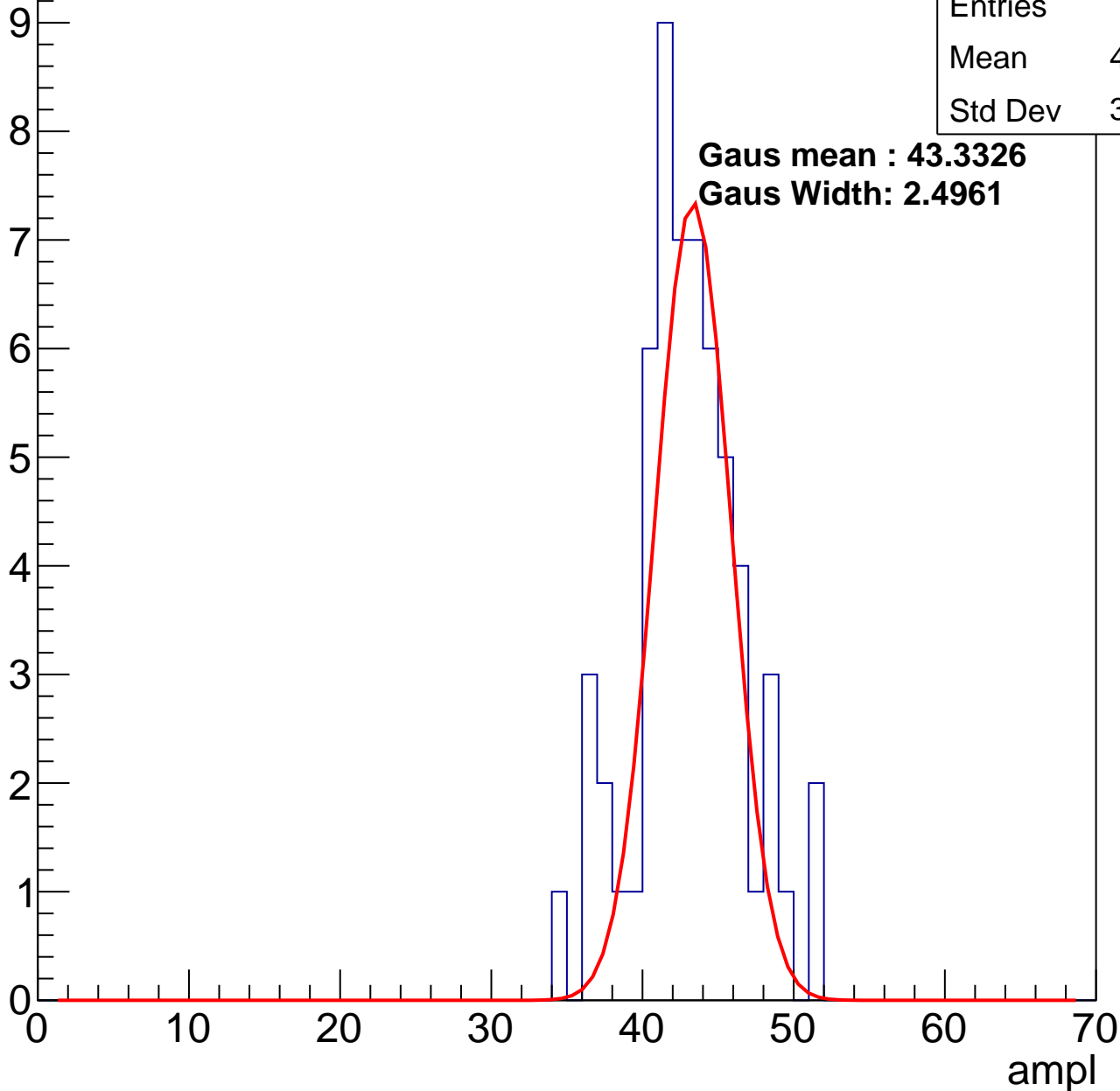
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.58
Std Dev	3.552

Gaus mean : 43.3326

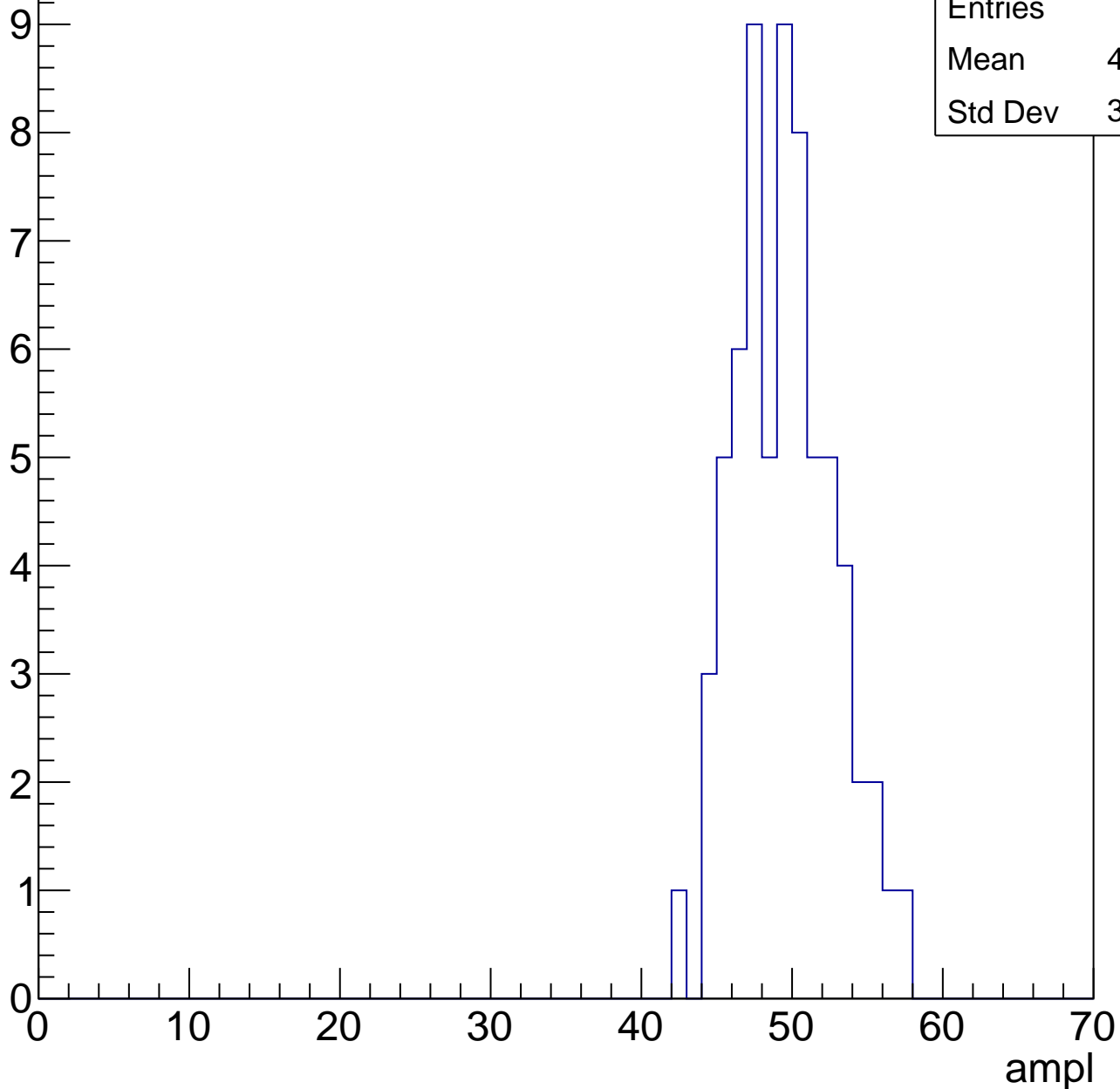
Gaus Width: 2.4961



B1L103S, U24-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

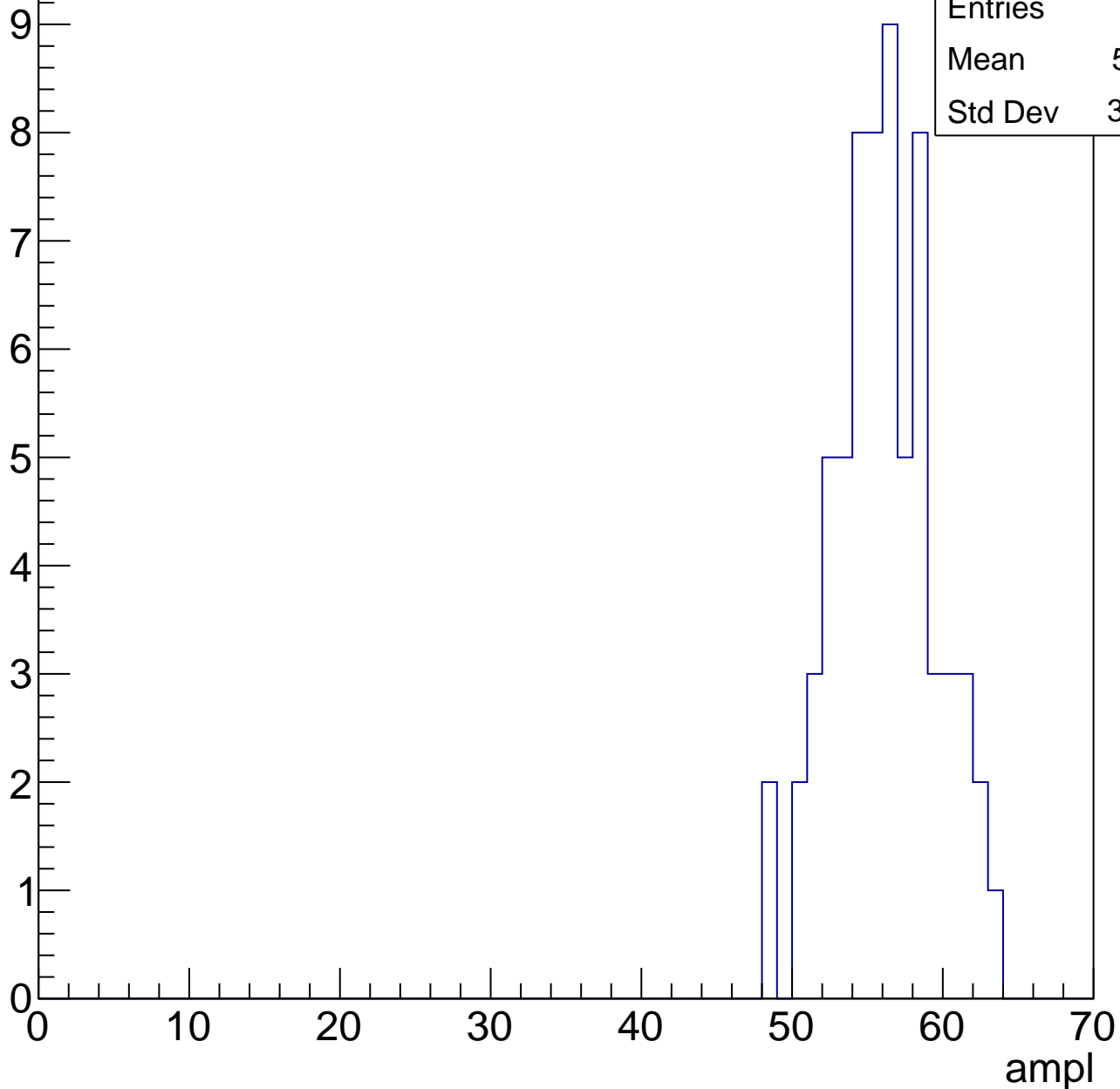


Entries	66
Mean	49.05
Std Dev	3.188

B1L103S, U24-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



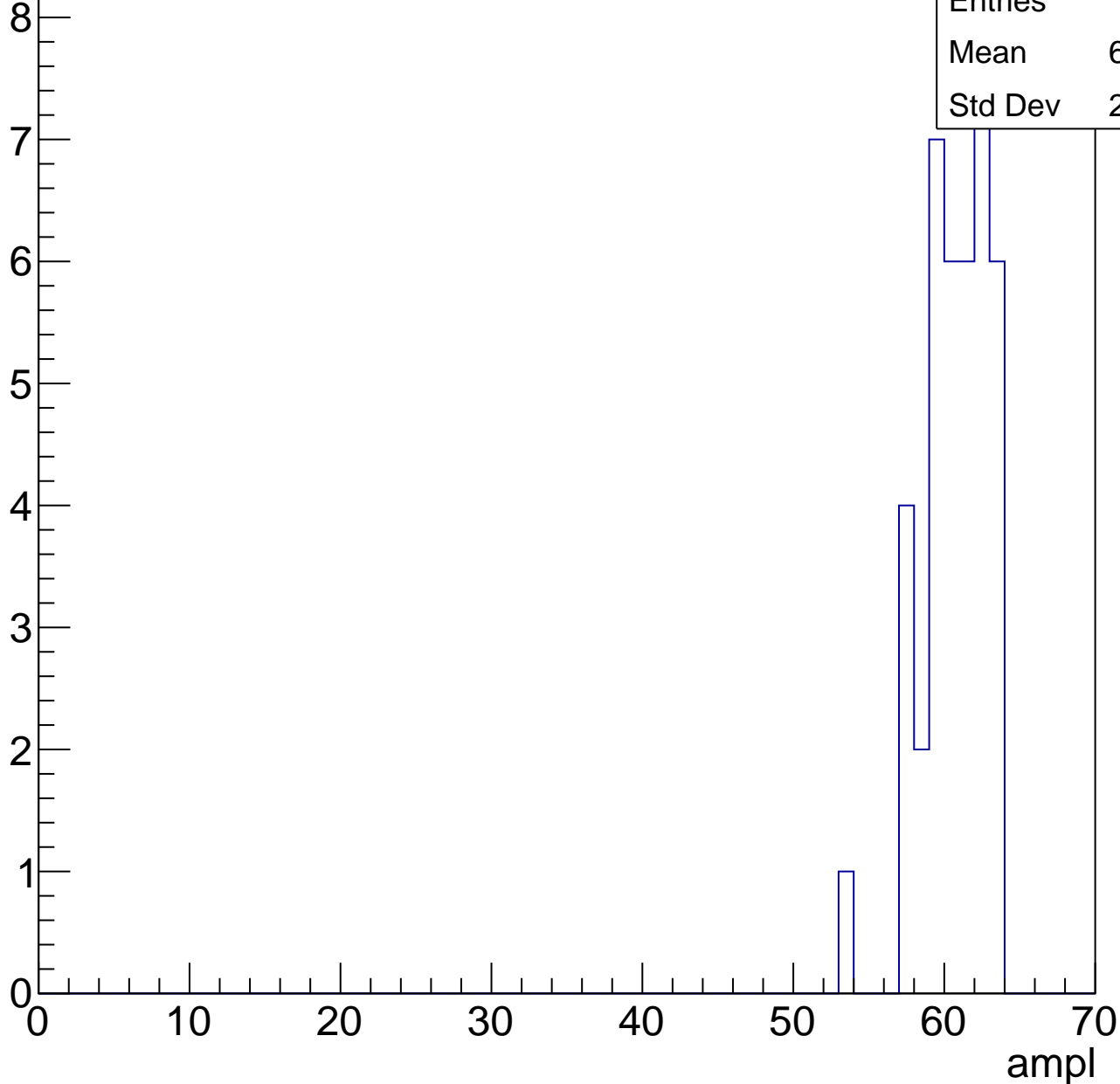
Entries	67
Mean	55.61
Std Dev	3.319

B1L103S, U24-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

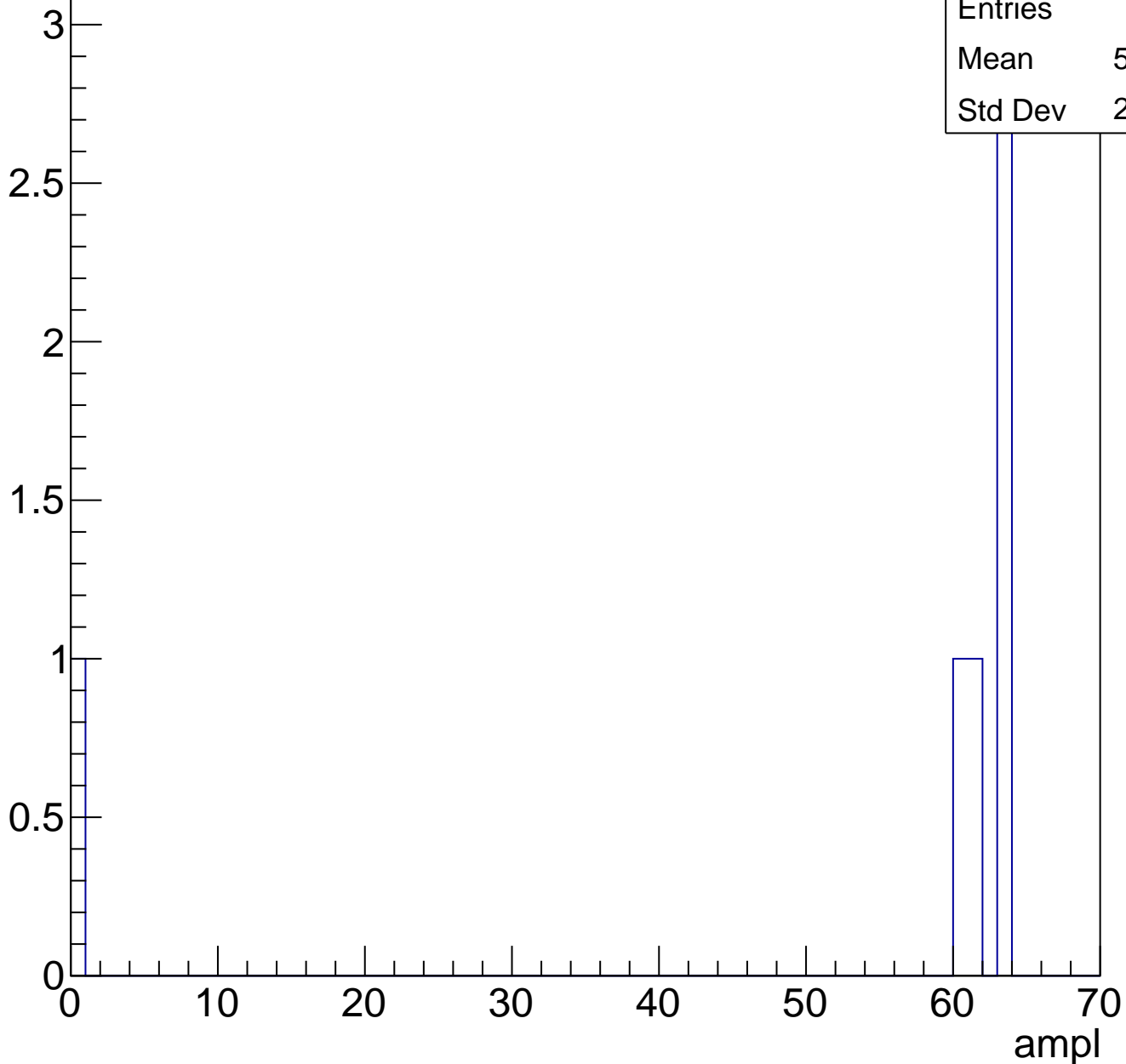
Entries	40
Mean	60.25
Std Dev	2.177



B1L103S, U24-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch64, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	28.63
Std Dev	7.051

Gaus mean : 30.7279

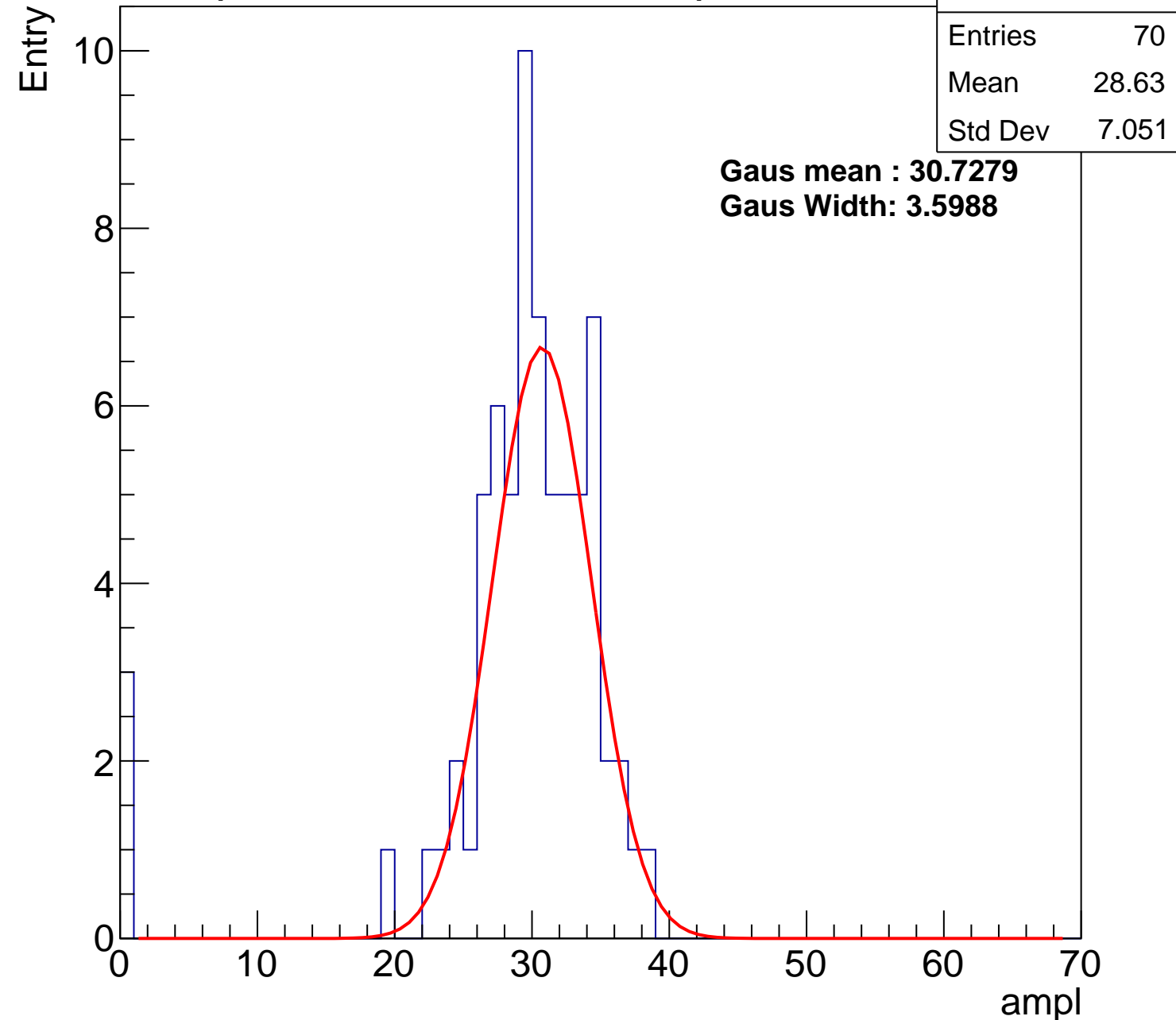
Gaus Width: 3.5988

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch64, adc1

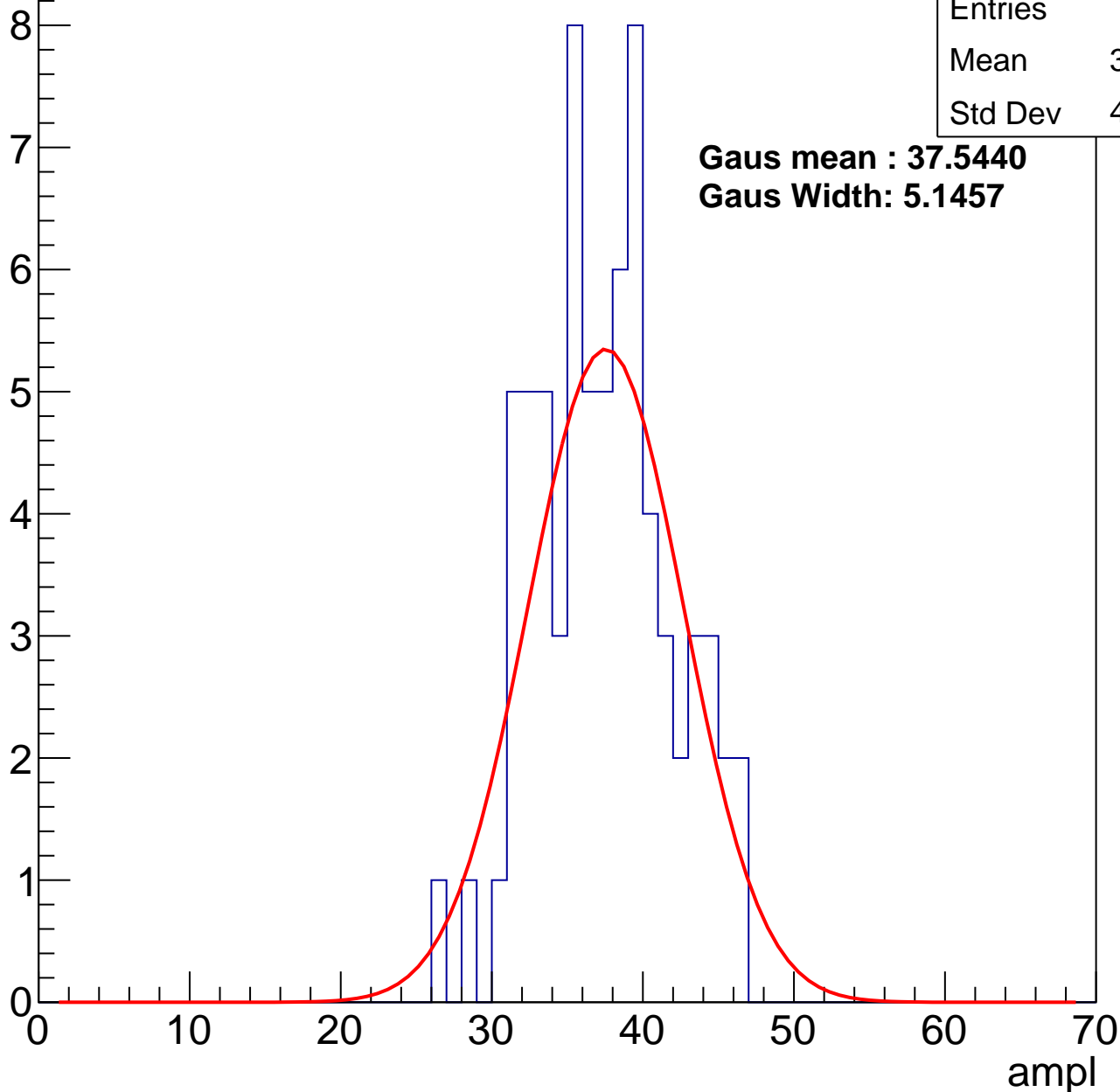
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	36.96
Std Dev	4.436

Gaus mean : 37.5440

Gaus Width: 5.1457



B1L103S, U24-ch64, adc2

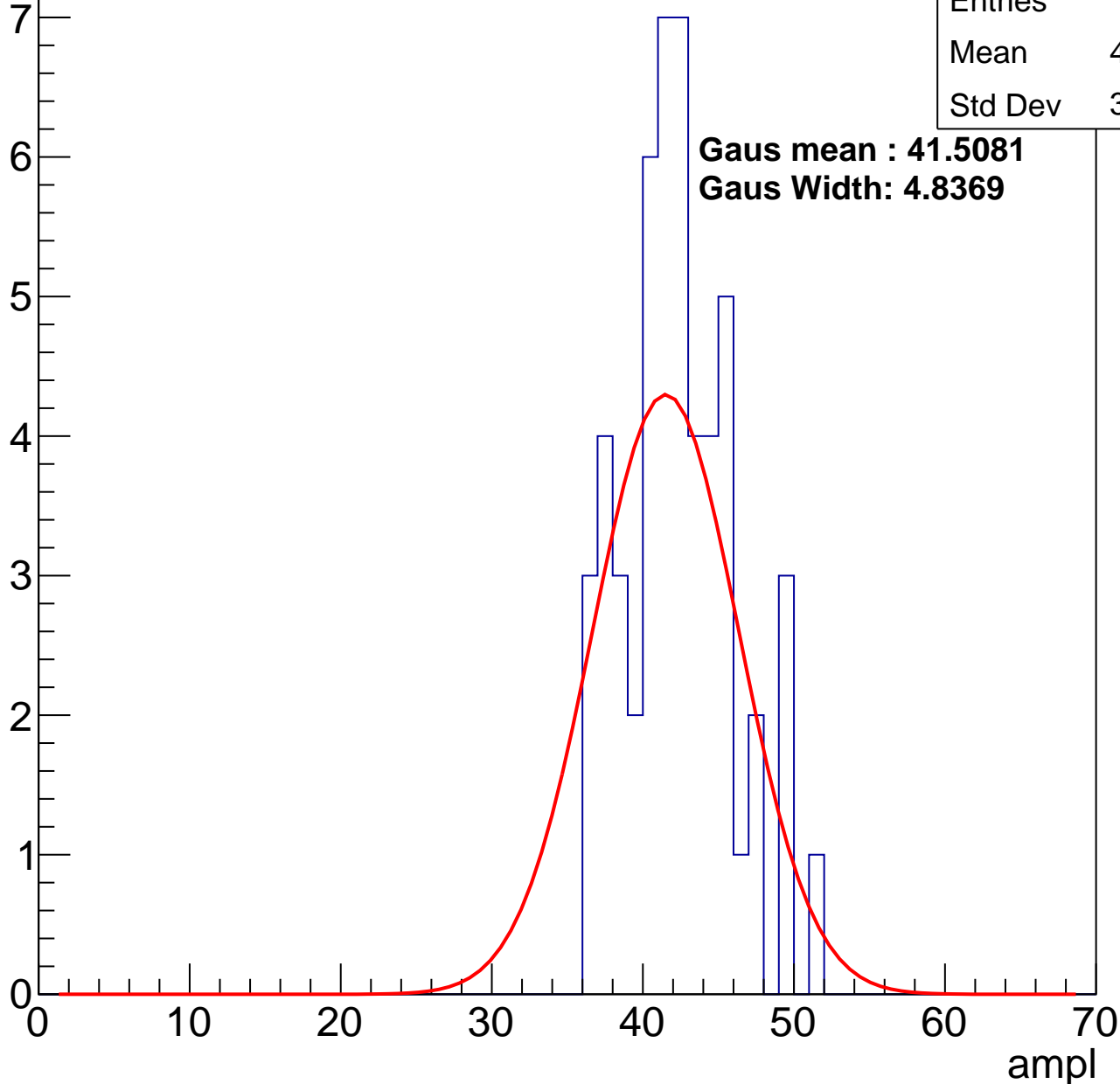
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.92
Std Dev	3.573

Gaus mean : 41.5081

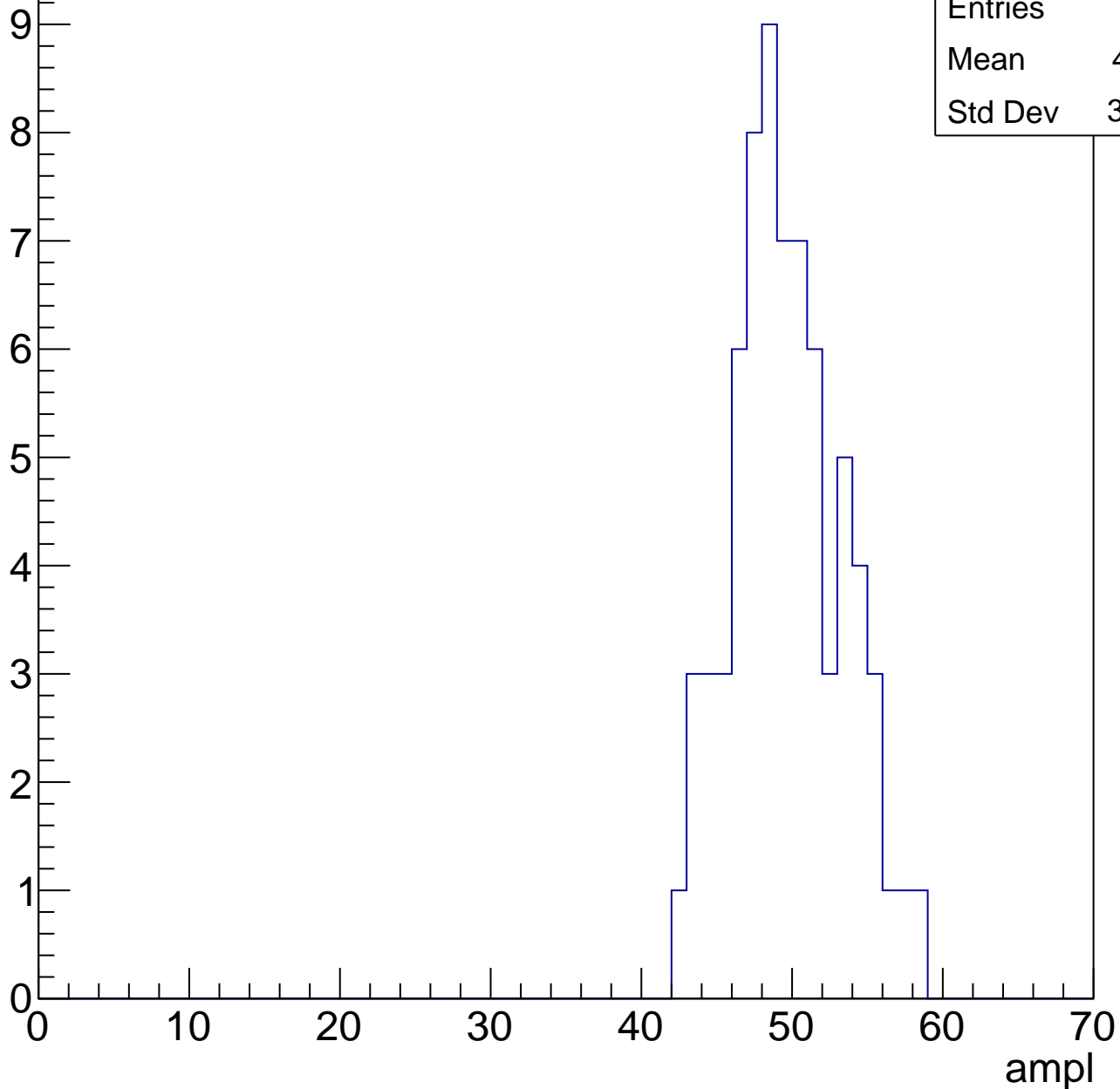
Gaus Width: 4.8369



B1L103S, U24-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



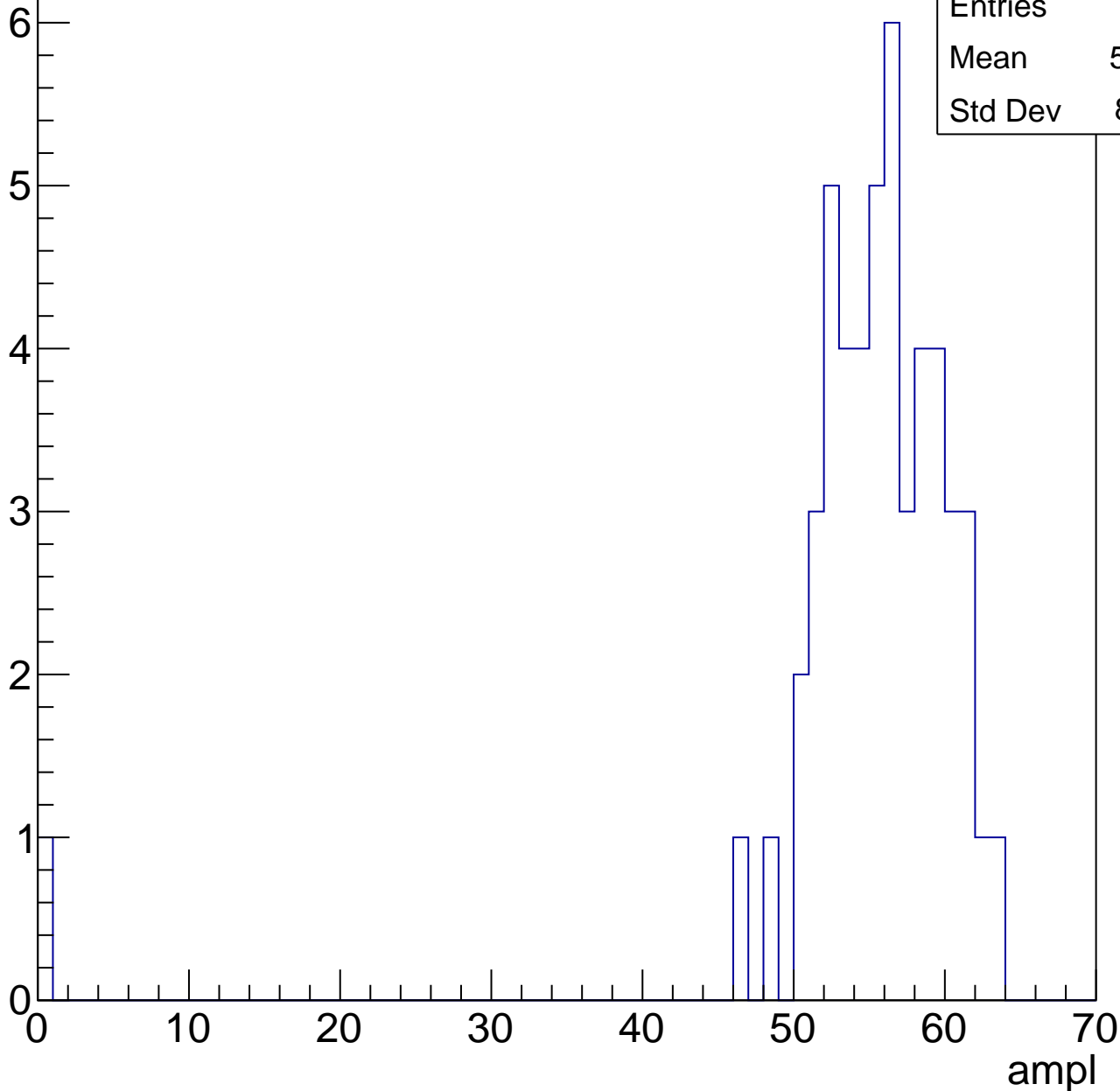
Entries	71
Mean	49.21
Std Dev	3.584

B1L103S, U24-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.37
Std Dev	8.531

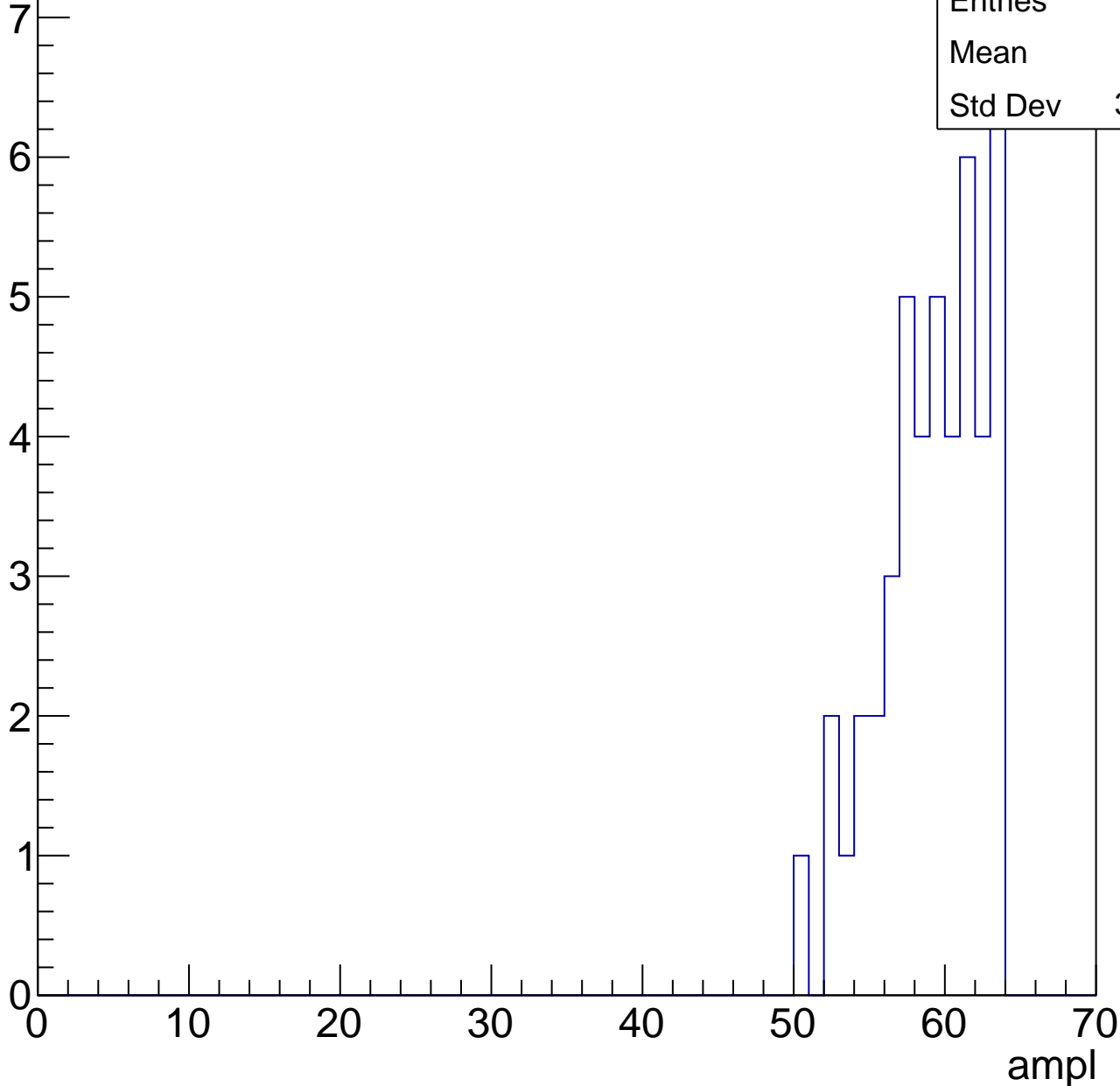


B1L103S, U24-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.7
Std Dev	3.361

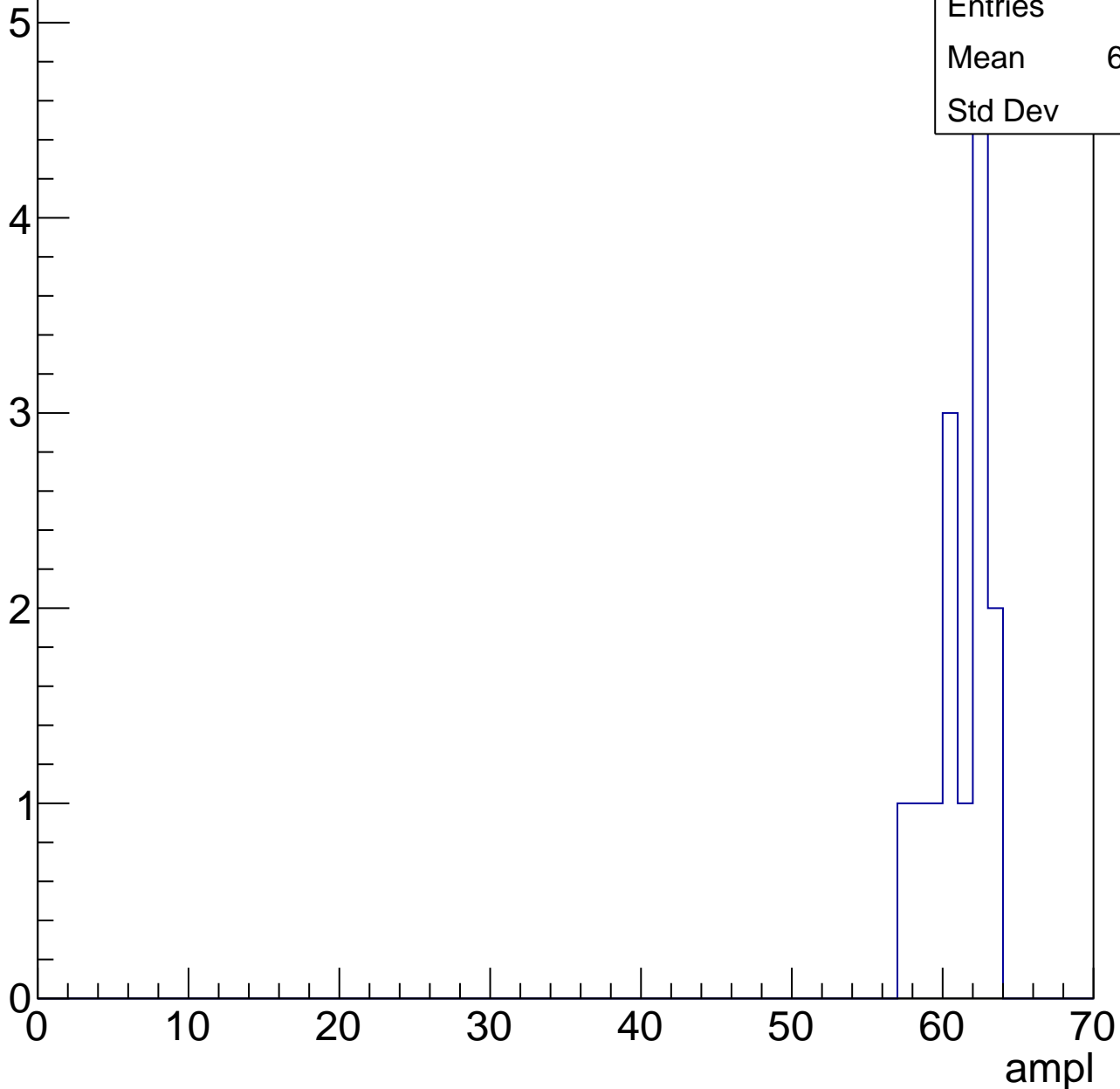


B1L103S, U24-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	60.79
Std Dev	1.78



B1L103S, U24-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch65, adc0

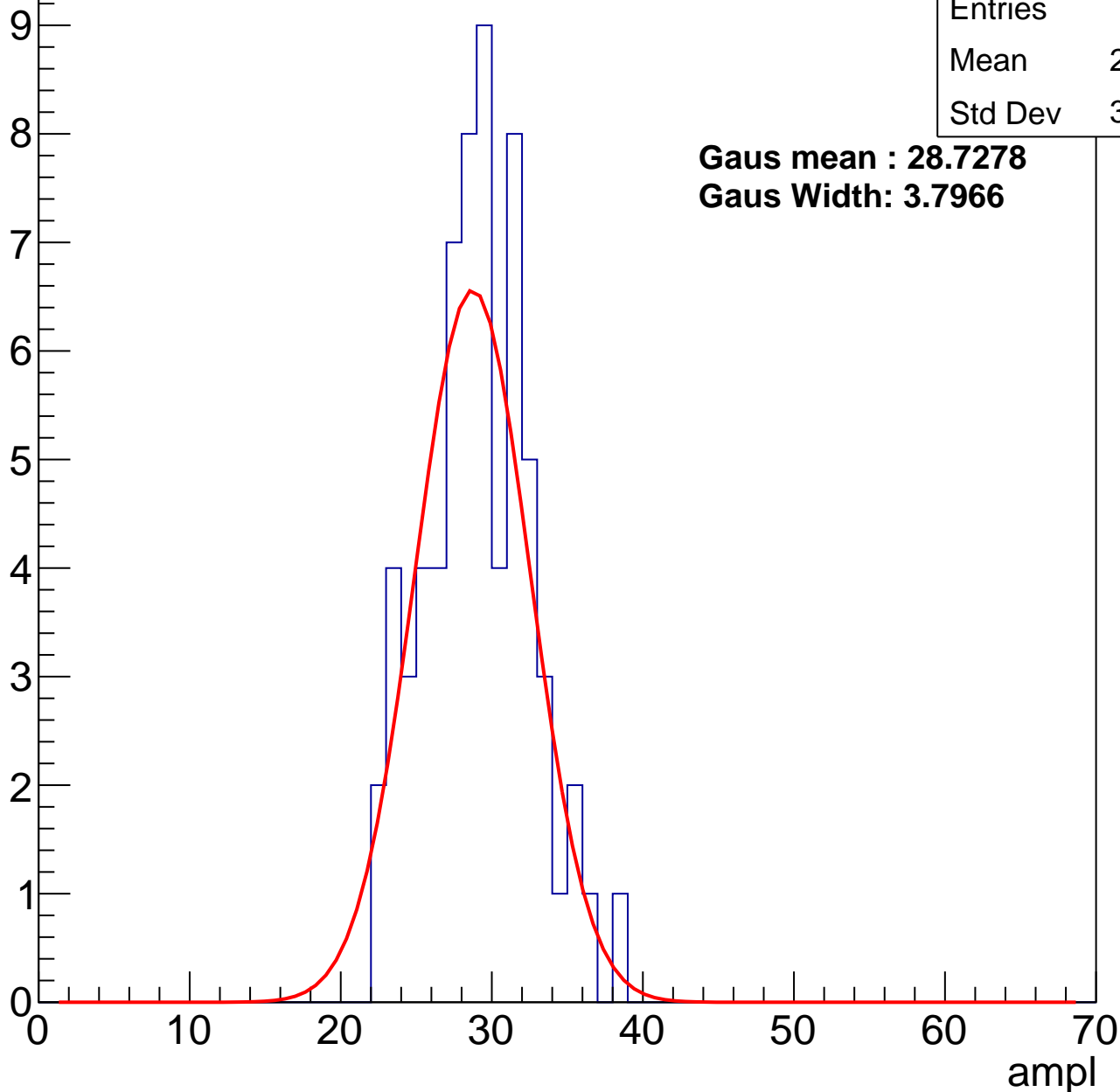
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.65
Std Dev	3.484

Gaus mean : 28.7278

Gaus Width: 3.7966



B1L103S, U24-ch65, adc1

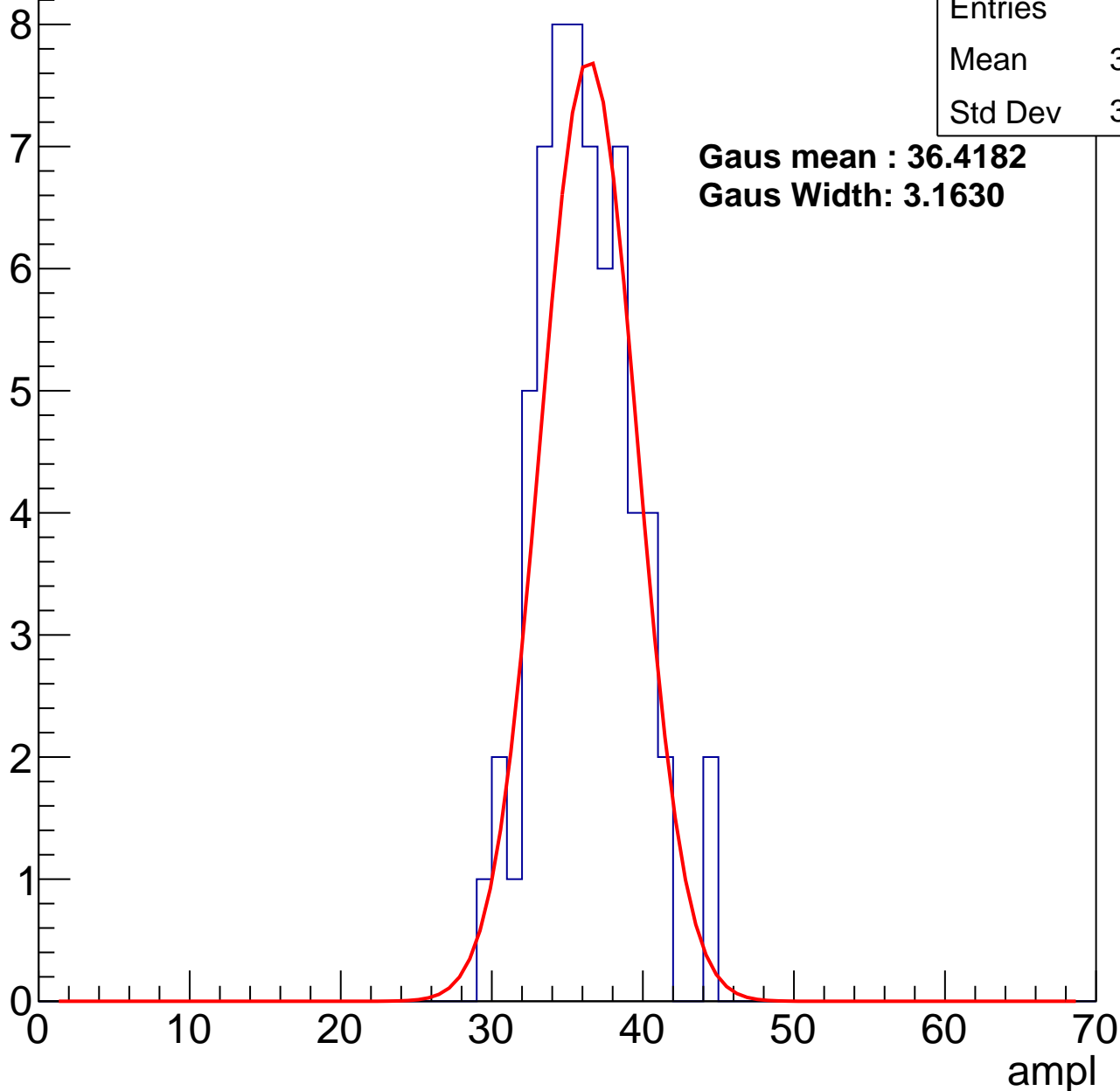
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.77
Std Dev	3.156

Gaus mean : 36.4182

Gaus Width: 3.1630



B1L103S, U24-ch65, adc2

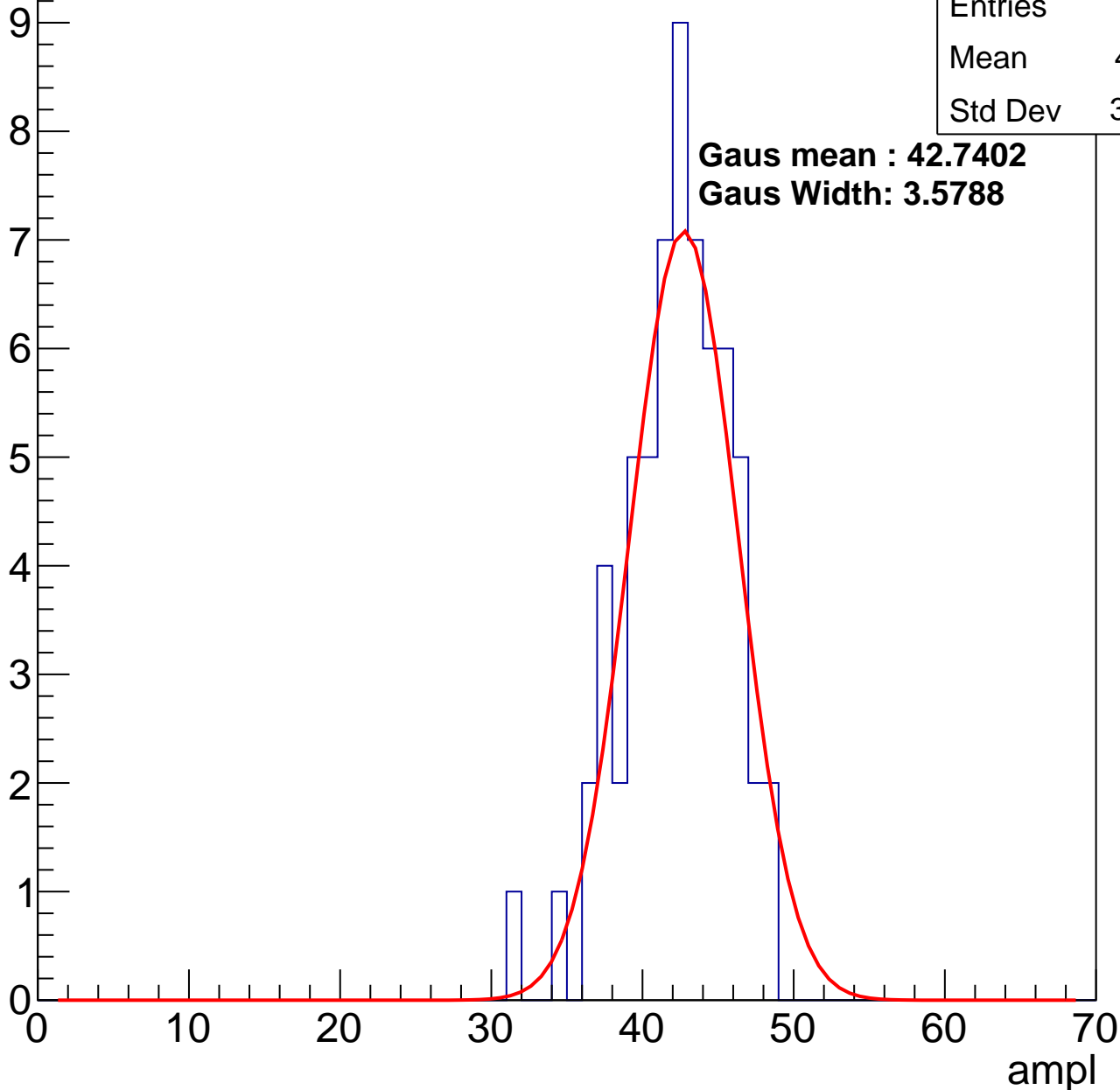
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.81
Std Dev	3.414

Gaus mean : 42.7402

Gaus Width: 3.5788

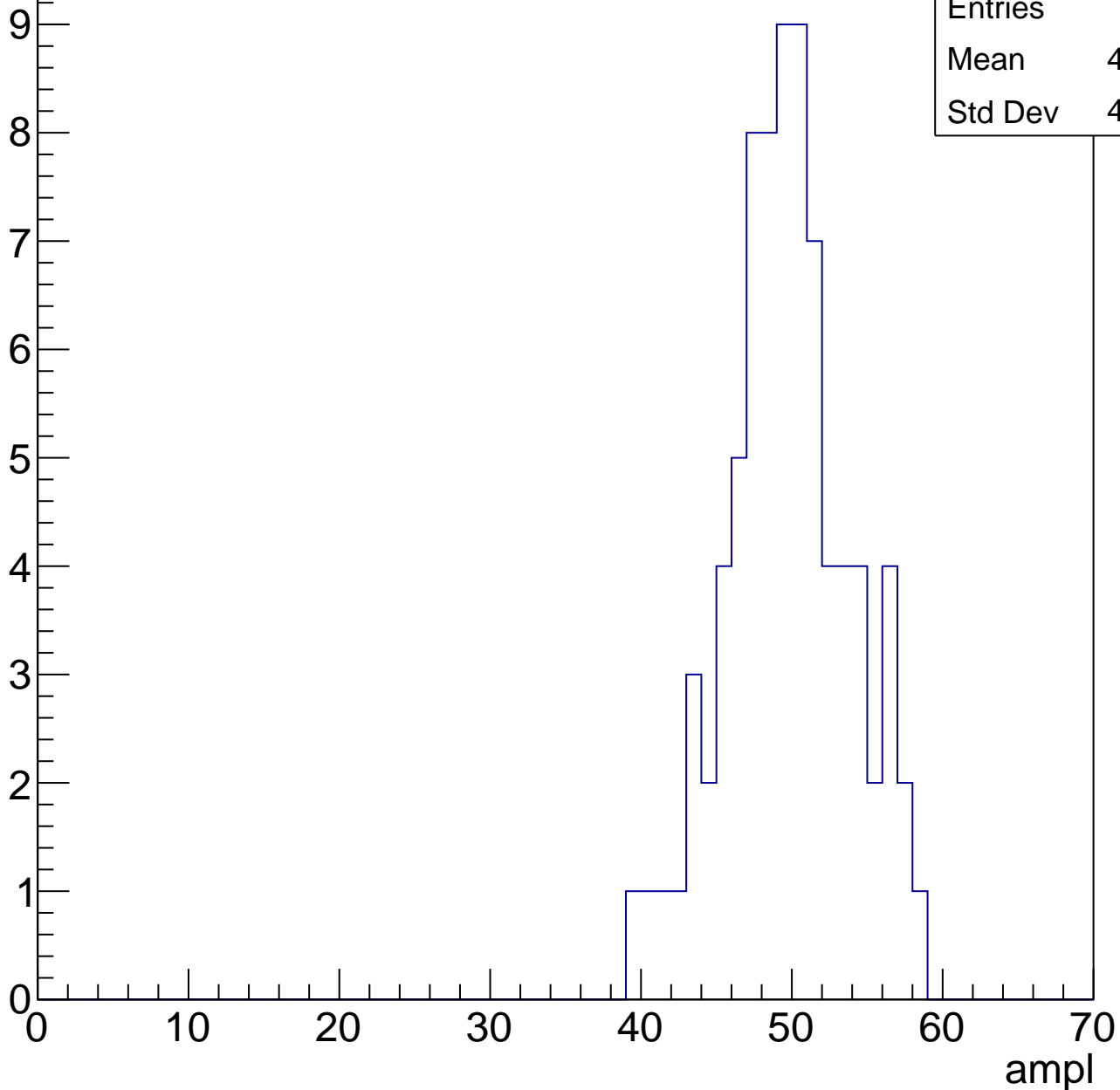


B1L103S, U24-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	49.24
Std Dev	4.047

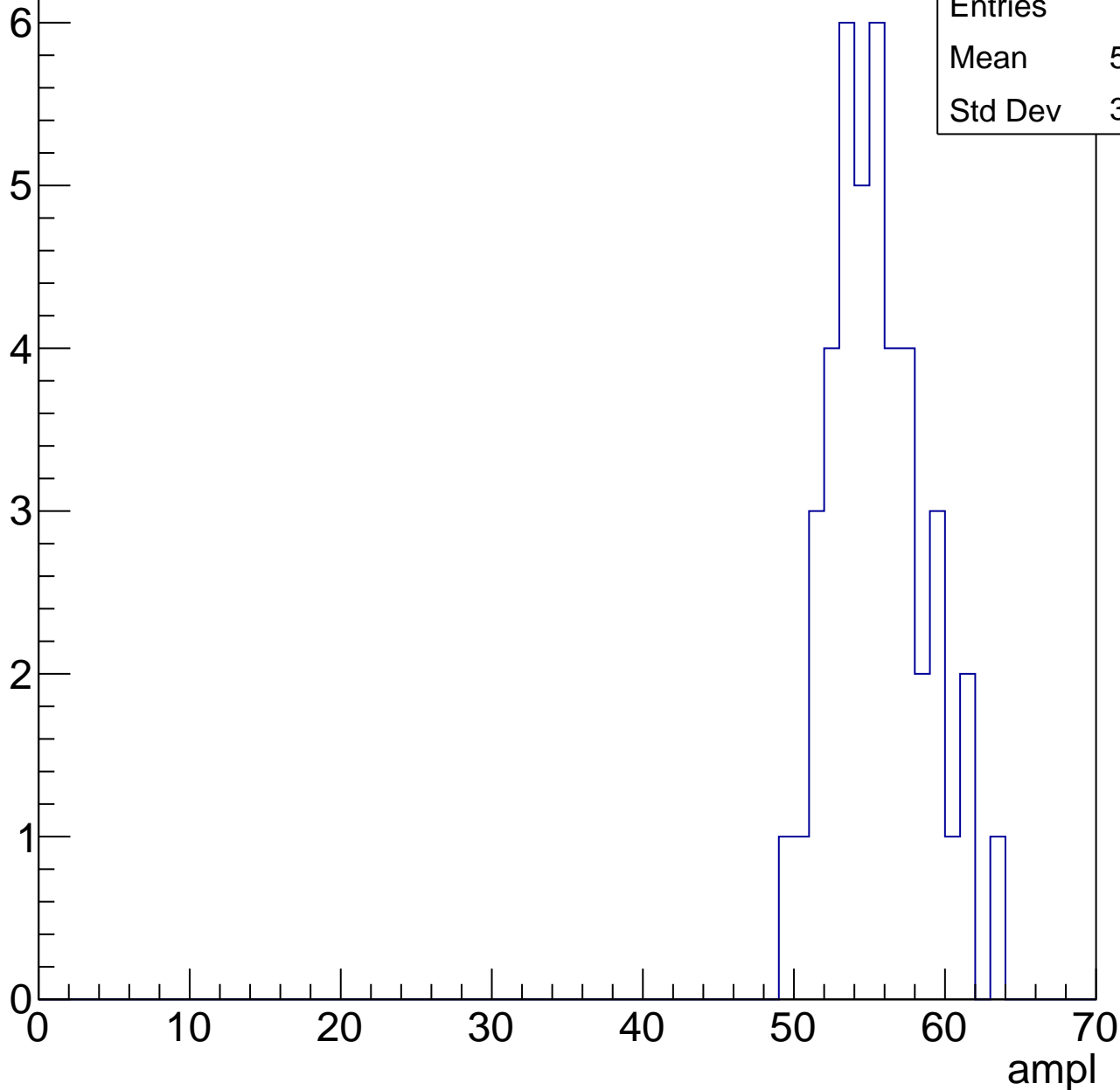


B1L103S, U24-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	55.07
Std Dev	3.128

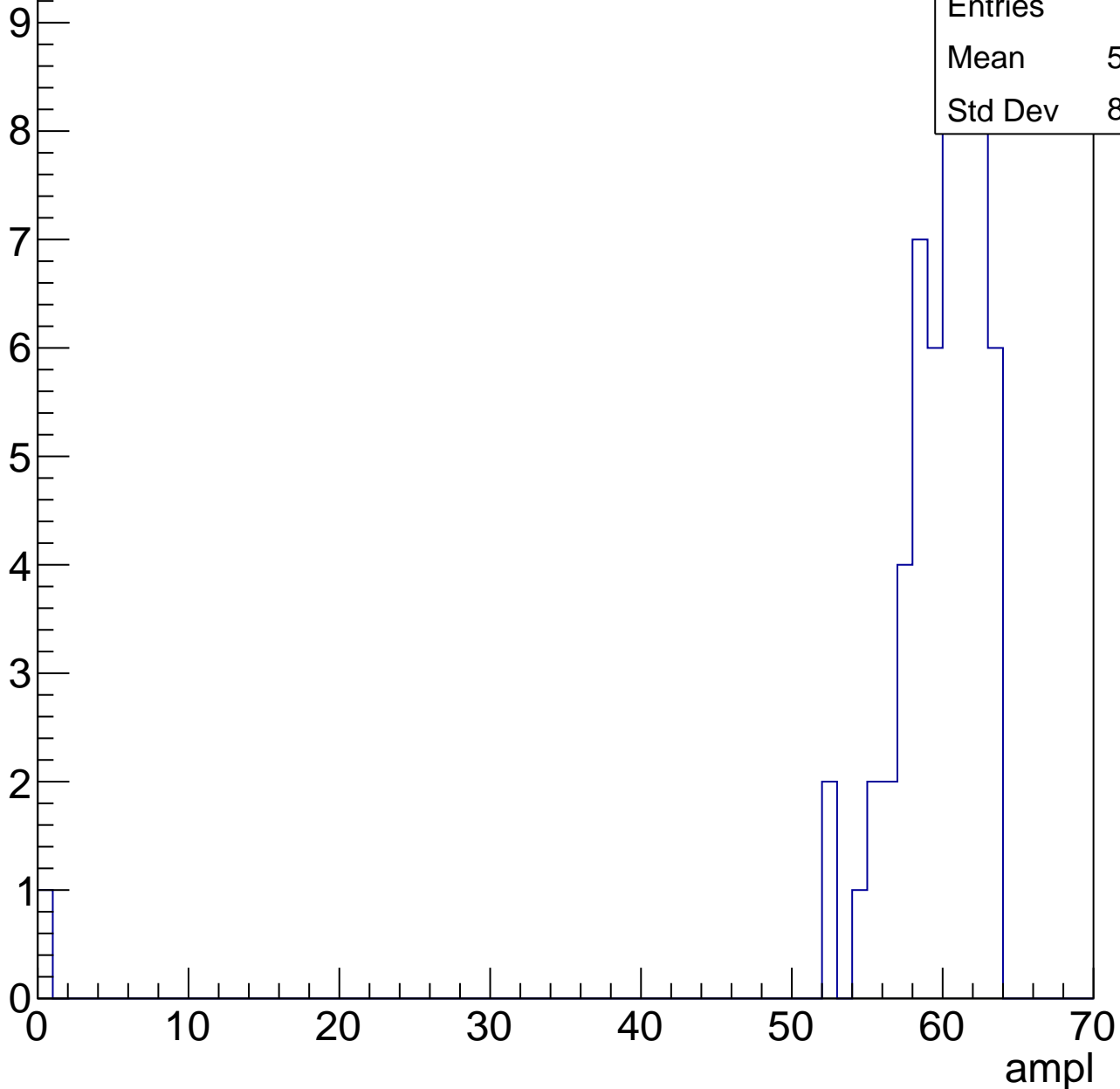


B1L103S, U24-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	58.39
Std Dev	8.308



B1L103S, U24-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch66, adc0

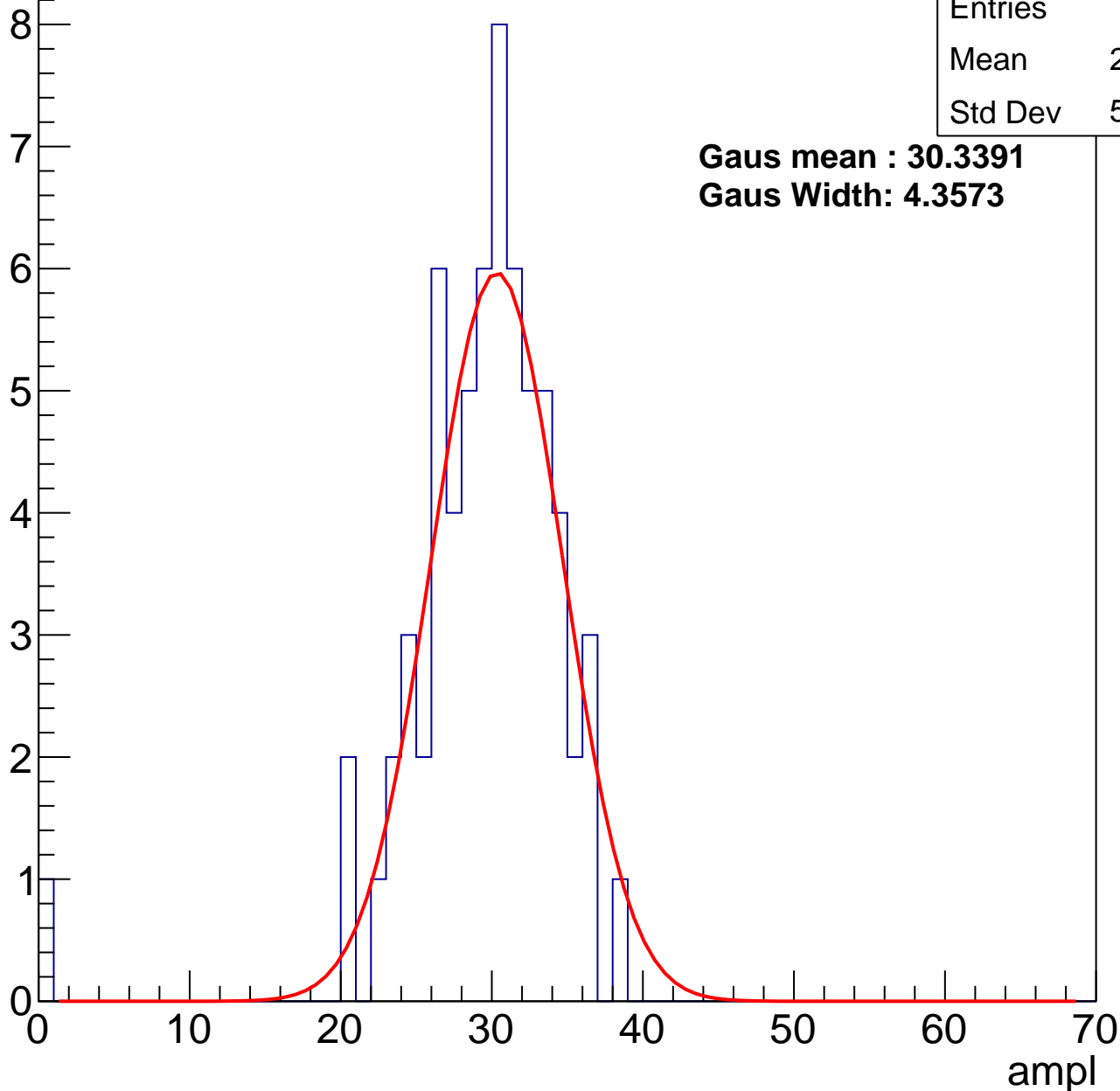
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.95
Std Dev	5.313

Gaus mean : 30.3391

Gaus Width: 4.3573



B1L103S, U24-ch66, adc1

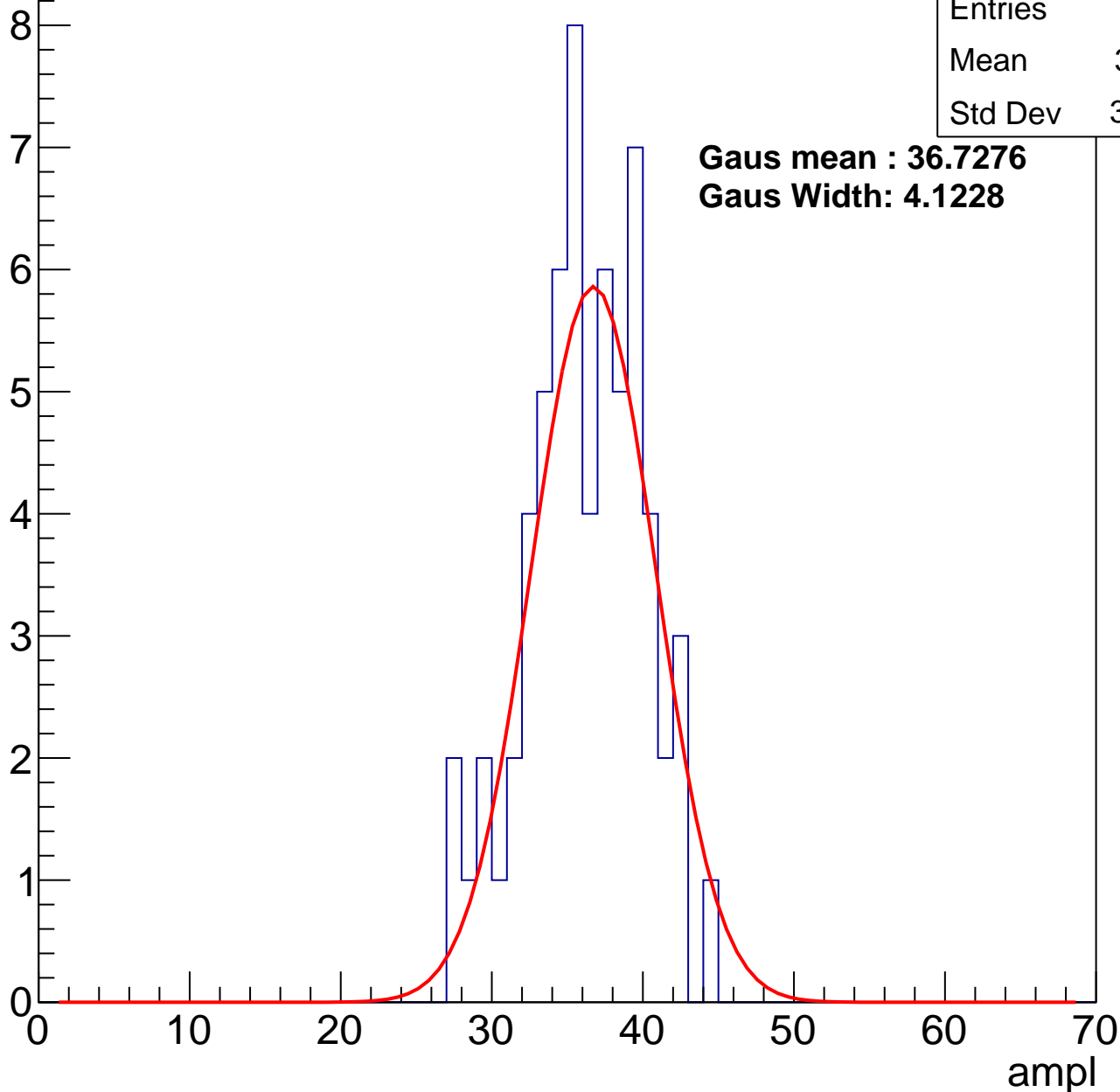
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.71
Std Dev	3.823

Gaus mean : 36.7276

Gaus Width: 4.1228



B1L103S, U24-ch66, adc2

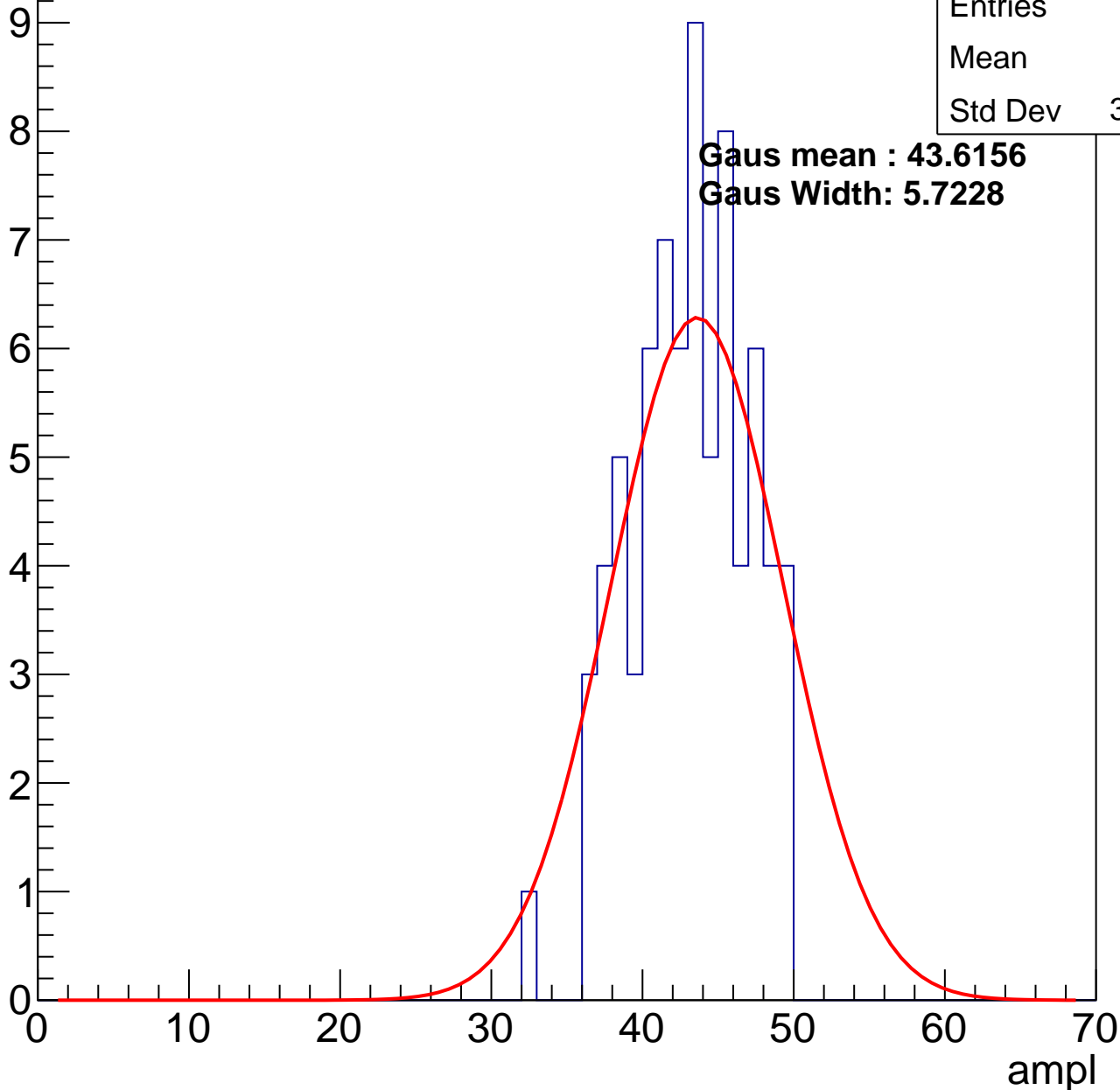
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.6
Std Dev	3.784

Gaus mean : 43.6156

Gaus Width: 5.7228

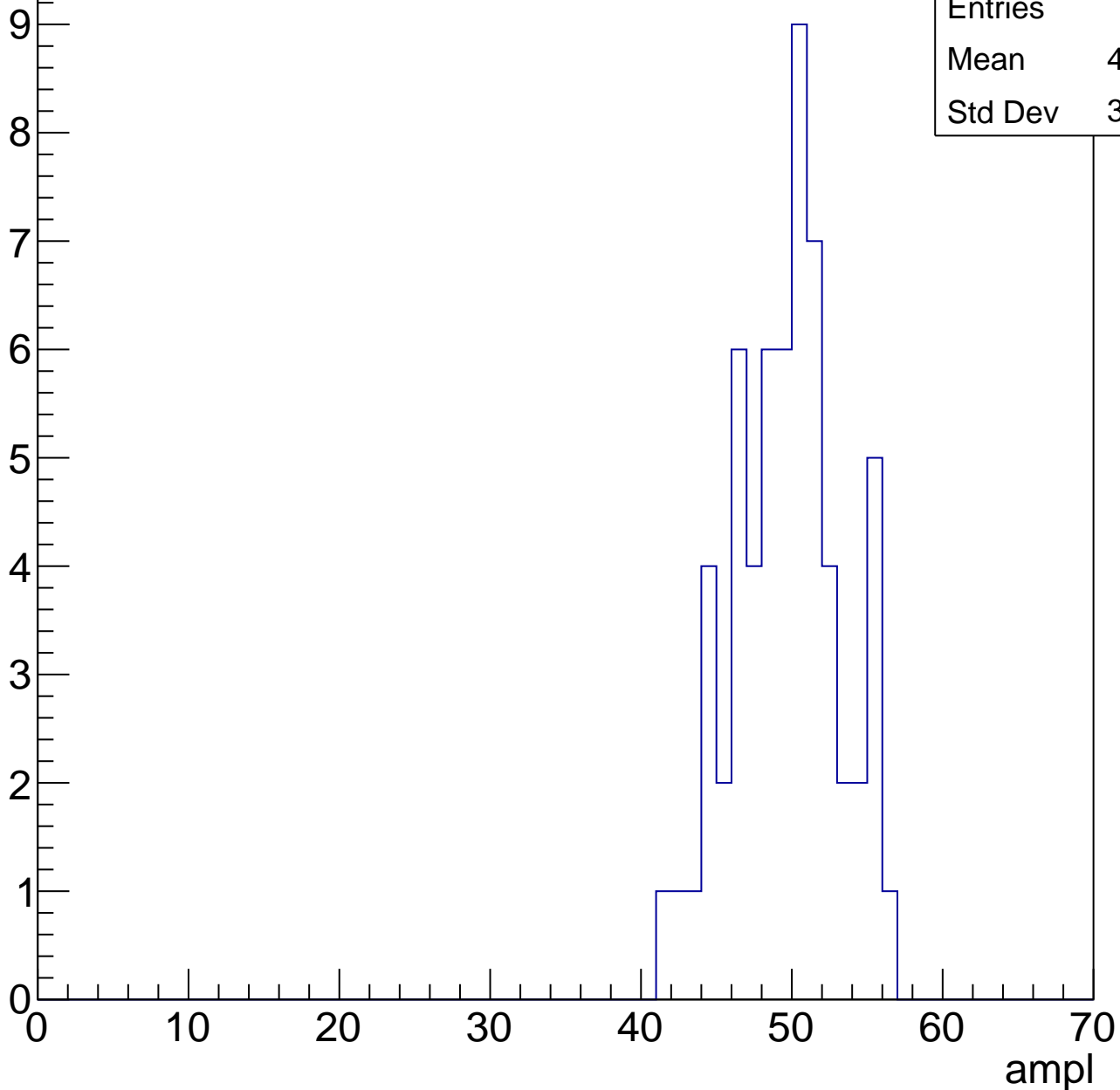


B1L103S, U24-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.15
Std Dev	3.482

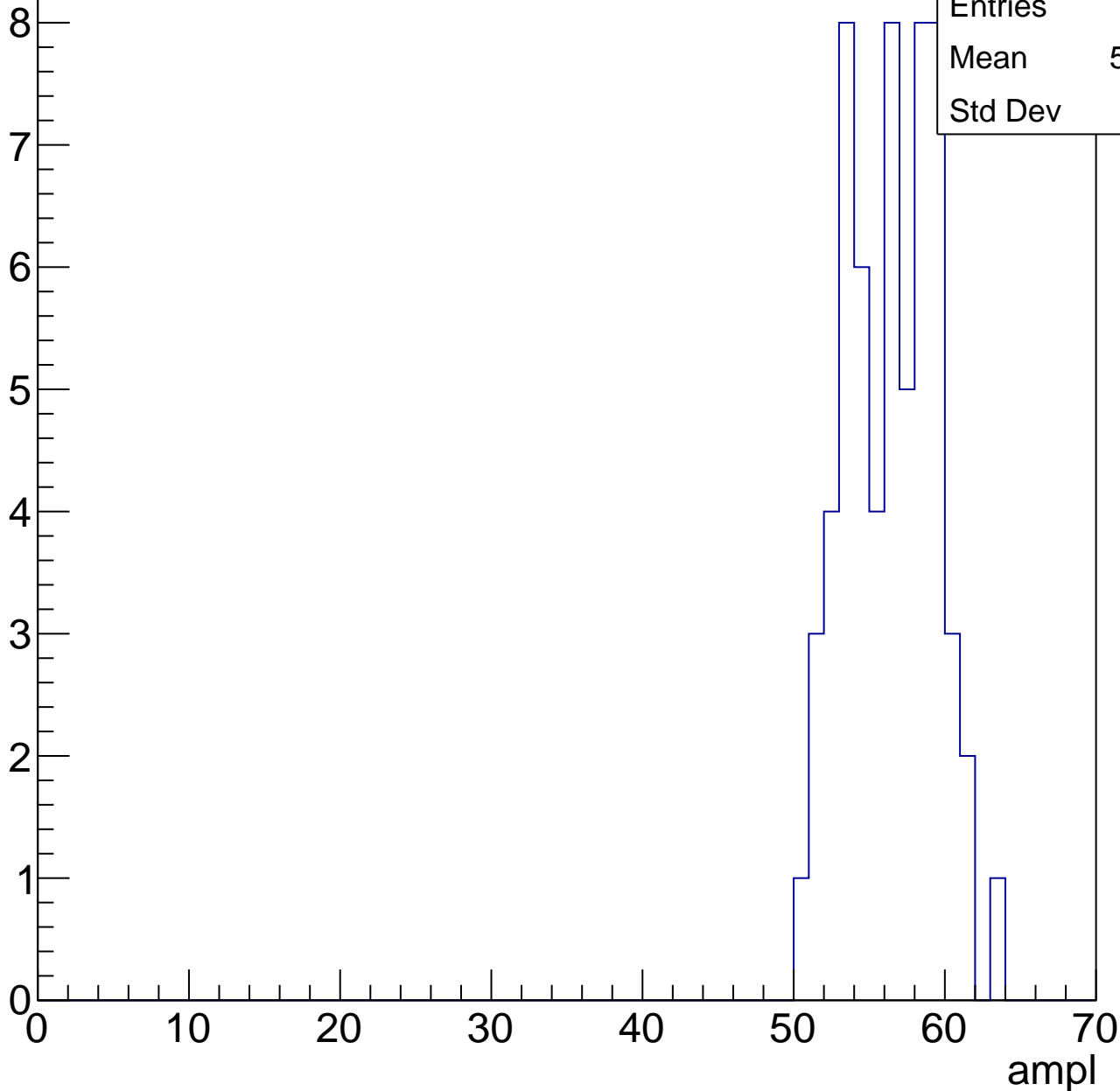


B1L103S, U24-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.95
Std Dev	2.95

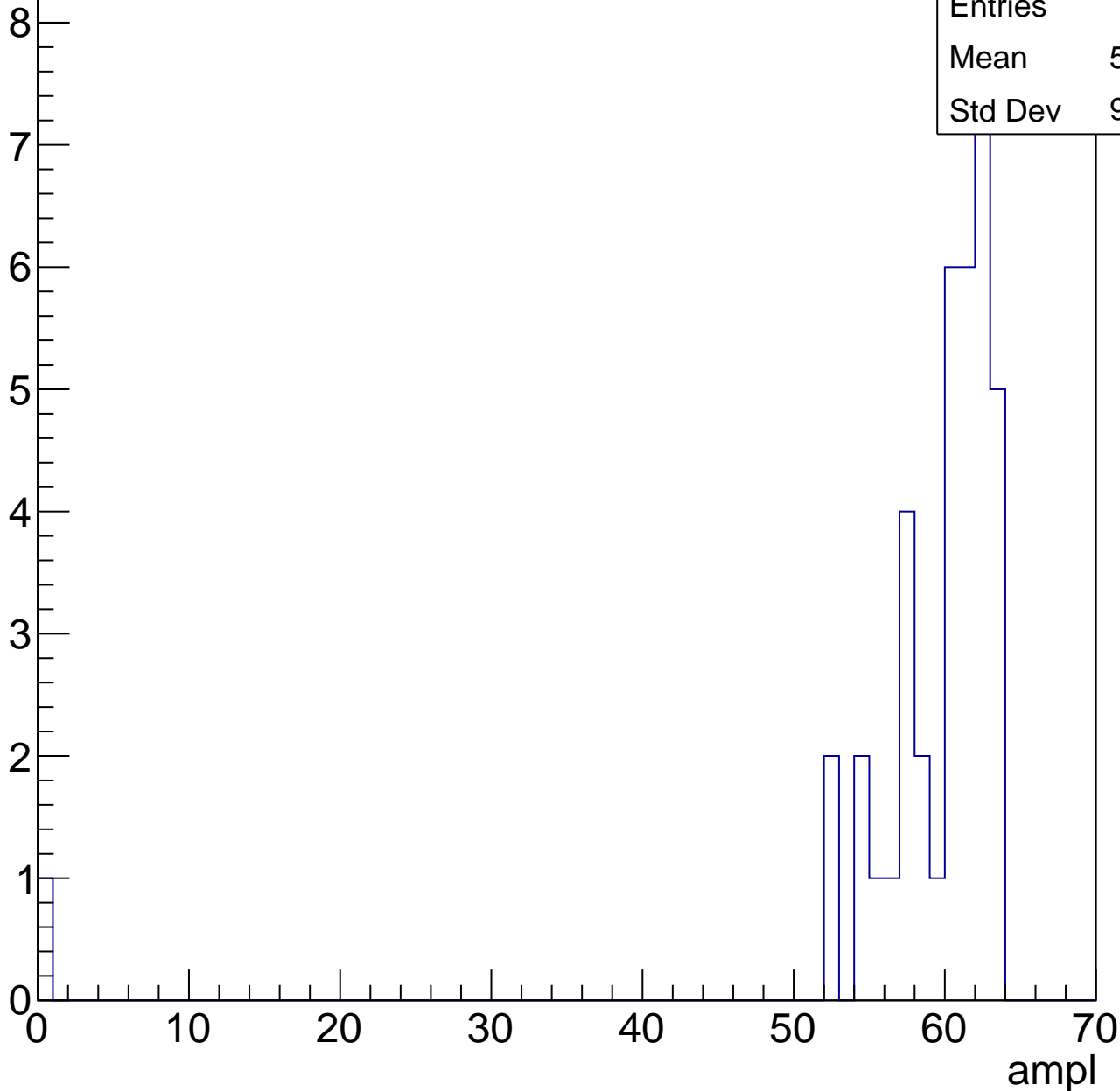


B1L103S, U24-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

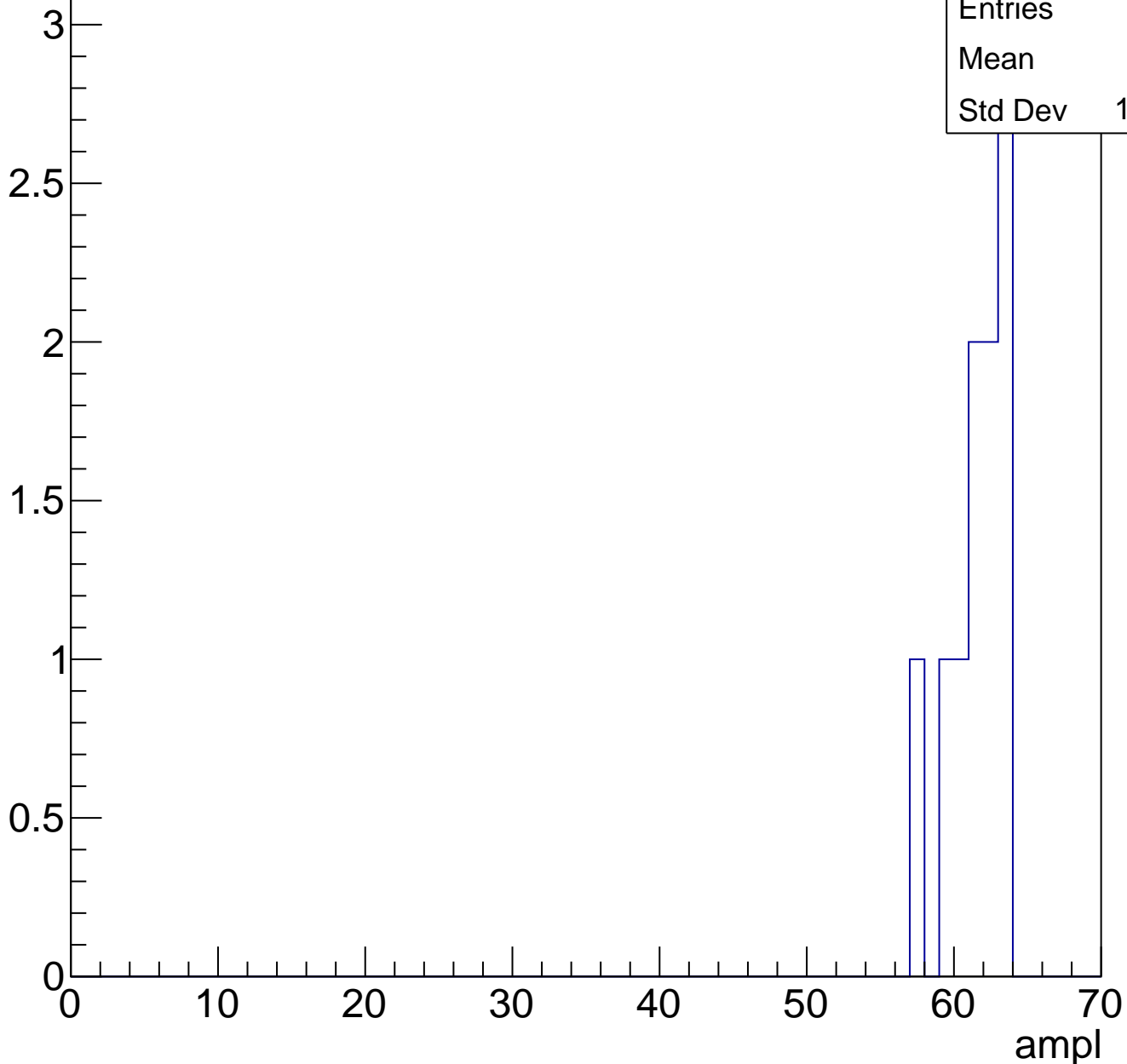
Entries	39
Mean	58.03
Std Dev	9.893



B1L103S, U24-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.1
Std Dev	1.868

B1L103S, U24-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch67, adc0

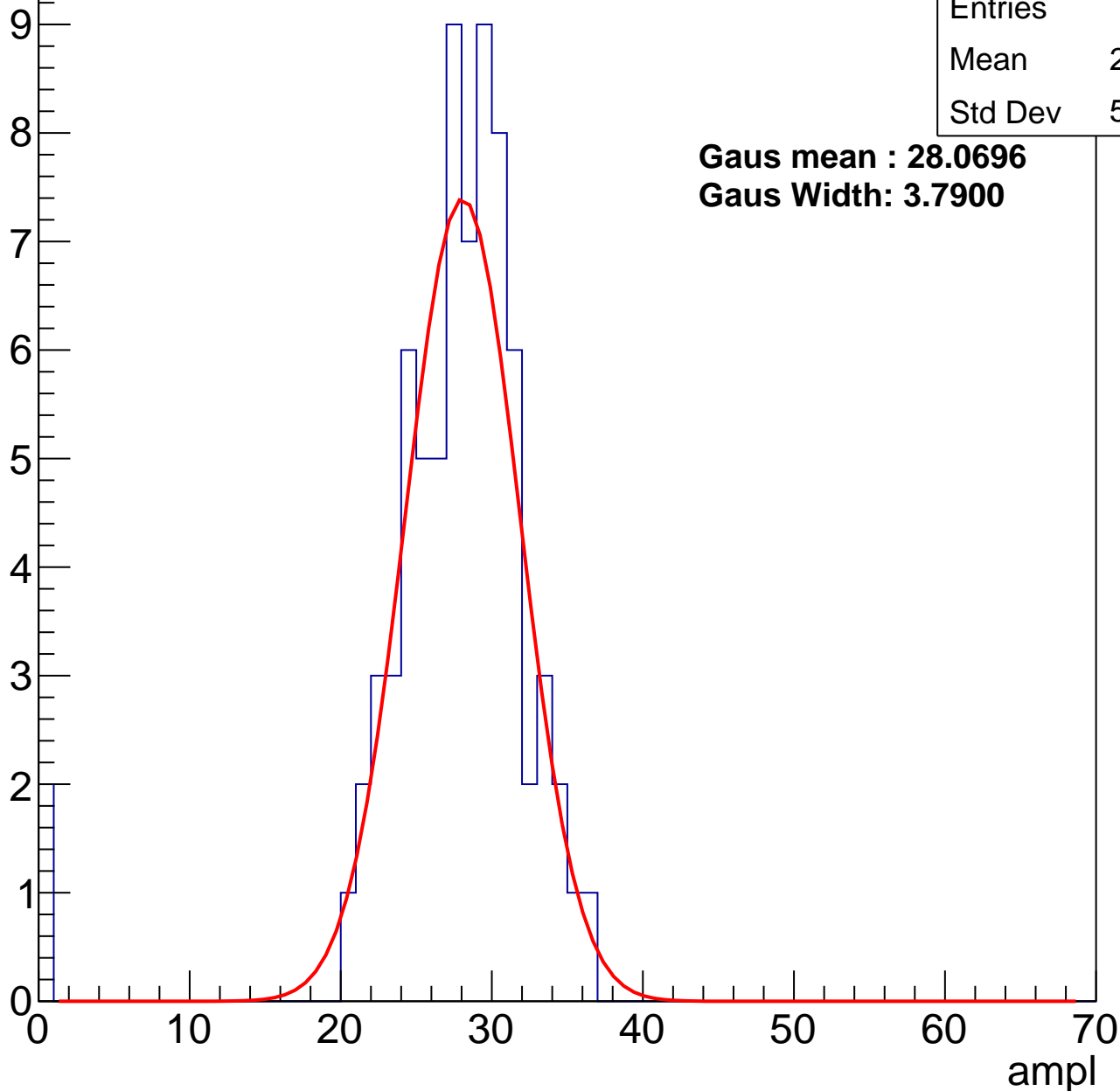
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	26.99
Std Dev	5.649

Gaus mean : 28.0696

Gaus Width: 3.7900



B1L103S, U24-ch67, adc1

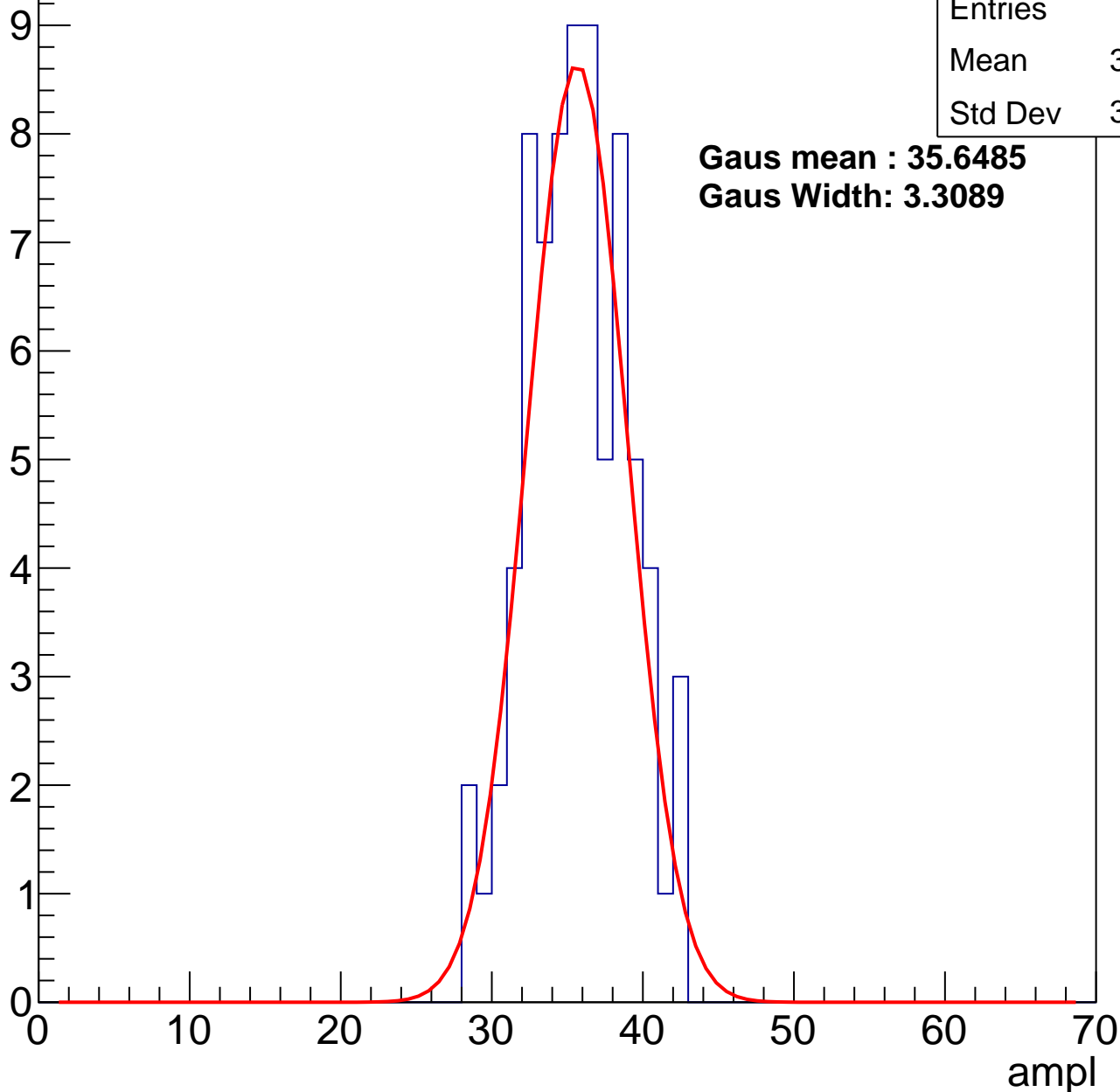
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.24
Std Dev	3.276

Gaus mean : 35.6485

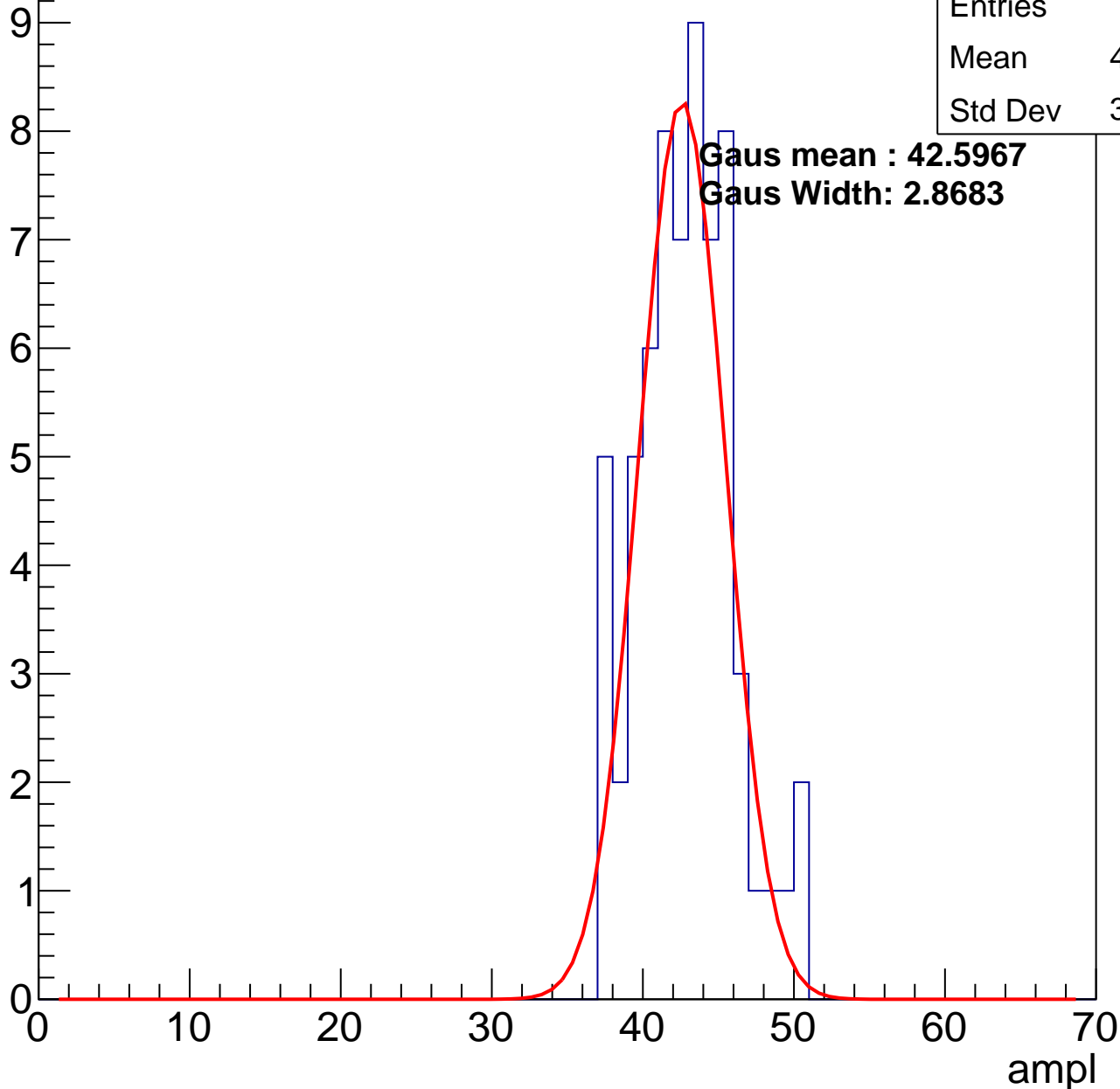
Gaus Width: 3.3089



B1L103S, U24-ch67, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

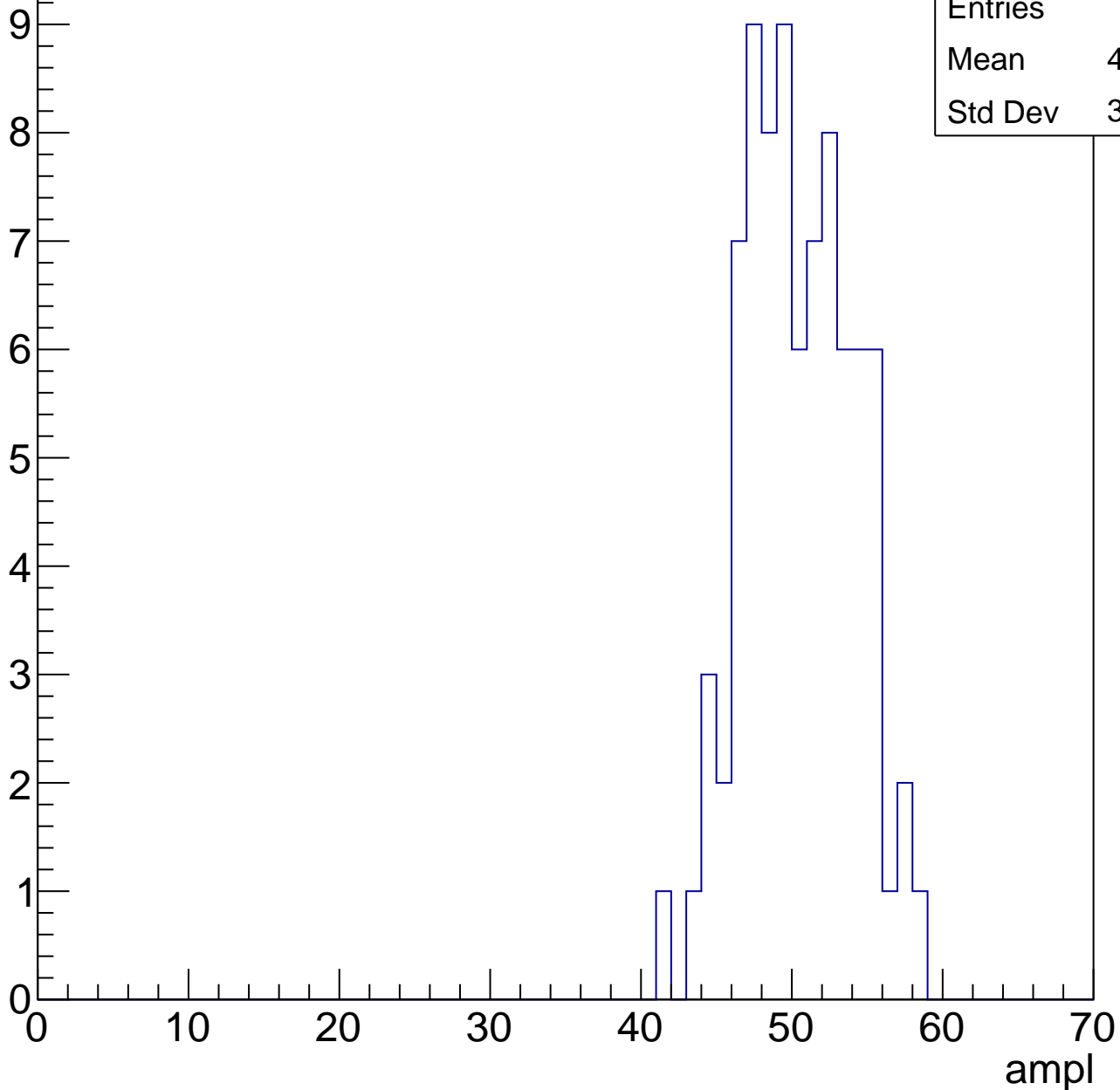


B1L103S, U24-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	49.99
Std Dev	3.572

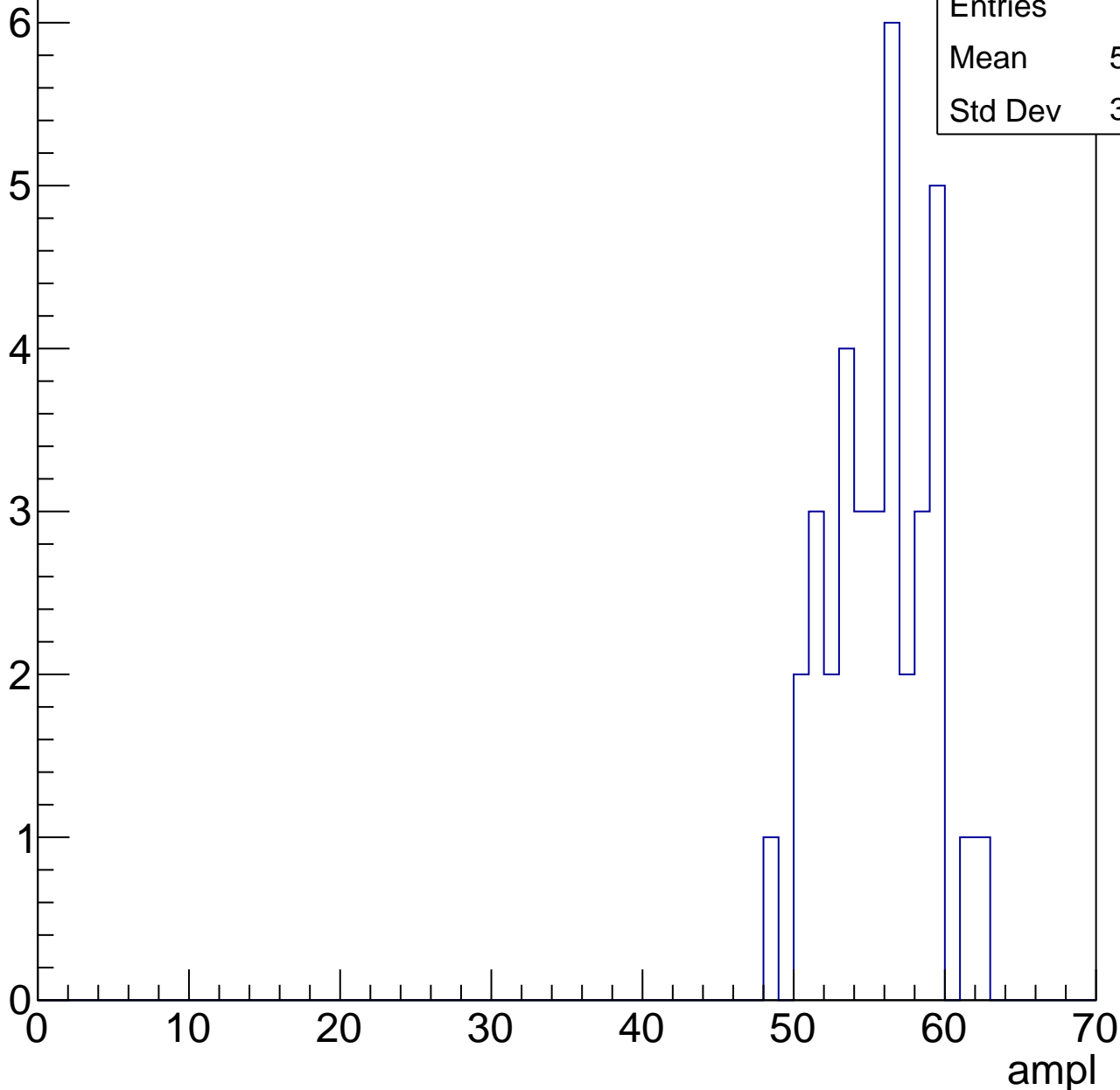


B1L103S, U24-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

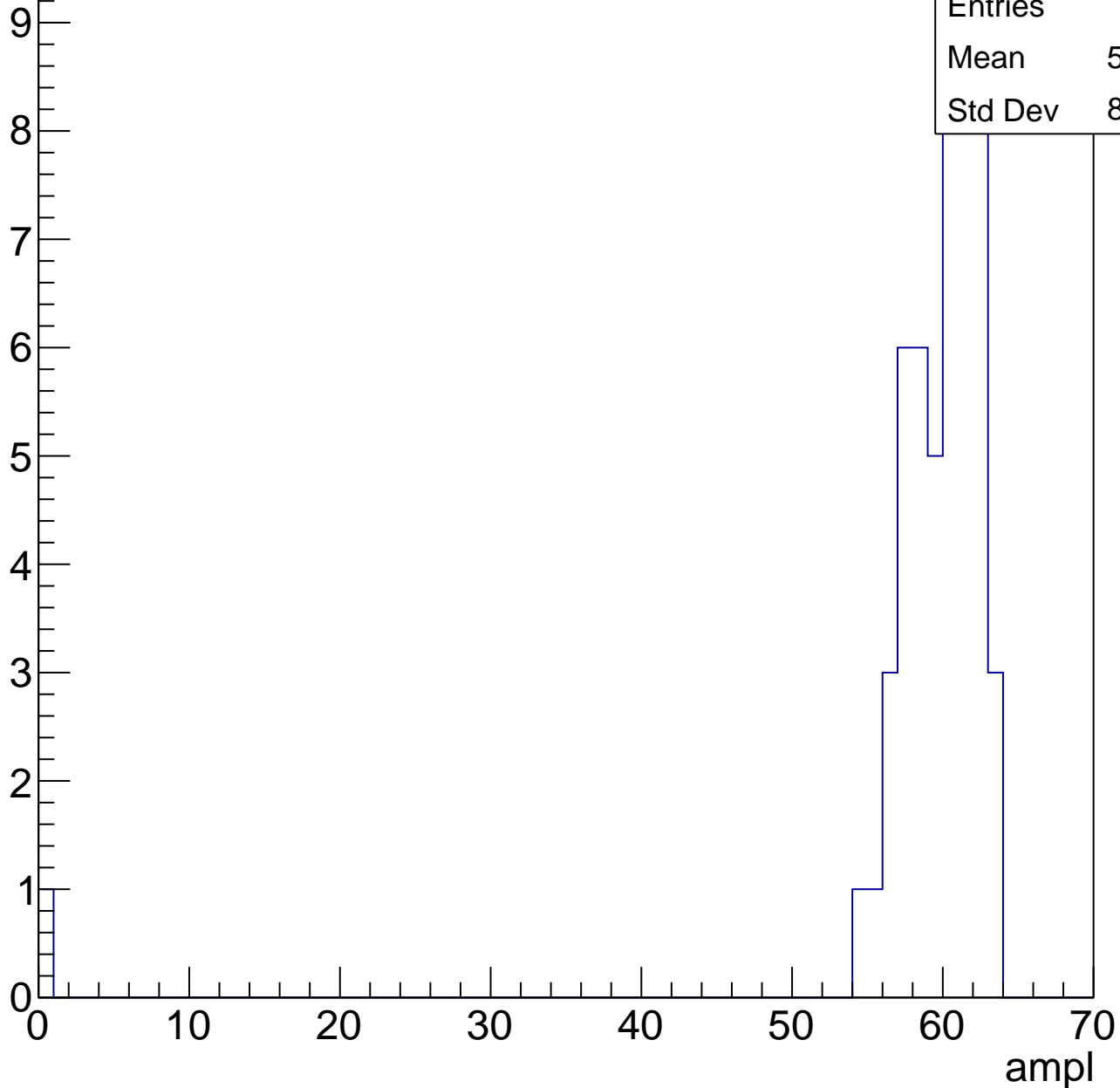
Entries	36
Mean	55.17
Std Dev	3.287



B1L103S, U24-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

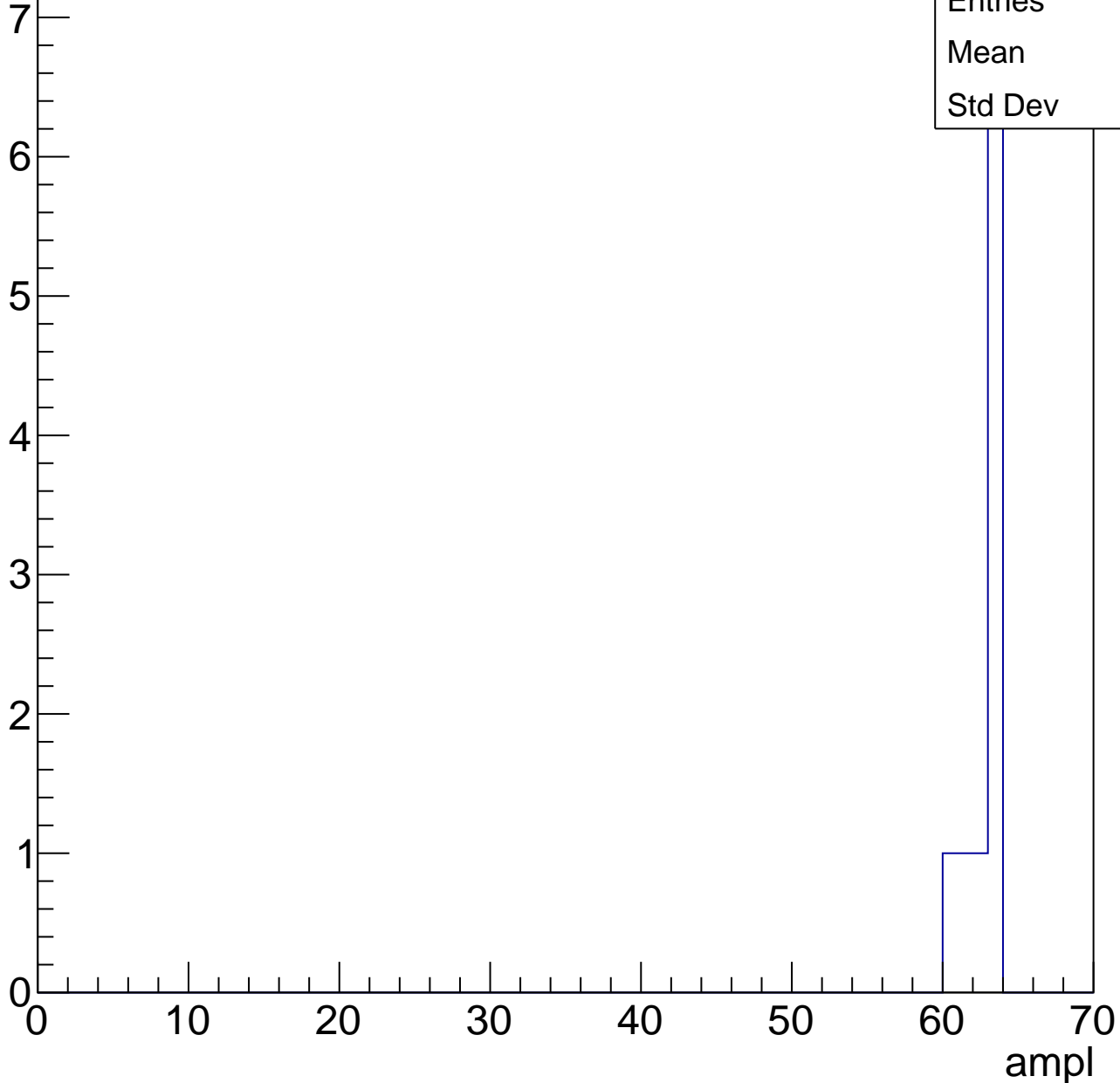


B1L103S, U24-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

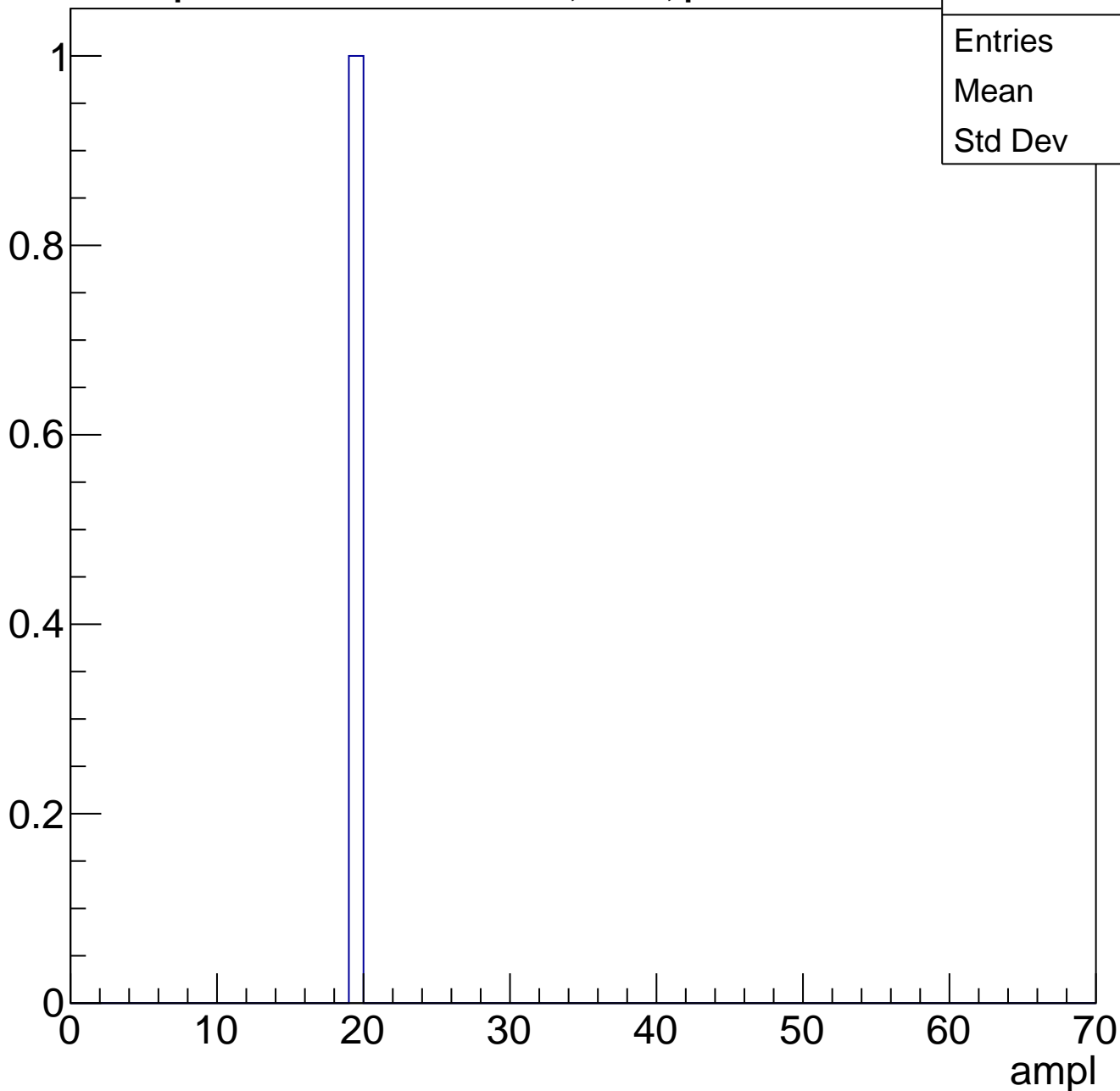
Entries	10
Mean	62.4
Std Dev	1.02



B1L103S, U24-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch68, adc0

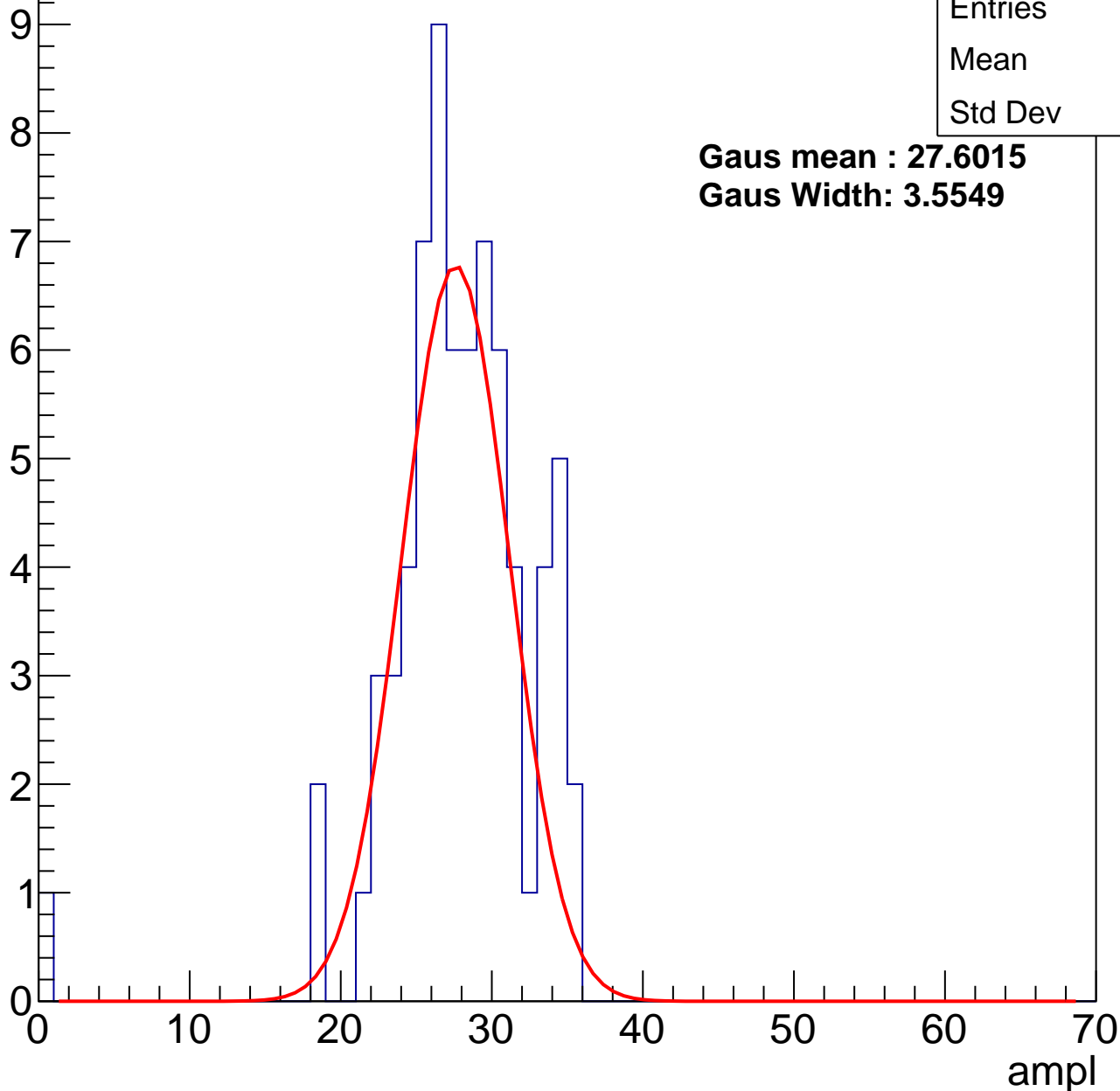
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	27.3
Std Dev	5.05

Gaus mean : 27.6015

Gaus Width: 3.5549



B1L103S, U24-ch68, adc1

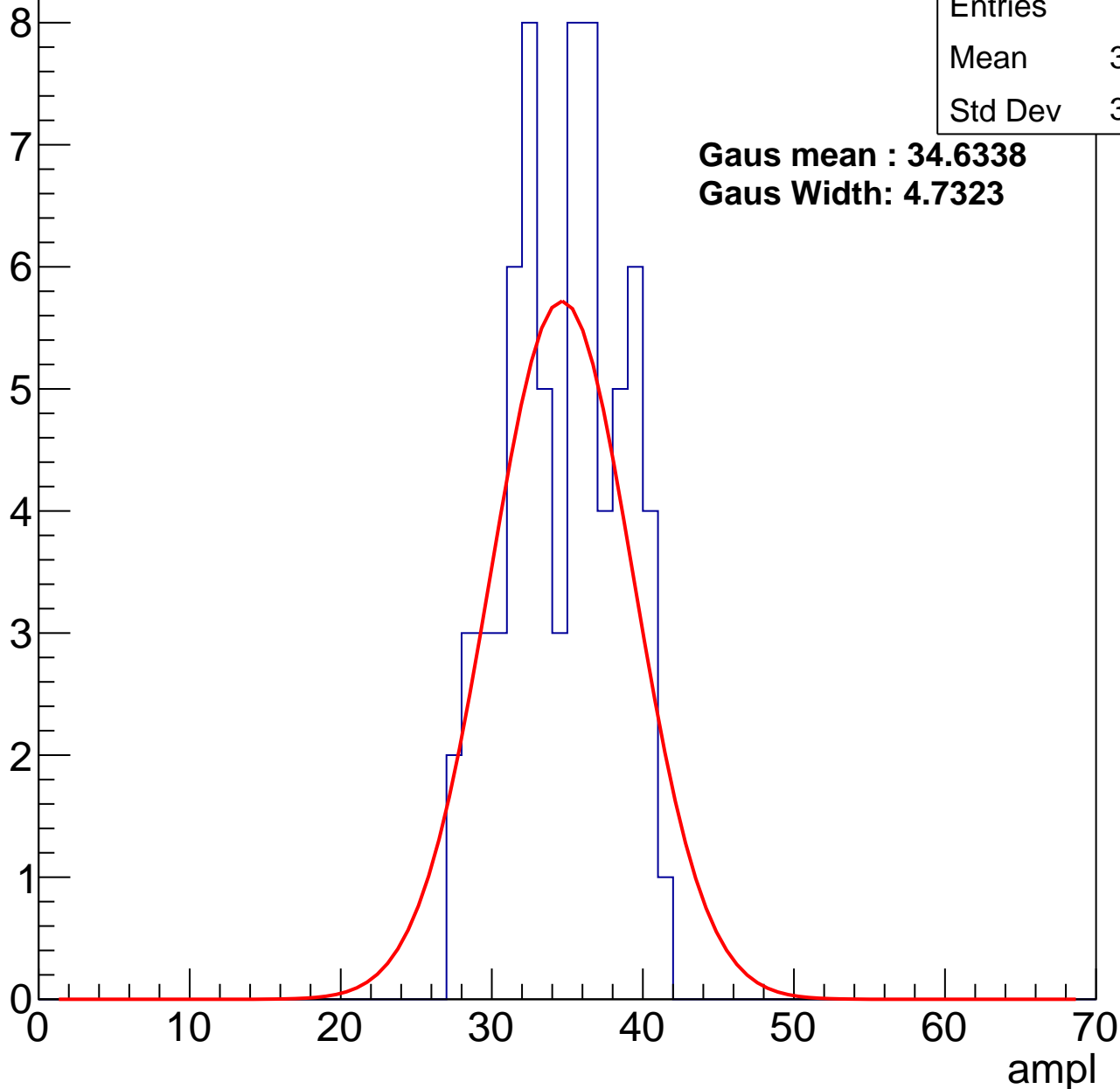
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.28
Std Dev	3.639

Gaus mean : 34.6338

Gaus Width: 4.7323



B1L103S, U24-ch68, adc2

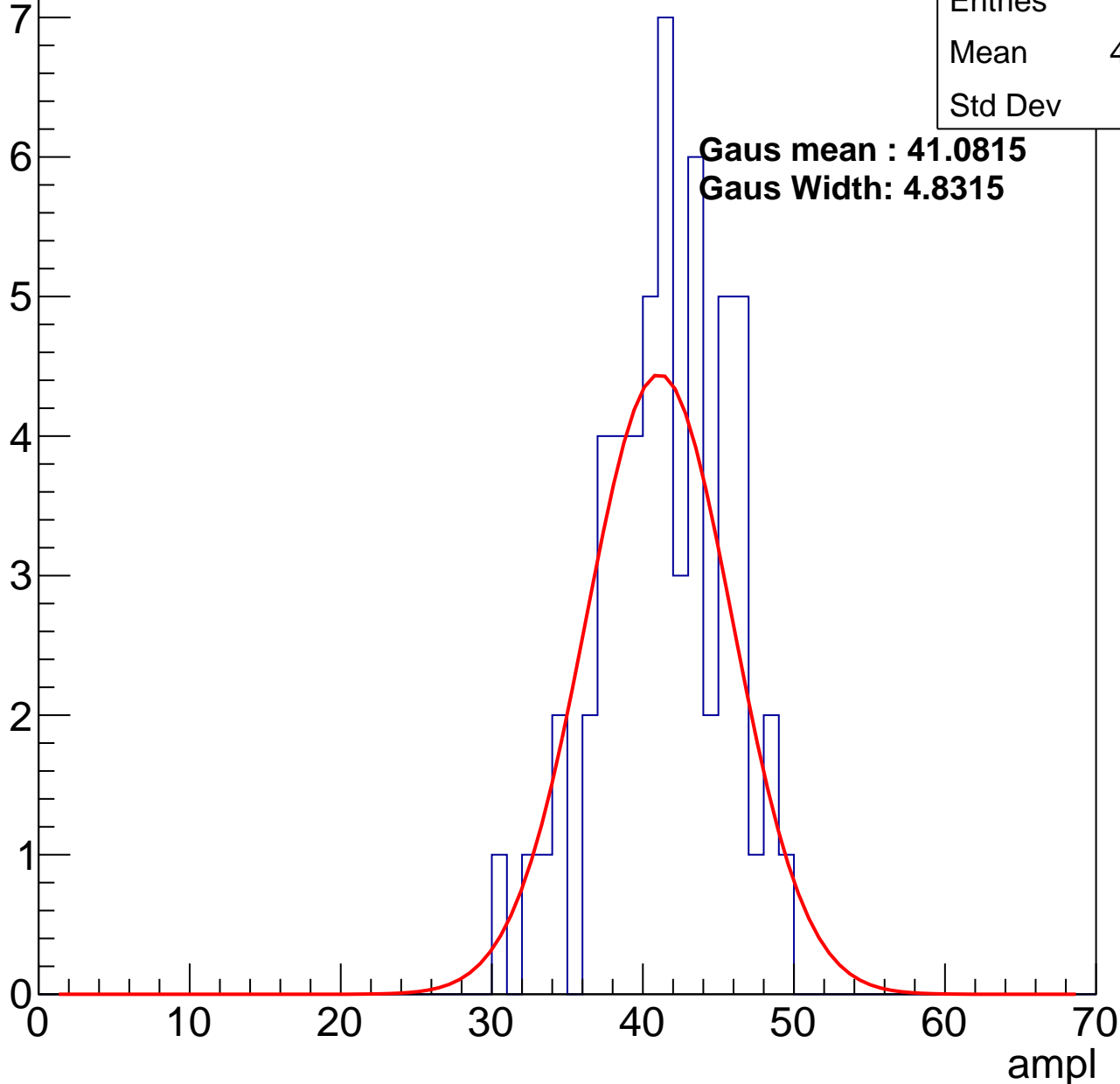
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	41.02
Std Dev	4.19

Gaus mean : 41.0815

Gaus Width: 4.8315



B1L103S, U24-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

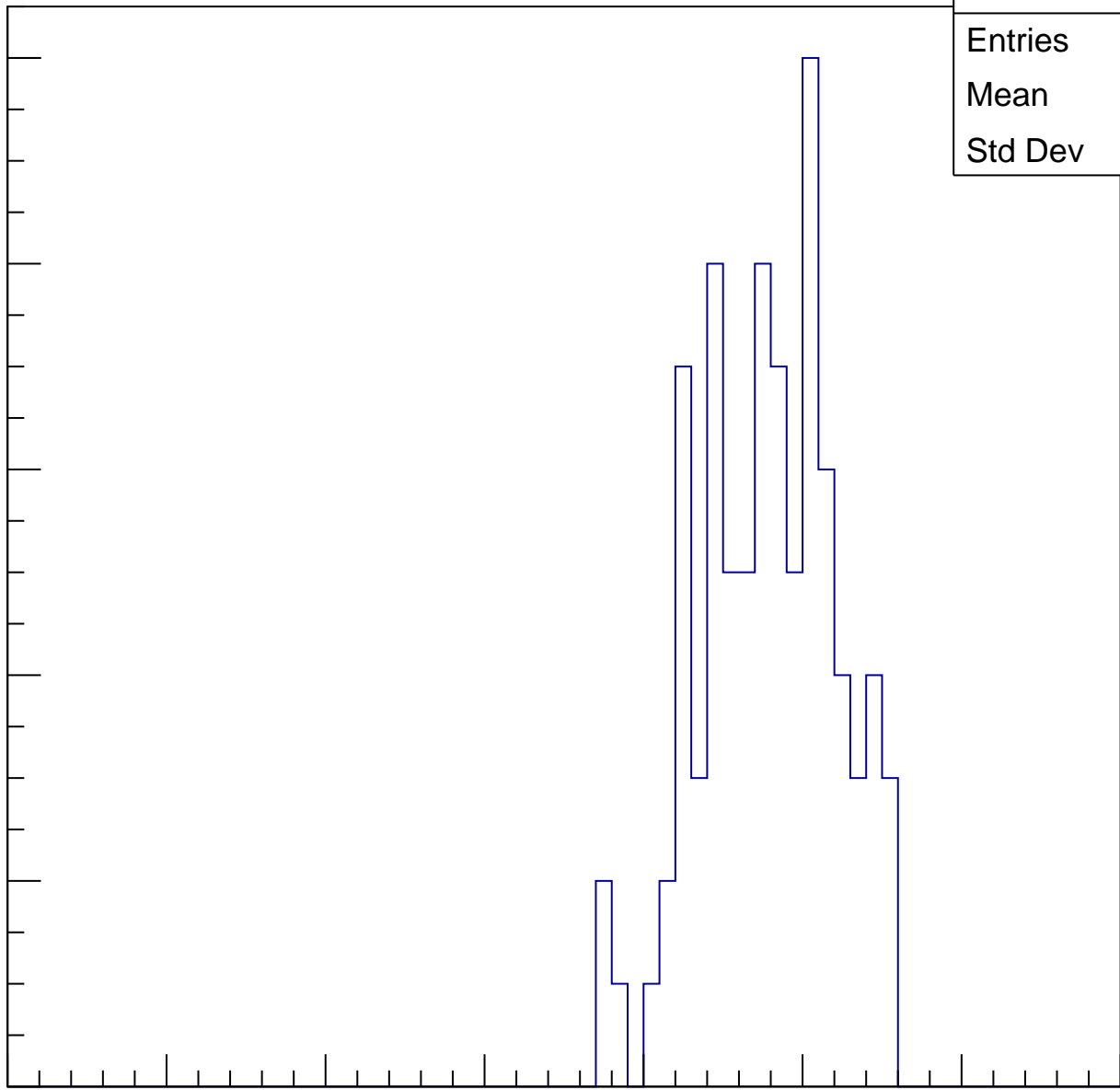
Entries	84
Mean	47.32
Std Dev	4.257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

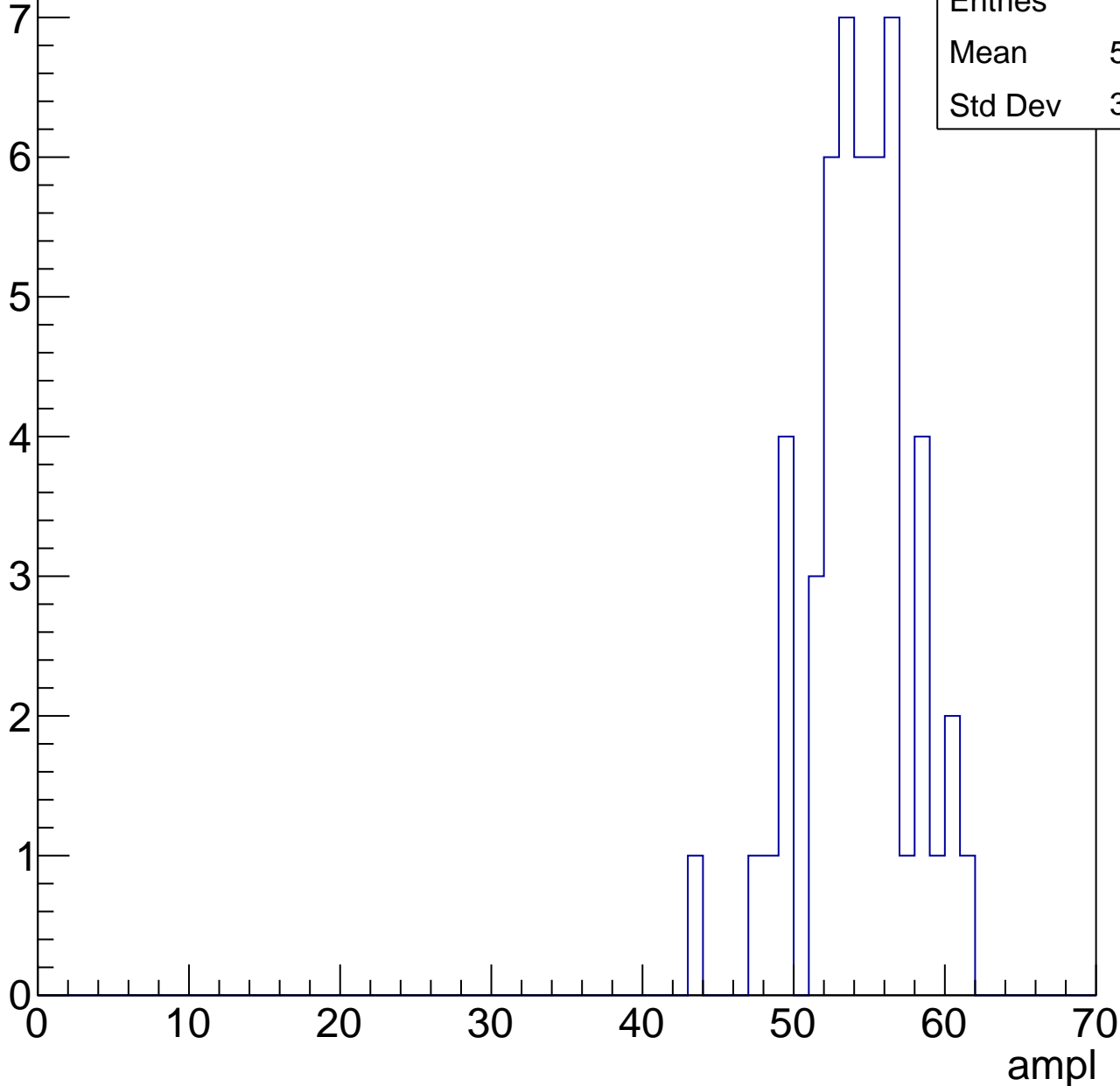


B1L103S, U24-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	53.82
Std Dev	3.474

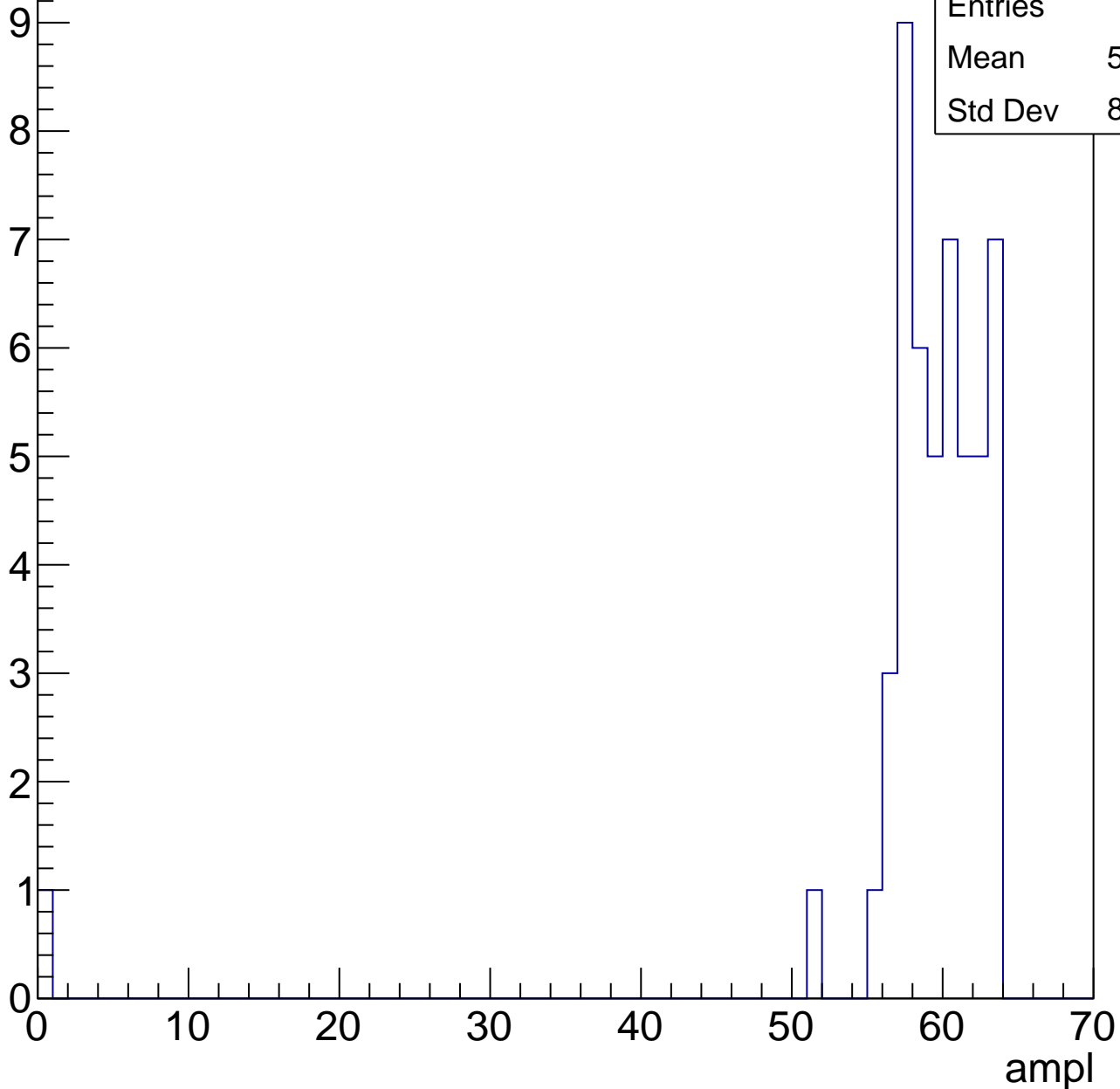


B1L103S, U24-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.12
Std Dev	8.689

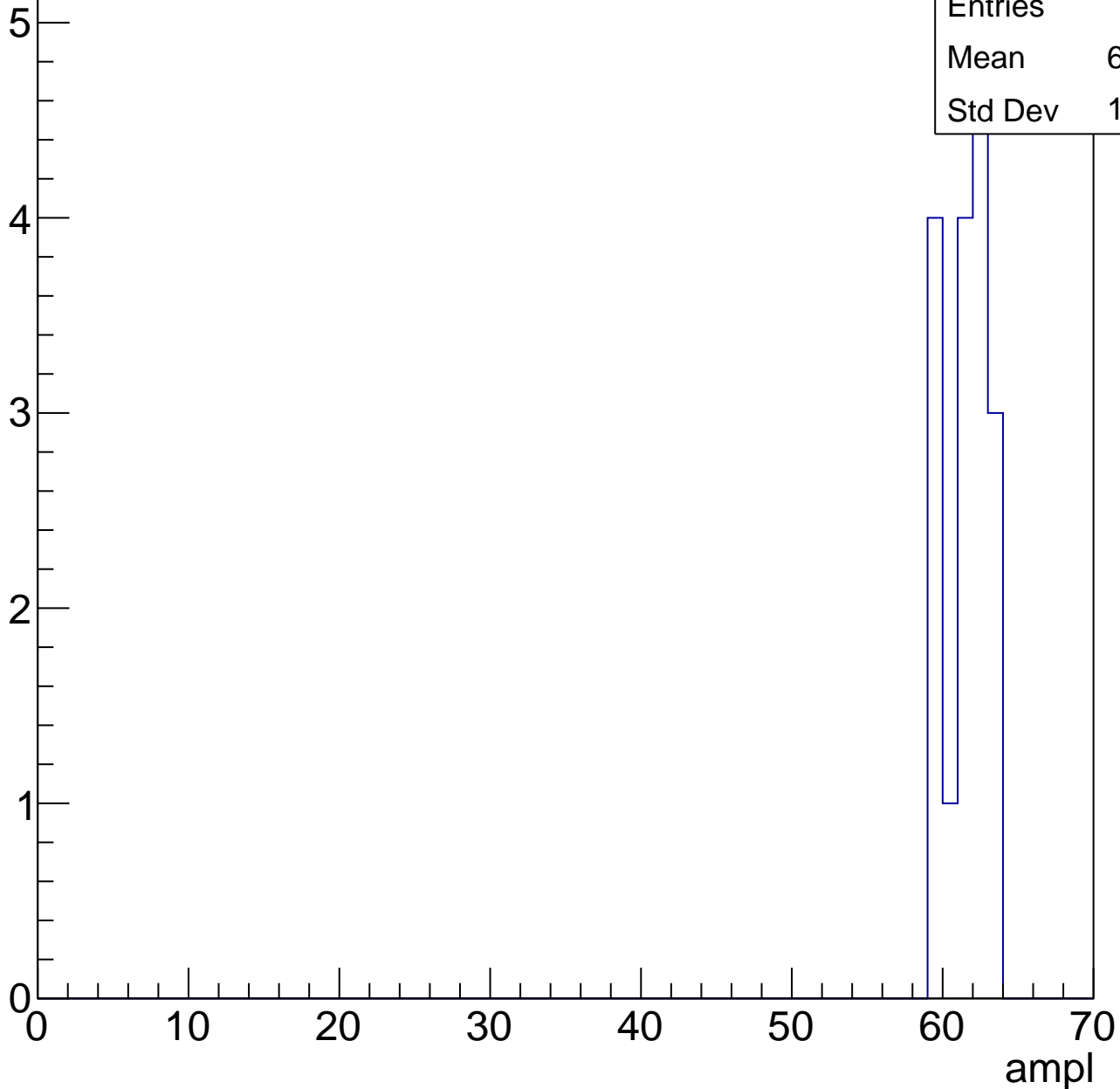


B1L103S, U24-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.12
Std Dev	1.409



B1L103S, U24-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch69, adc0

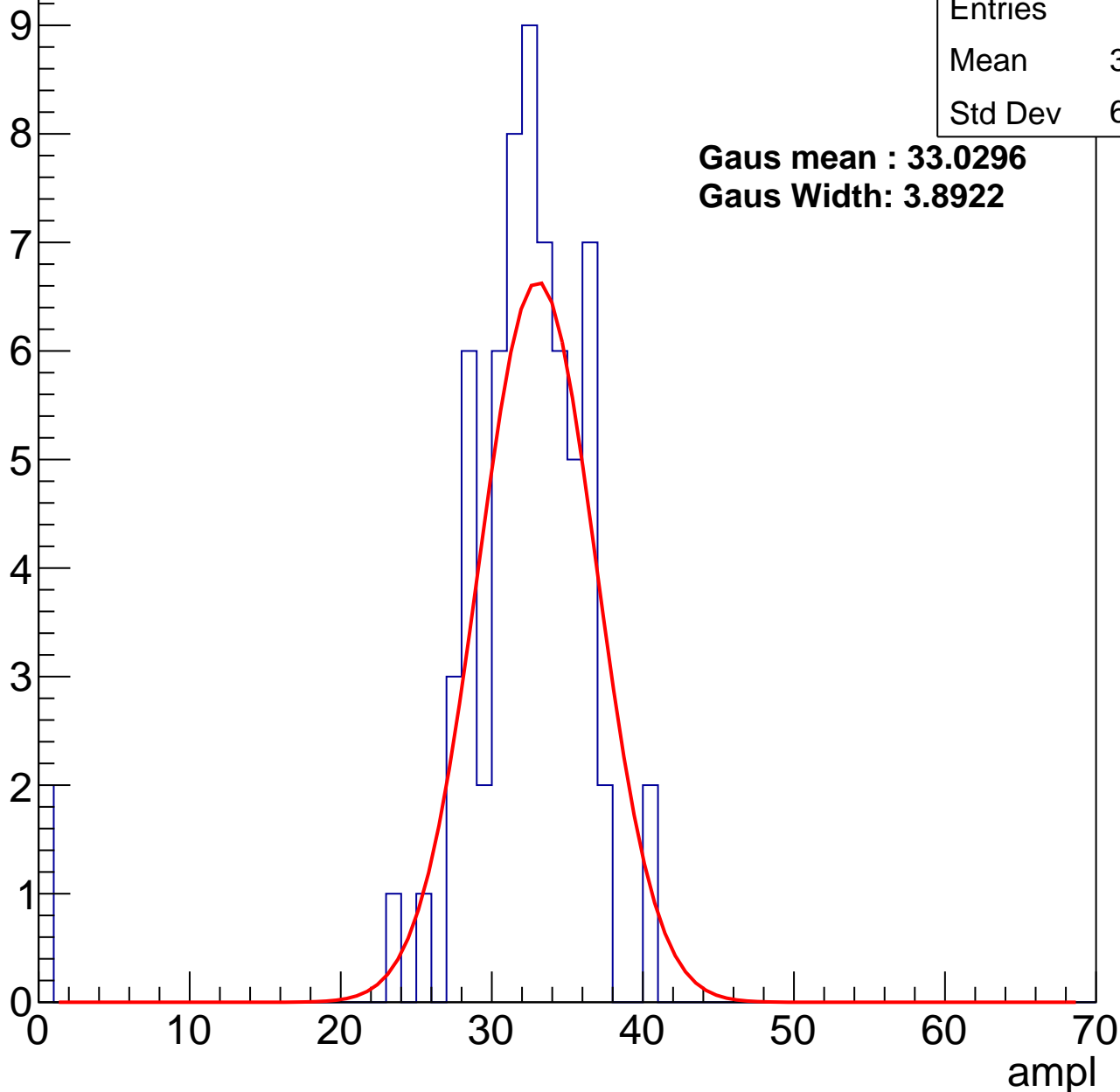
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	31.15
Std Dev	6.367

Gaus mean : 33.0296

Gaus Width: 3.8922



B1L103S, U24-ch69, adc1

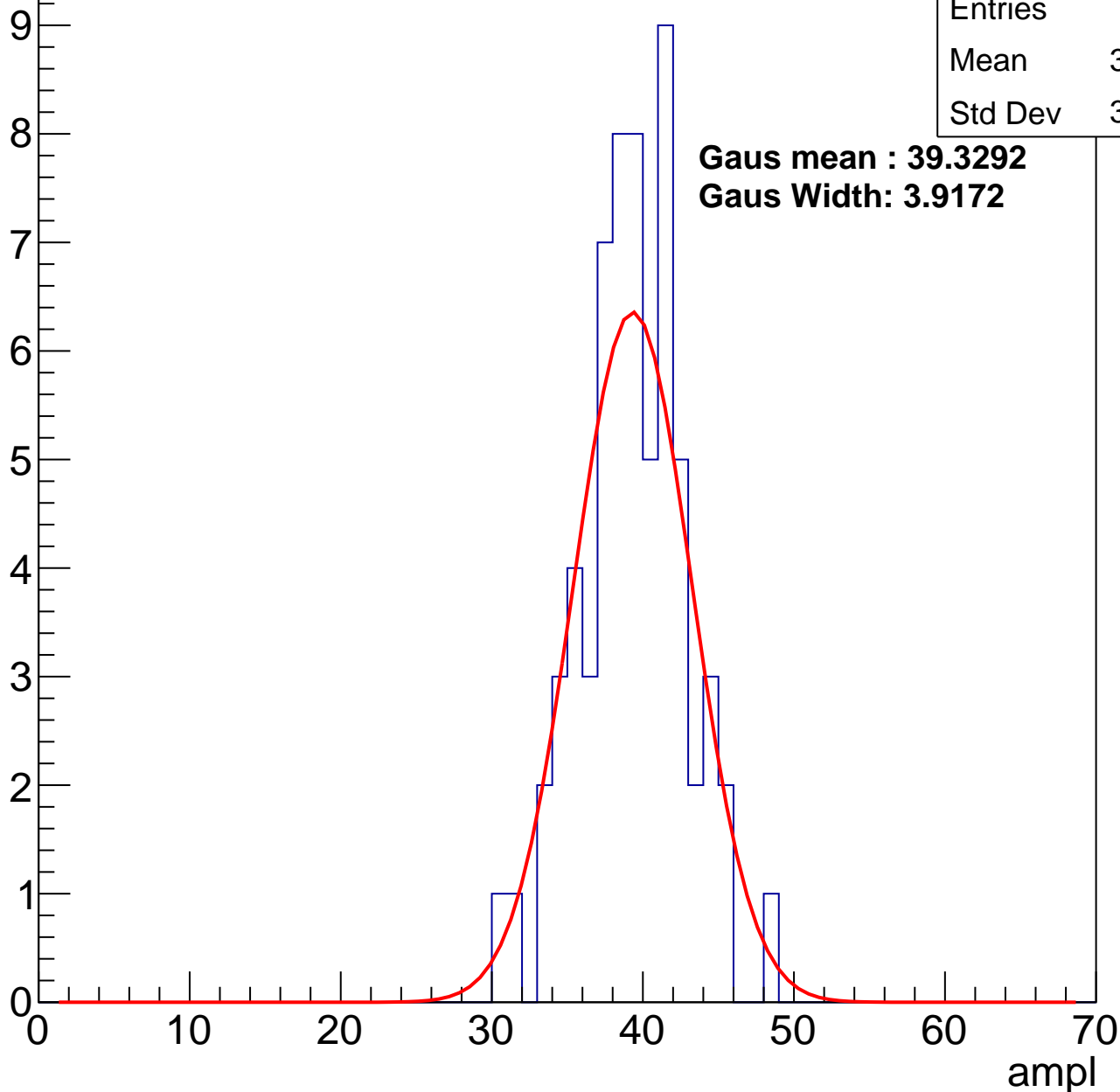
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	38.86
Std Dev	3.454

Gaus mean : 39.3292

Gaus Width: 3.9172



B1L103S, U24-ch69, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

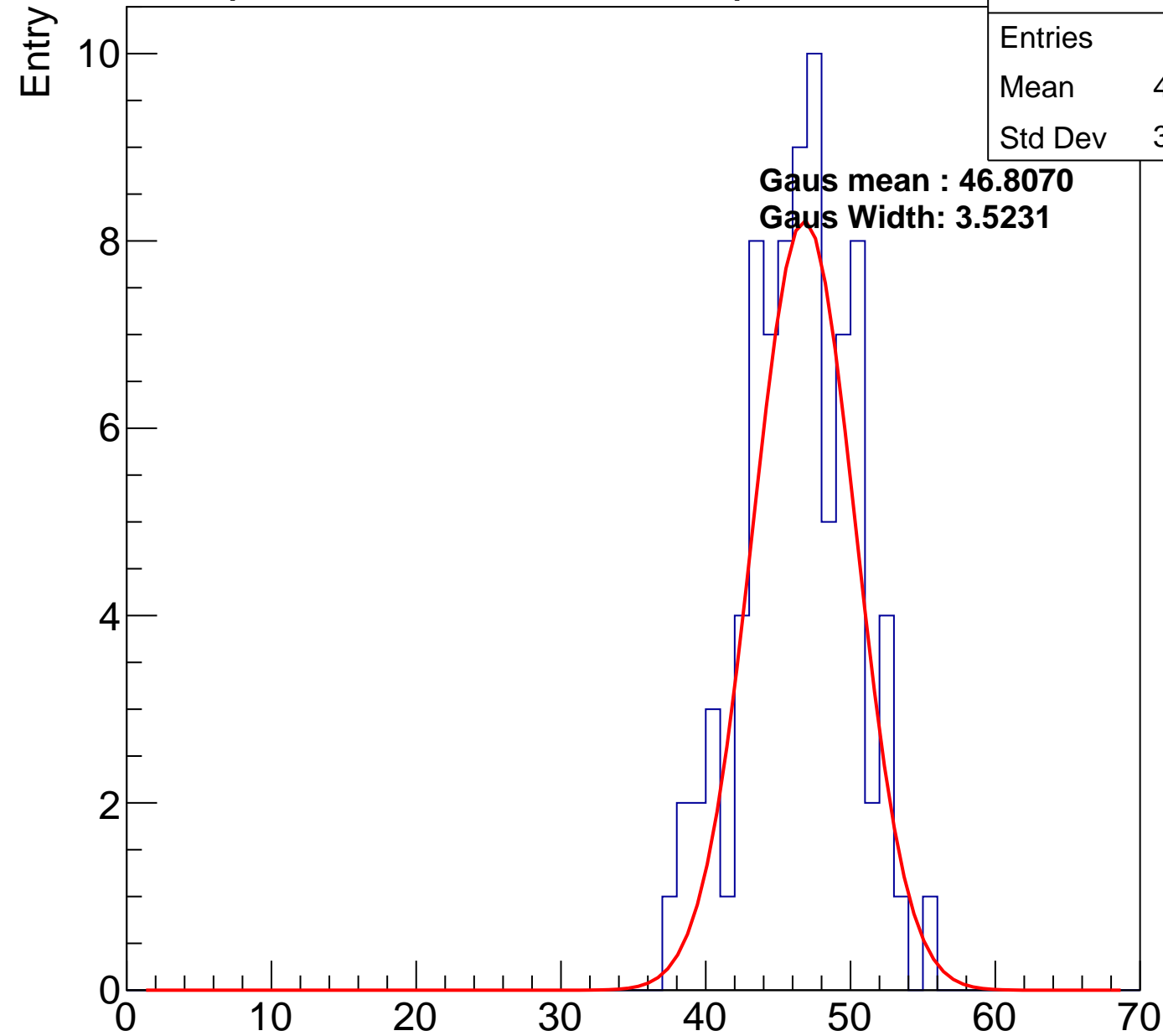
Entries	83
Mean	45.99
Std Dev	3.734

Gaus mean : 46.8070

Gaus Width: 3.5231

10
8
6
4
2
0

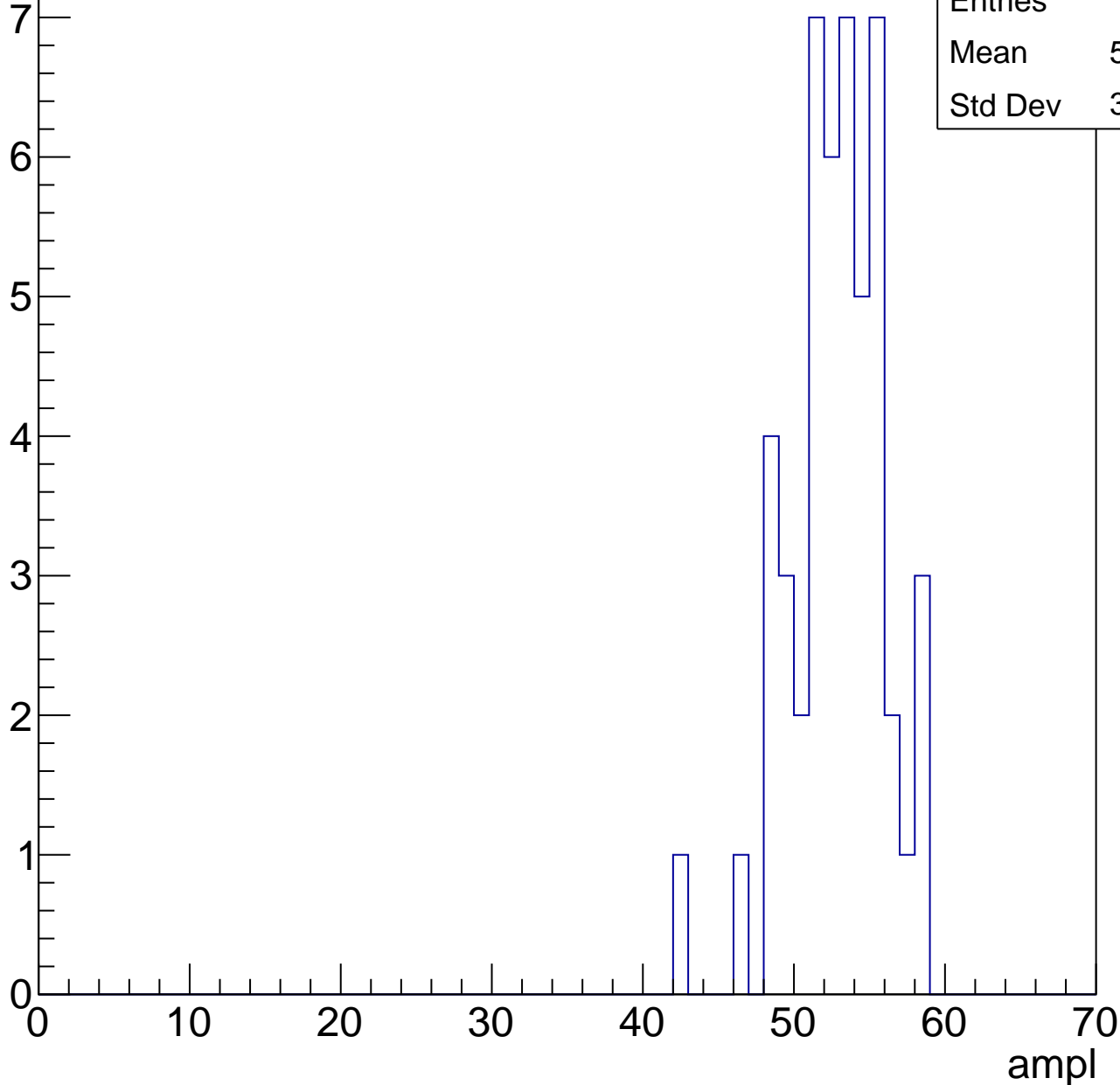
ampl



B1L103S, U24-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



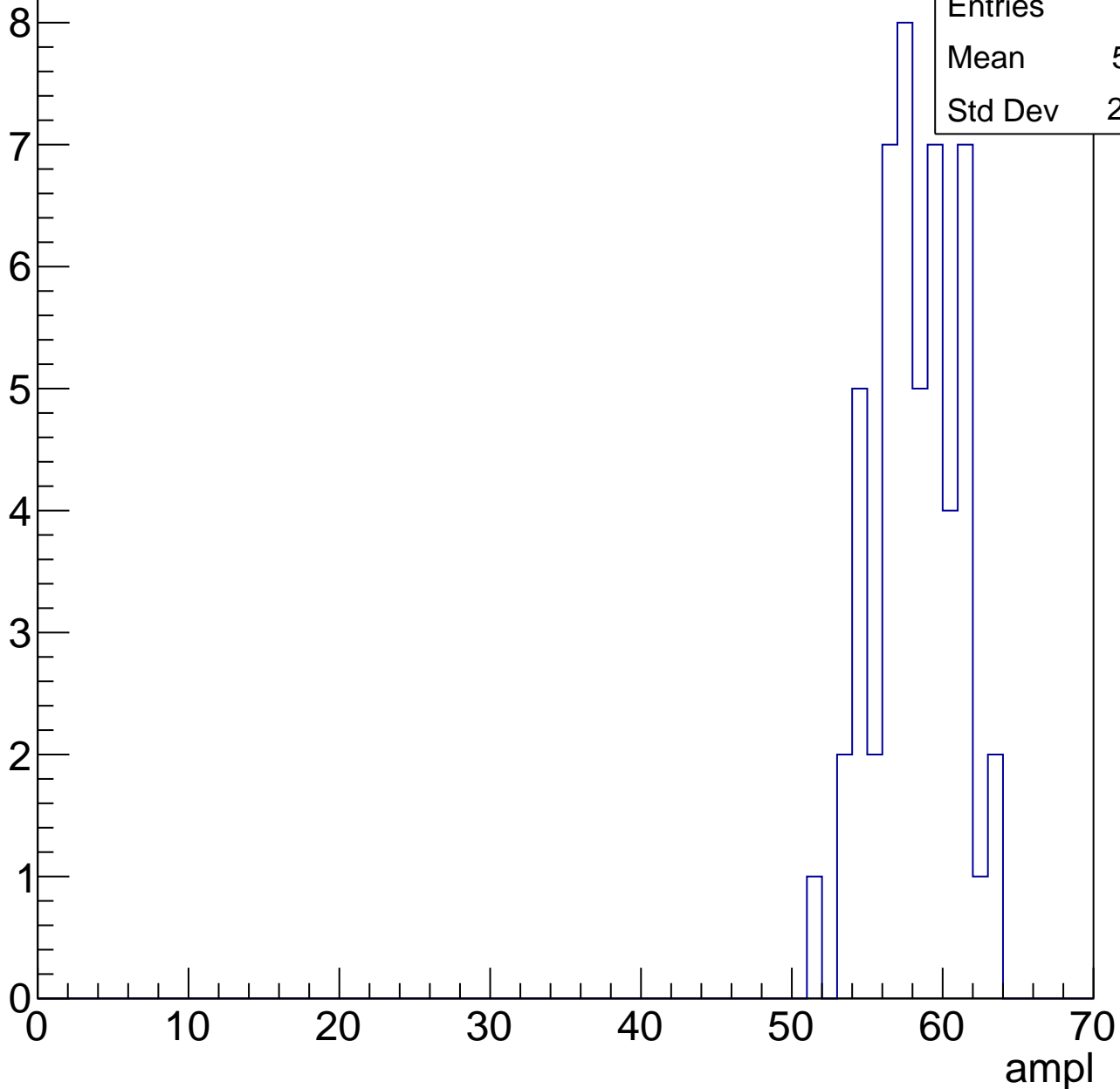
Entries	49
Mean	52.35
Std Dev	3.166

B1L103S, U24-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	57.71
Std Dev	2.732

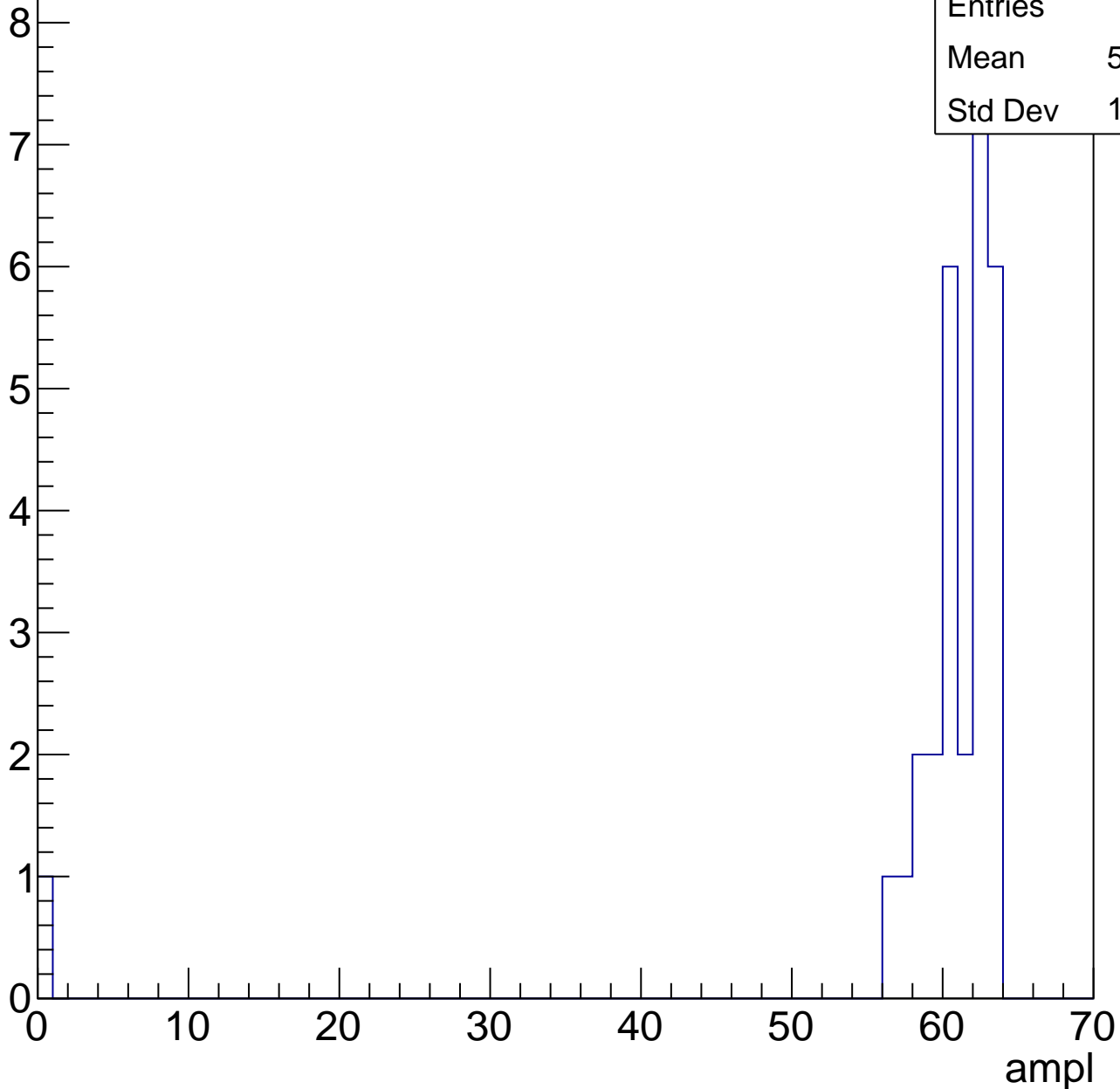


B1L103S, U24-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	58.72
Std Dev	11.26



B1L103S, U24-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

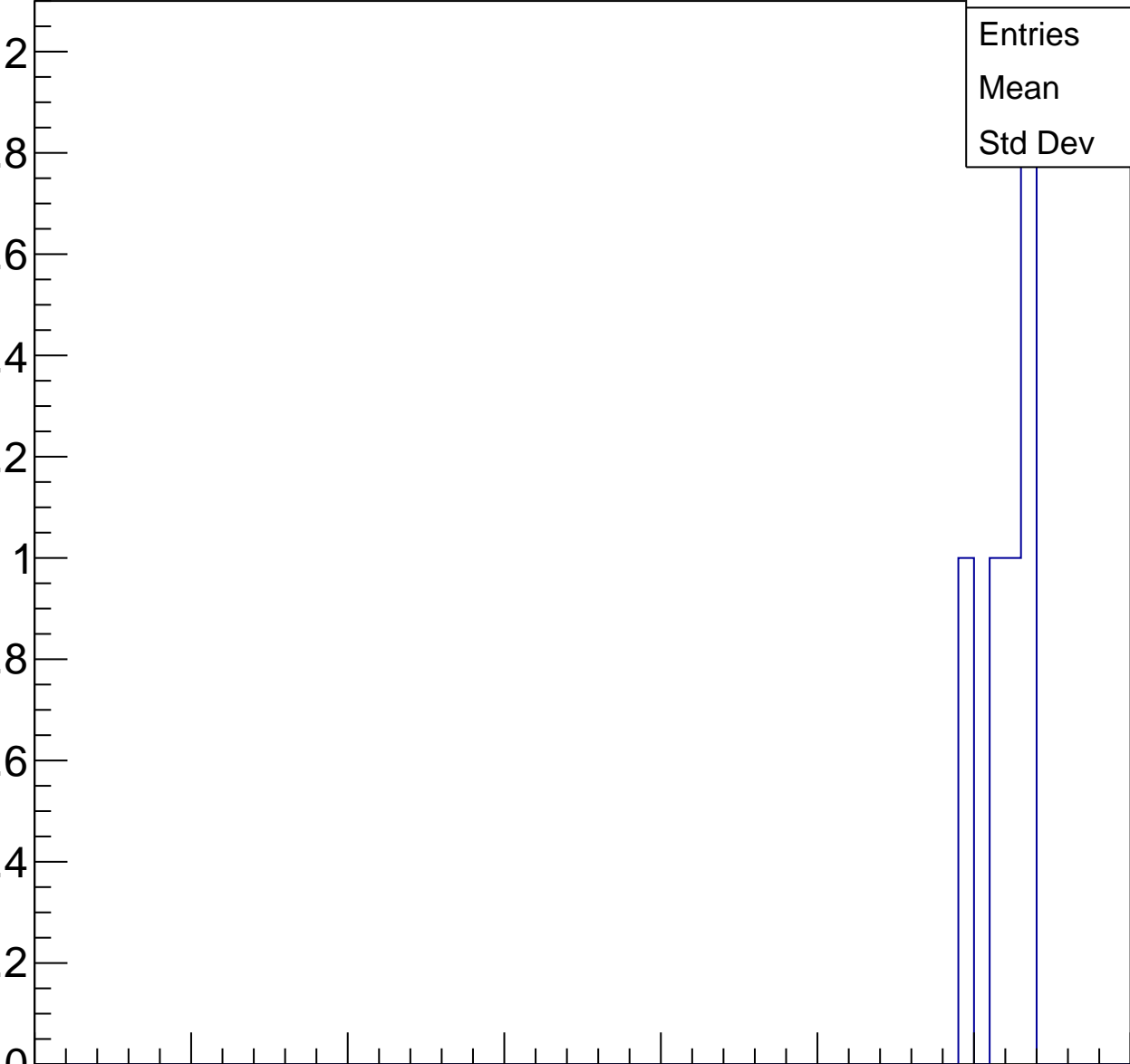
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.497

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch70, adc0

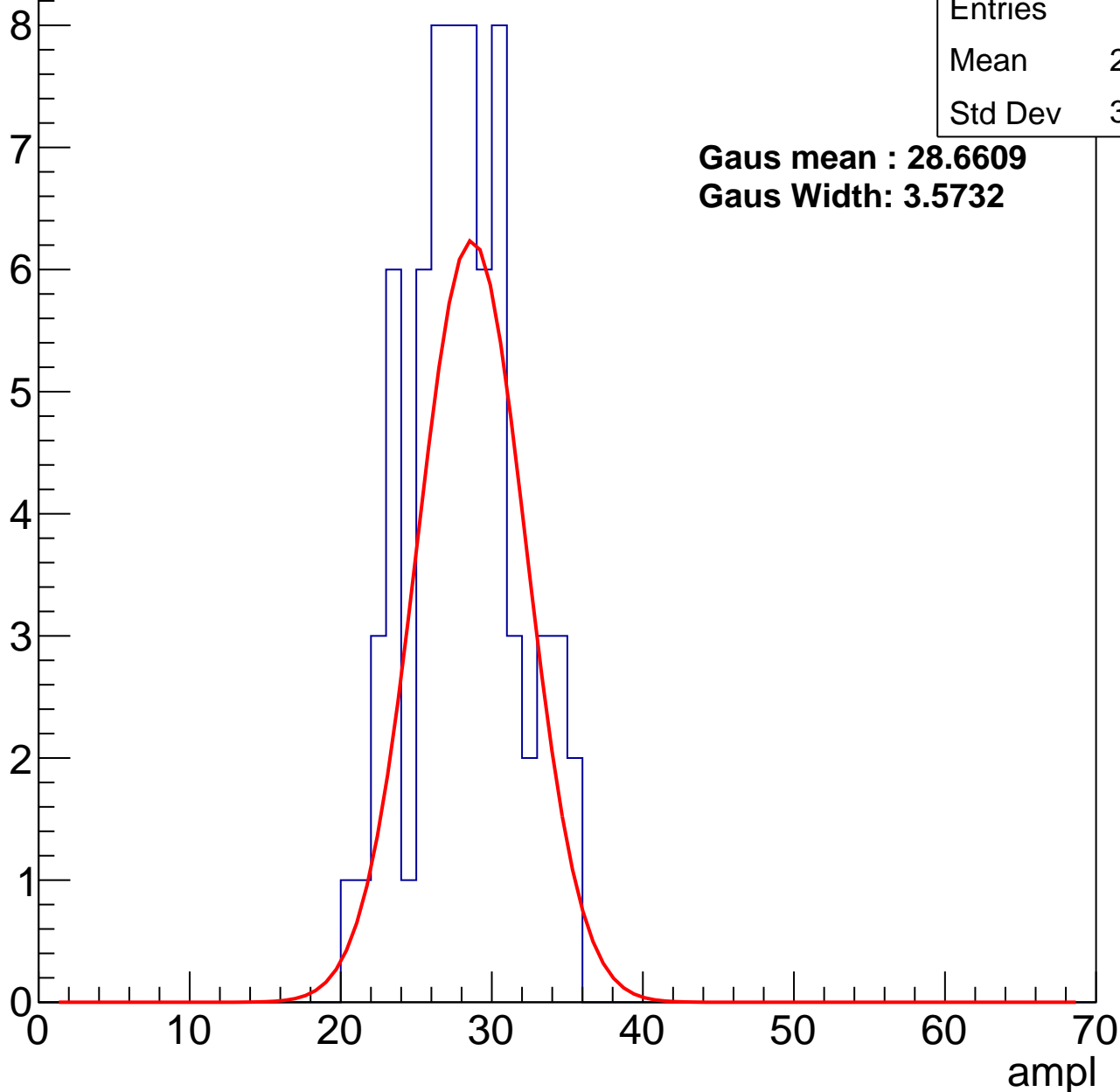
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.67
Std Dev	3.513

Gaus mean : 28.6609

Gaus Width: 3.5732



B1L103S, U24-ch70, adc1

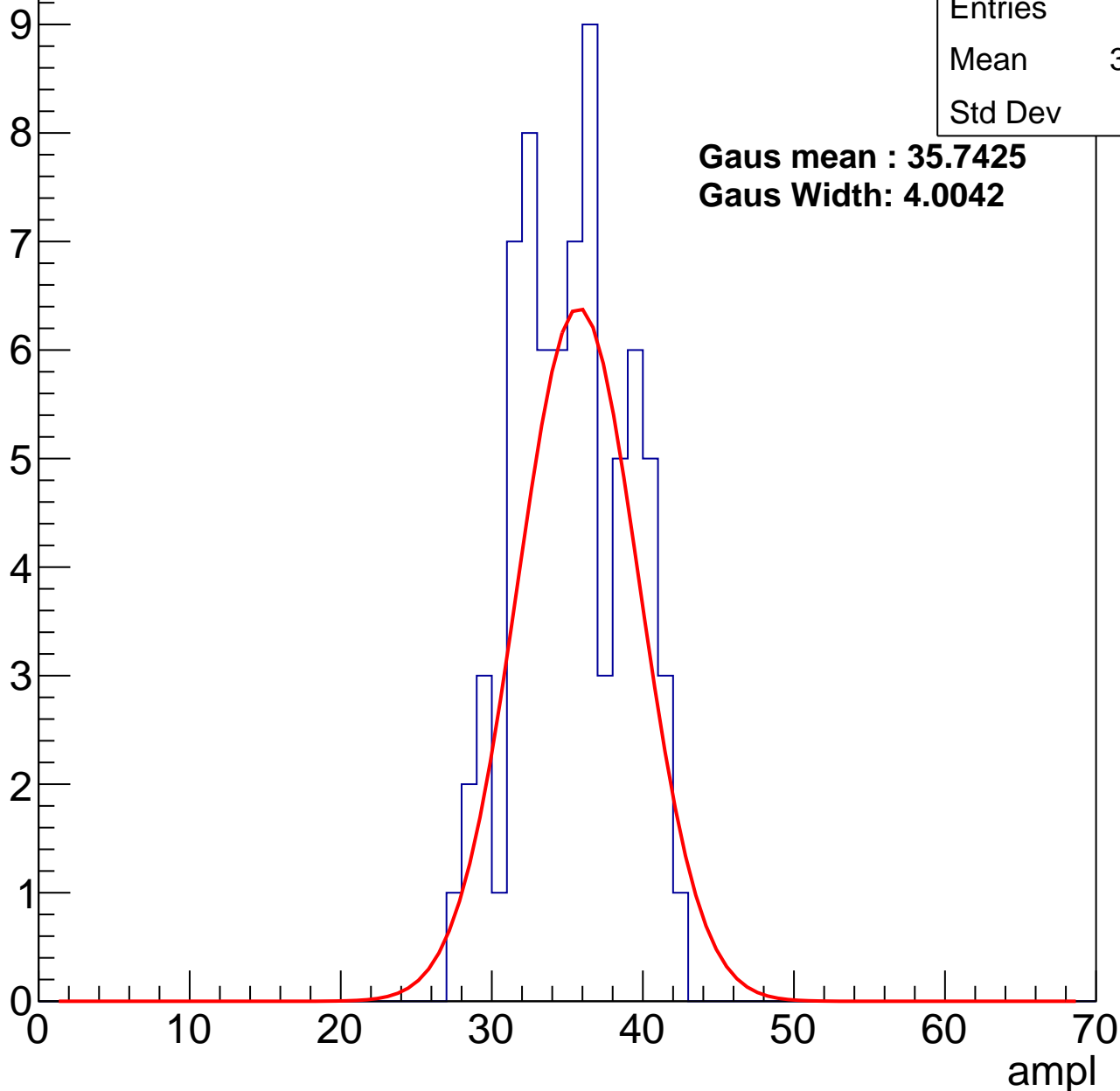
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	34.85
Std Dev	3.61

Gaus mean : 35.7425

Gaus Width: 4.0042



B1L103S, U24-ch70, adc2

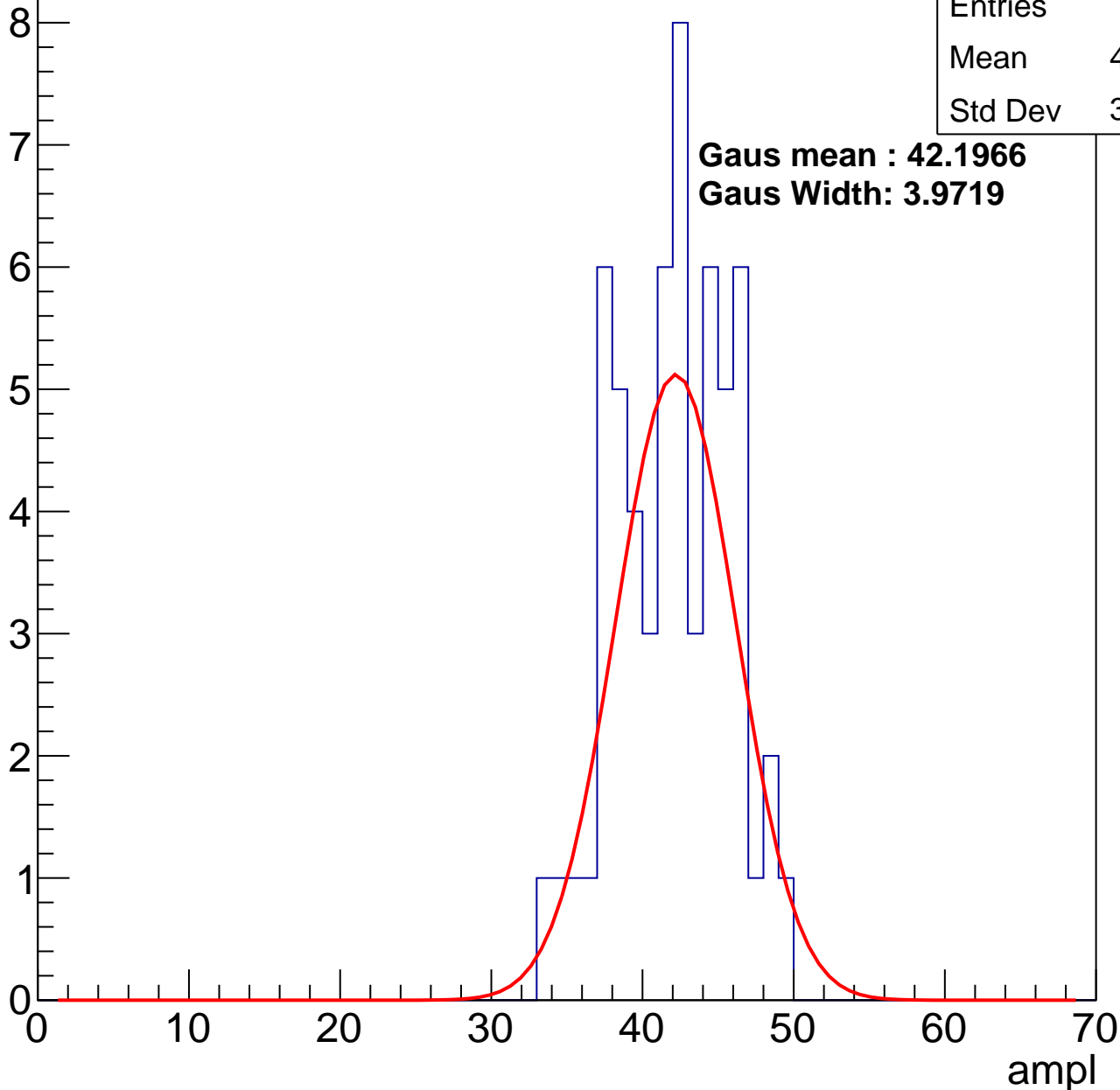
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	41.57
Std Dev	3.689

Gaus mean : 42.1966

Gaus Width: 3.9719

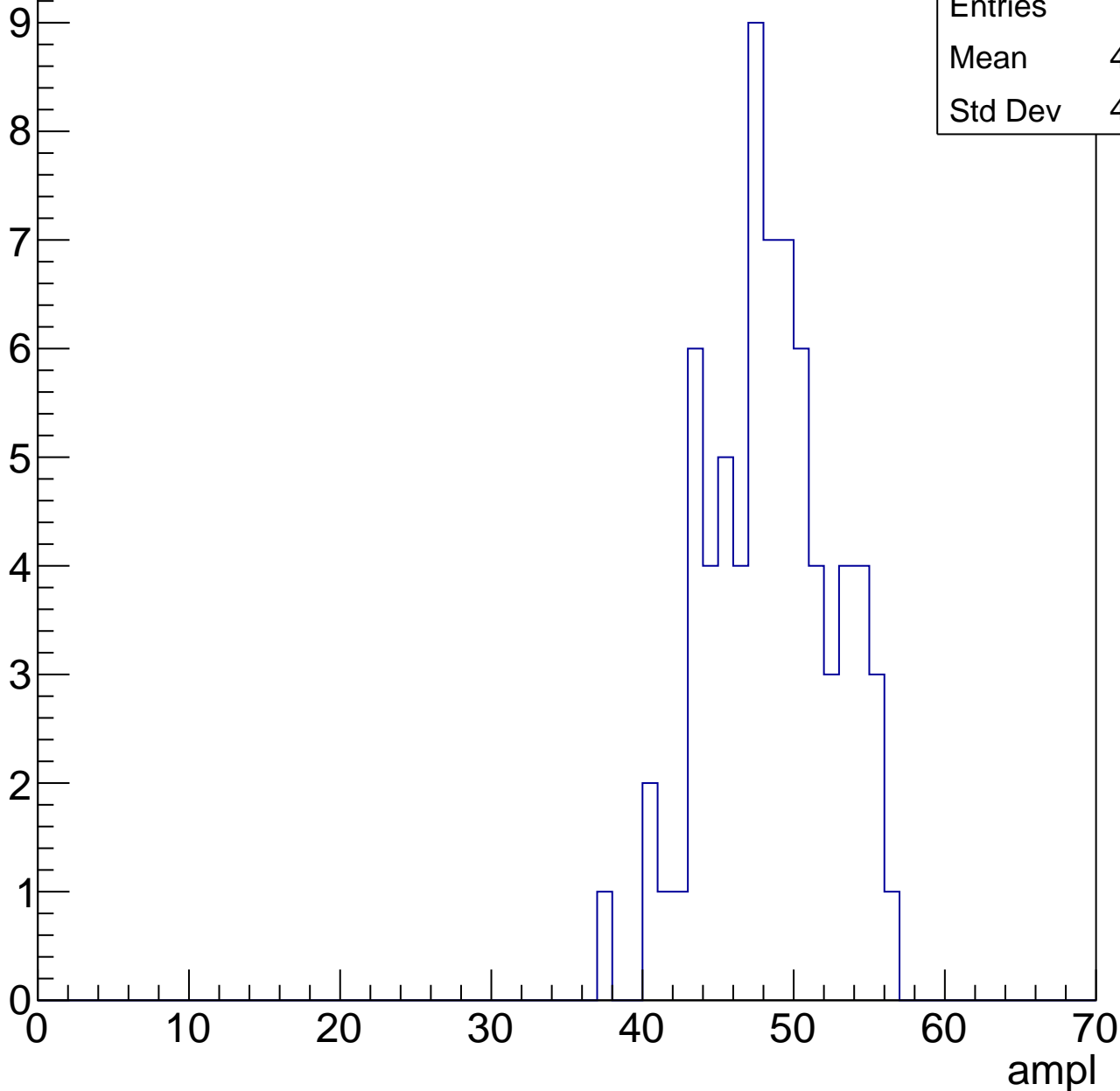


B1L103S, U24-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	47.97
Std Dev	4.062

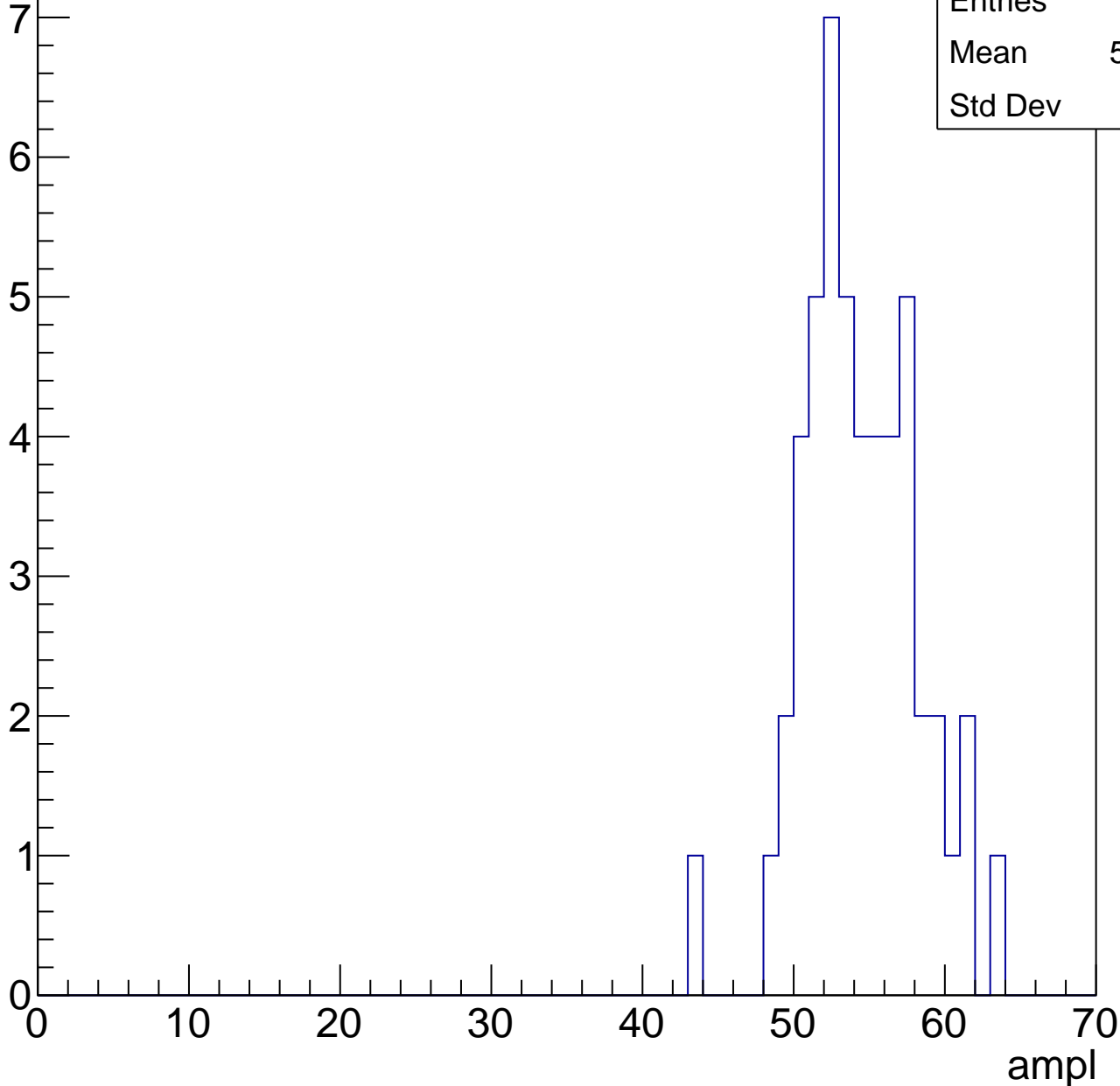


B1L103S, U24-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.94
Std Dev	3.77

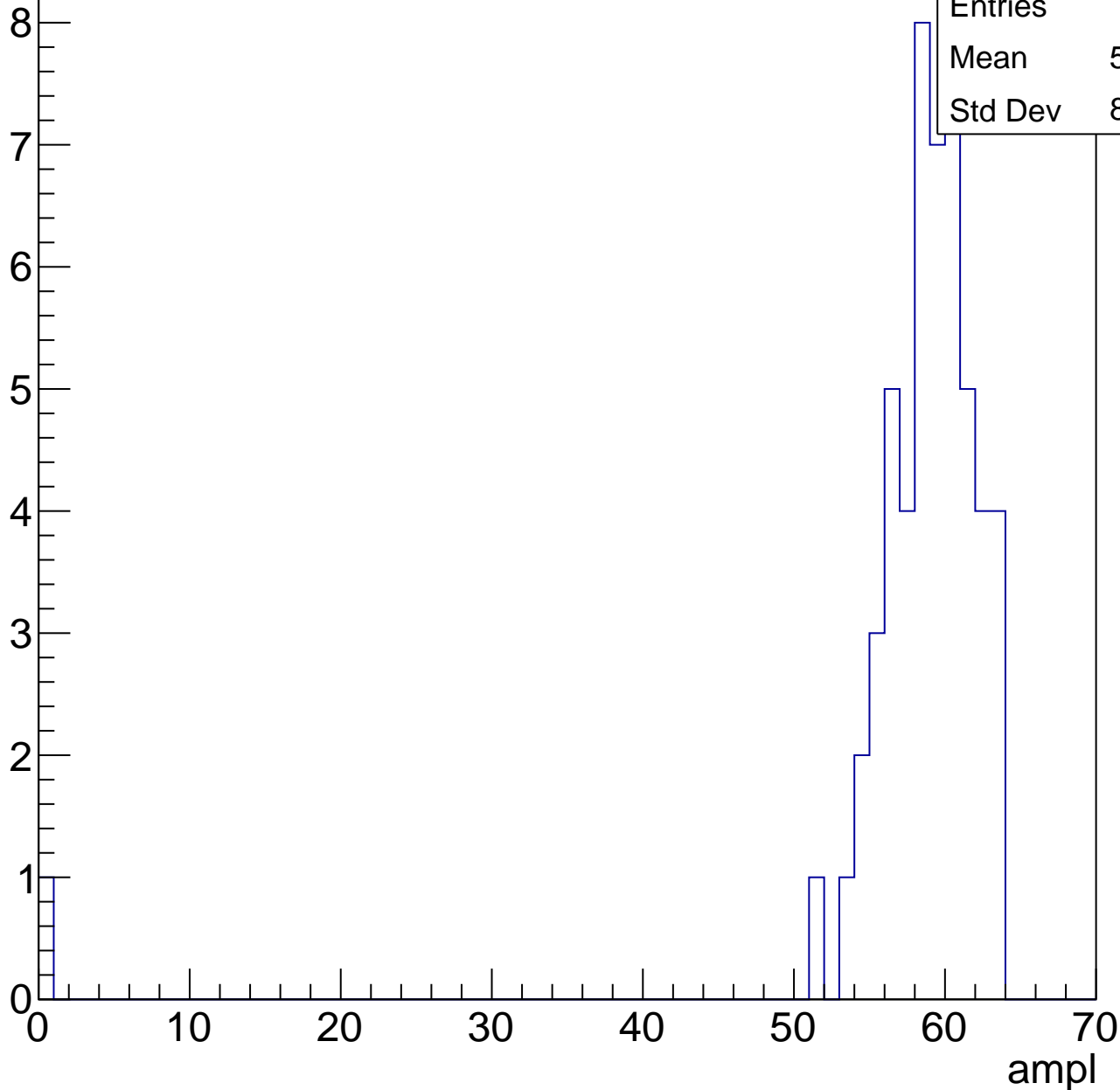


B1L103S, U24-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	57.49
Std Dev	8.417

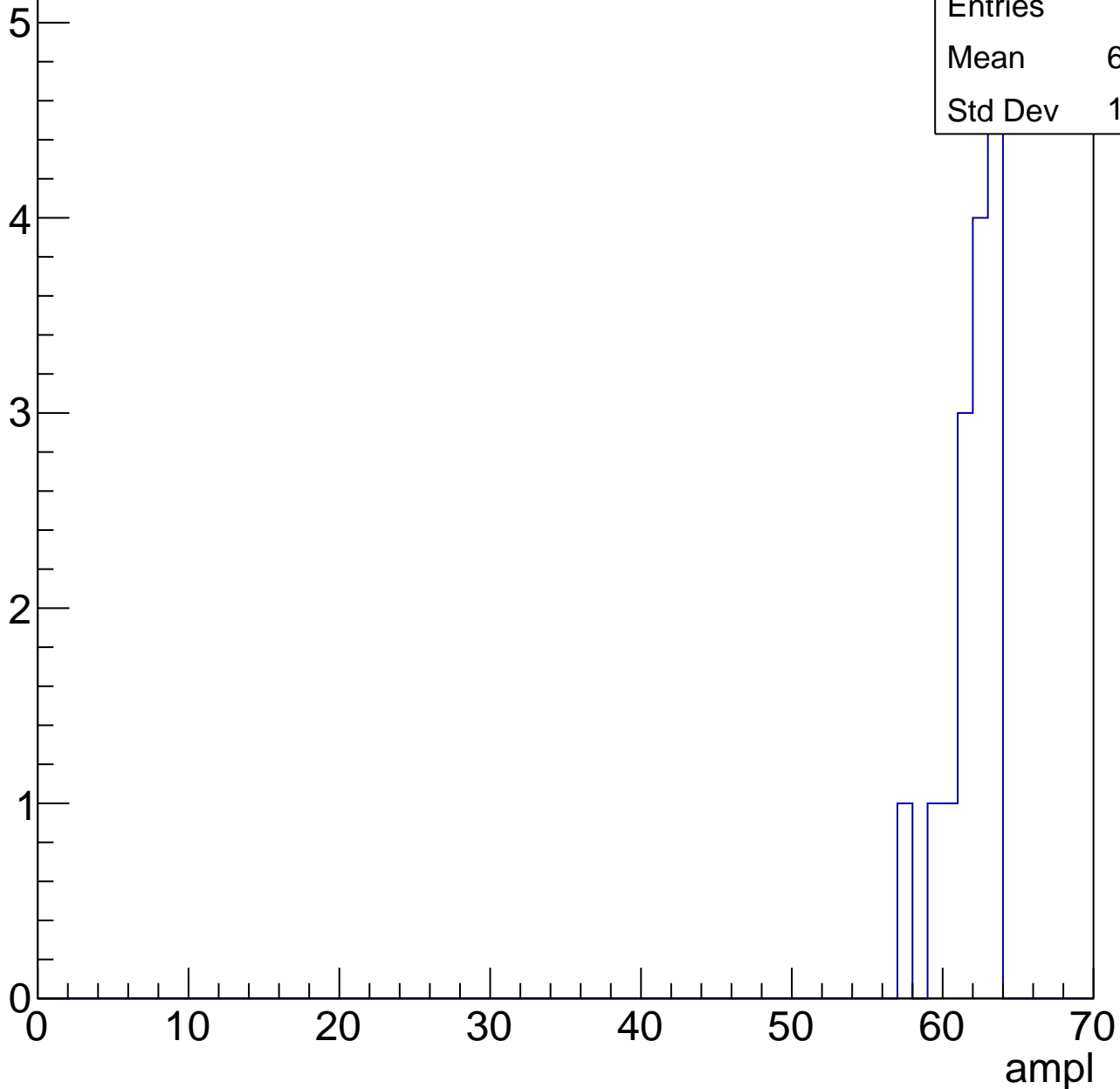


B1L103S, U24-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

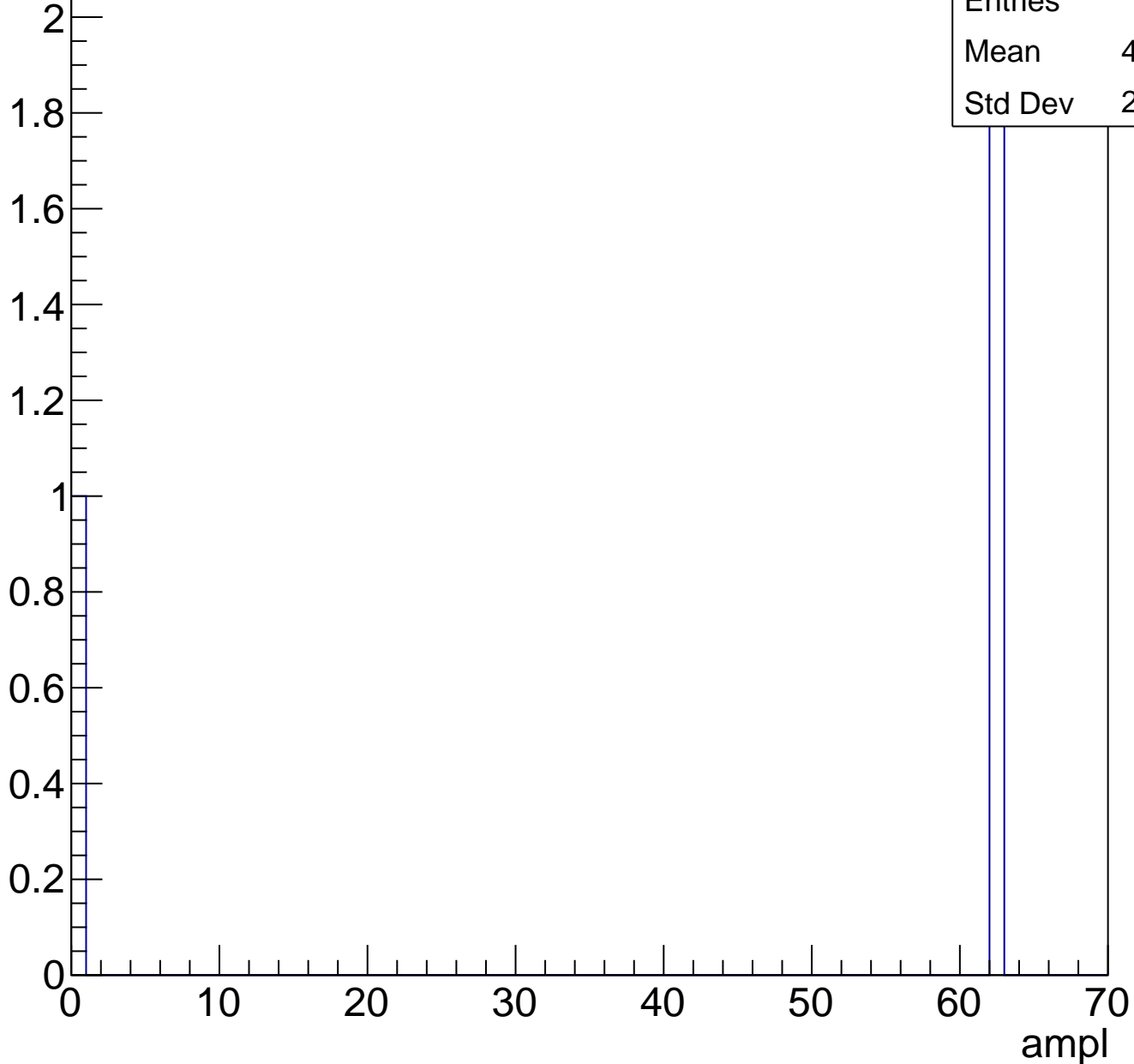
Entries	15
Mean	61.47
Std Dev	1.668



B1L103S, U24-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch71, adc0

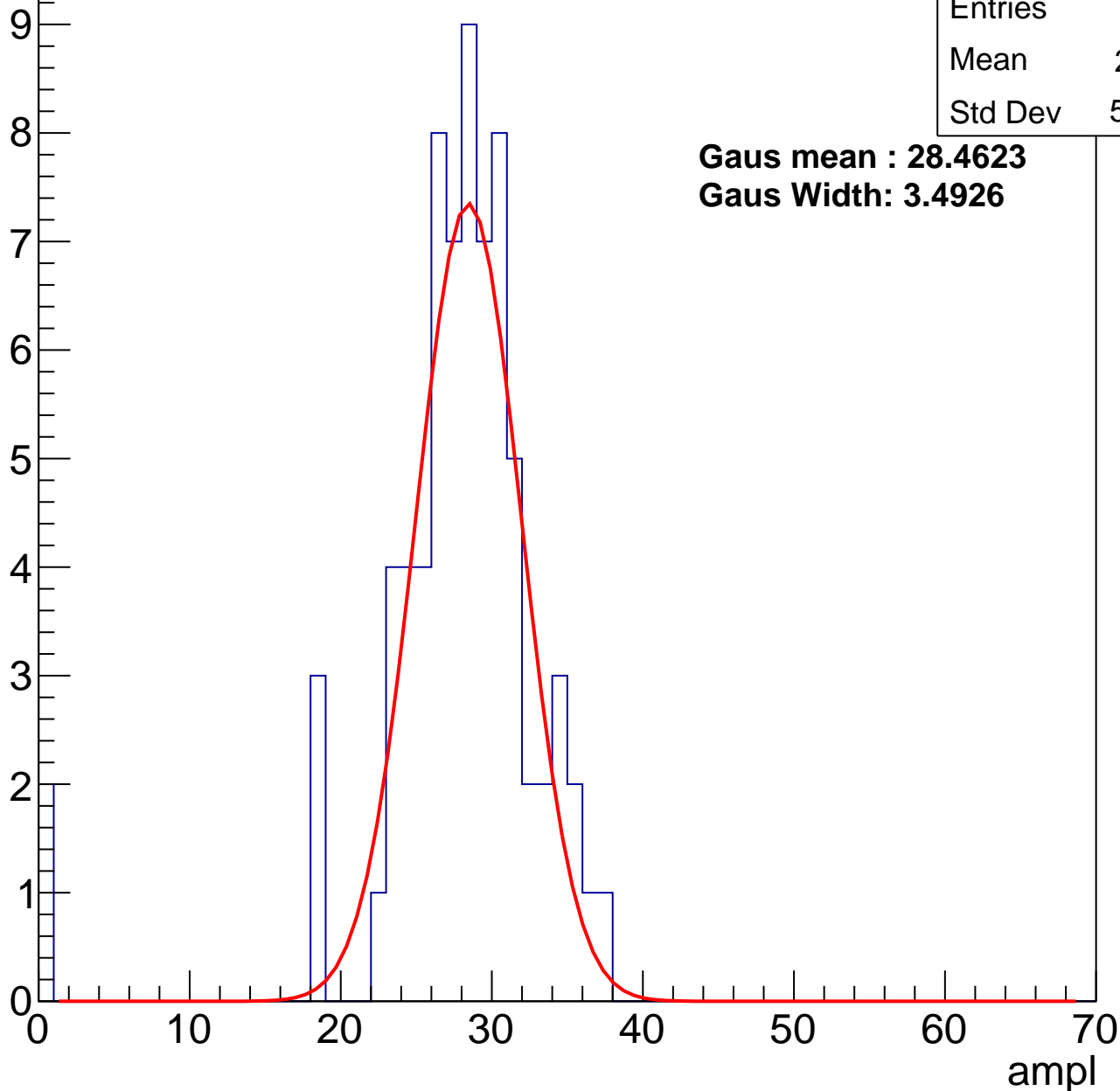
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.21
Std Dev	5.984

Gaus mean : 28.4623

Gaus Width: 3.4926



B1L103S, U24-ch71, adc1

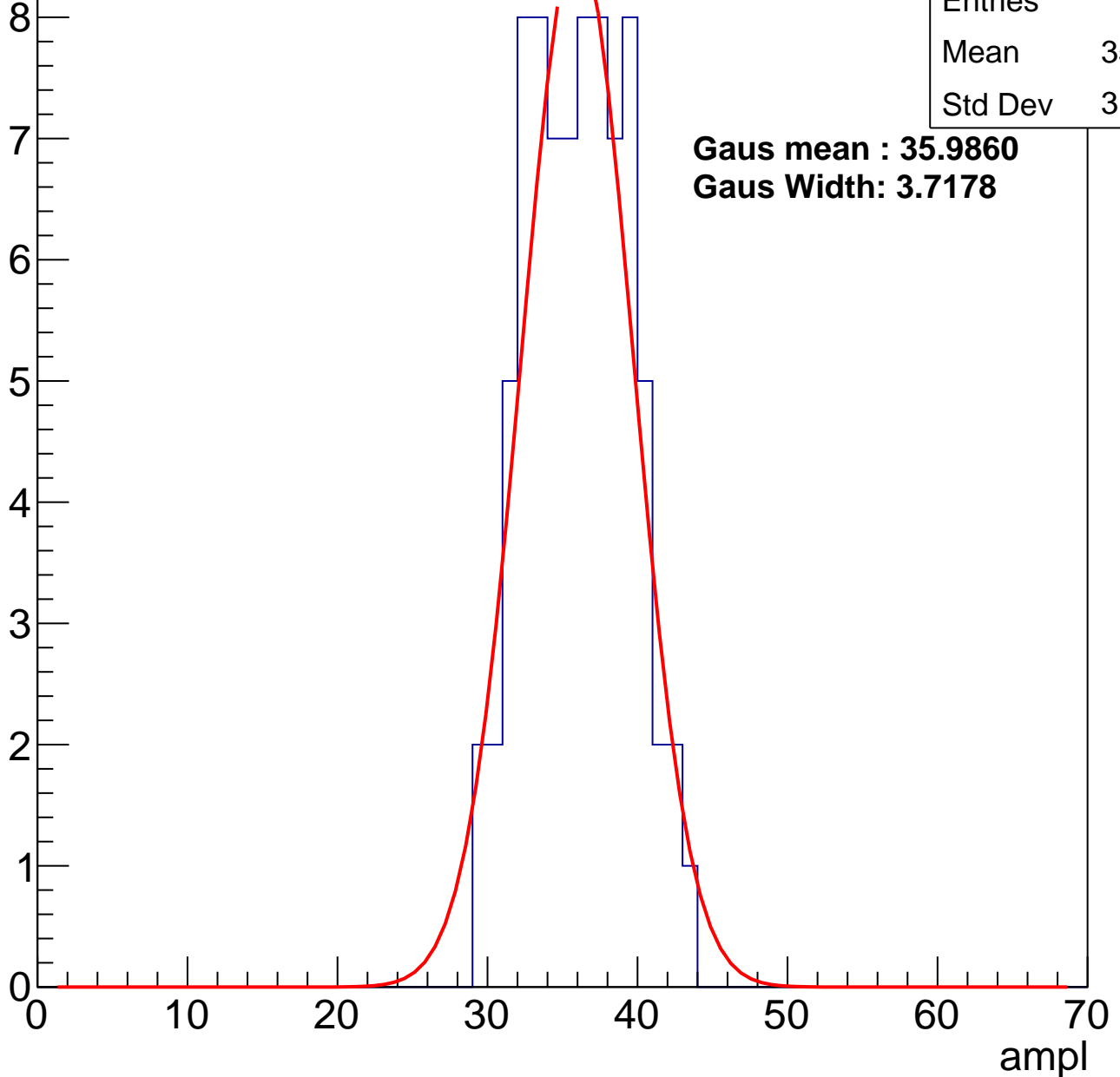
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.59
Std Dev	3.308

Gaus mean : 35.9860

Gaus Width: 3.7178



B1L103S, U24-ch71, adc2

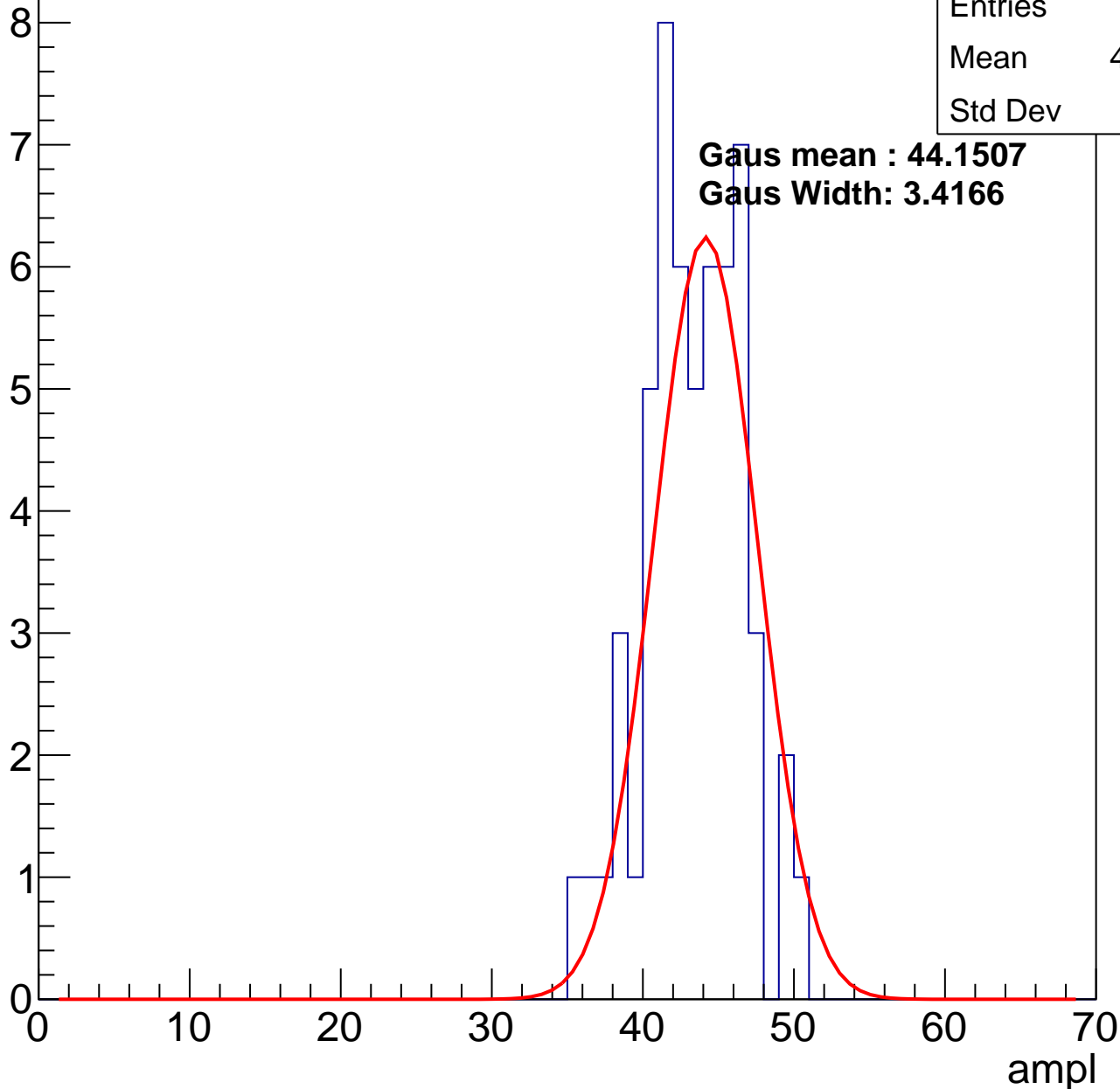
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.88
Std Dev	3.23

Gaus mean : 44.1507

Gaus Width: 3.4166

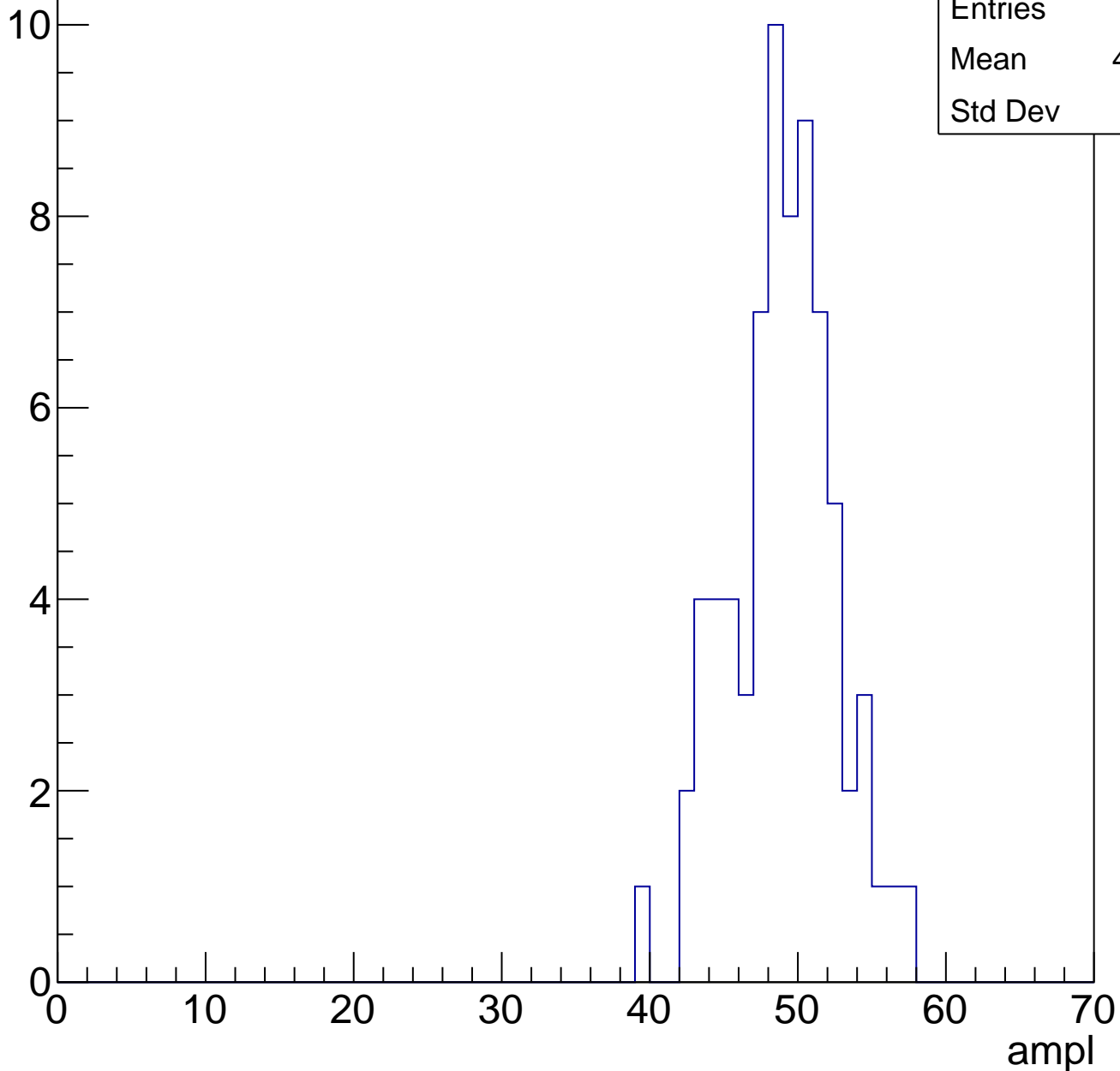


B1L103S, U24-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	48.51
Std Dev	3.52

Entry

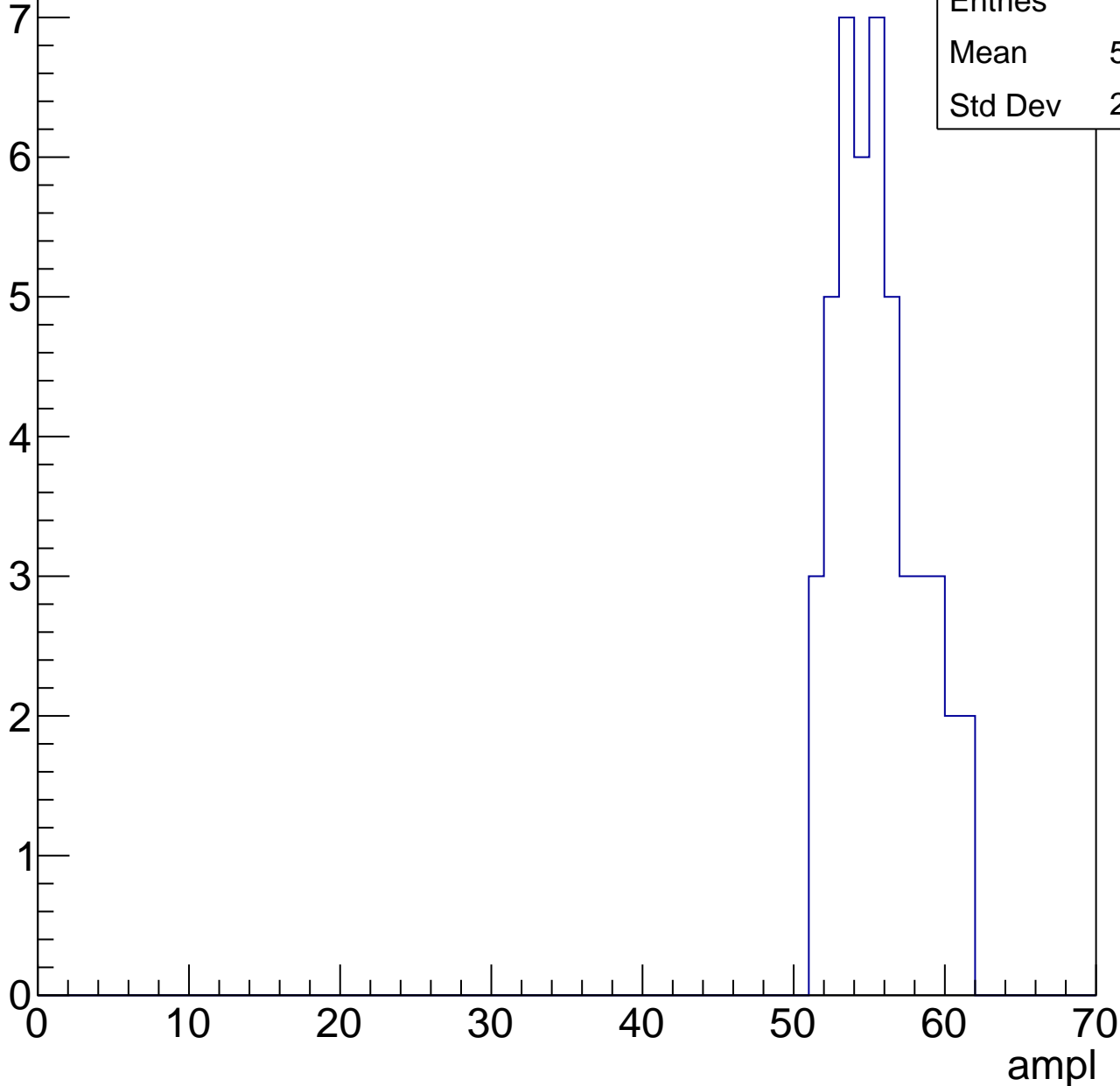


B1L103S, U24-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	55.15
Std Dev	2.718

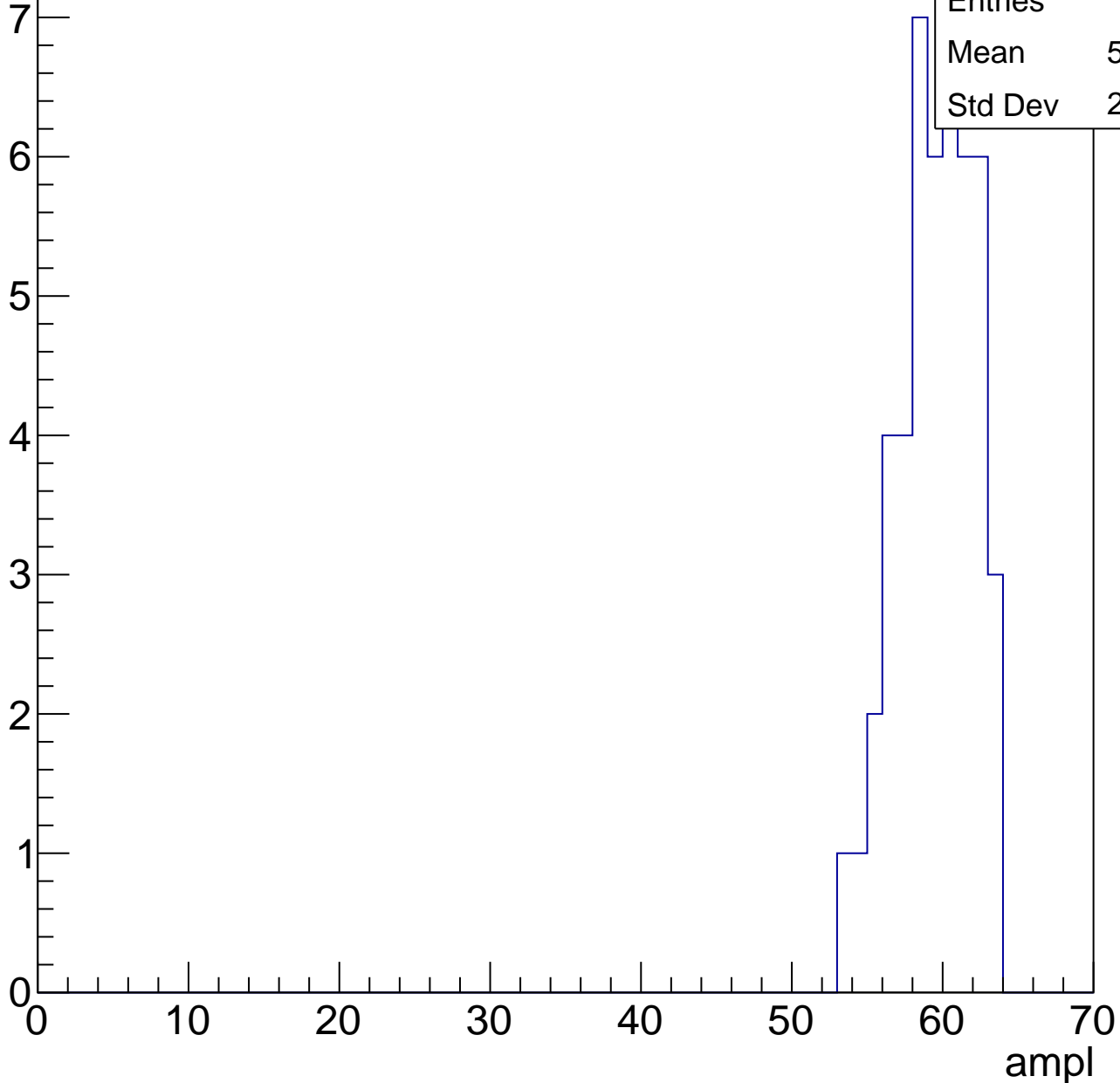


B1L103S, U24-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	59.06
Std Dev	2.462

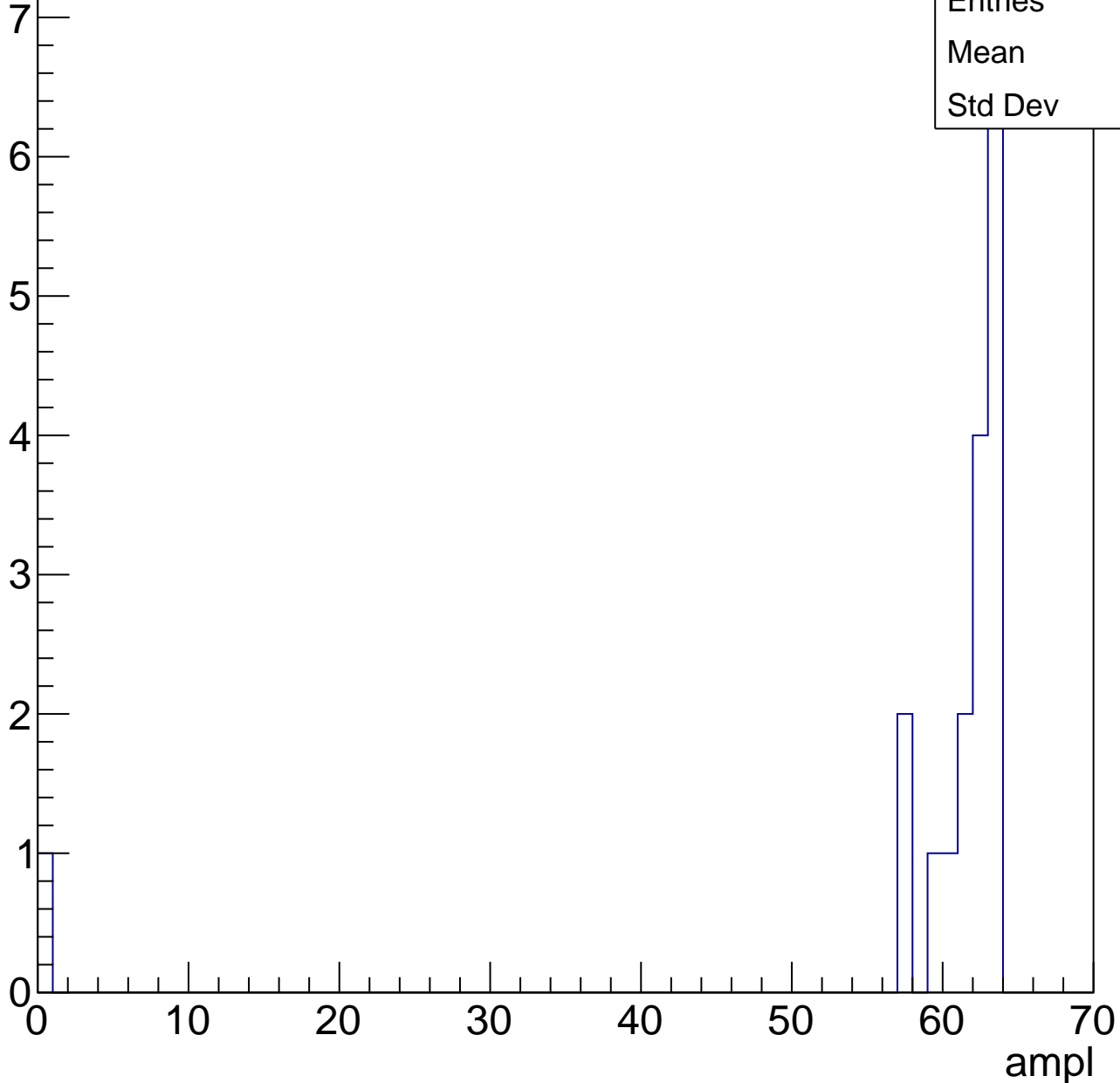


B1L103S, U24-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	18
Mean	58
Std Dev	14.2



B1L103S, U24-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch72, adc0

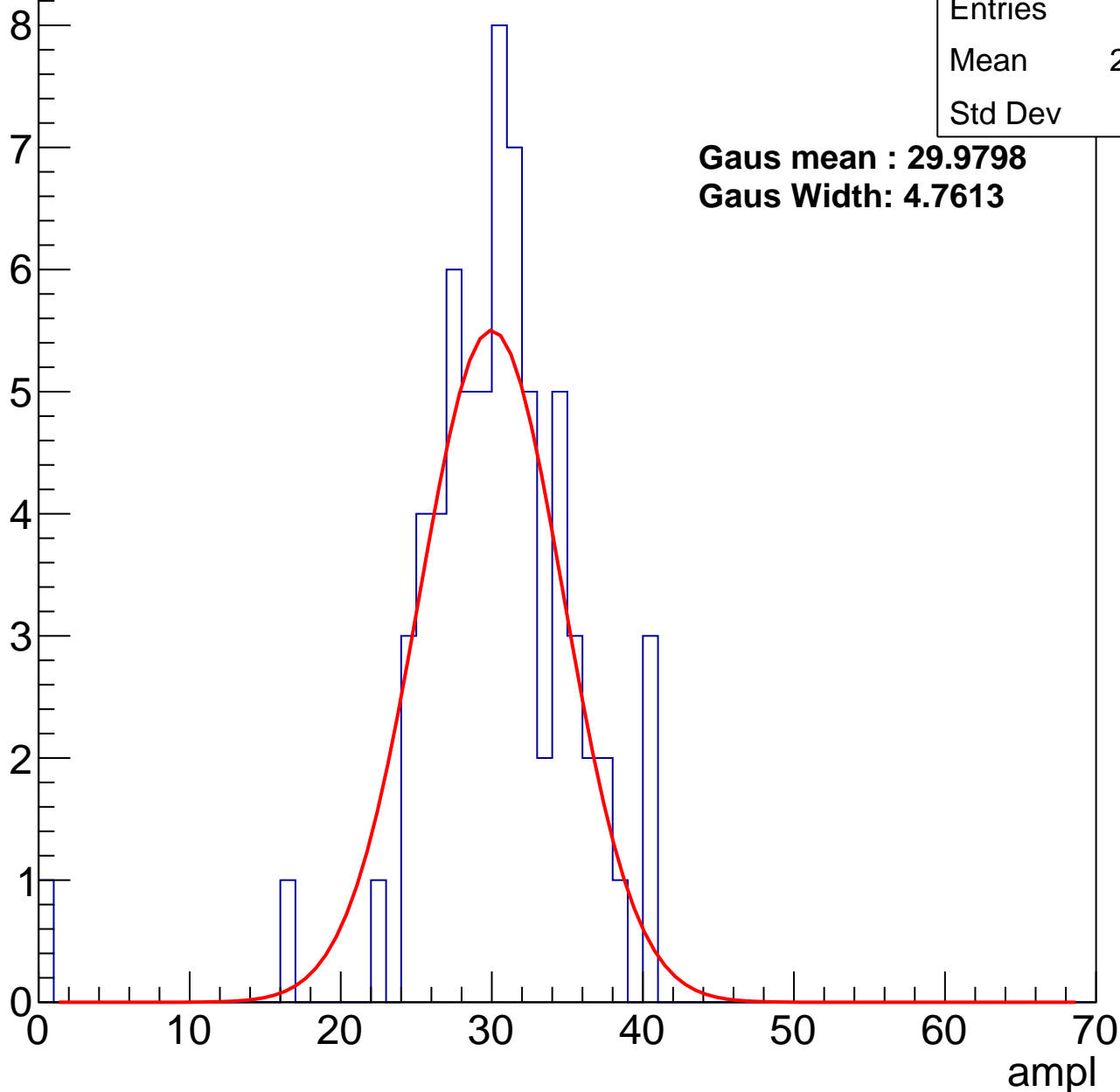
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.75
Std Dev	5.74

Gaus mean : 29.9798

Gaus Width: 4.7613



B1L103S, U24-ch72, adc1

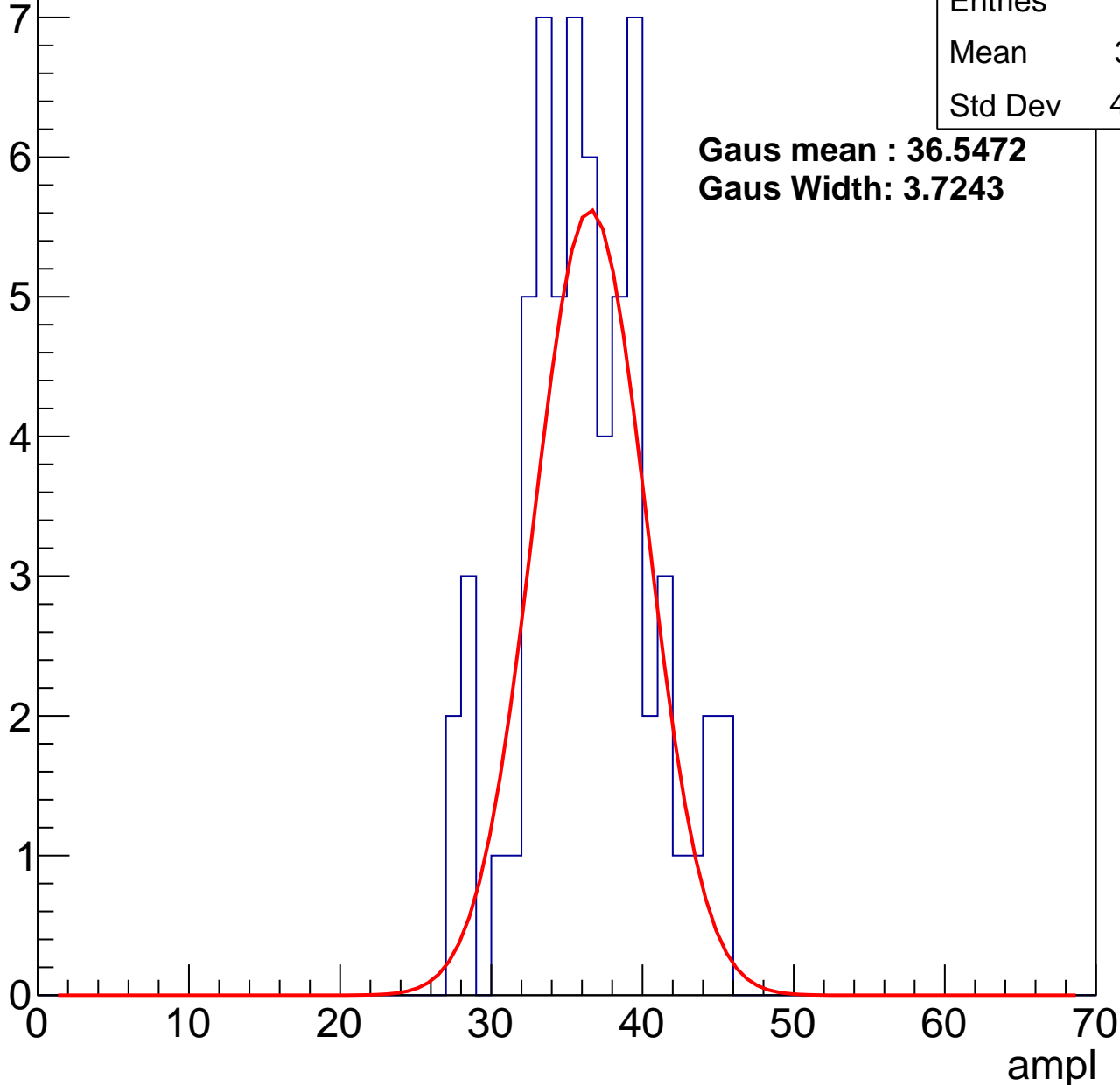
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.91
Std Dev	4.245

Gaus mean : 36.5472

Gaus Width: 3.7243



B1L103S, U24-ch72, adc2

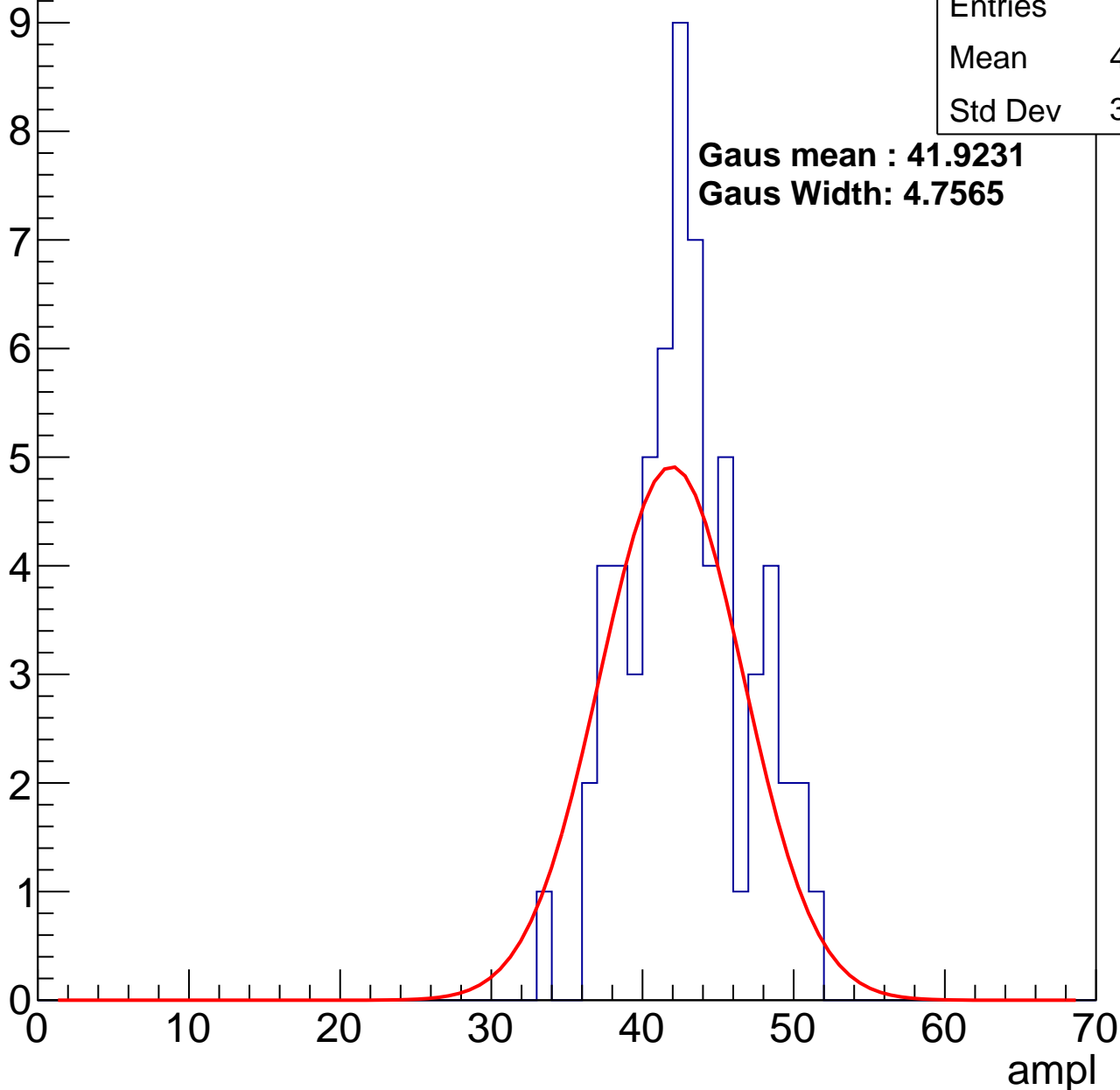
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.48
Std Dev	3.915

Gaus mean : 41.9231

Gaus Width: 4.7565

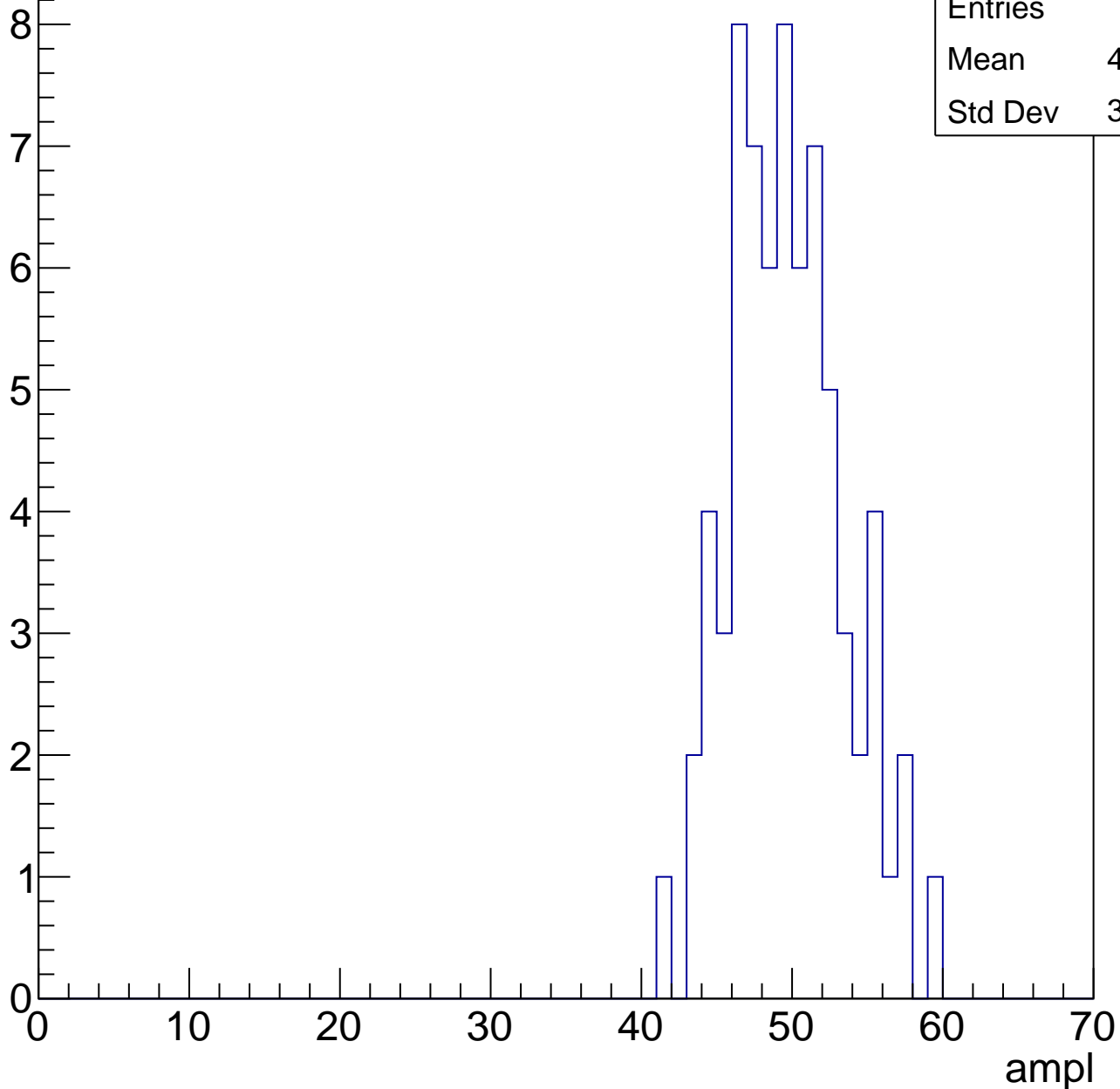


B1L103S, U24-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.26
Std Dev	3.748

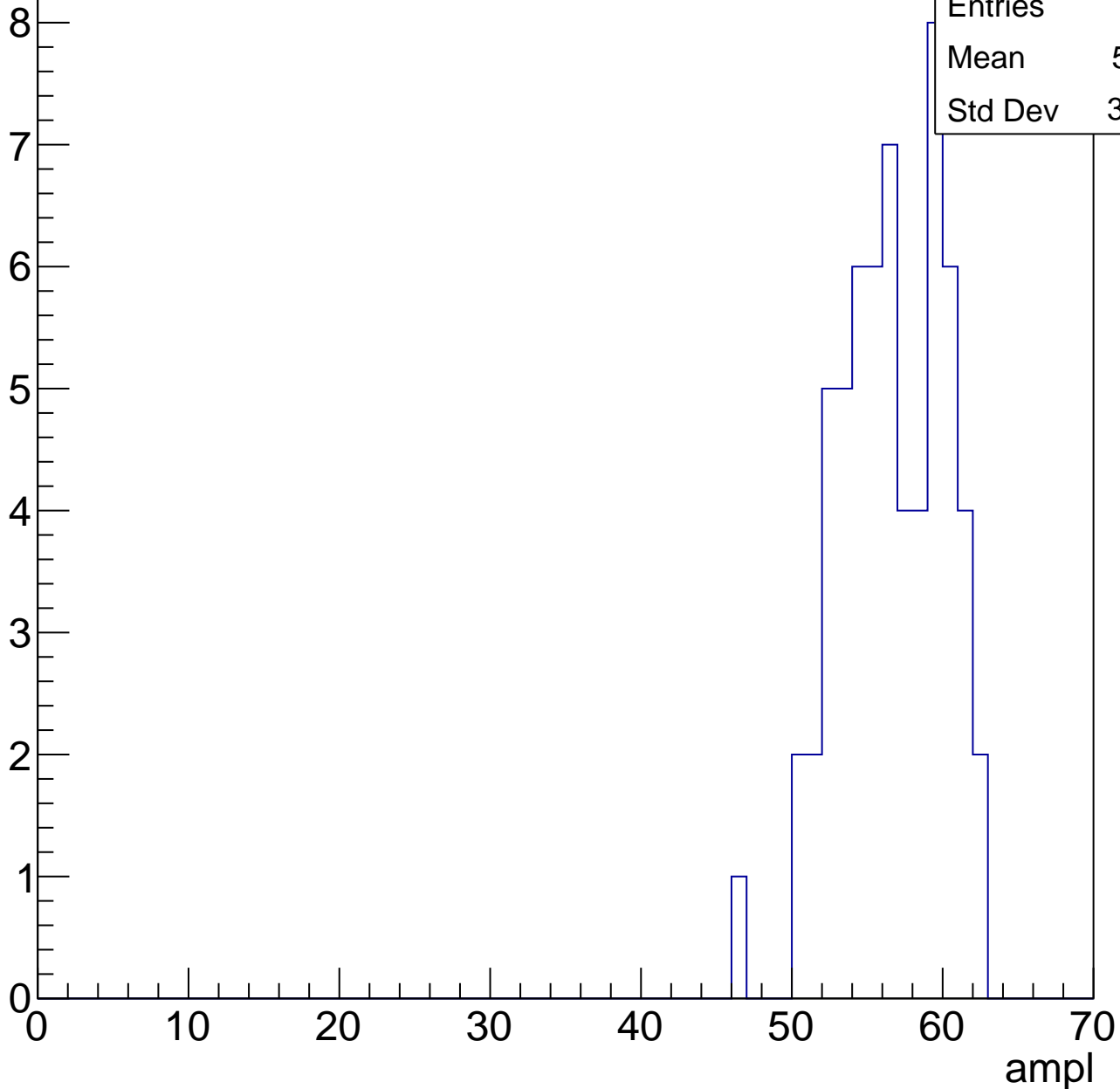


B1L103S, U24-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

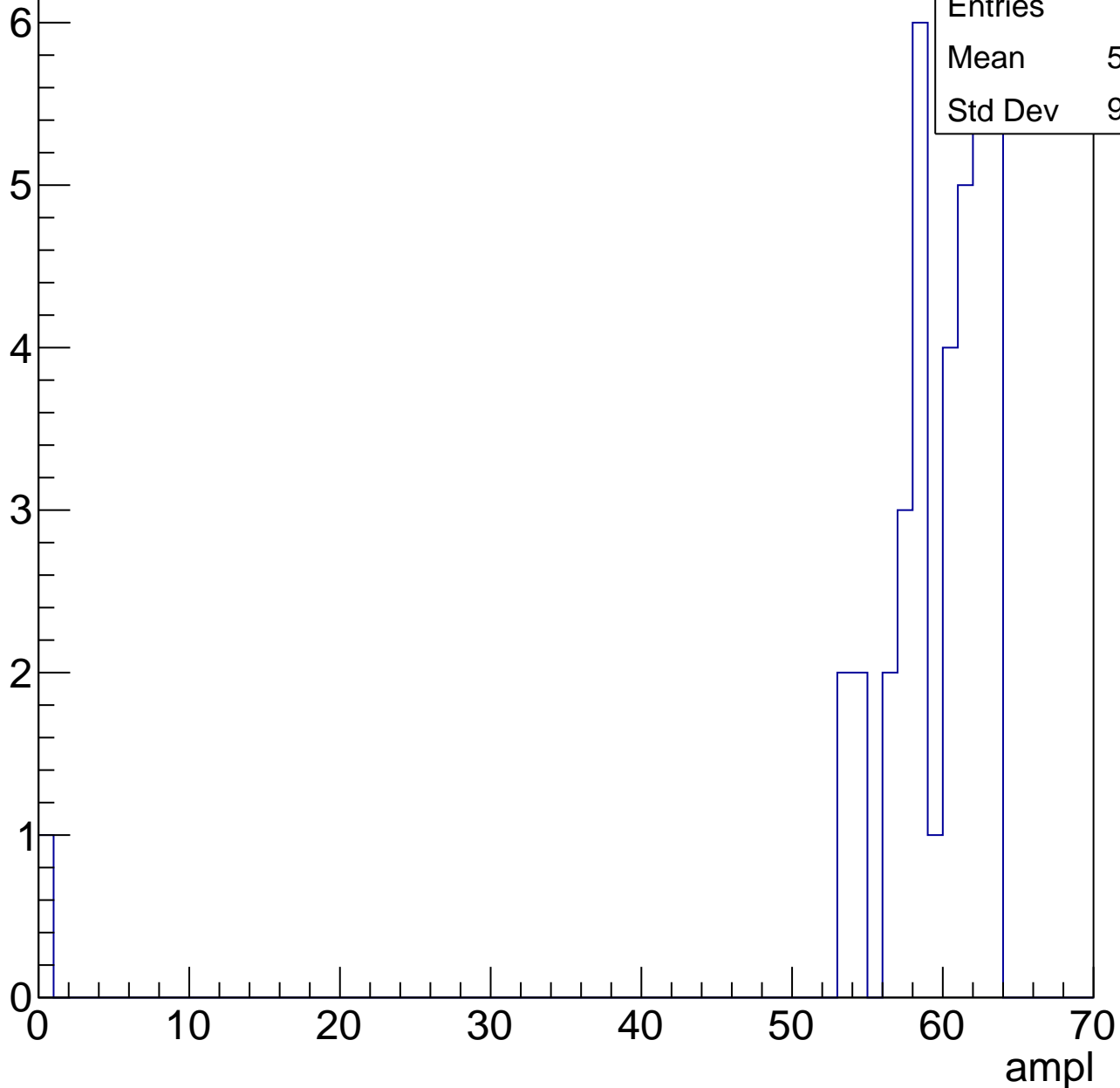
Entries	62
Mean	56.11
Std Dev	3.446



B1L103S, U24-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

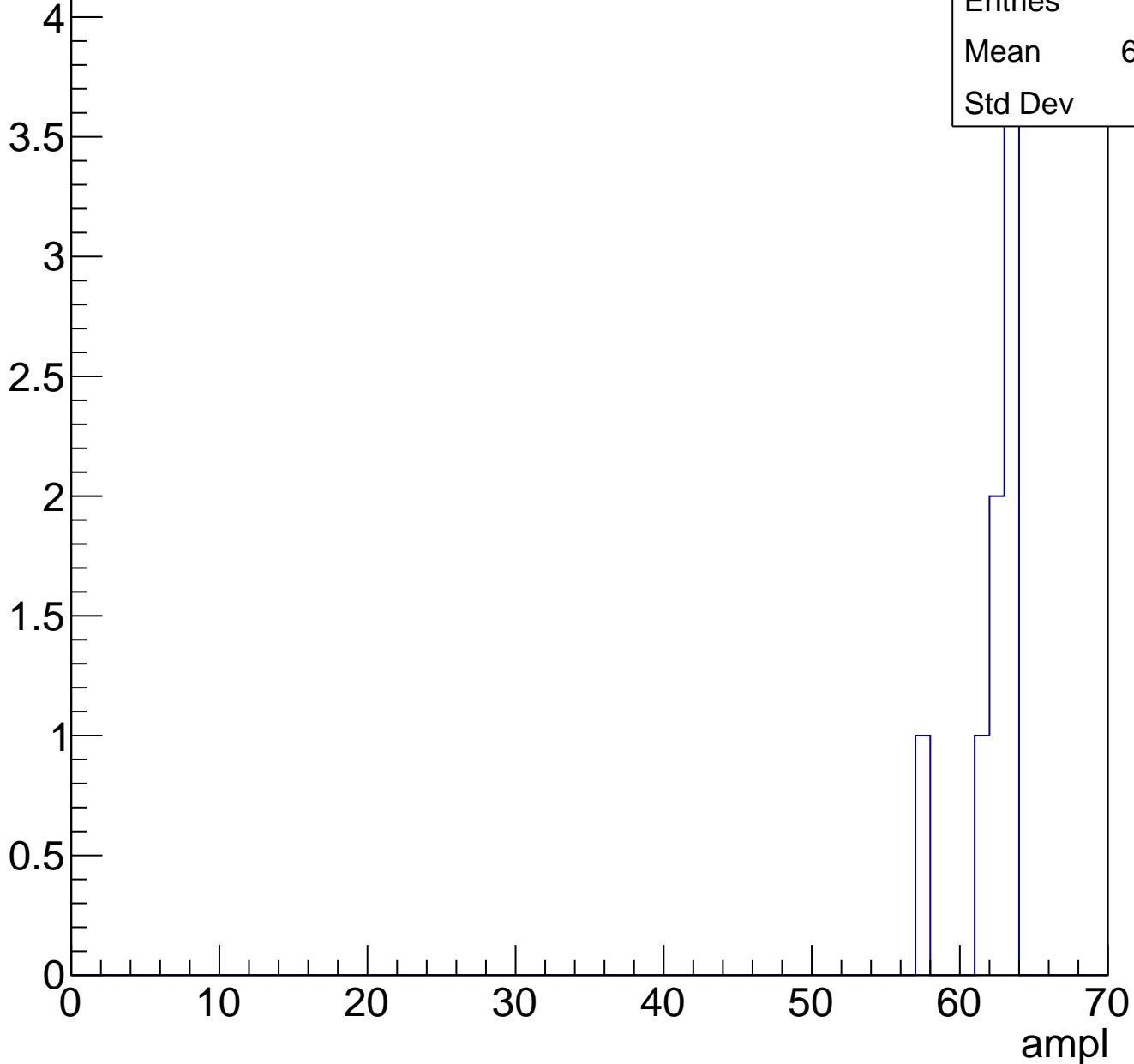
Entry



B1L103S, U24-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch73, adc0

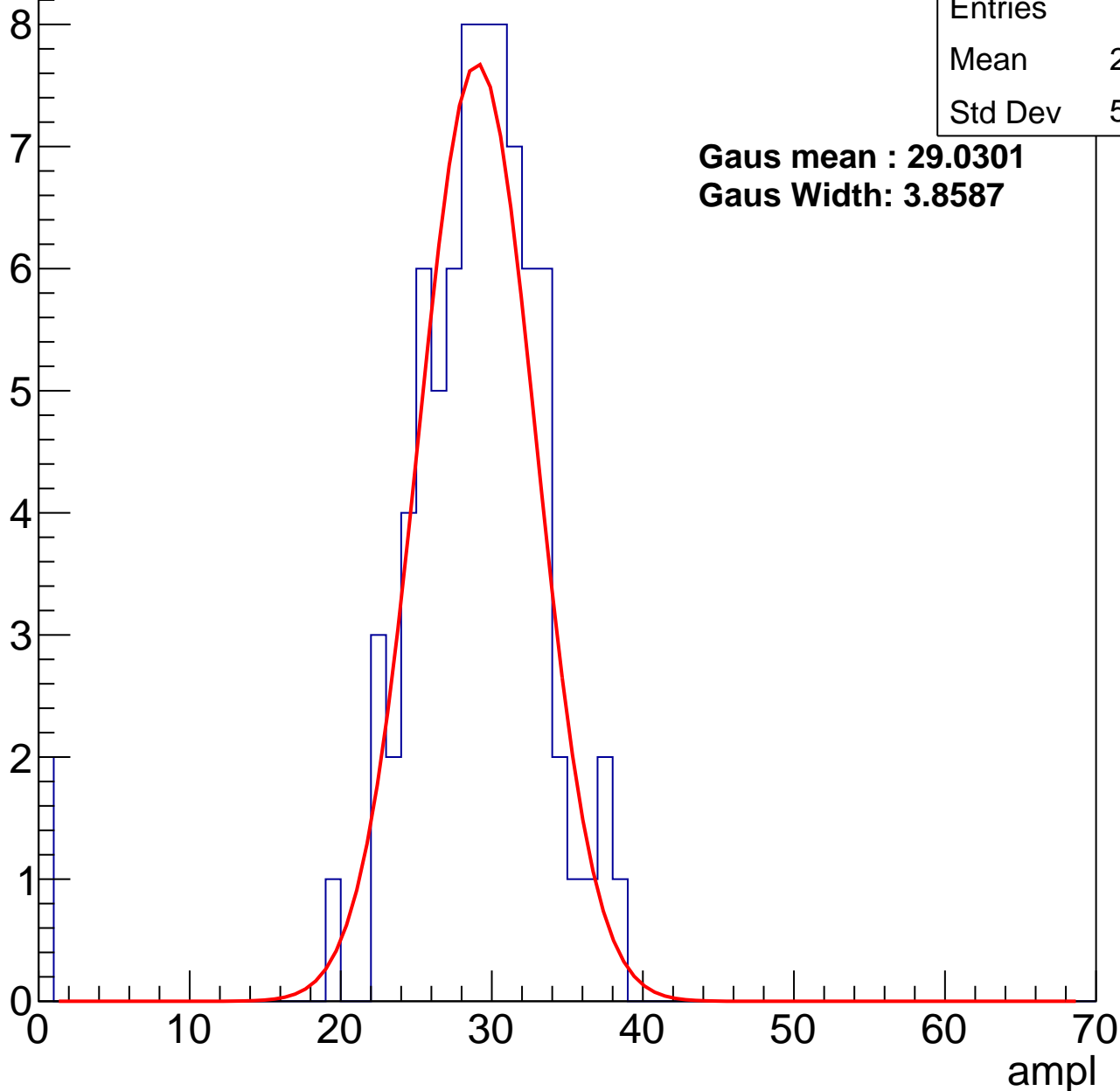
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.14
Std Dev	5.895

Gaus mean : 29.0301

Gaus Width: 3.8587



B1L103S, U24-ch73, adc1

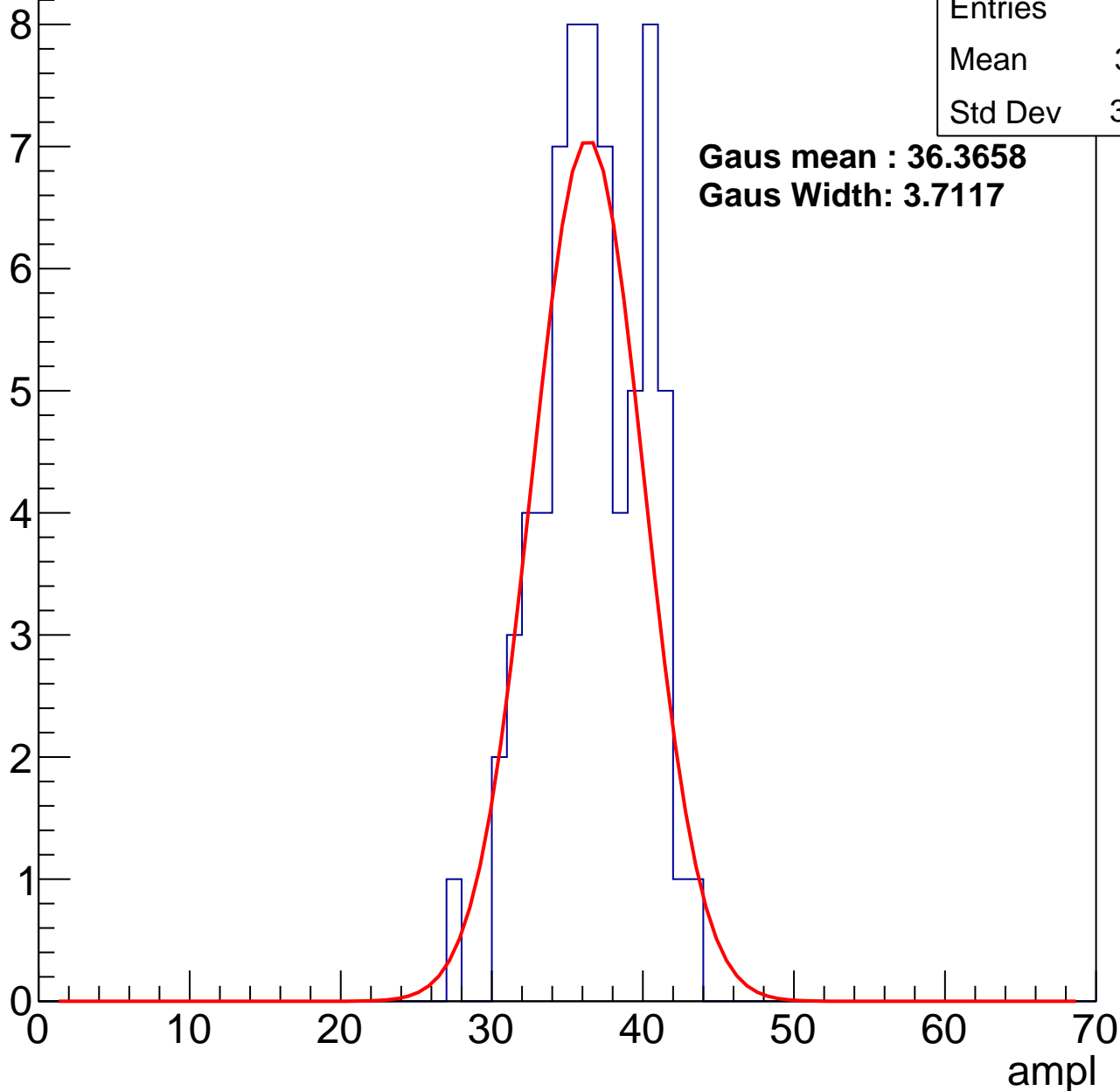
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.21
Std Dev	3.359

Gaus mean : 36.3658

Gaus Width: 3.7117



B1L103S, U24-ch73, adc2

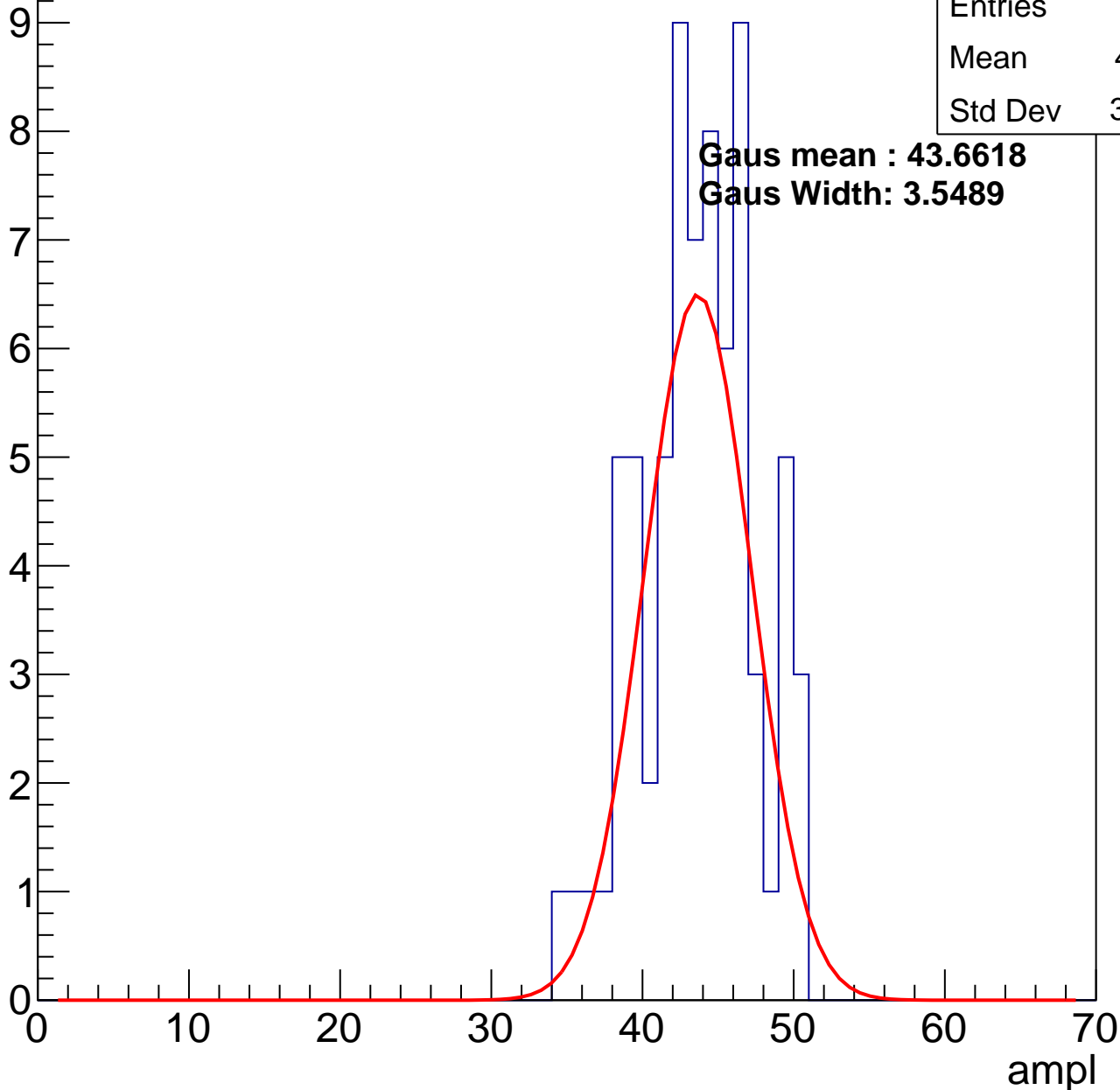
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.21
Std Dev	3.712

Gaus mean : 43.6618

Gaus Width: 3.5489

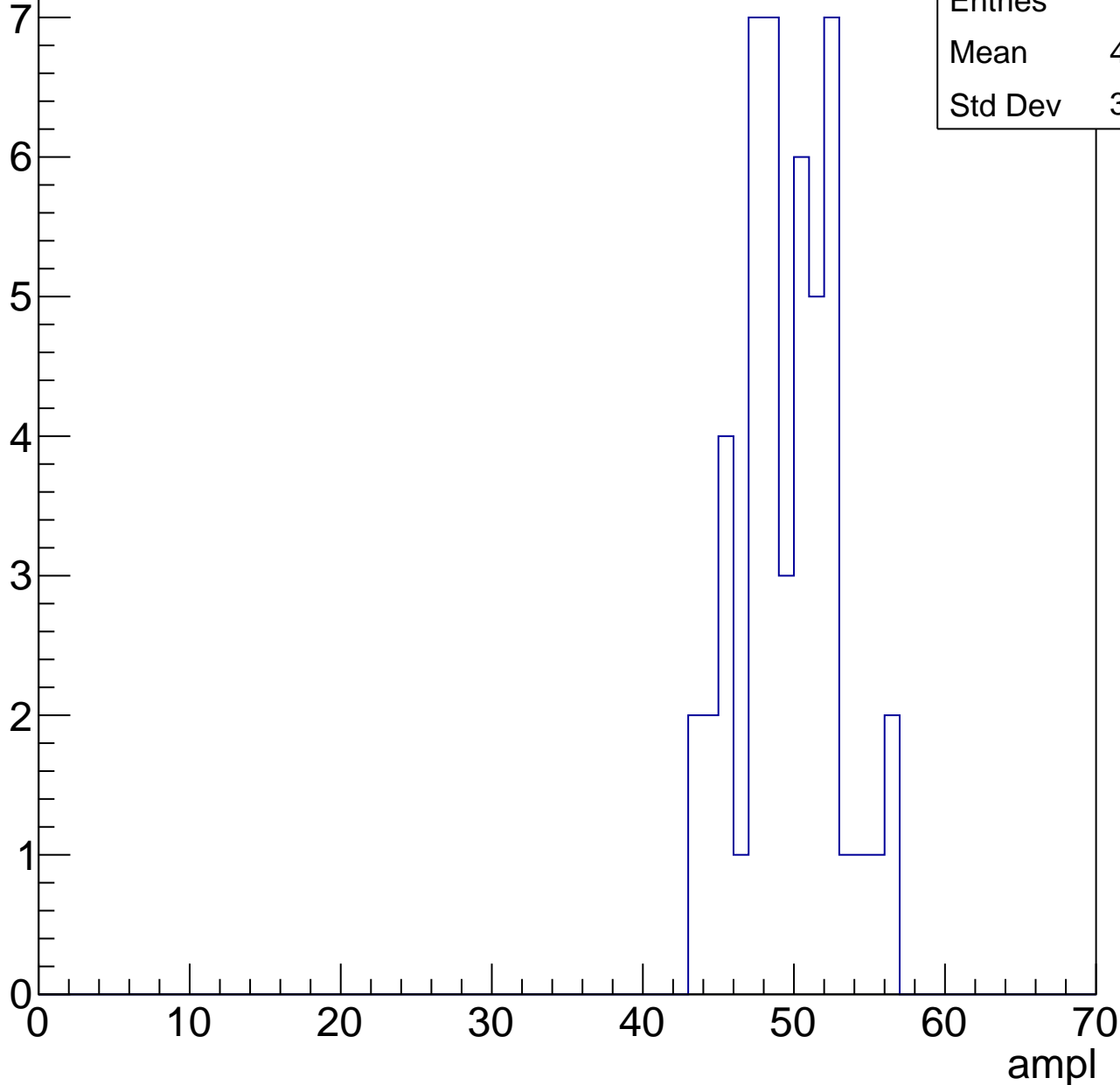


B1L103S, U24-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	49.08
Std Dev	3.174

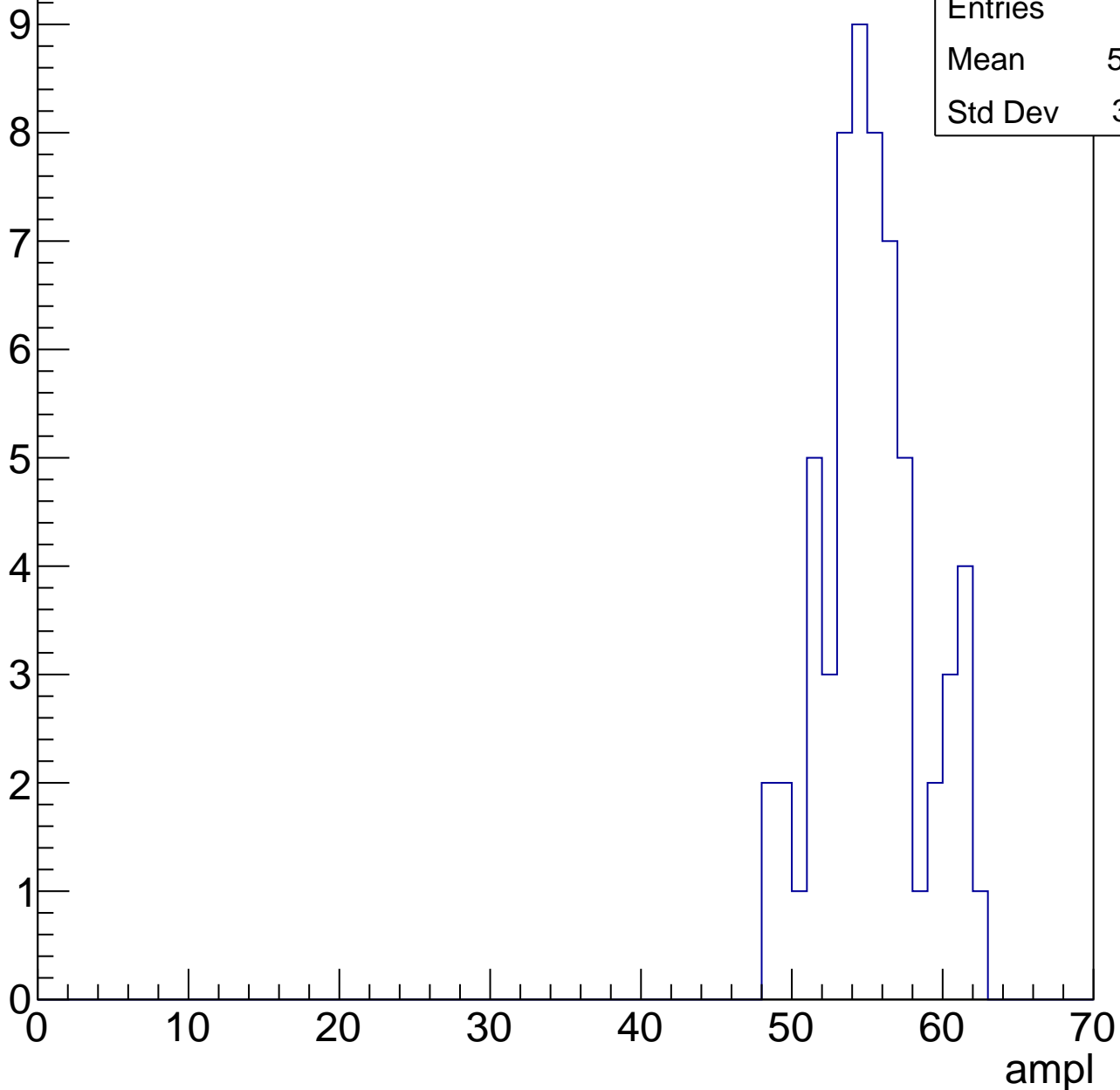


B1L103S, U24-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

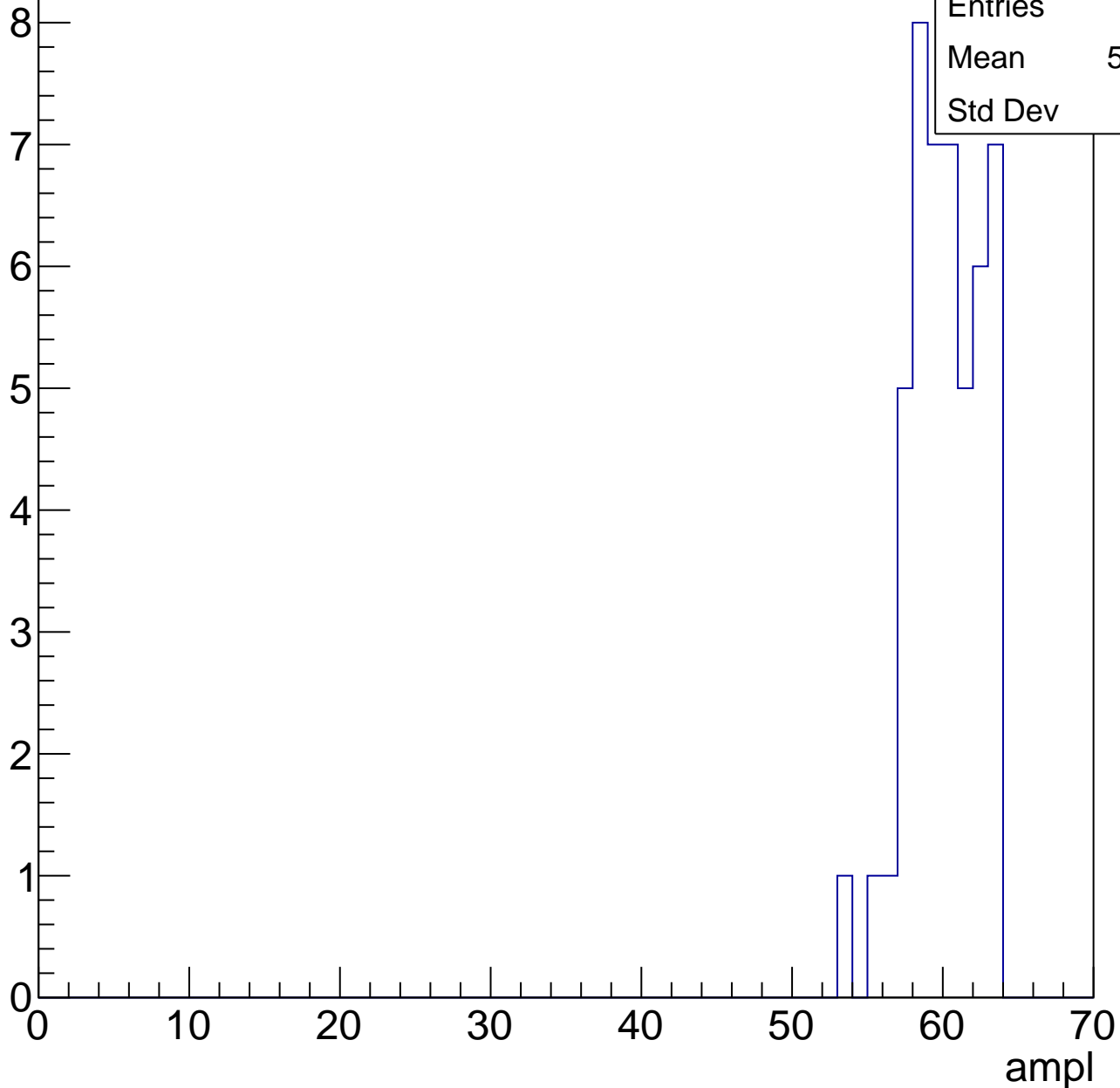
Entries	61
Mean	54.82
Std Dev	3.331



B1L103S, U24-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

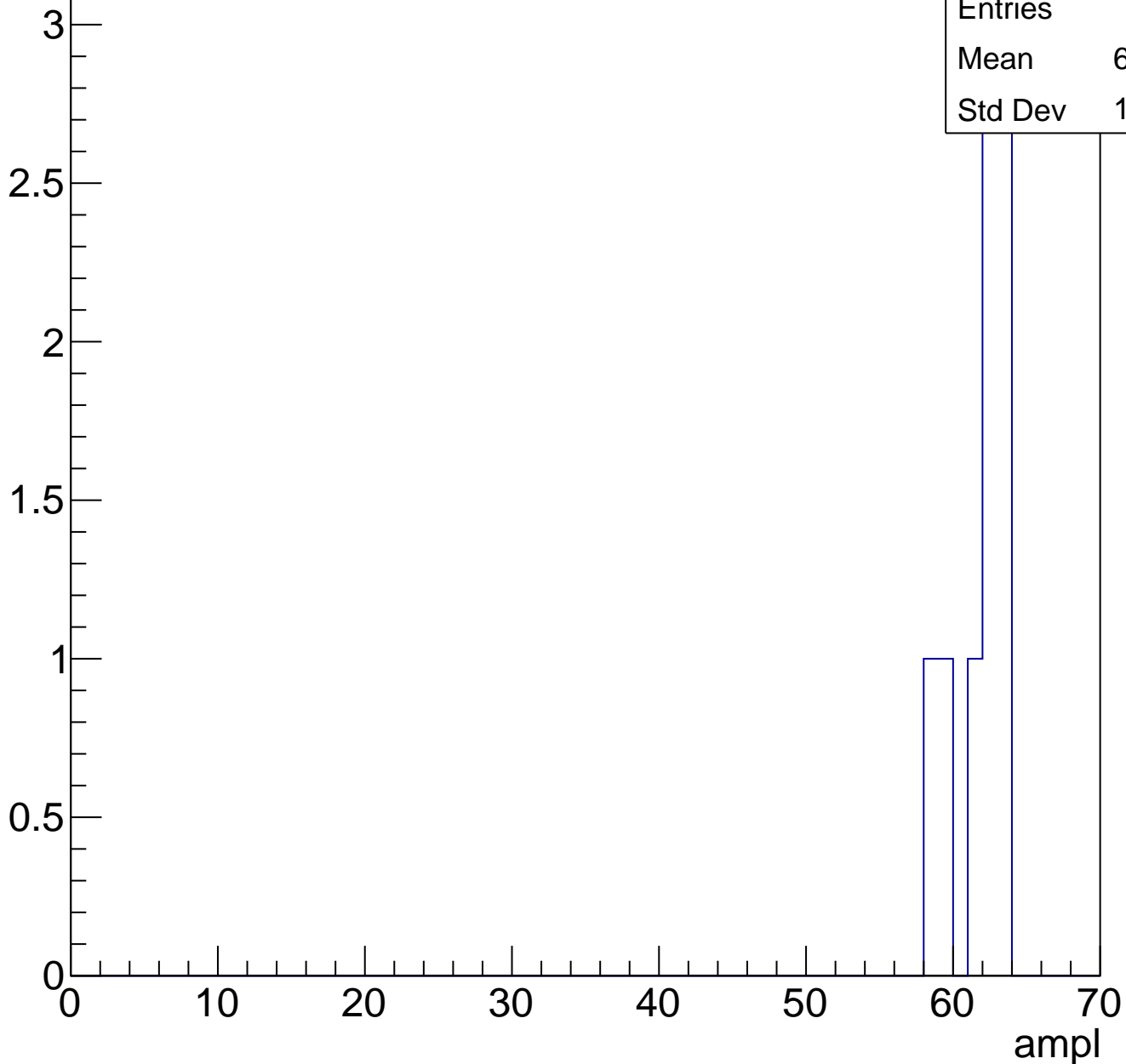
Entry



B1L103S, U24-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch74, adc0

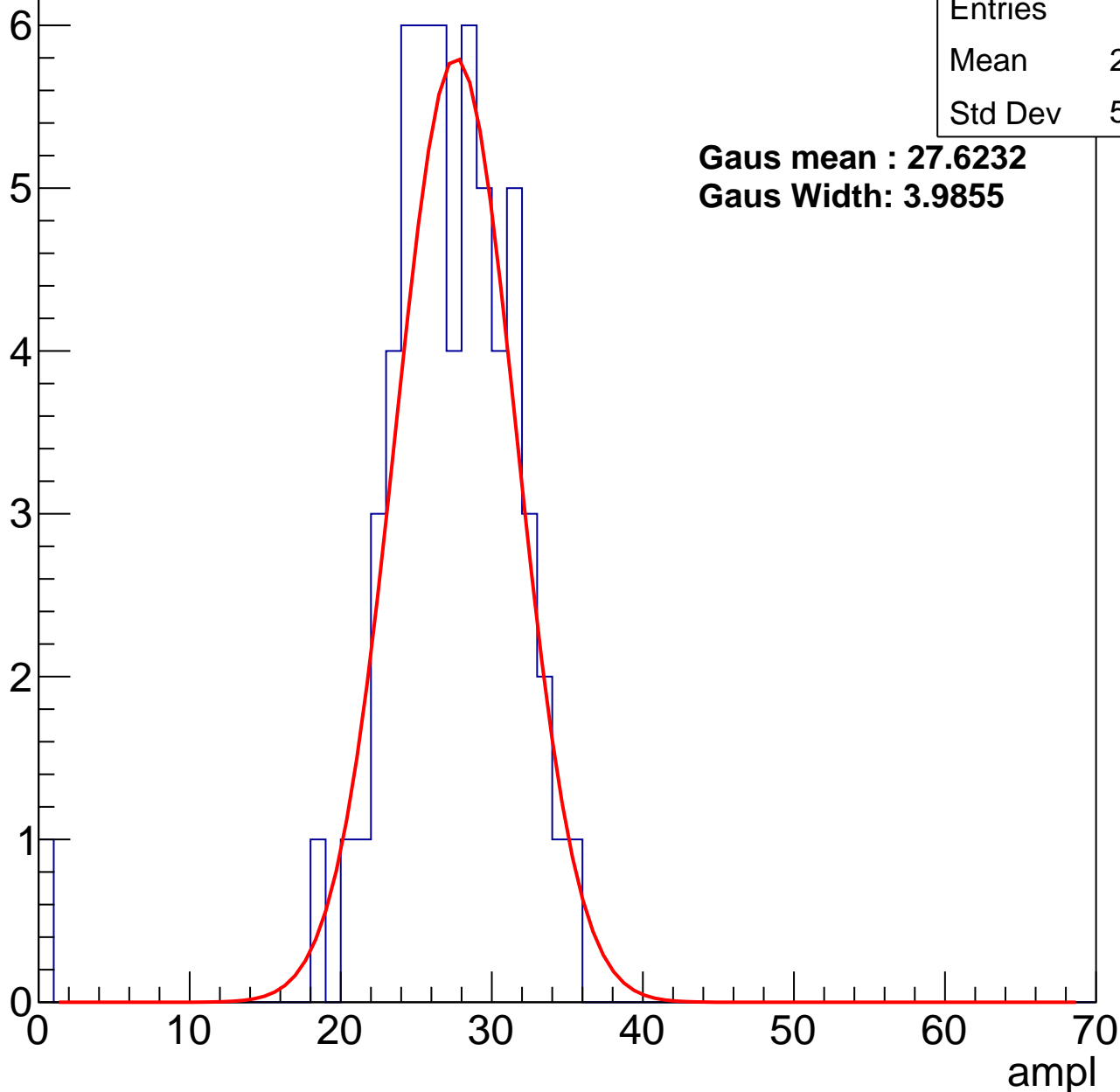
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	26.57
Std Dev	5.015

Gaus mean : 27.6232

Gaus Width: 3.9855



B1L103S, U24-ch74, adc1

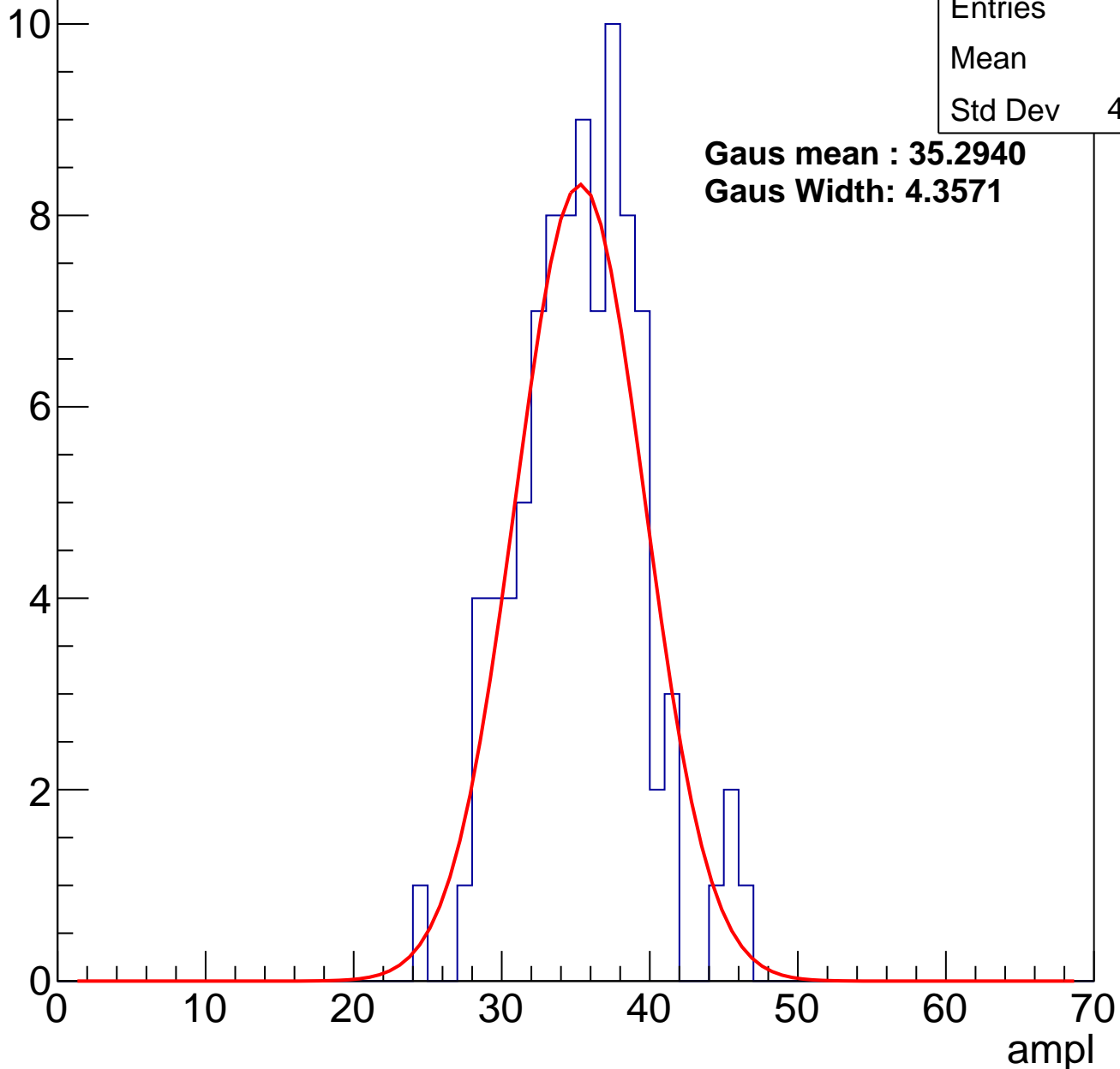
calib_packv5_042523_0143.root, FC#7, port C2

Entries	92
Mean	34.9
Std Dev	4.168

Gaus mean : 35.2940

Gaus Width: 4.3571

Entry



B1L103S, U24-ch74, adc2

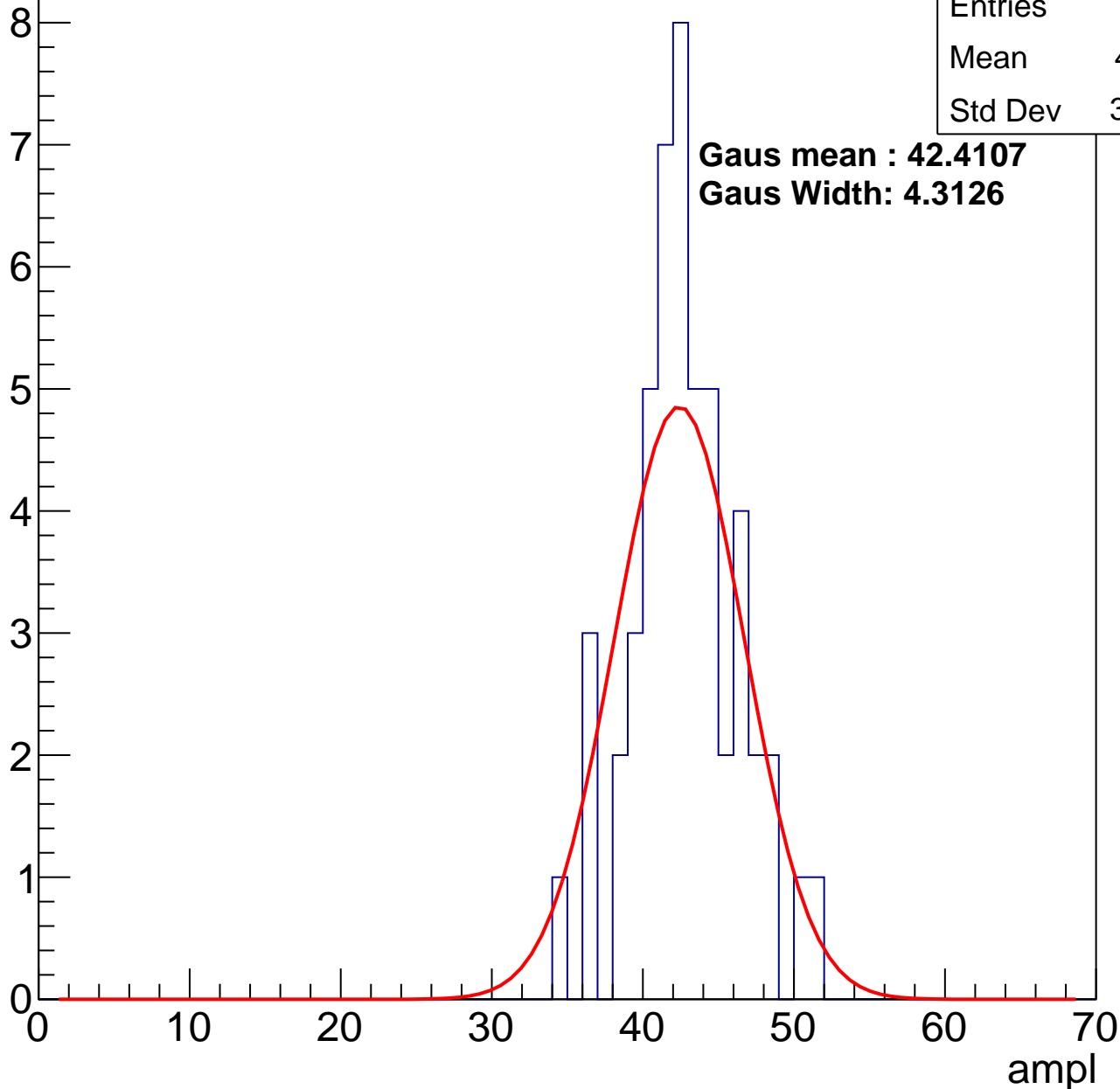
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	42.31
Std Dev	3.506

Gaus mean : 42.4107

Gaus Width: 4.3126

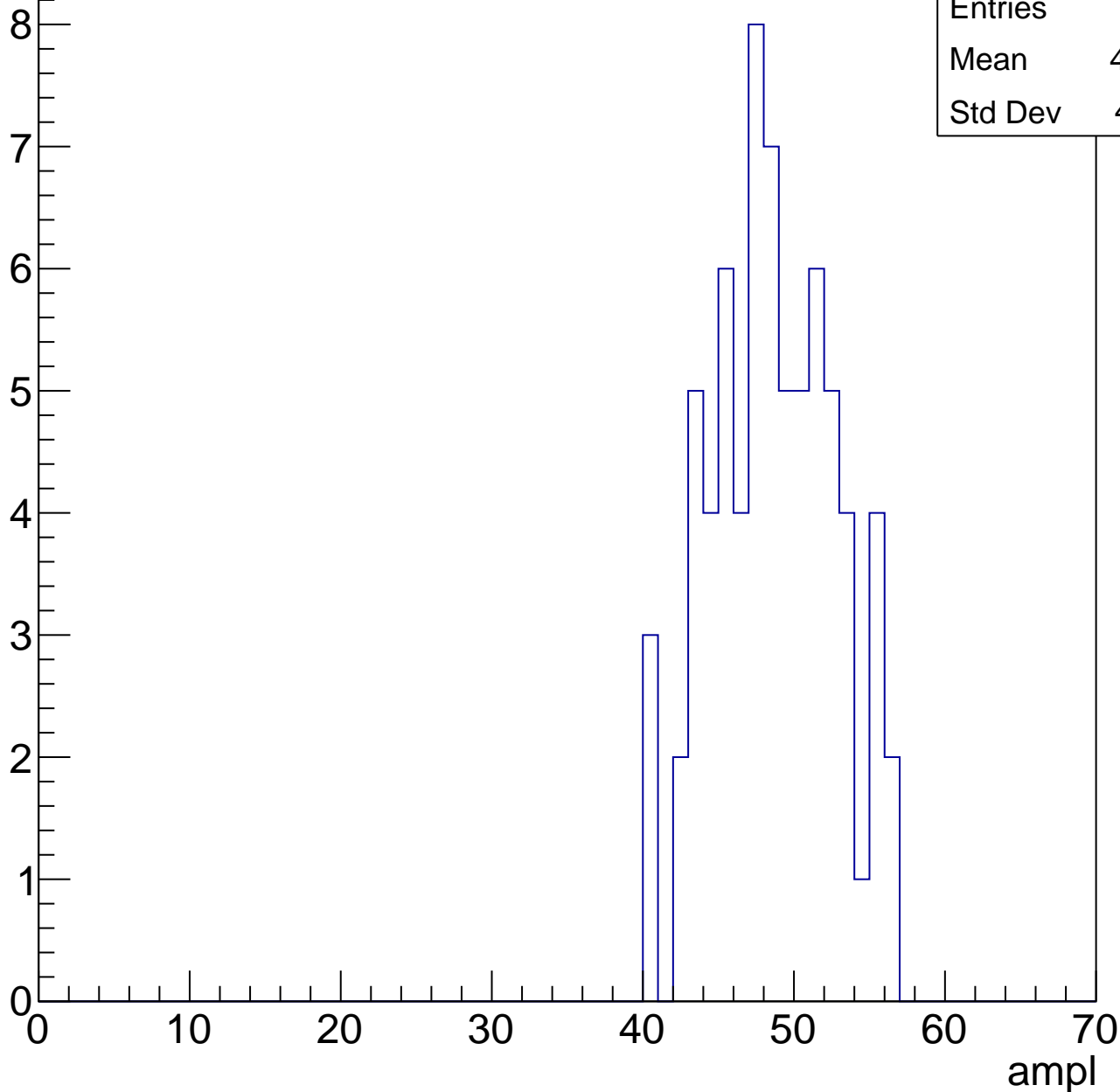


B1L103S, U24-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.17
Std Dev	4.021

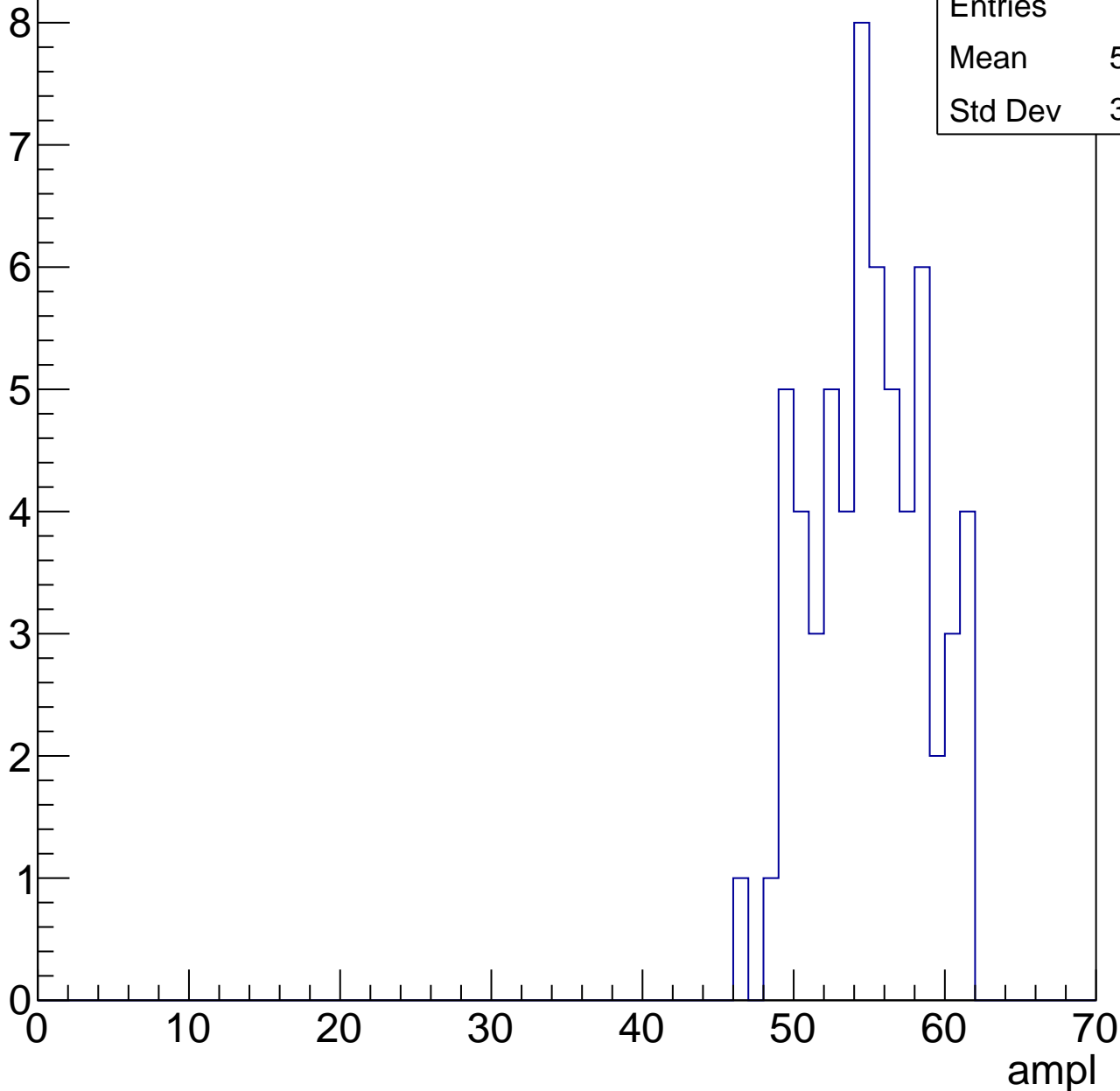


B1L103S, U24-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.49
Std Dev	3.705

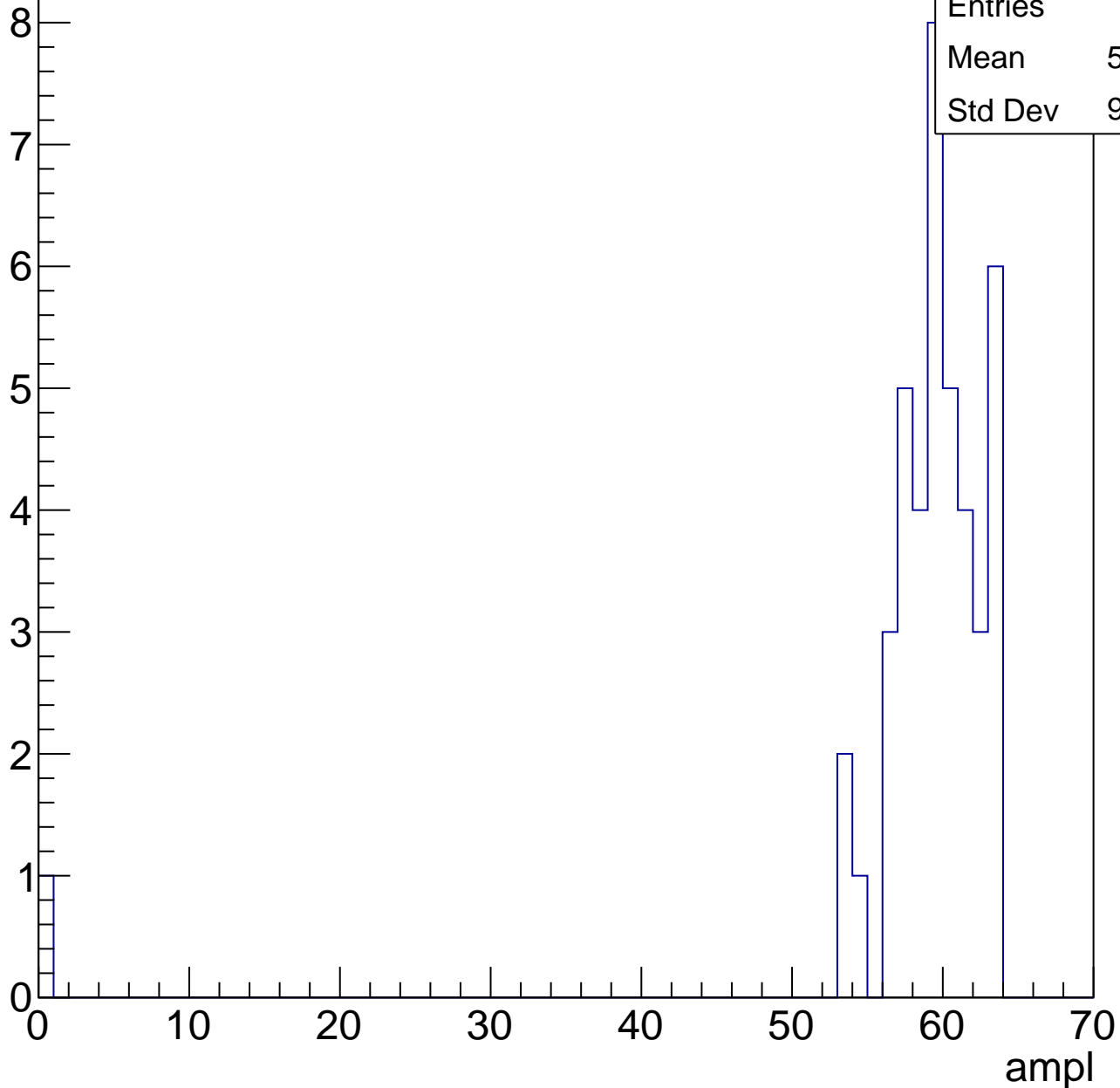


B1L103S, U24-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

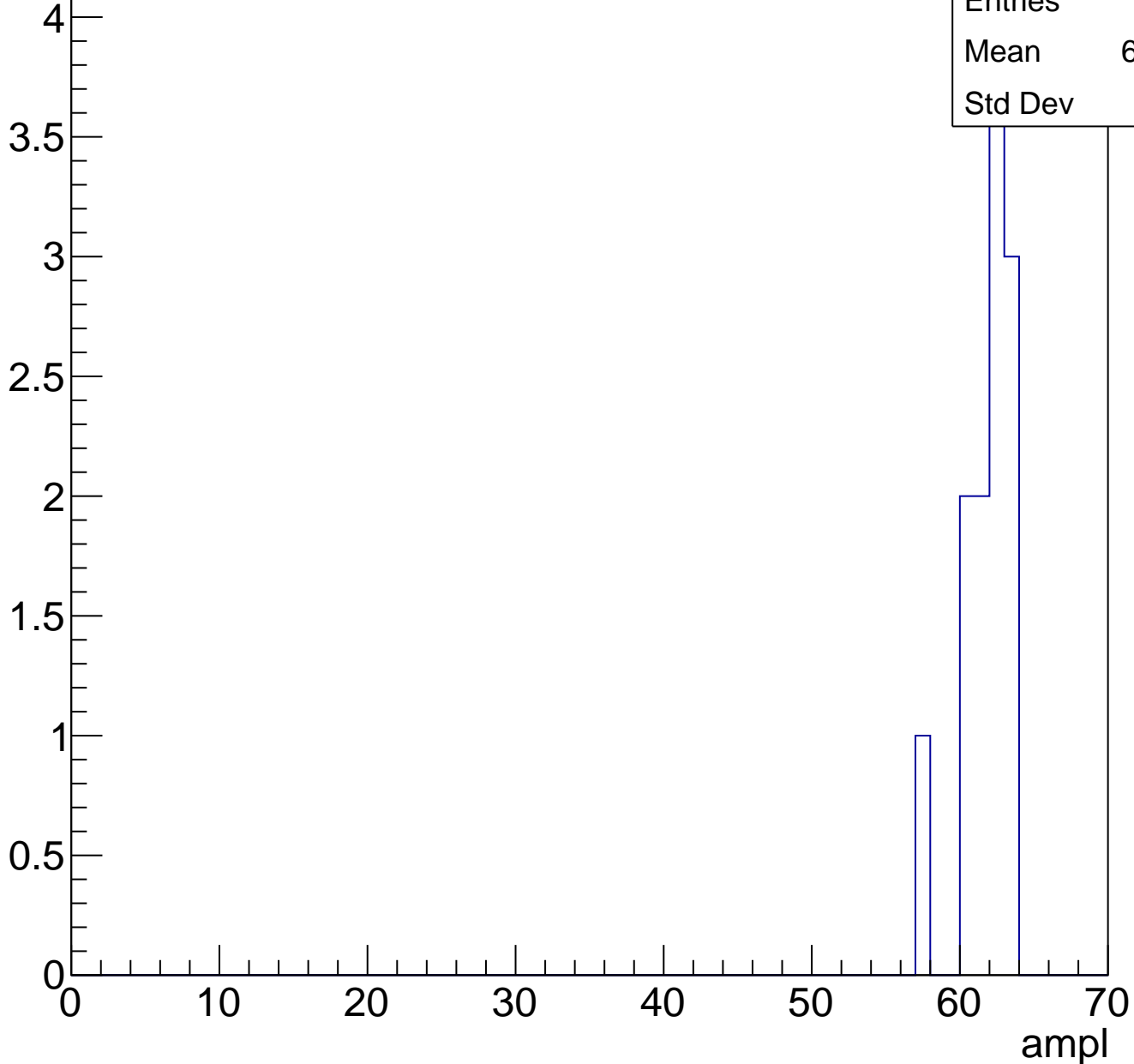
Entries	42
Mean	57.74
Std Dev	9.394



B1L103S, U24-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

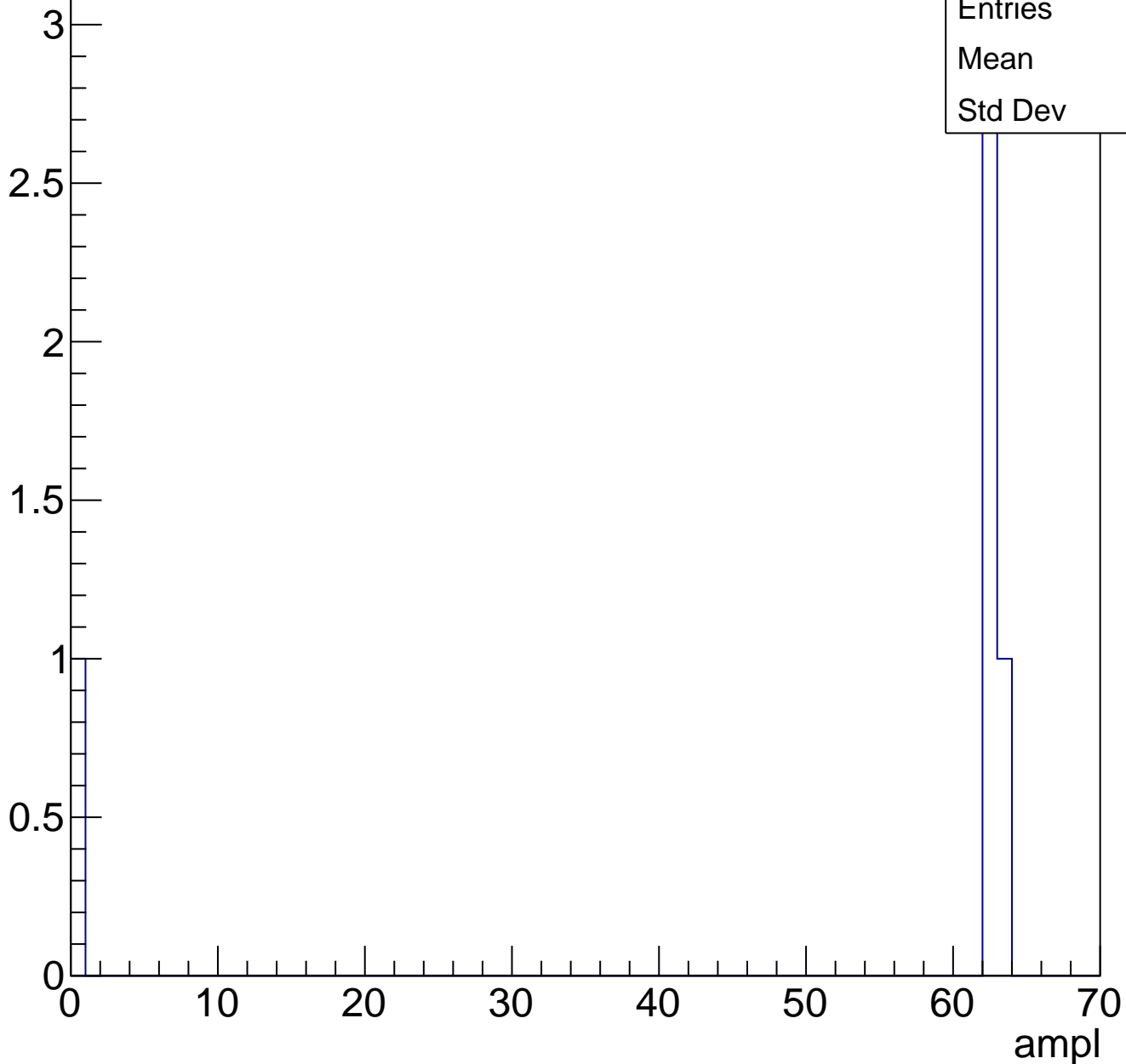
Entry



B1L103S, U24-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch75, adc0

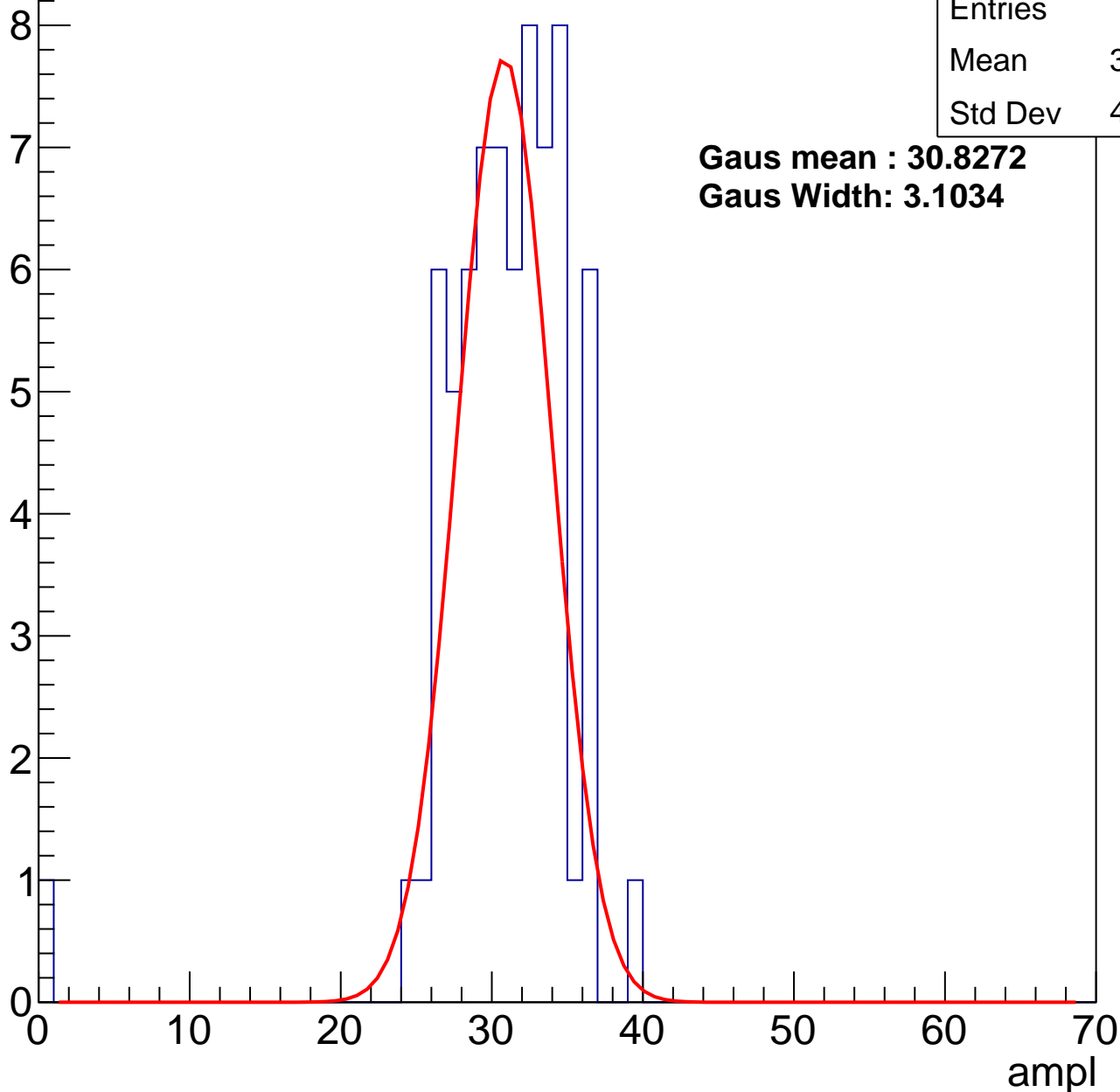
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.37
Std Dev	4.856

Gaus mean : 30.8272

Gaus Width: 3.1034



B1L103S, U24-ch75, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	38.35
Std Dev	3.473

Gaus mean : 38.6615

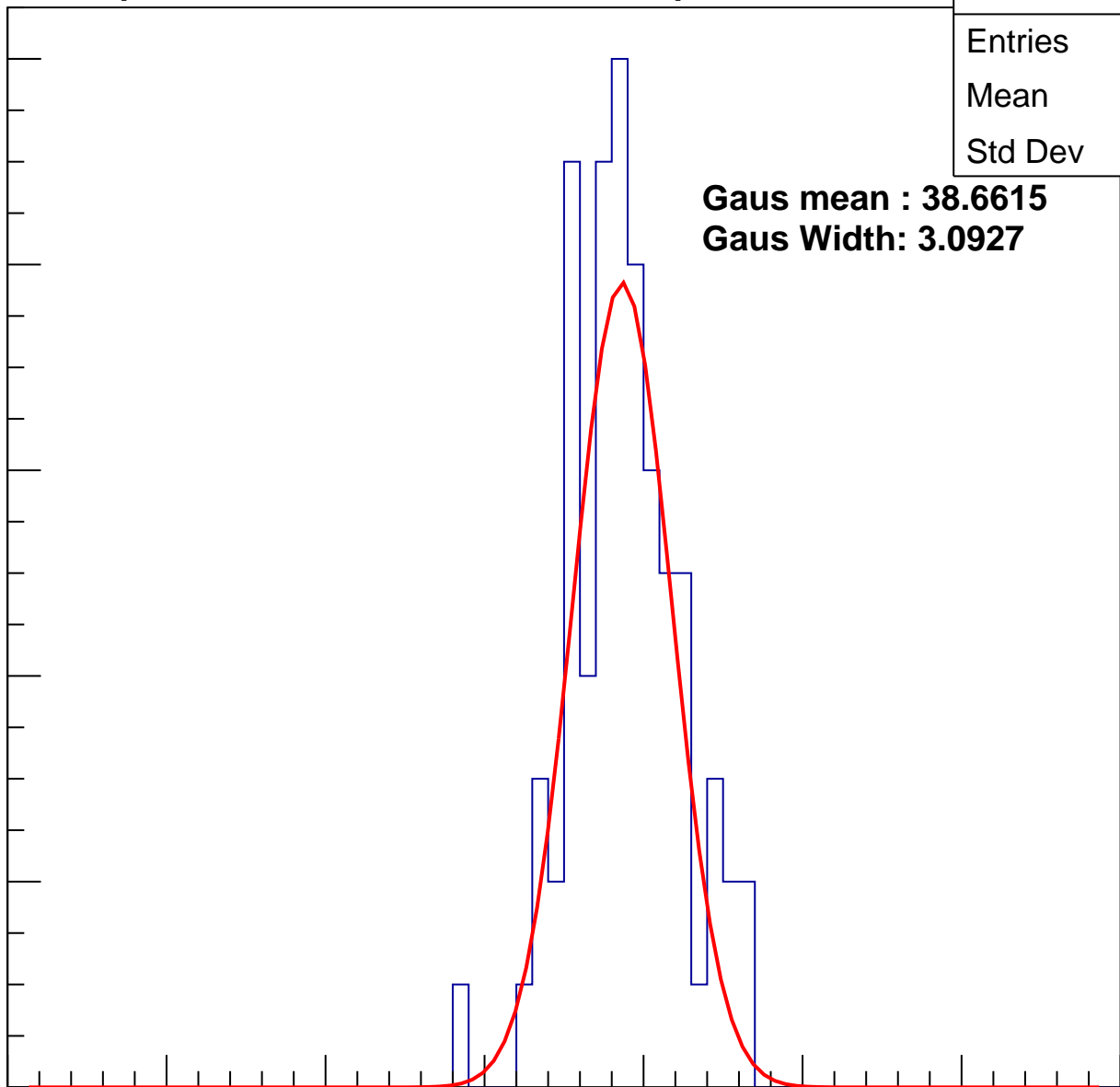
Gaus Width: 3.0927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch75, adc2

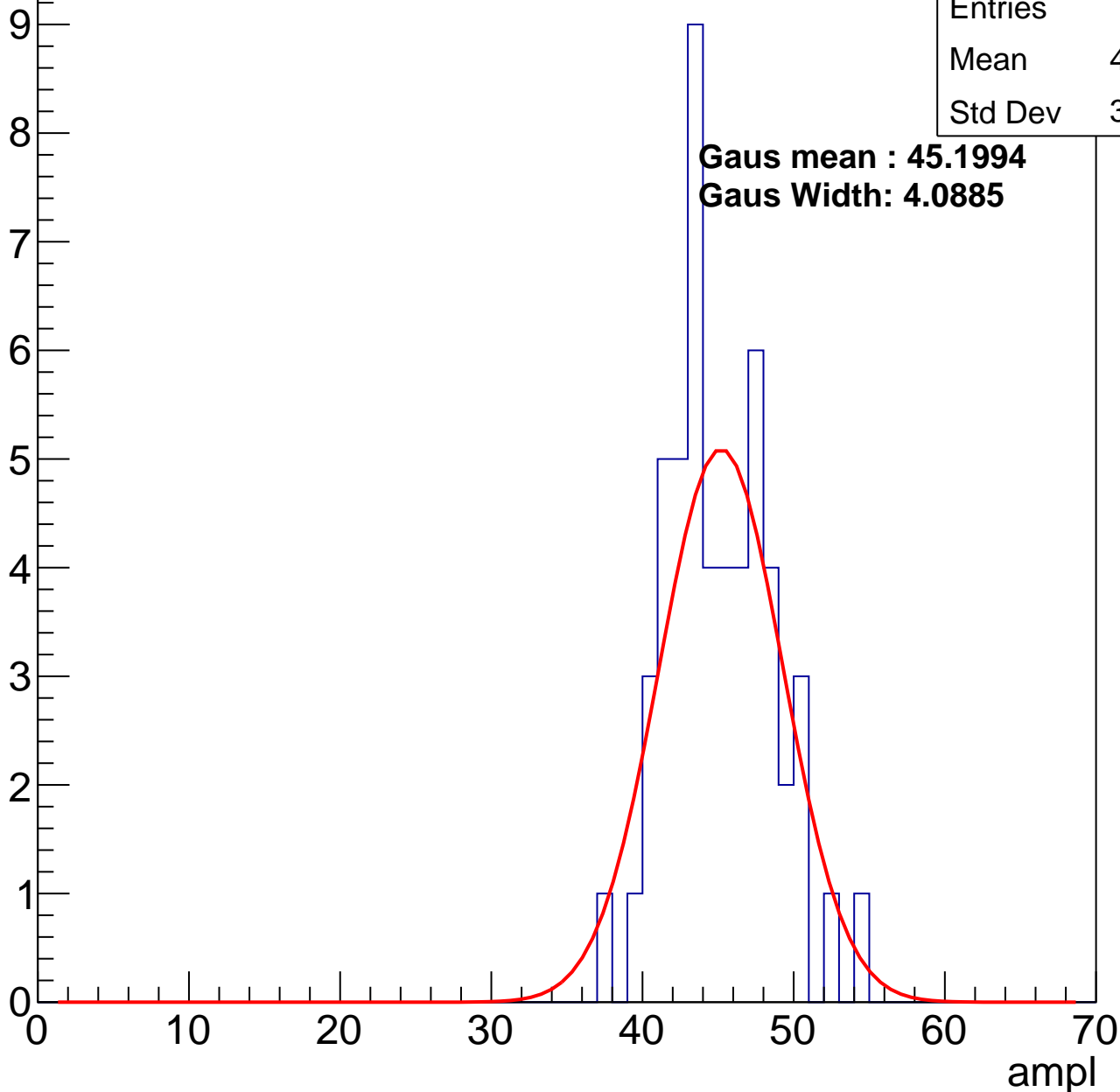
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.64
Std Dev	3.459

Gaus mean : 45.1994

Gaus Width: 4.0885

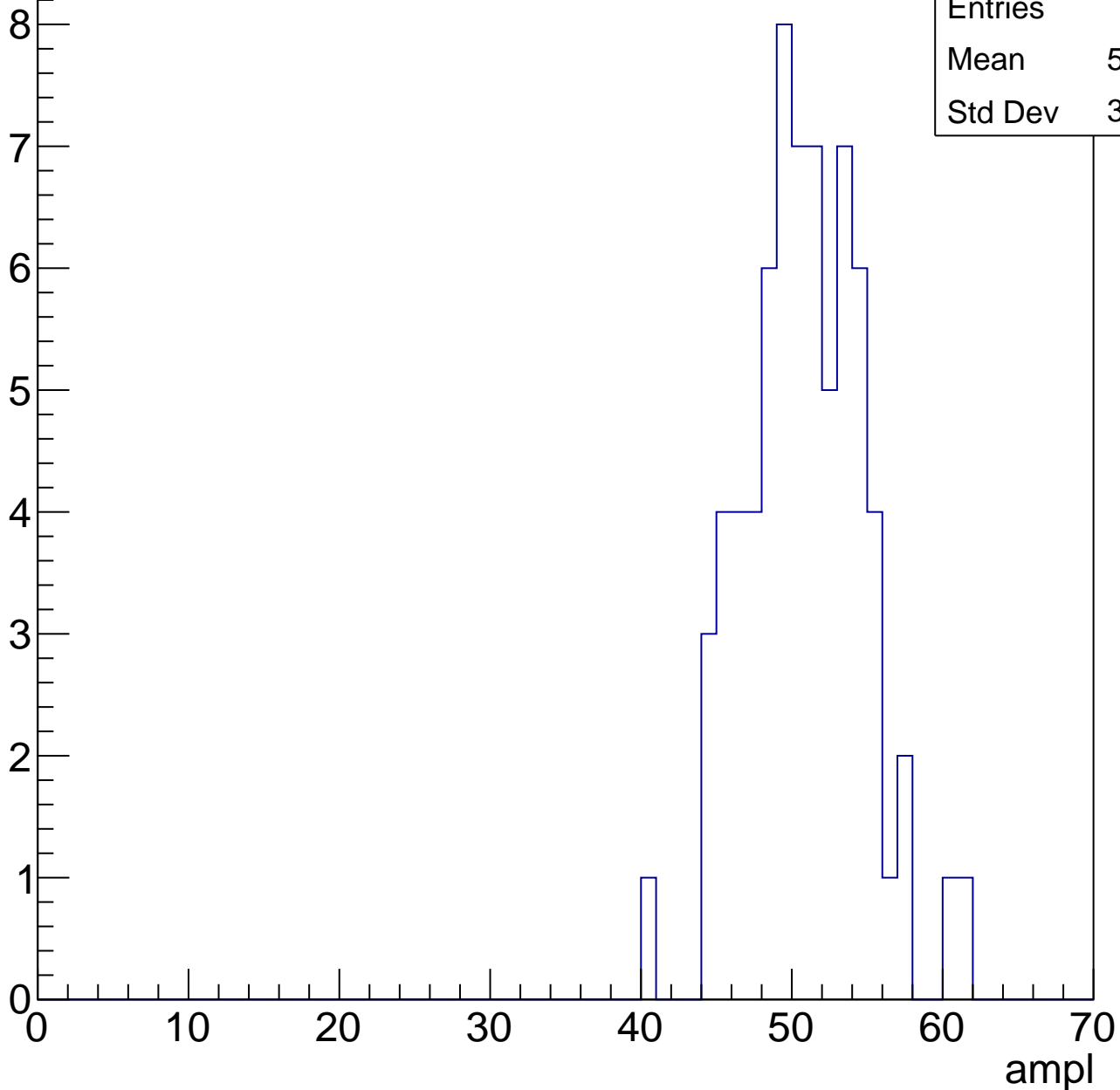


B1L103S, U24-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.38
Std Dev	3.883

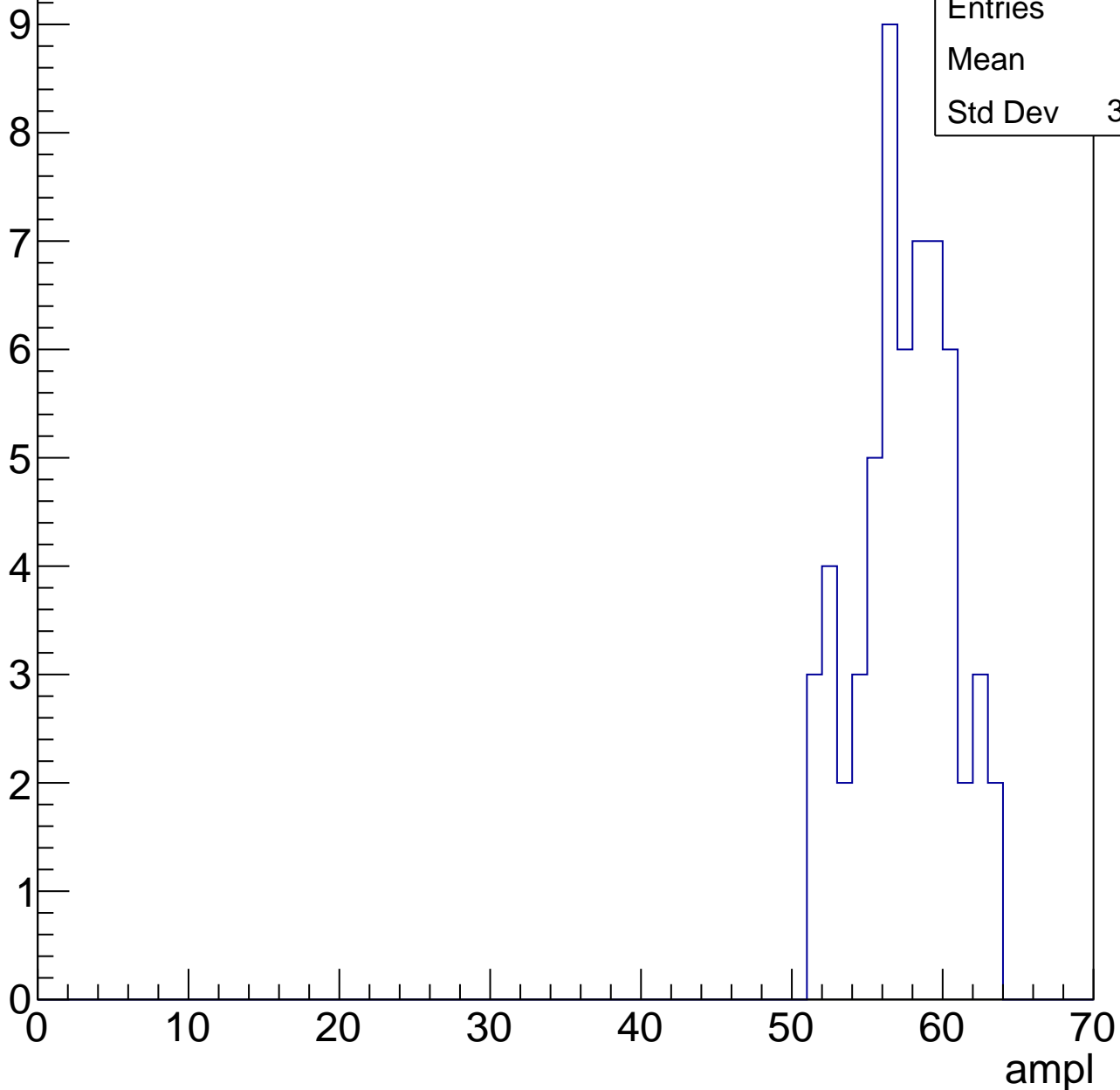


B1L103S, U24-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	57
Std Dev	3.092

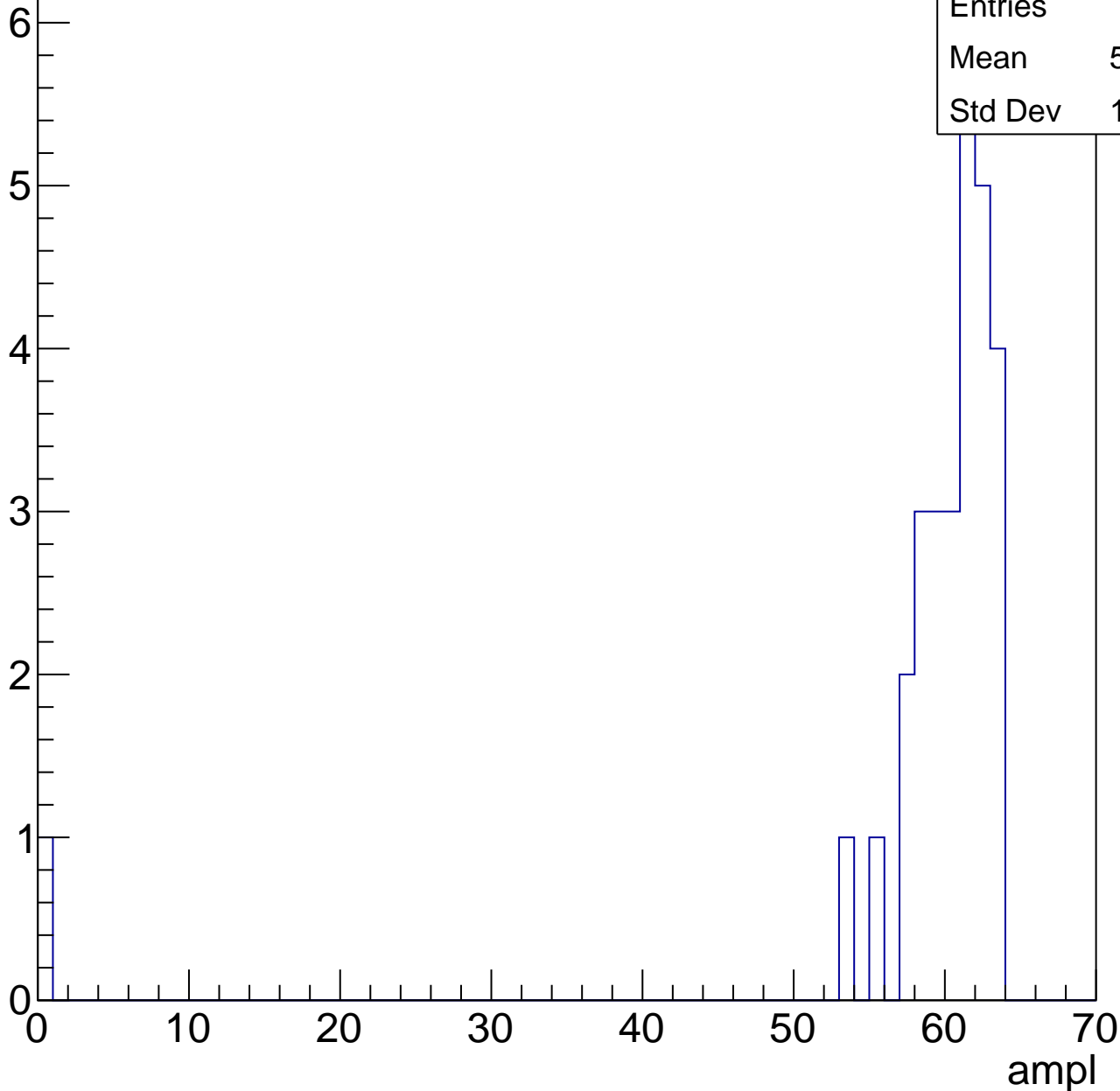


B1L103S, U24-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	57.97
Std Dev	11.22



B1L103S, U24-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

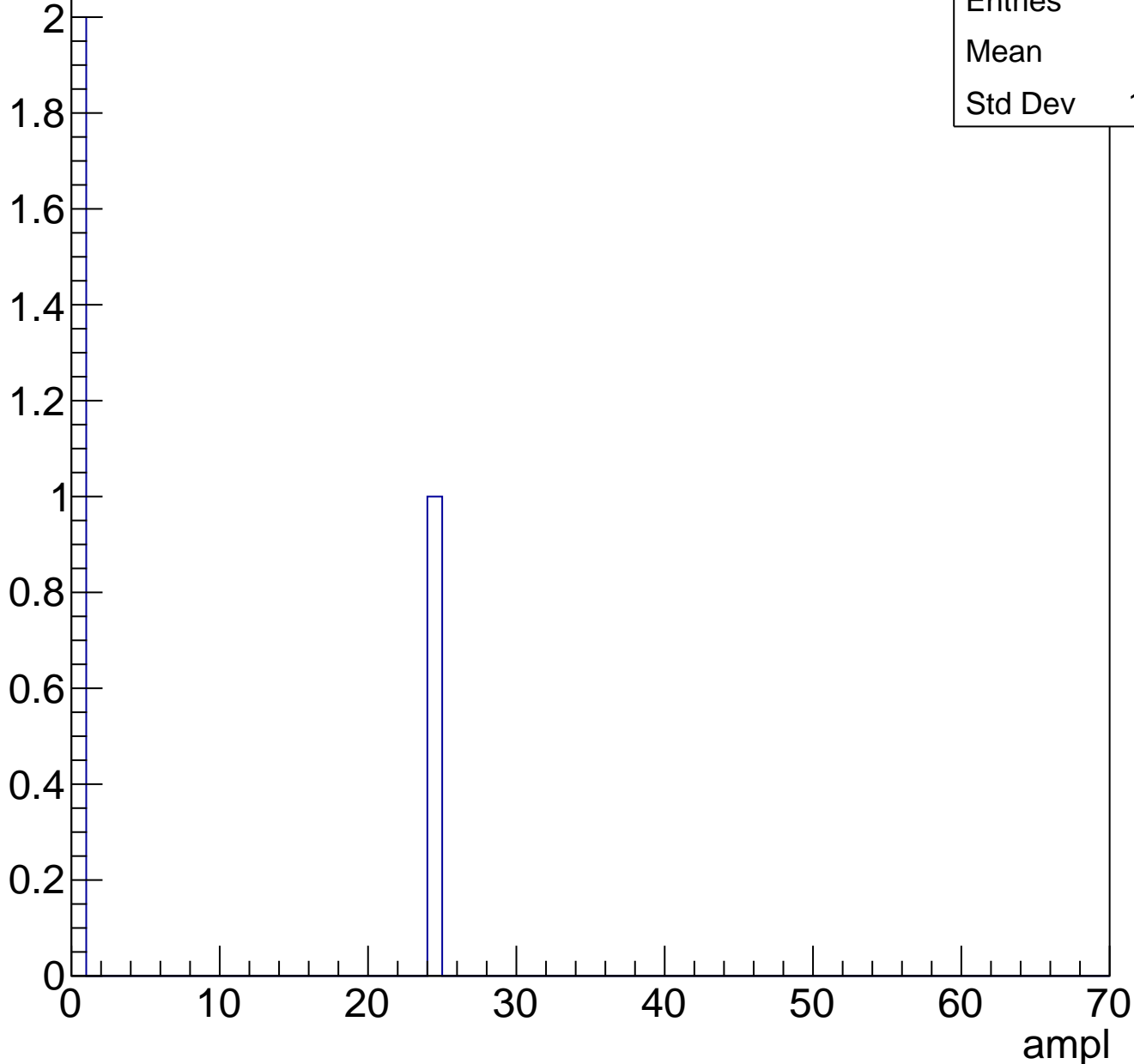
Entry



B1L103S, U24-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch76, adc0

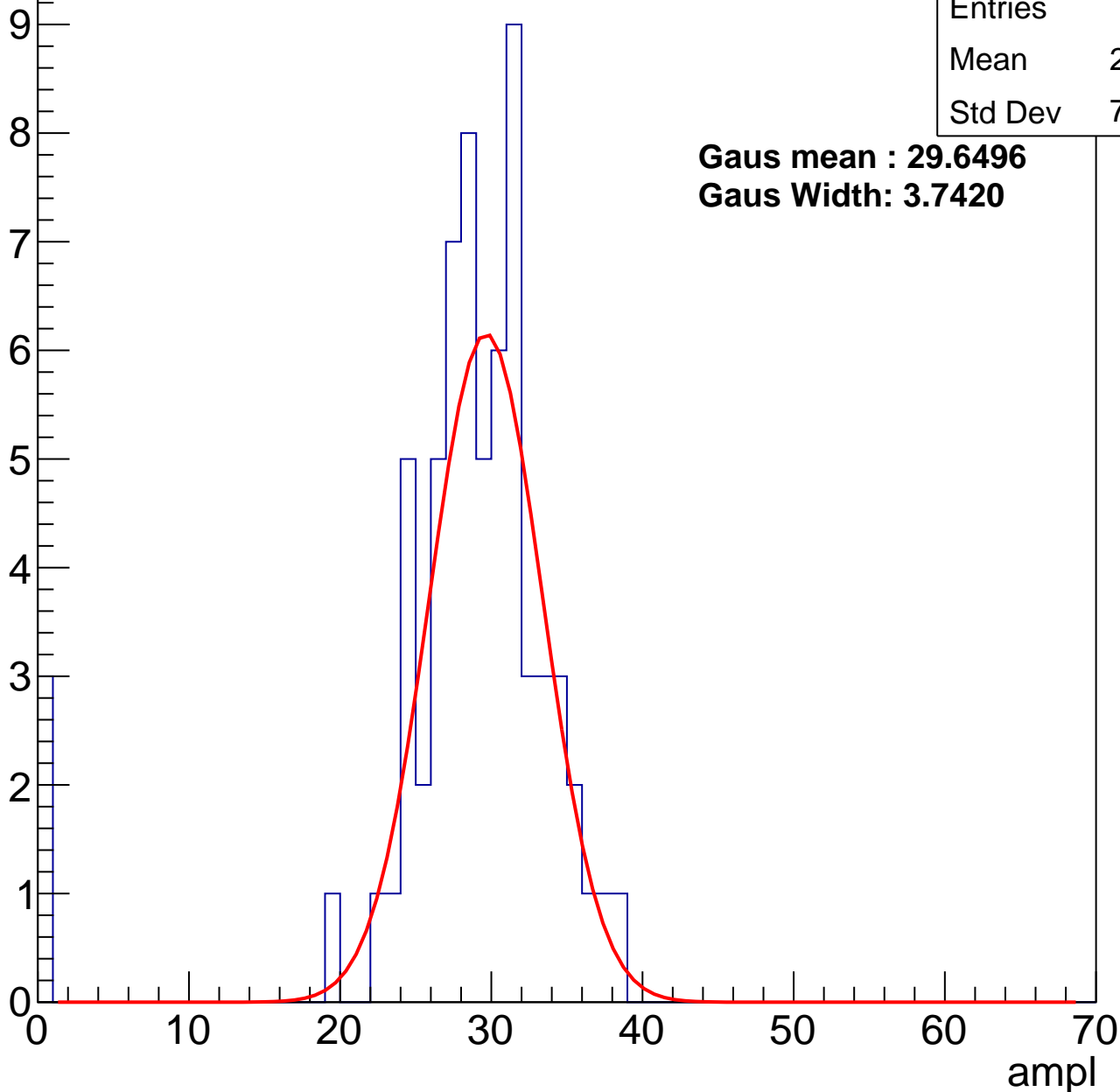
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.75
Std Dev	7.017

Gaus mean : 29.6496

Gaus Width: 3.7420



B1L103S, U24-ch76, adc1

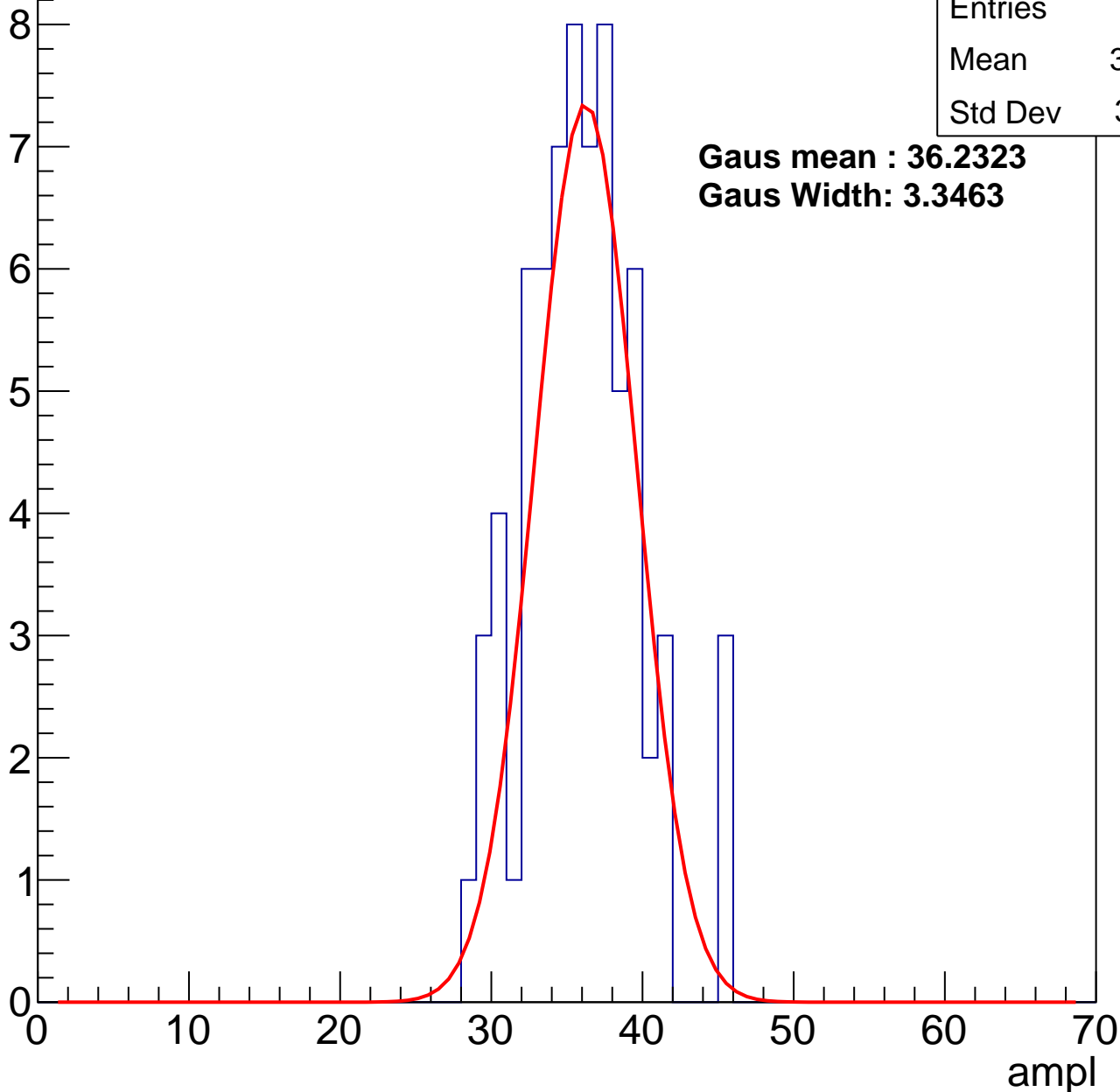
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.49
Std Dev	3.741

Gaus mean : 36.2323

Gaus Width: 3.3463



B1L103S, U24-ch76, adc2

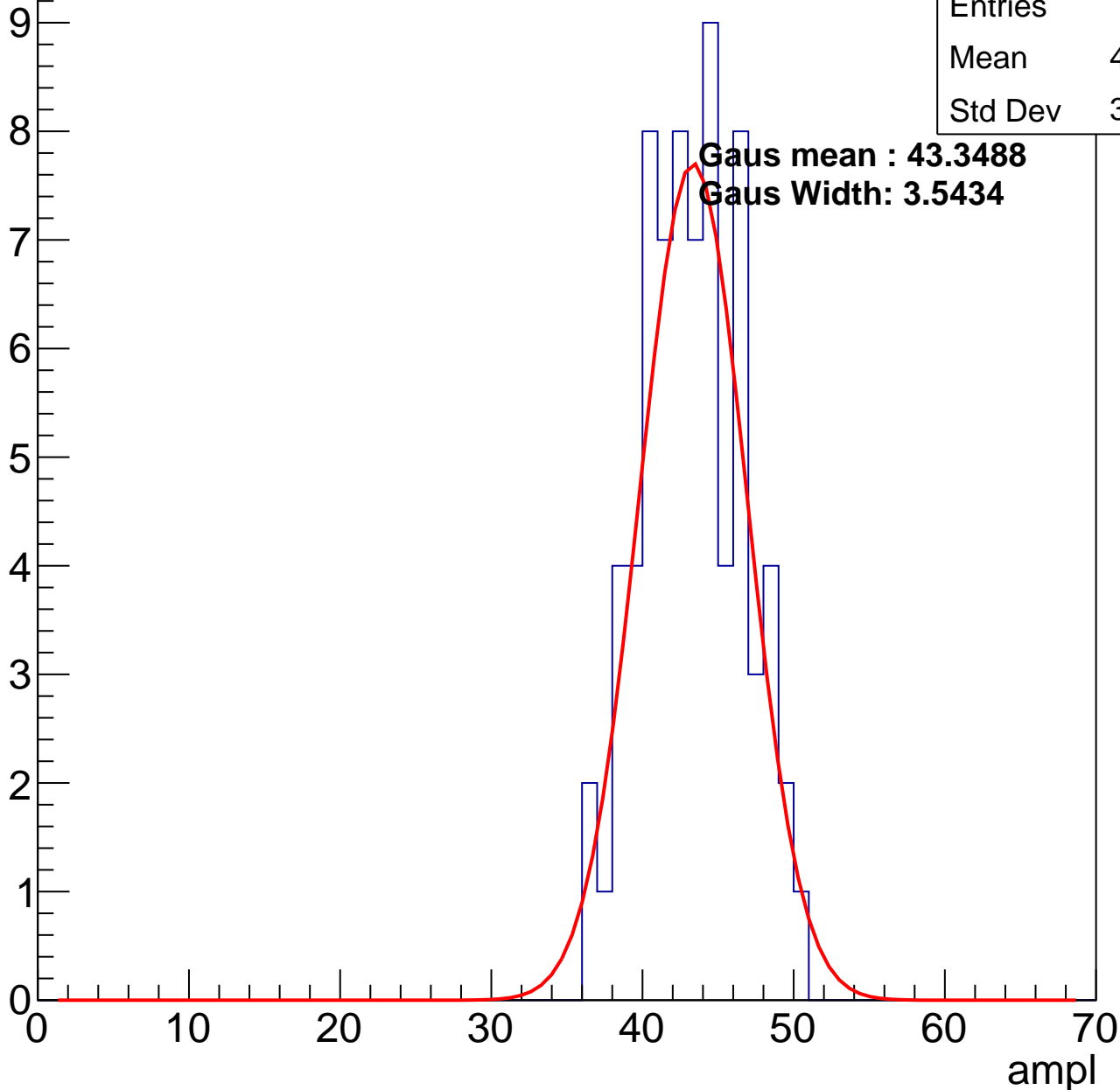
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.86
Std Dev	3.272

Gaus mean : 43.3488

Gaus Width: 3.5434

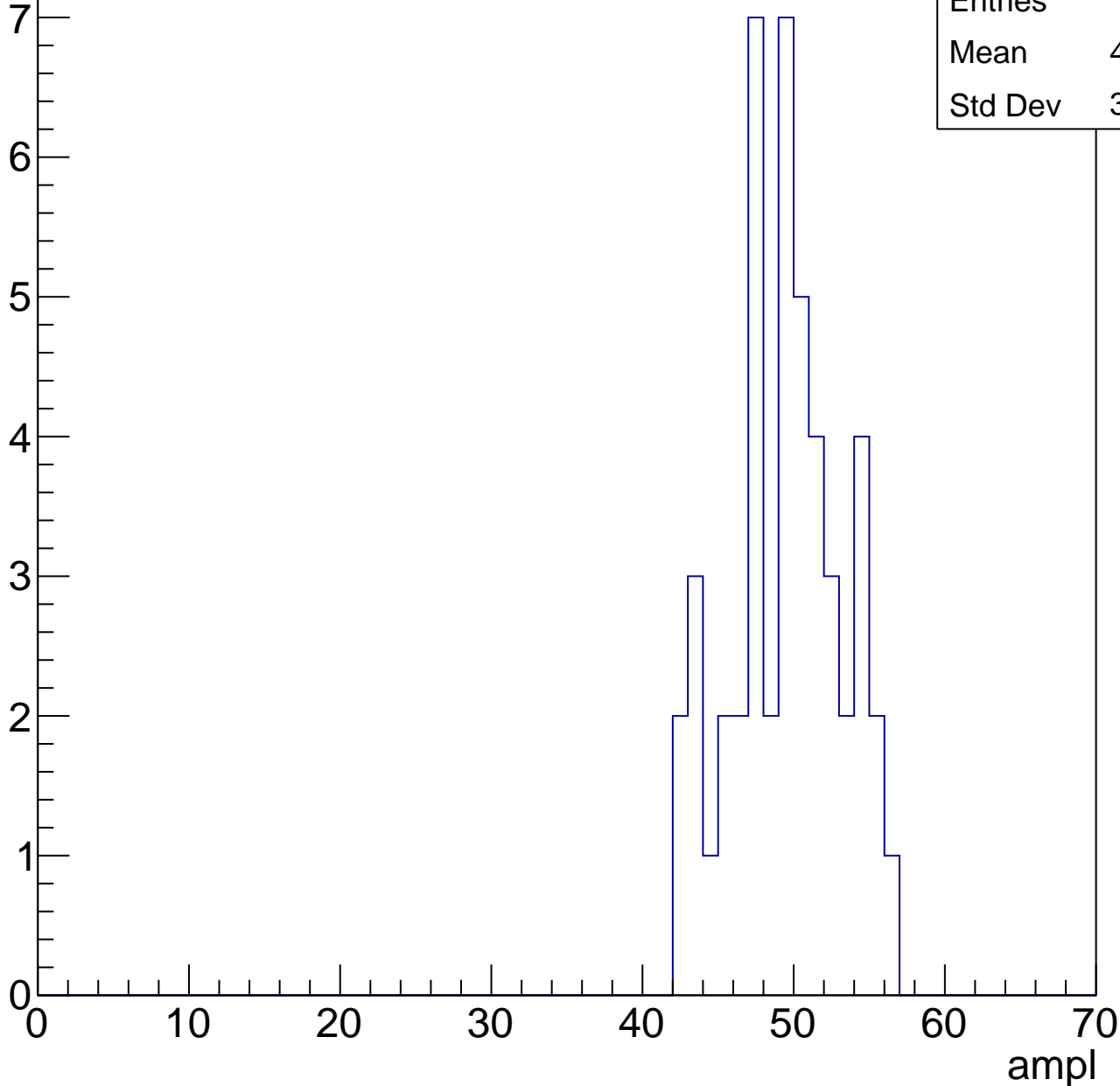


B1L103S, U24-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	49.04
Std Dev	3.608

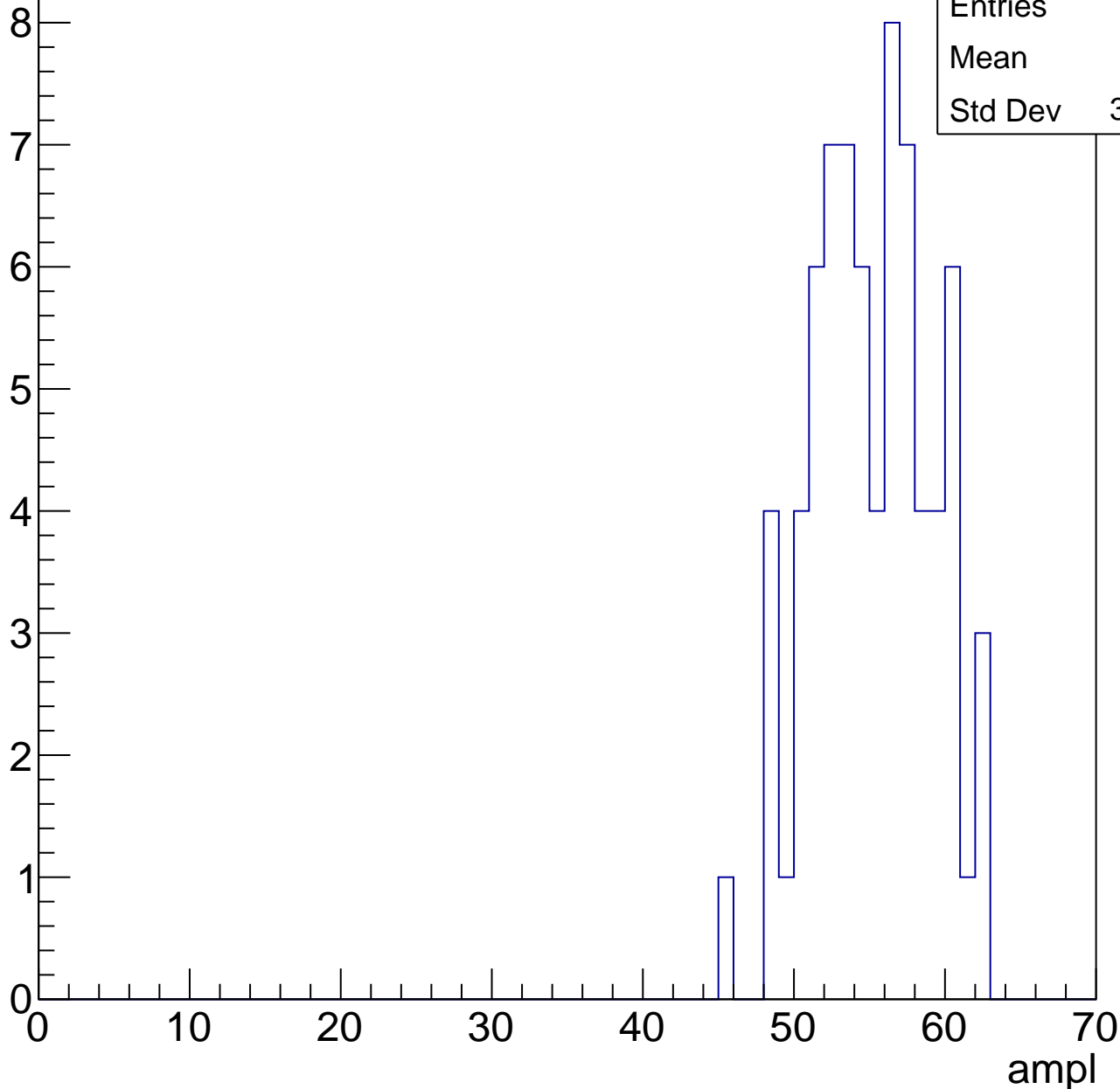


B1L103S, U24-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	54.7
Std Dev	3.859

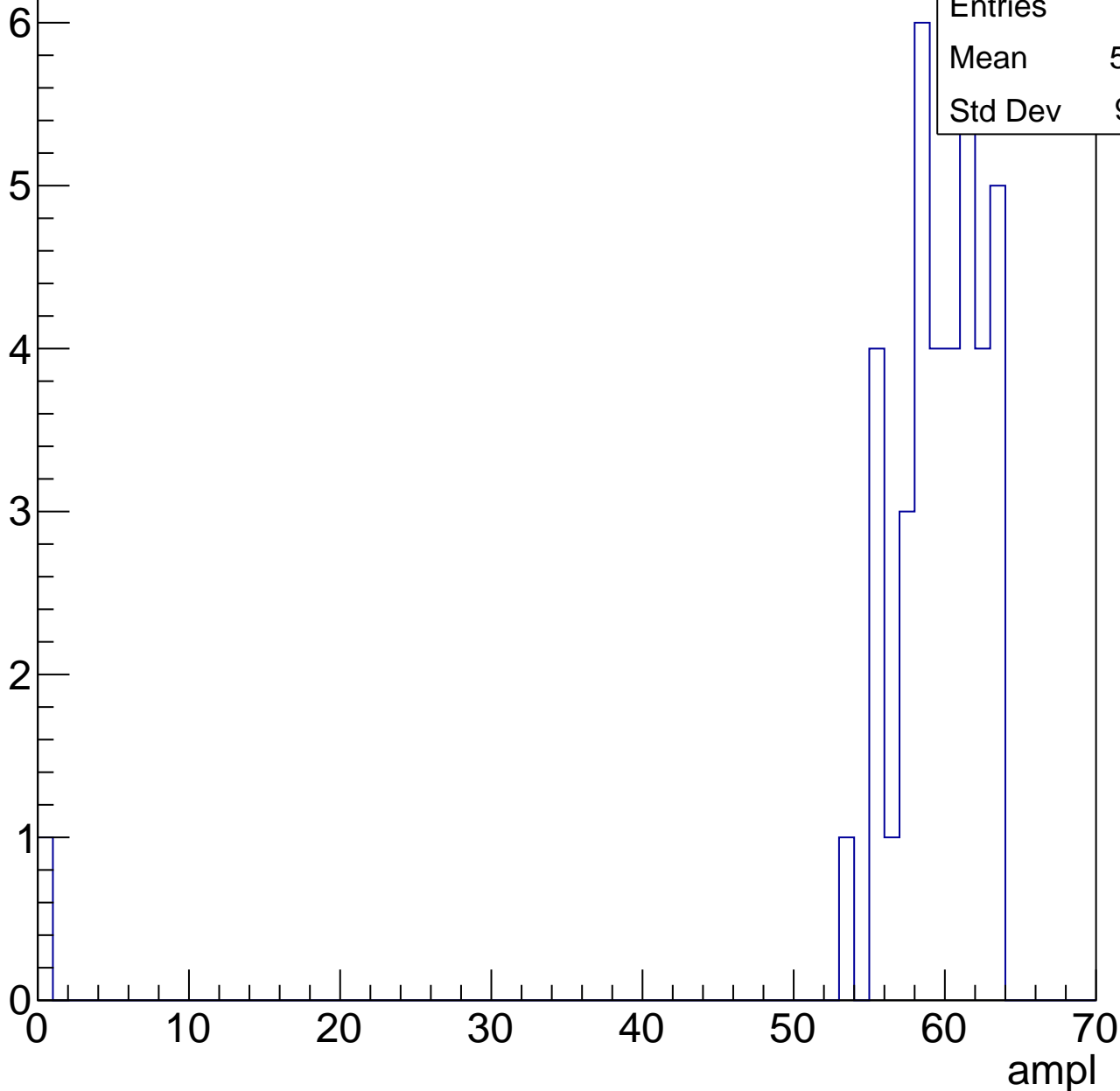


B1L103S, U24-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

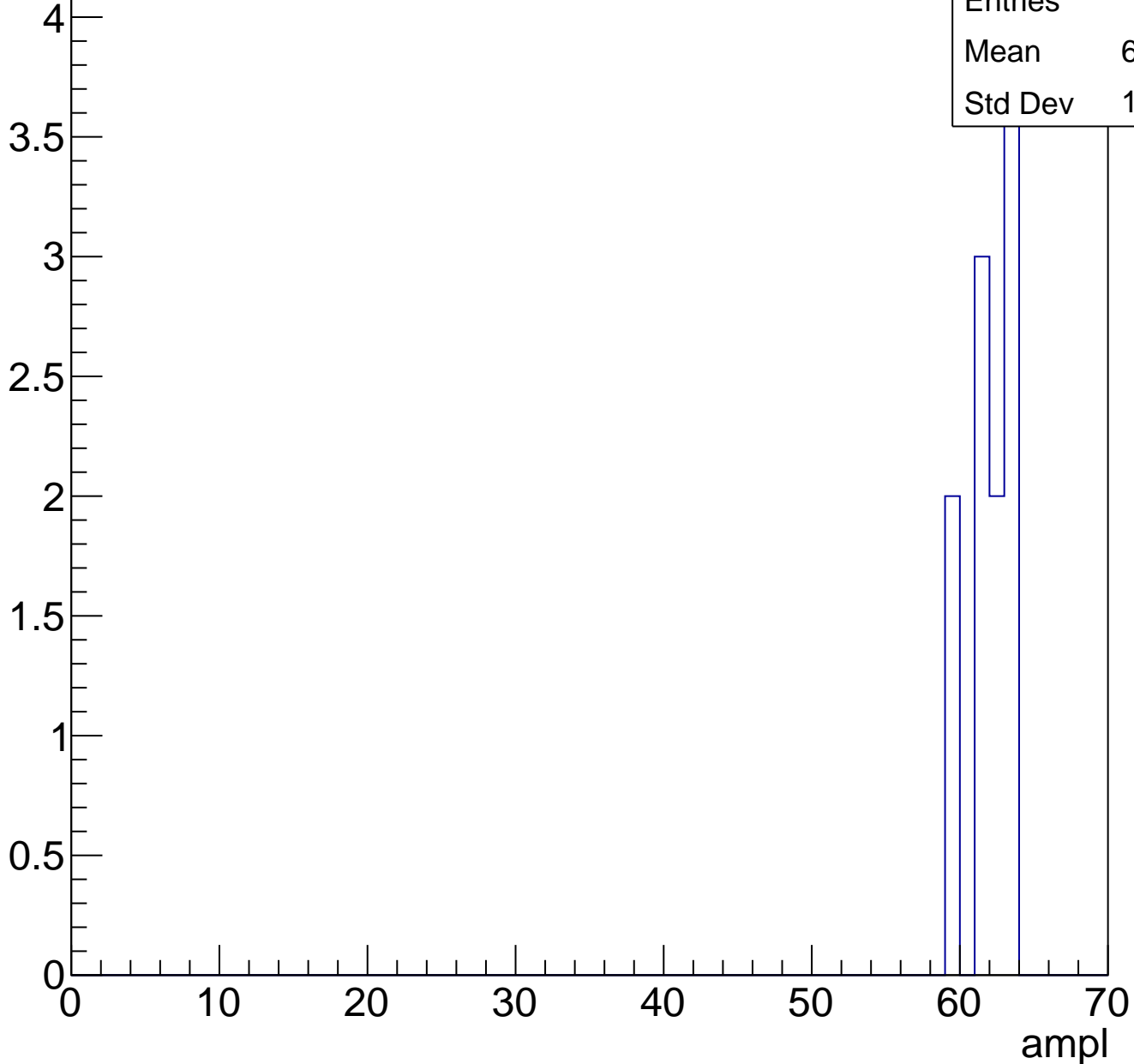
Entries	39
Mean	57.77
Std Dev	9.731



B1L103S, U24-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch77, adc0

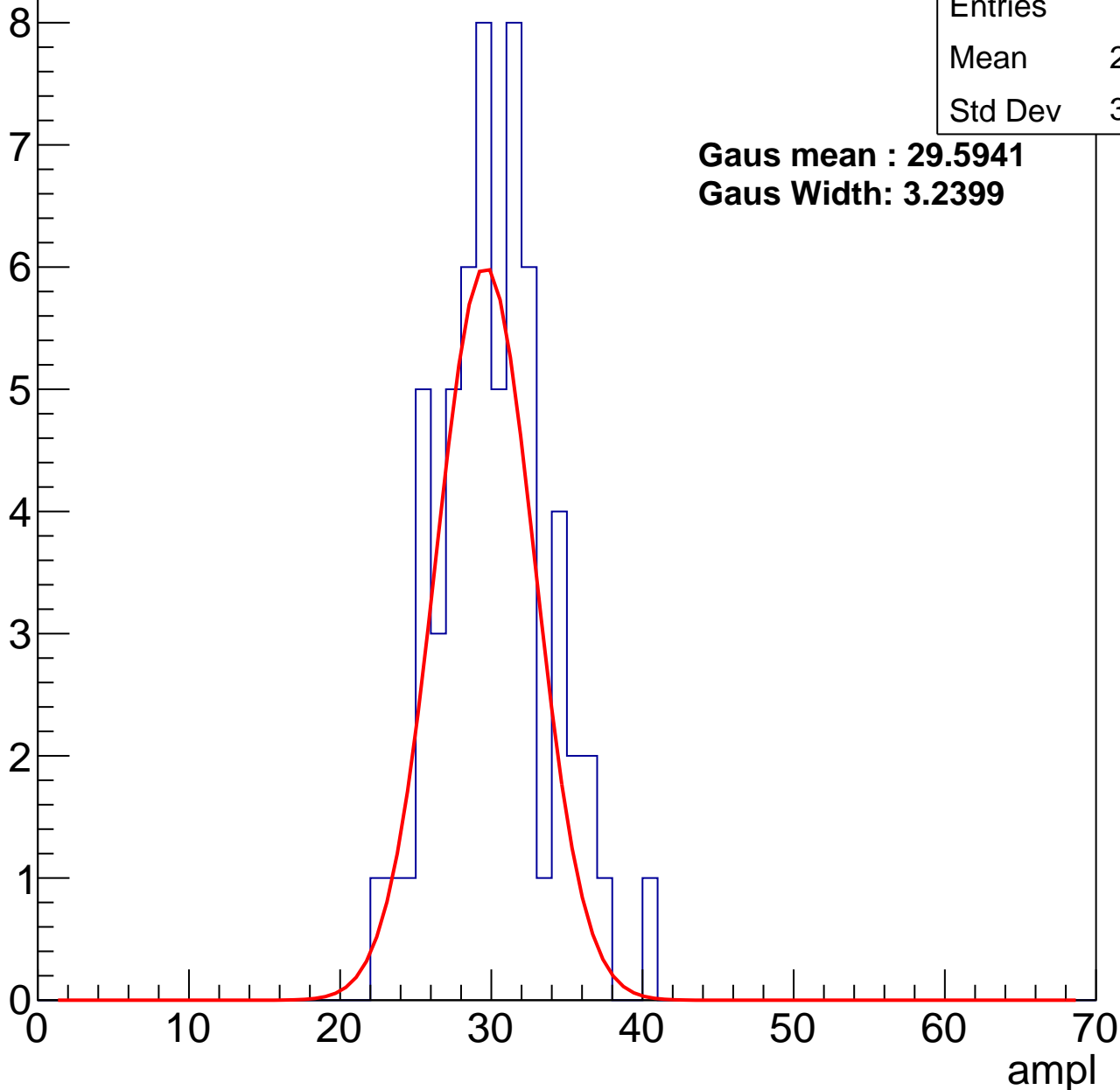
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	29.75
Std Dev	3.585

Gaus mean : 29.5941

Gaus Width: 3.2399



B1L103S, U24-ch77, adc1

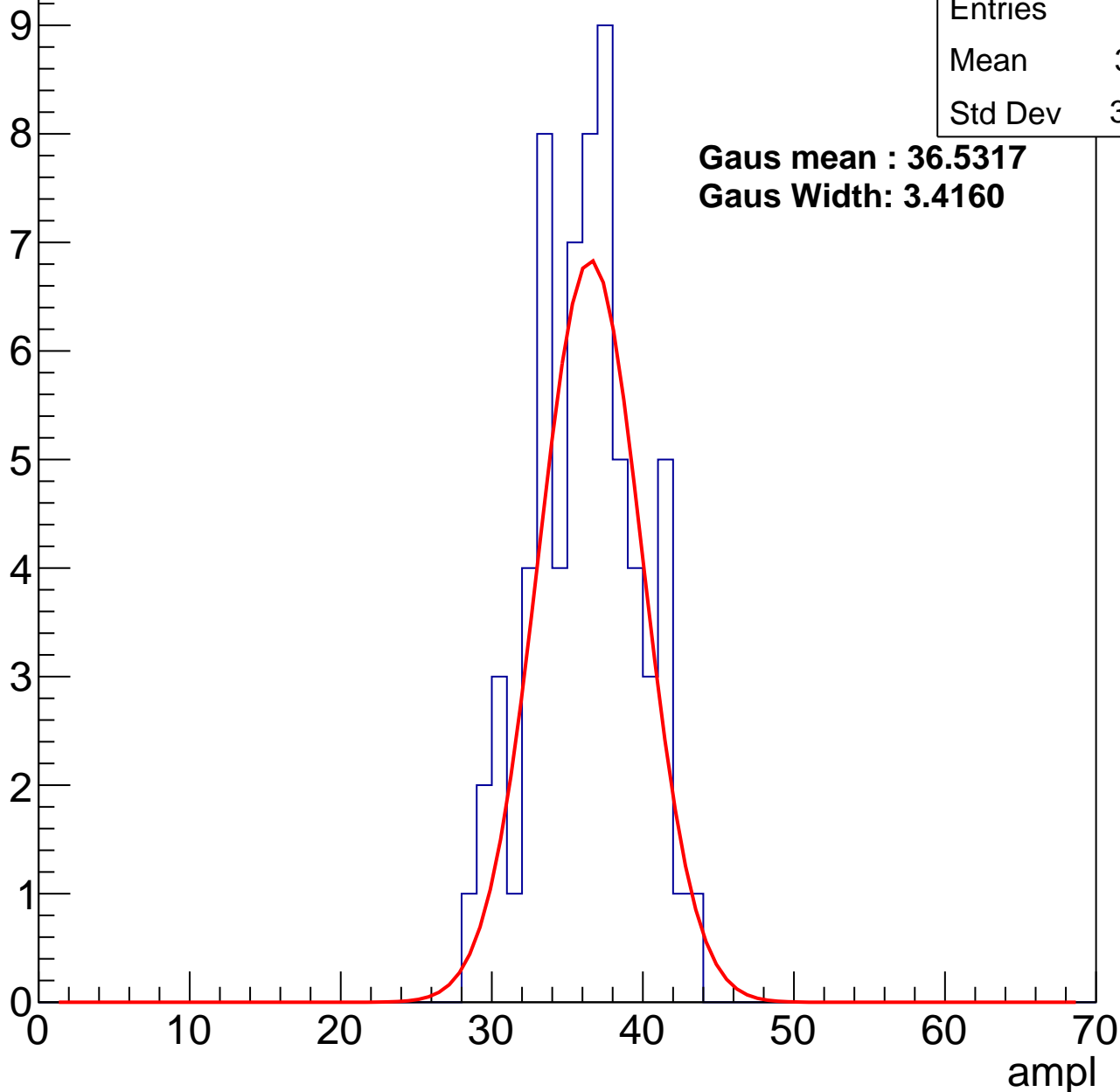
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.71
Std Dev	3.419

Gaus mean : 36.5317

Gaus Width: 3.4160



B1L103S, U24-ch77, adc2

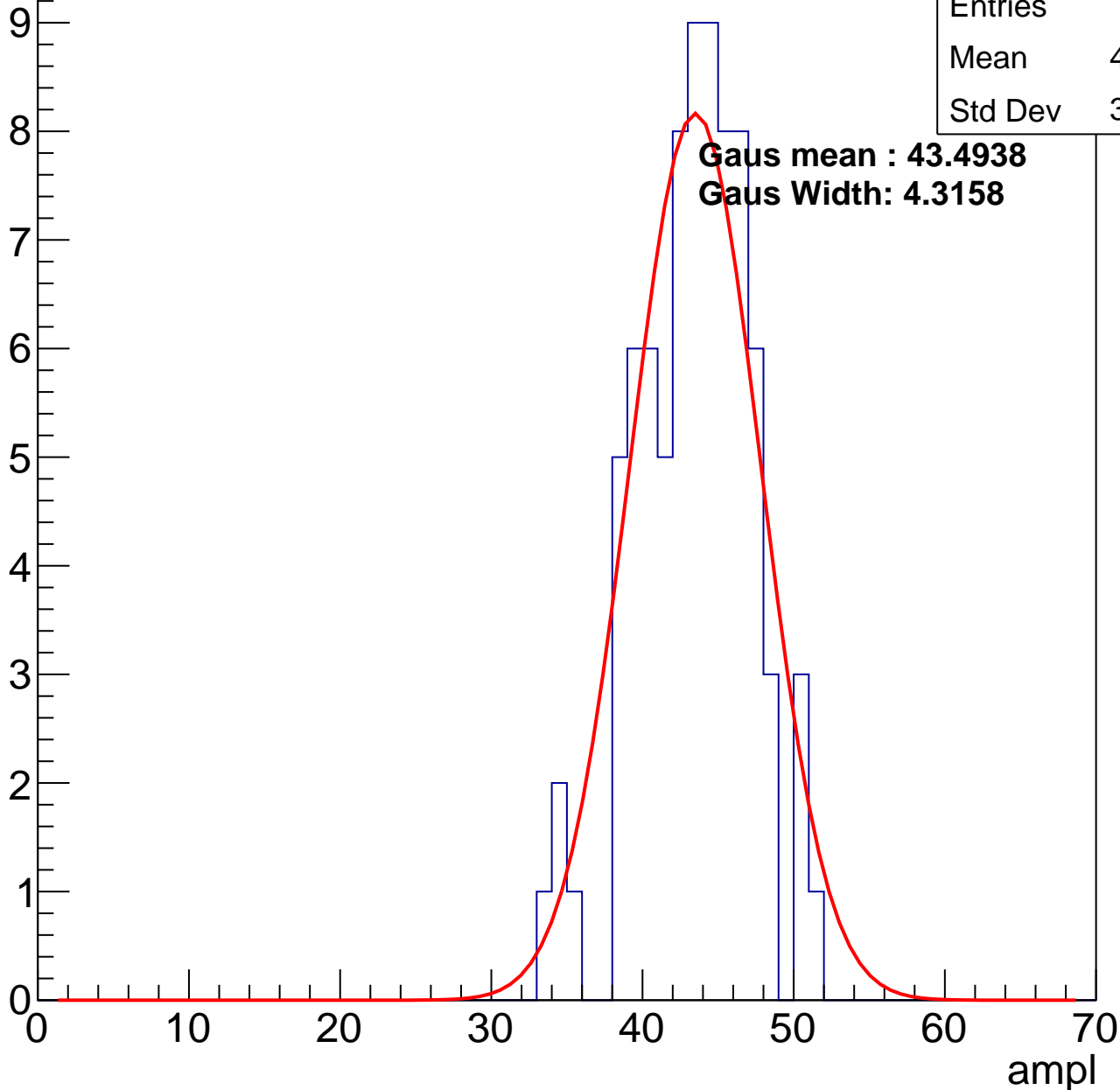
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	42.95
Std Dev	3.725

Gaus mean : 43.4938

Gaus Width: 4.3158



B1L103S, U24-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

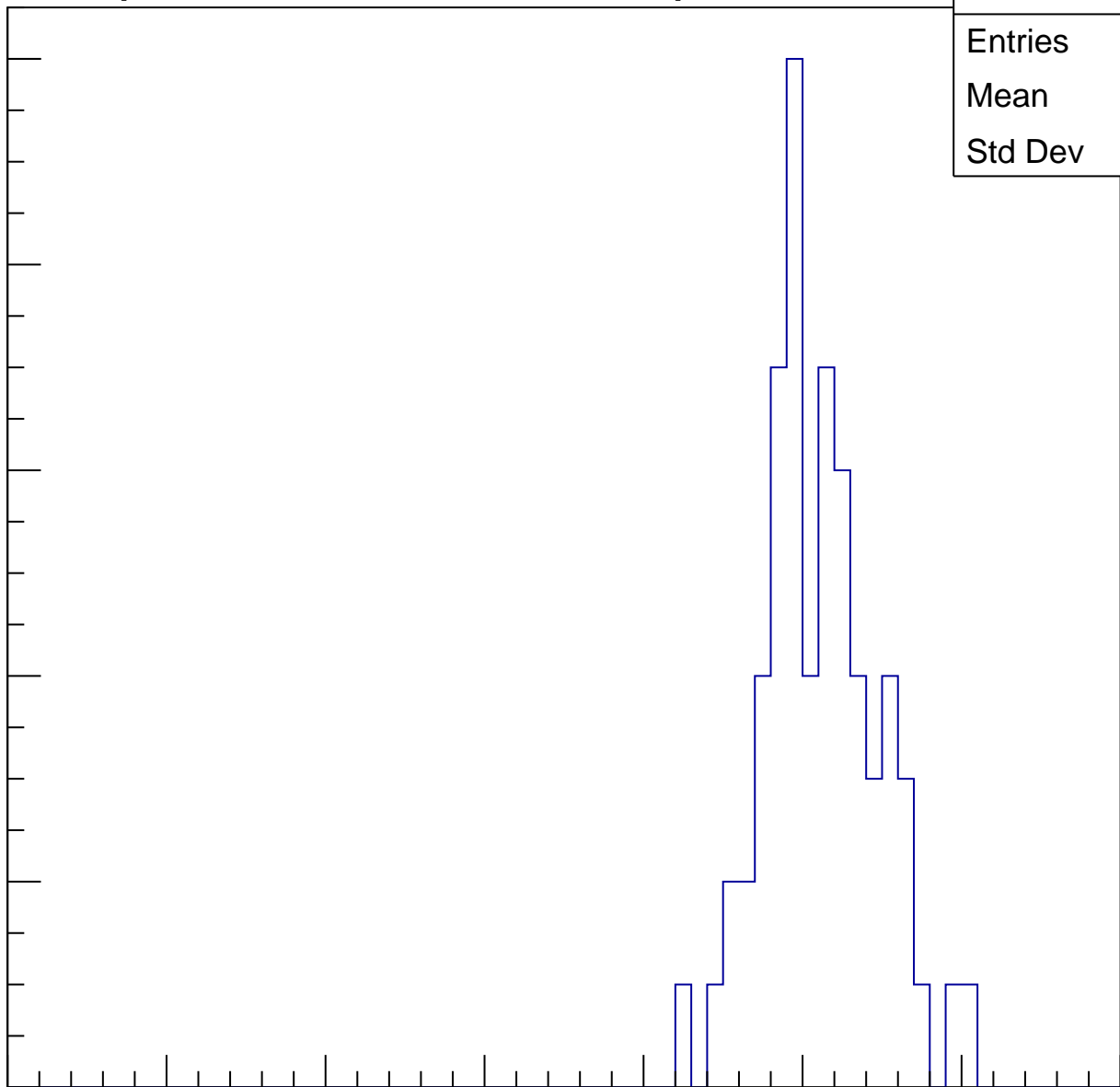
Entries	61
Mean	50.64
Std Dev	3.585

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

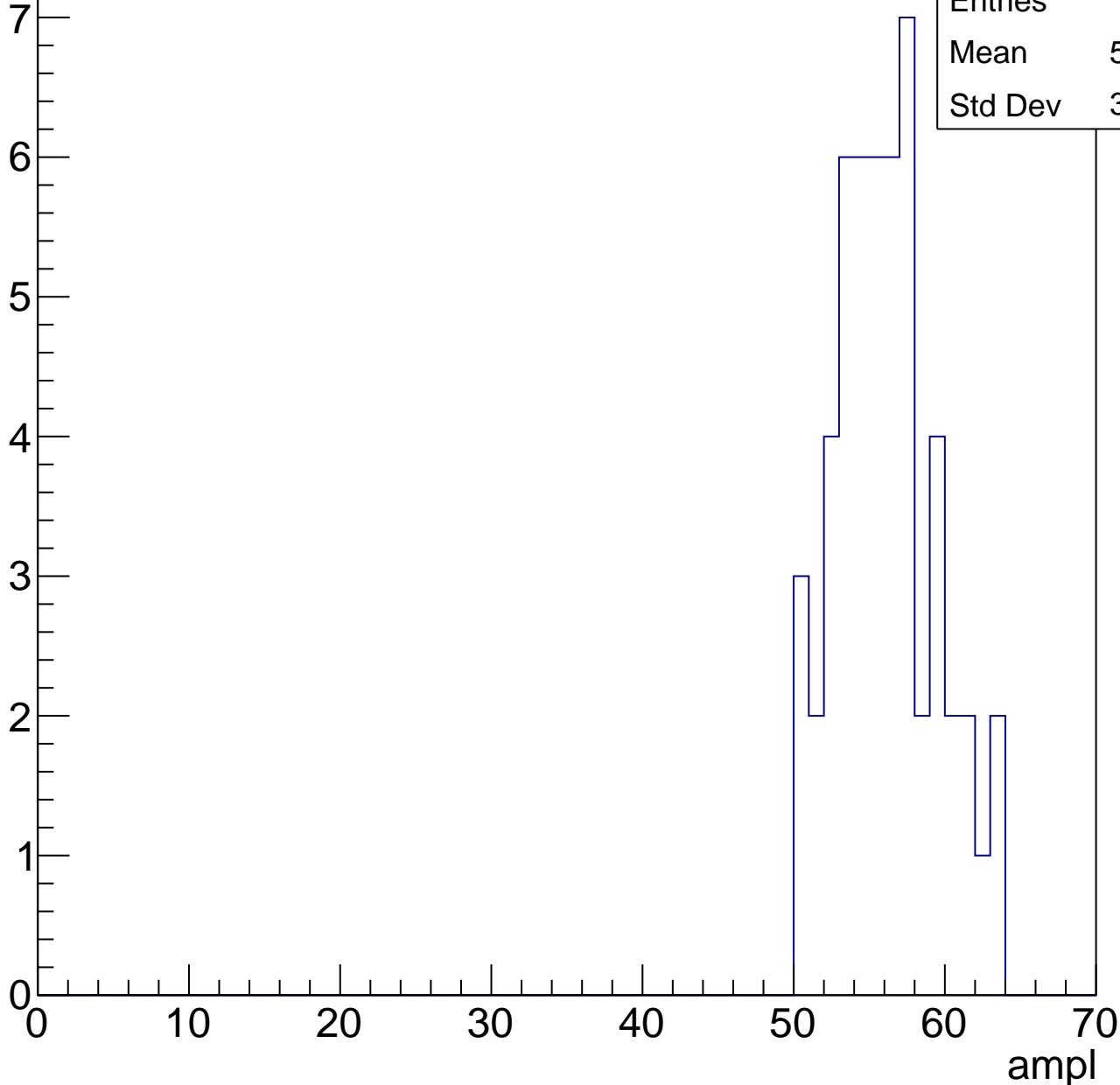


B1L103S, U24-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.64
Std Dev	3.268

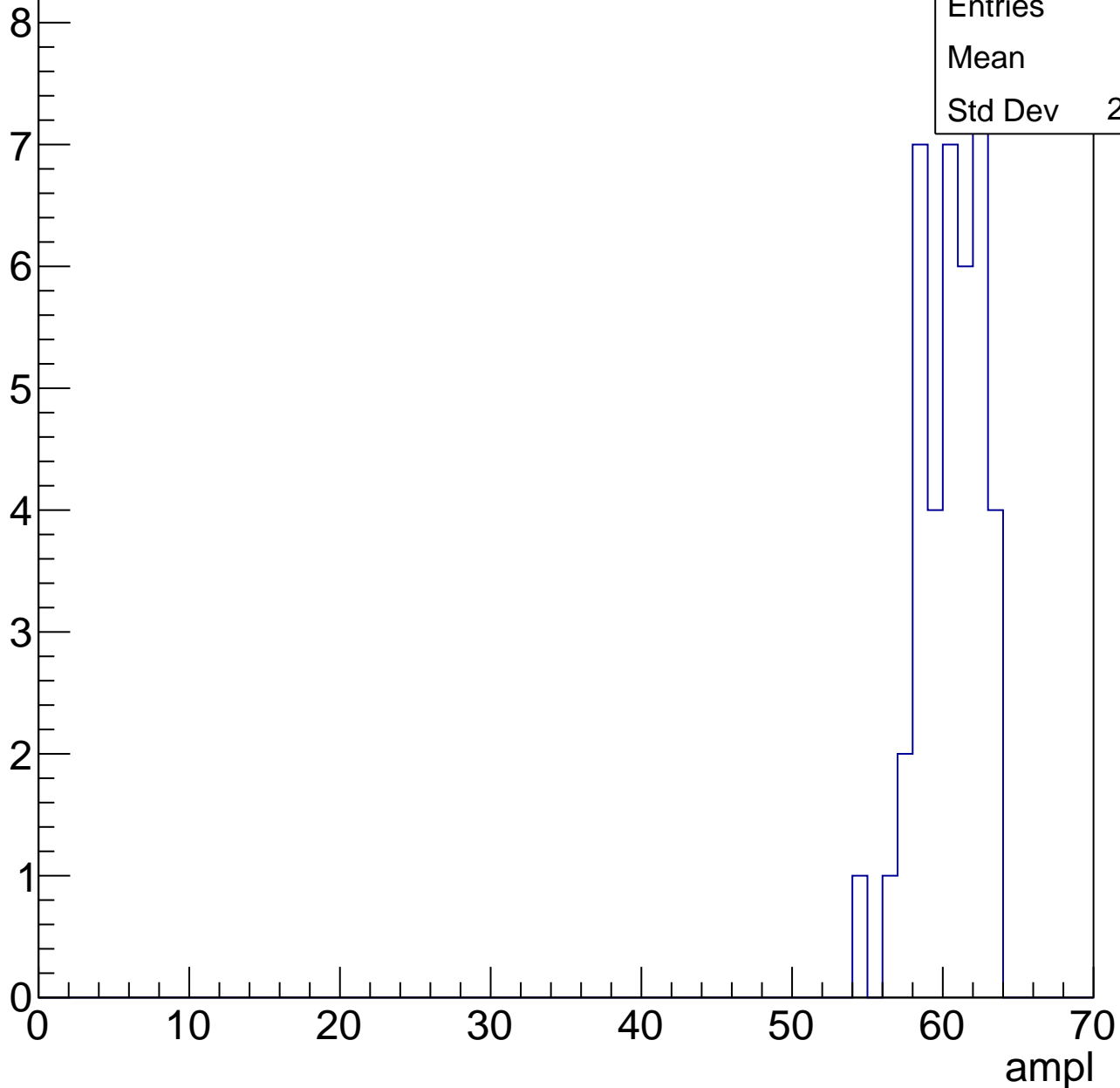


B1L103S, U24-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

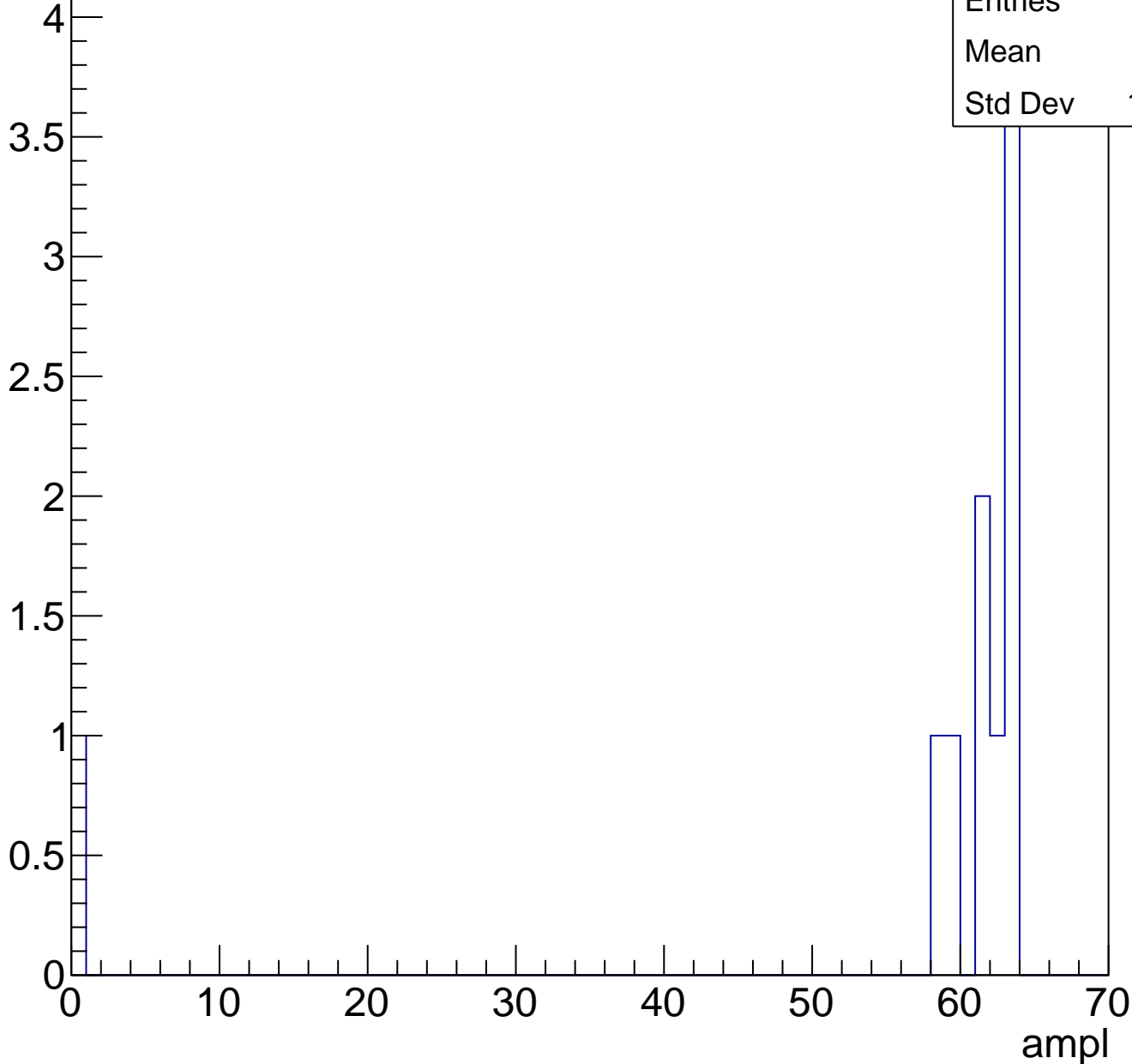
Entries	40
Mean	60
Std Dev	2.098



B1L103S, U24-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

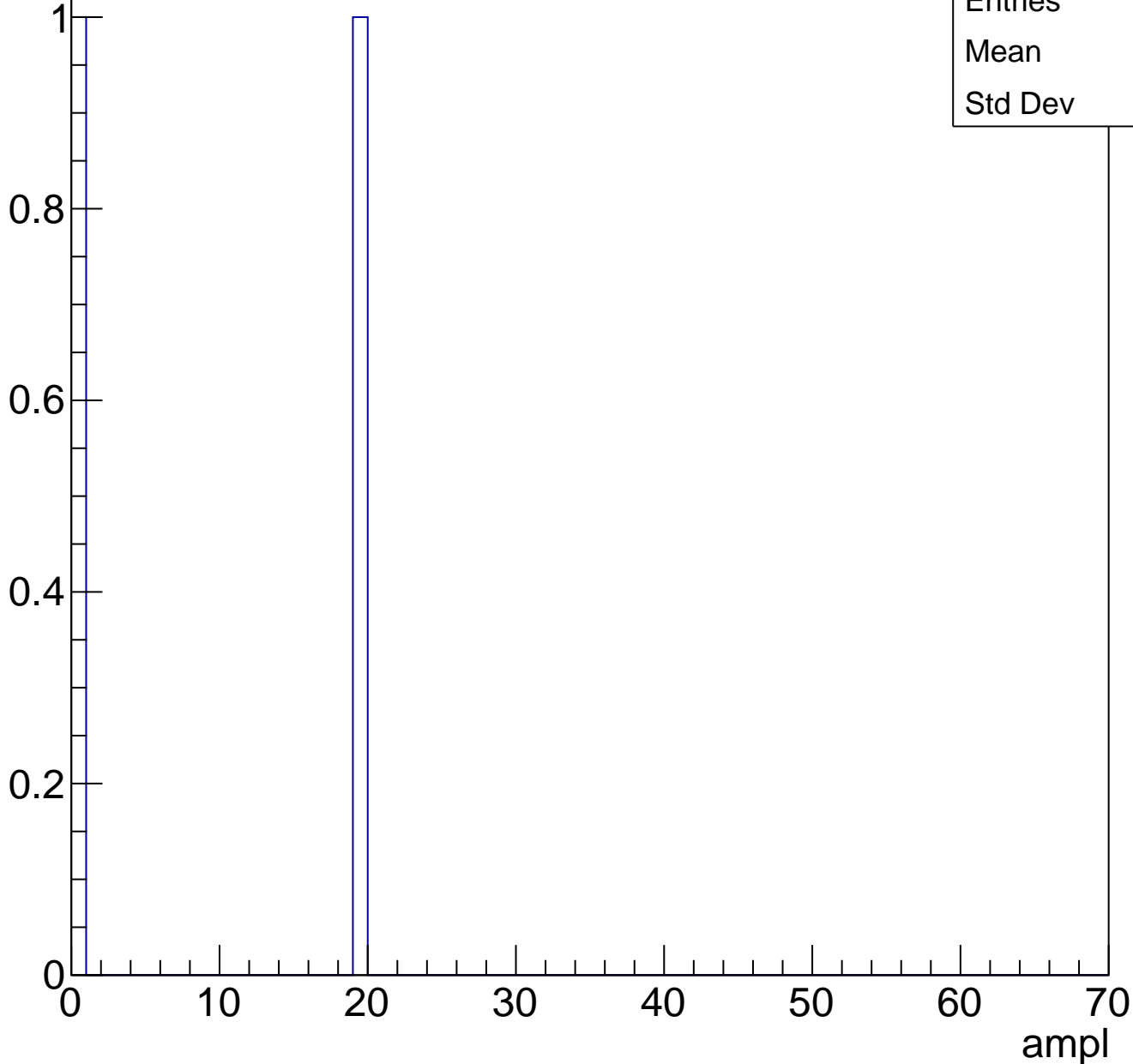
Entry



B1L103S, U24-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L103S, U24-ch78, adc0

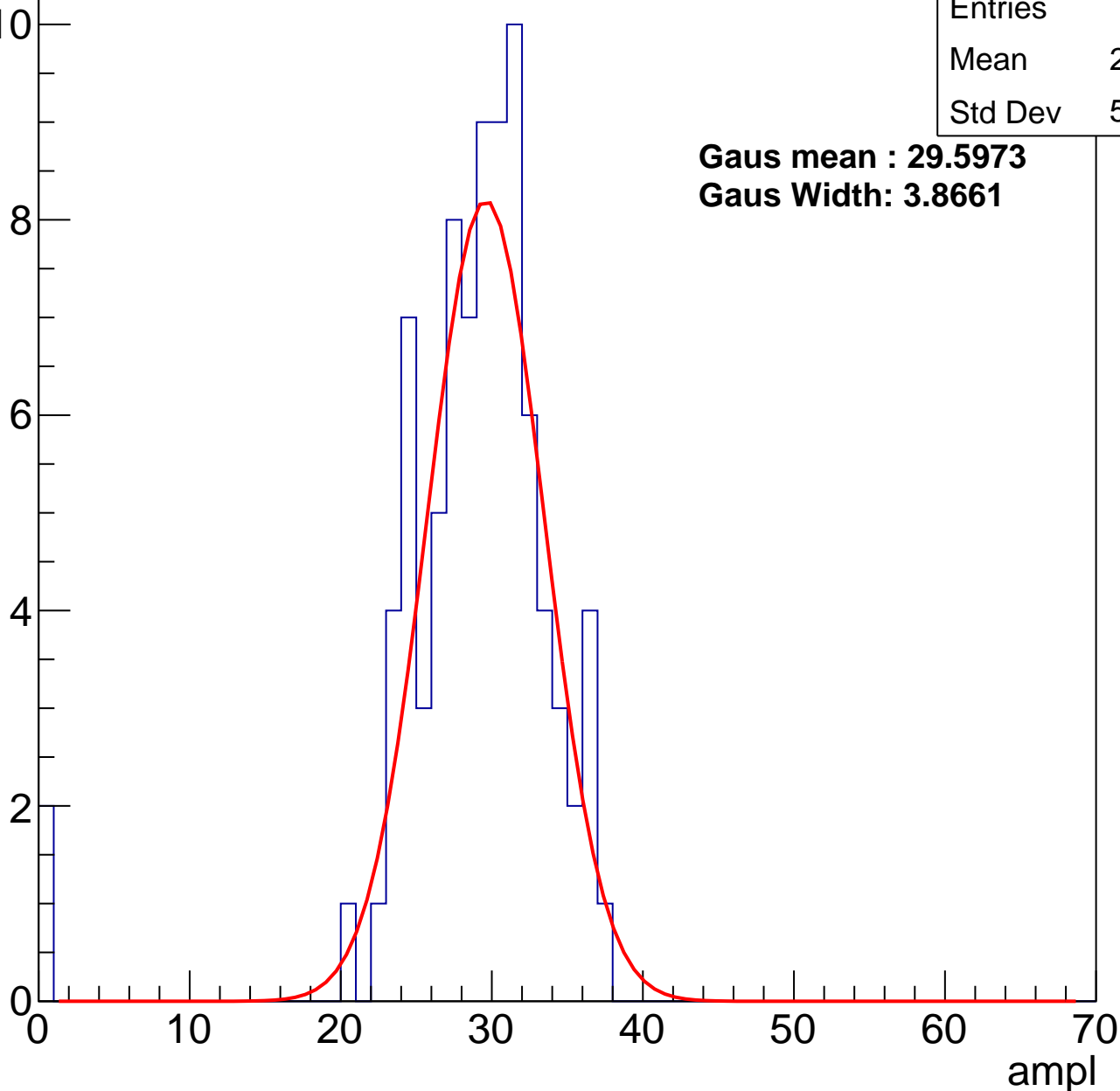
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	28.34
Std Dev	5.705

Gaus mean : 29.5973

Gaus Width: 3.8661



B1L103S, U24-ch78, adc1

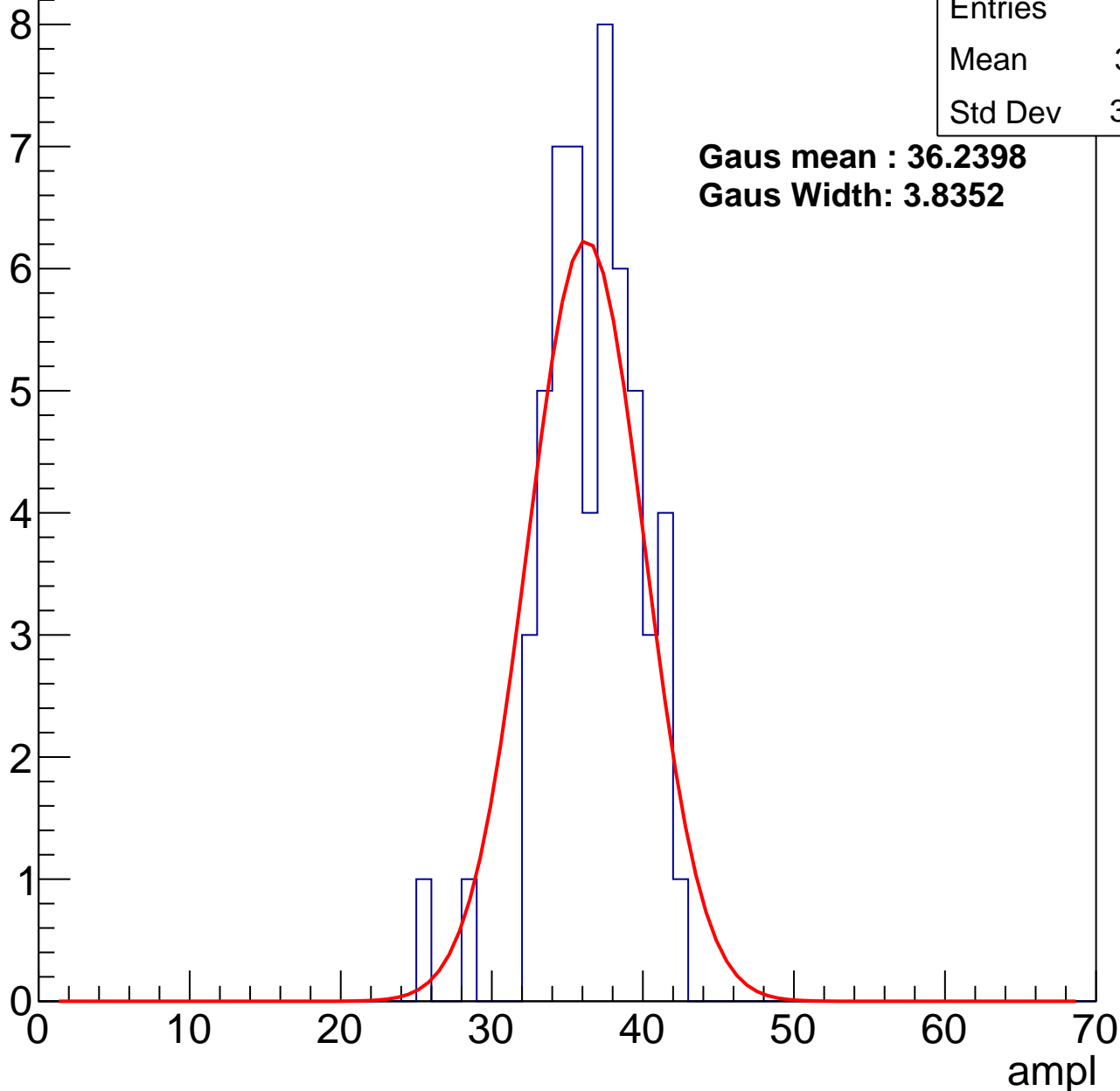
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	36.11
Std Dev	3.229

Gaus mean : 36.2398

Gaus Width: 3.8352



B1L103S, U24-ch78, adc2

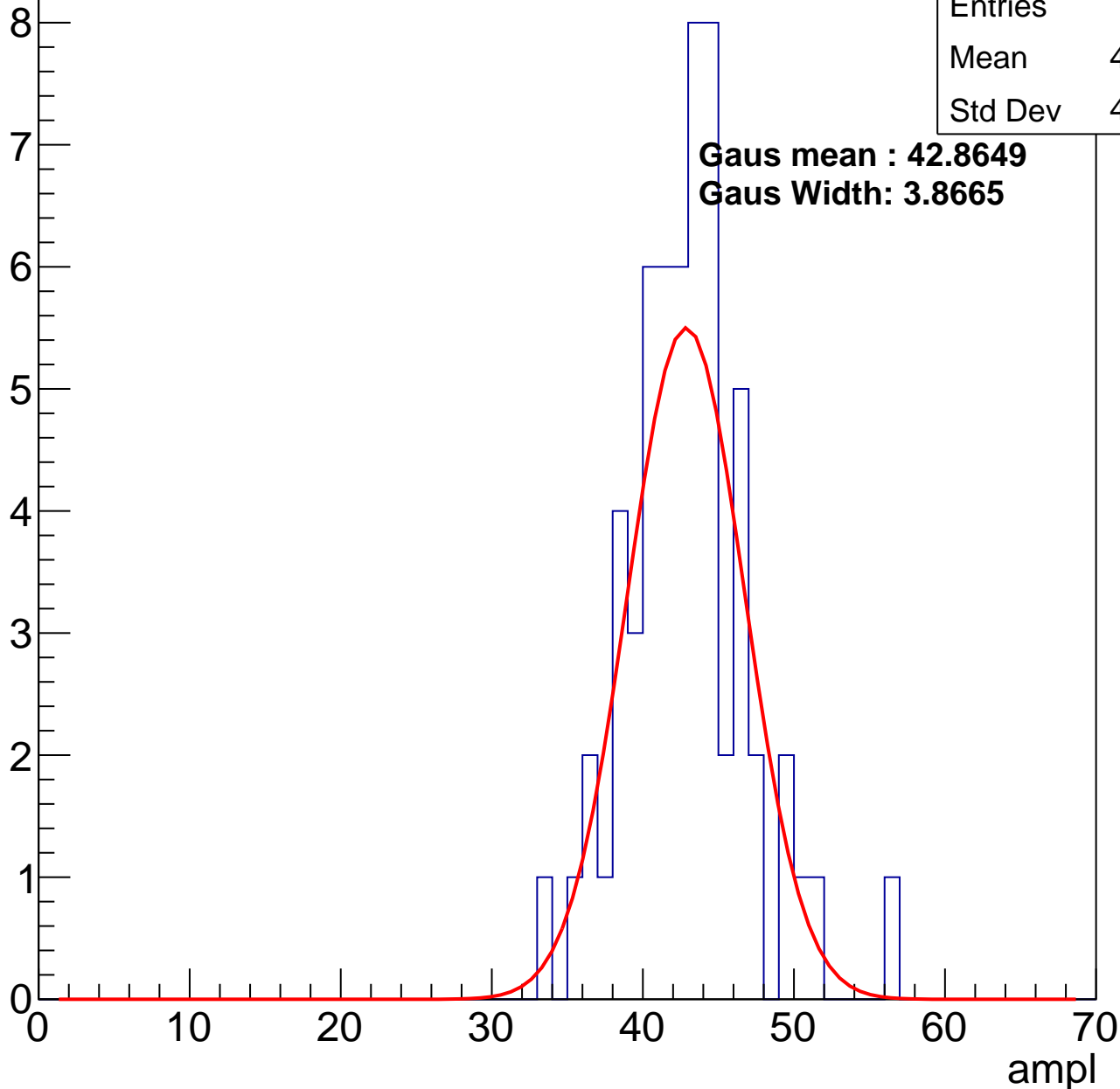
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.48
Std Dev	4.002

Gaus mean : 42.8649

Gaus Width: 3.8665

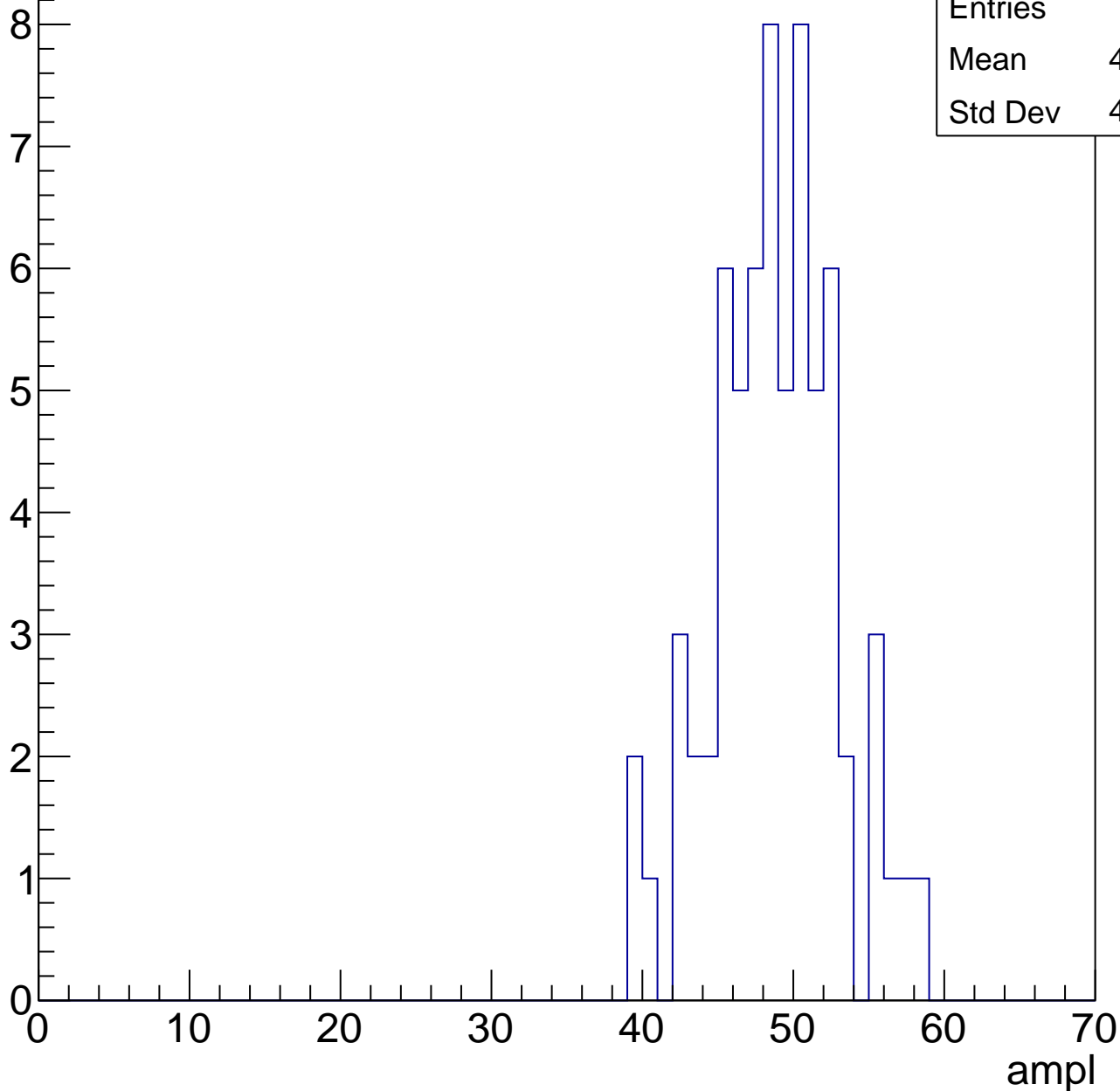


B1L103S, U24-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.33
Std Dev	4.068

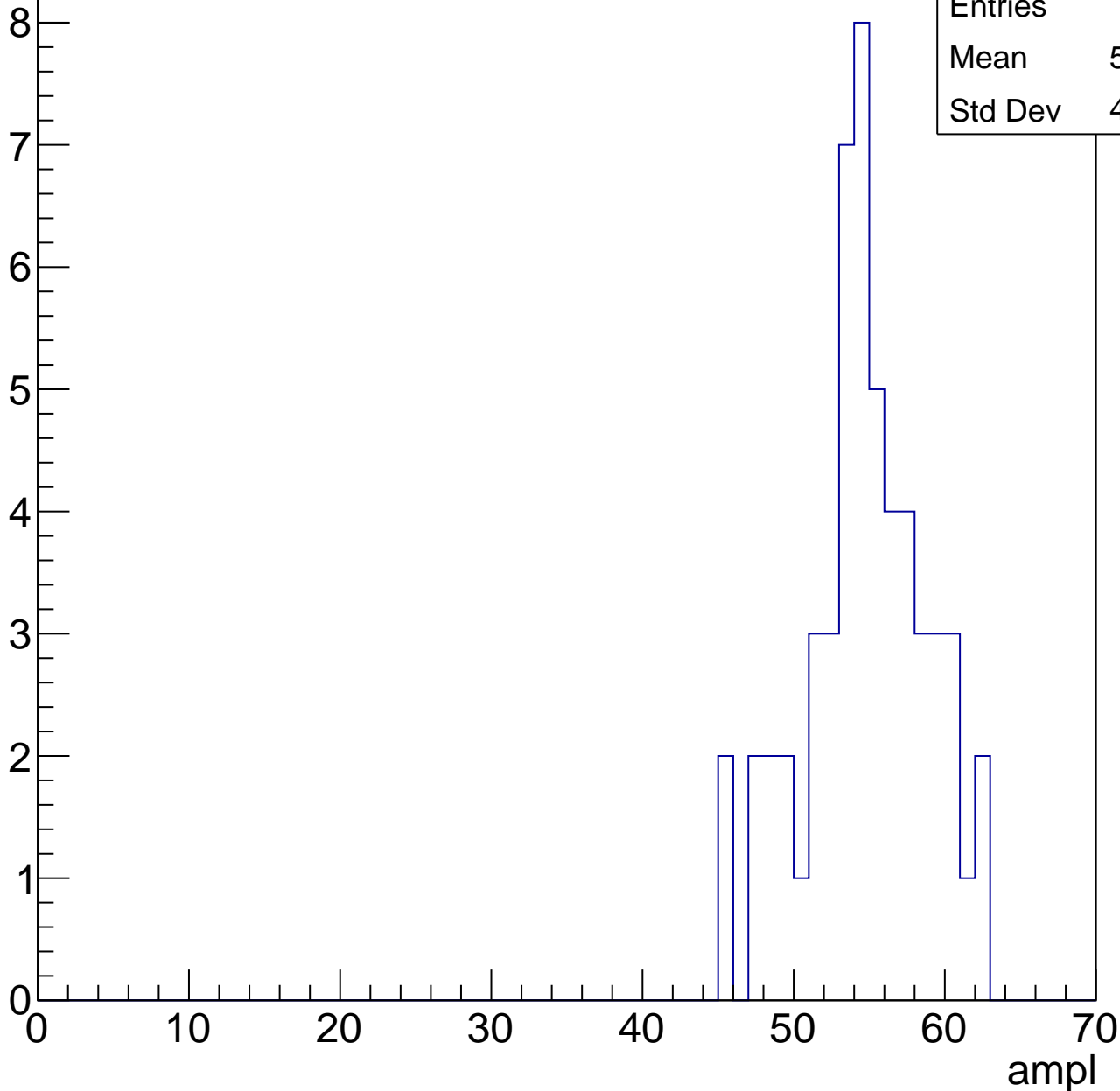


B1L103S, U24-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.24
Std Dev	4.058



B1L103S, U24-ch78, adc5

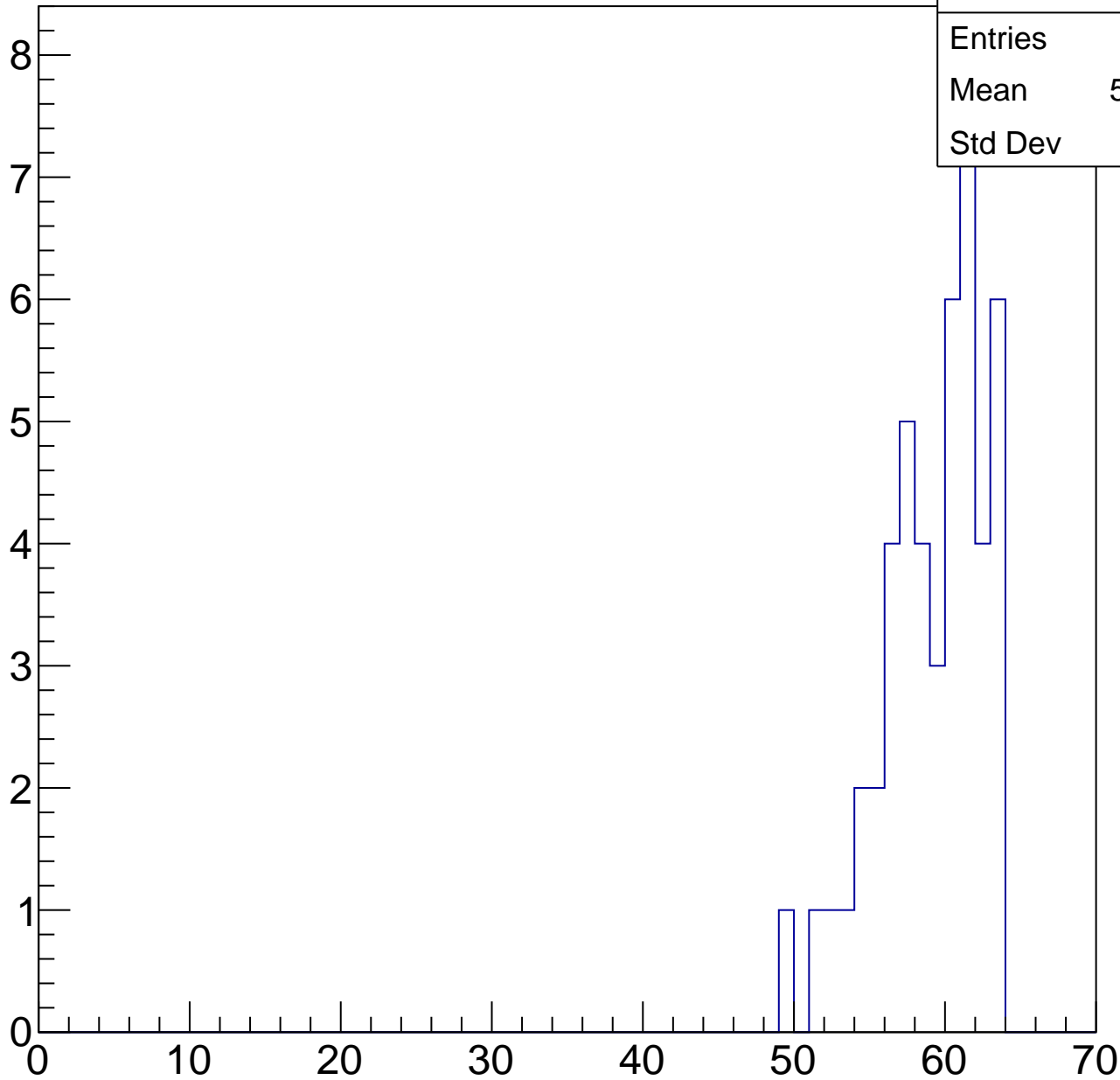
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.65
Std Dev	3.4

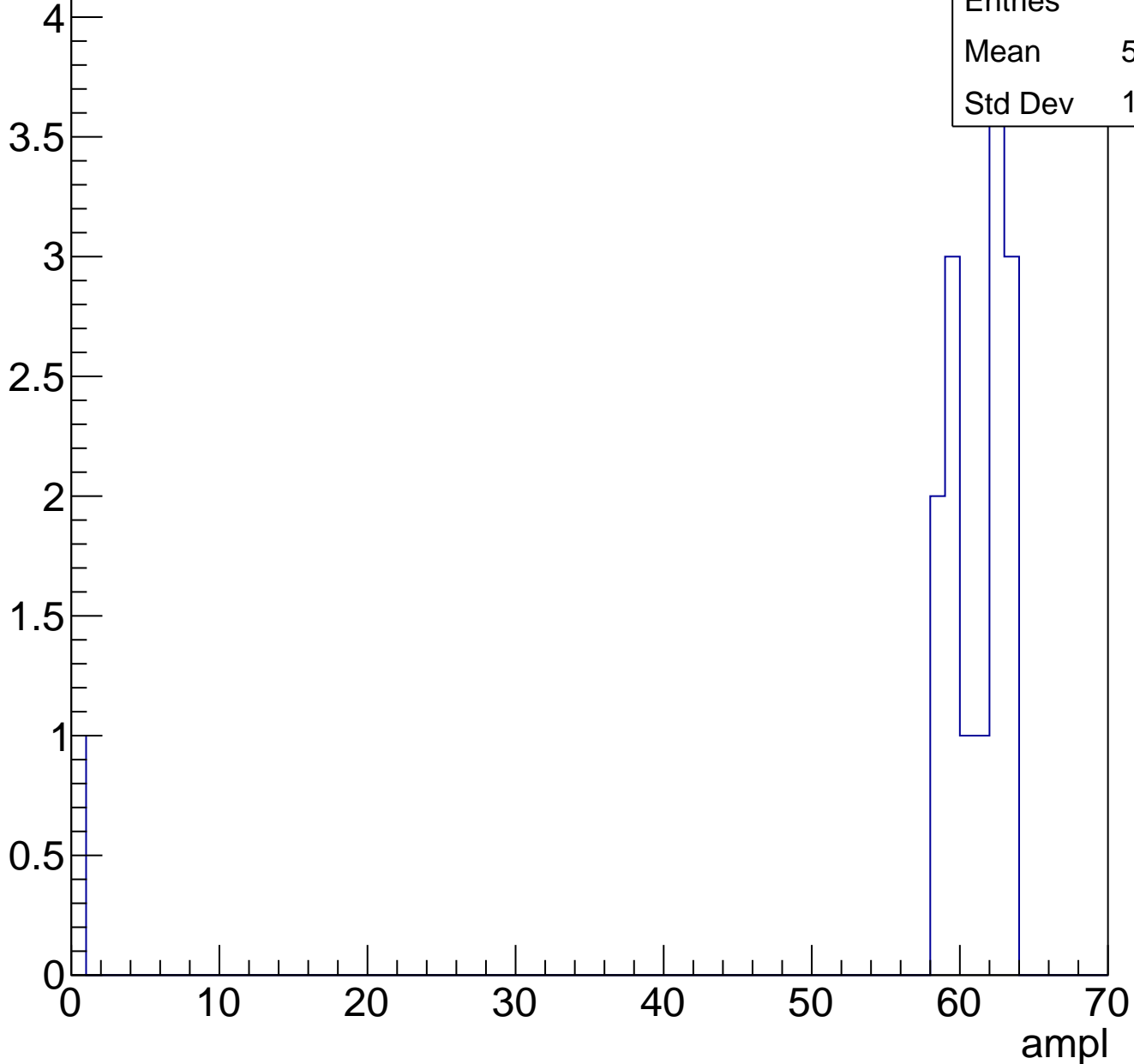
ampl



B1L103S, U24-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

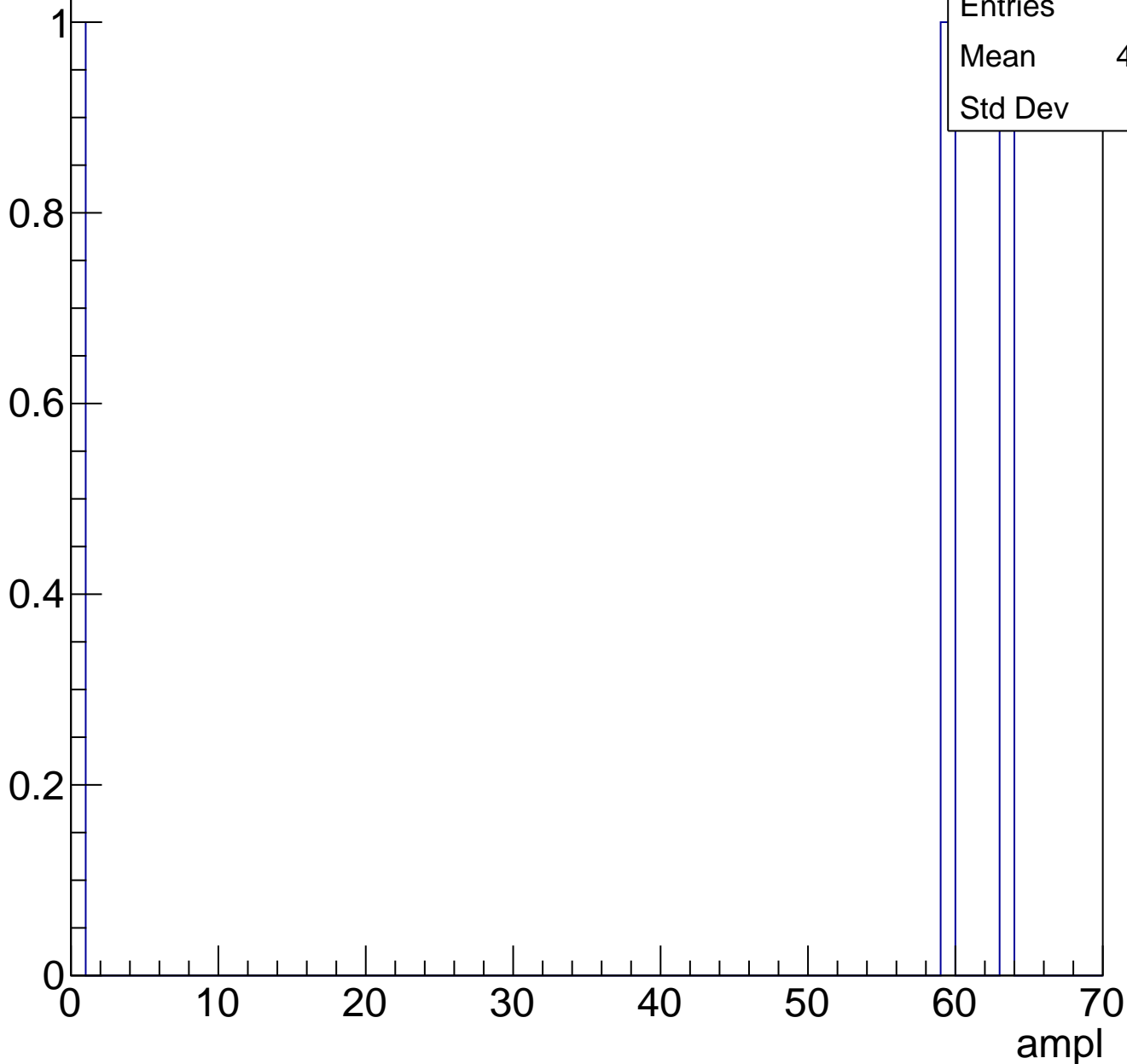
Entry



B1L103S, U24-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch79, adc0

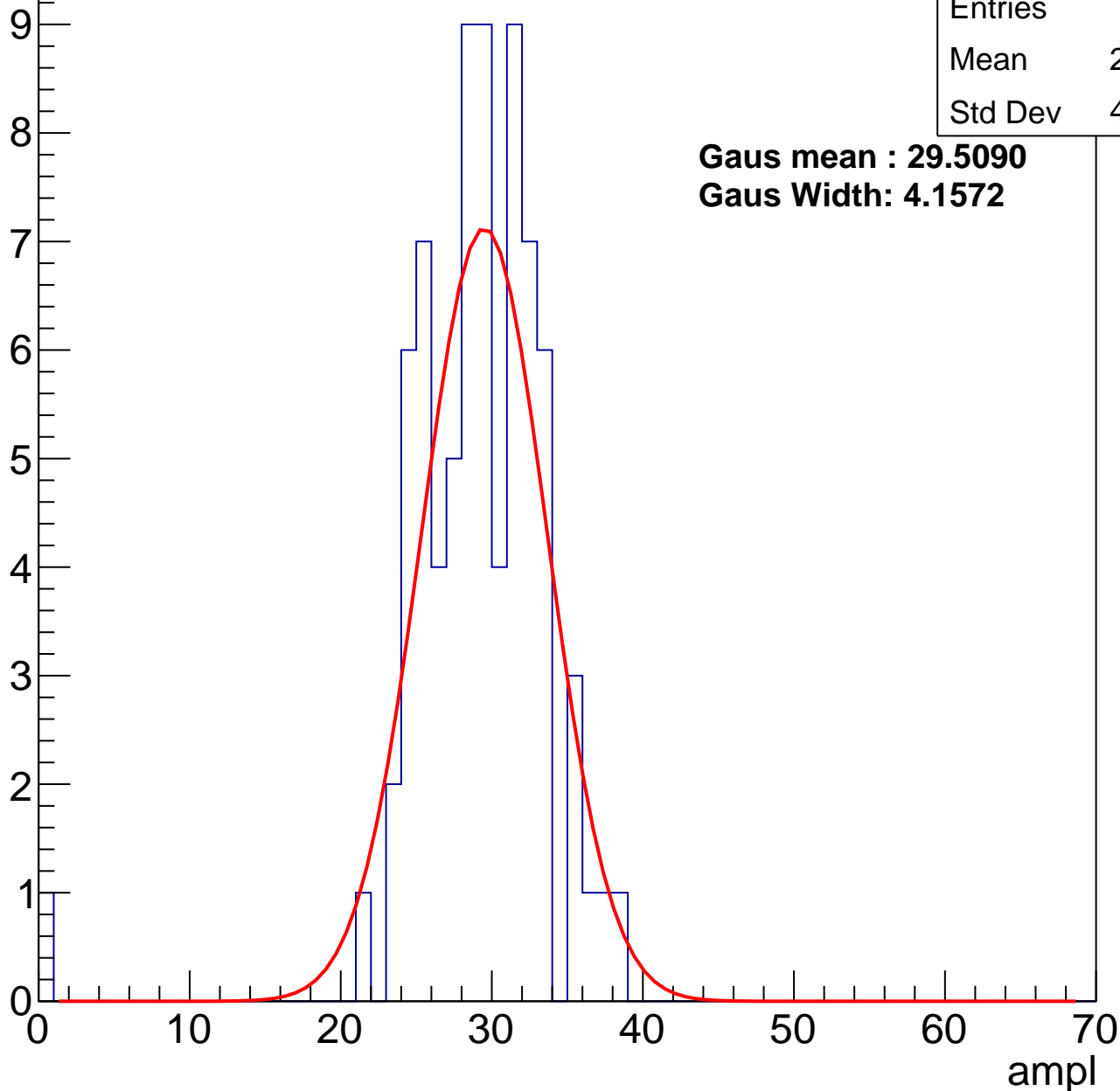
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.62
Std Dev	4.858

Gaus mean : 29.5090

Gaus Width: 4.1572



B1L103S, U24-ch79, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	58
Mean	35.79
Std Dev	3.576

Gaus mean : 36.6549

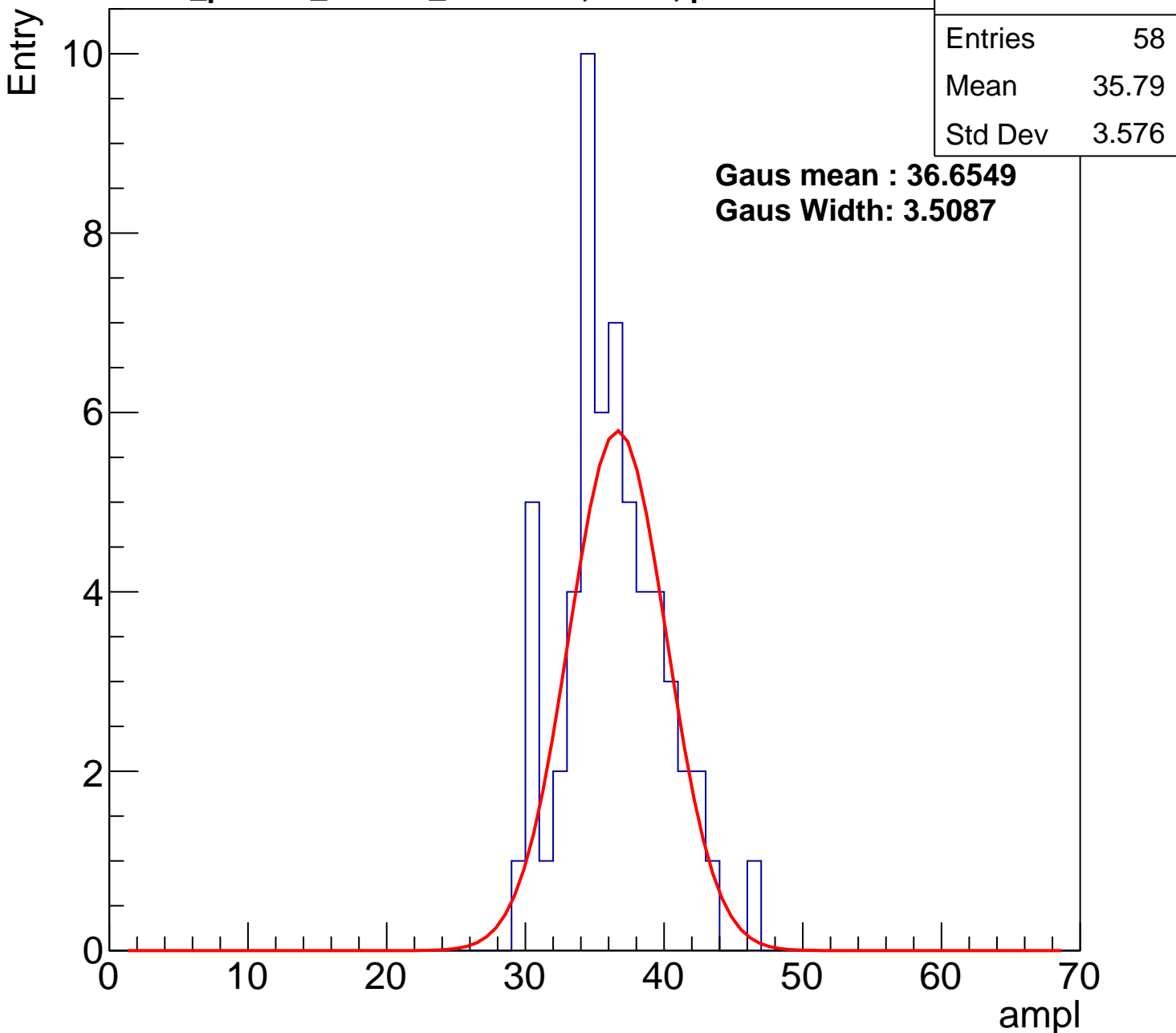
Gaus Width: 3.5087

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch79, adc2

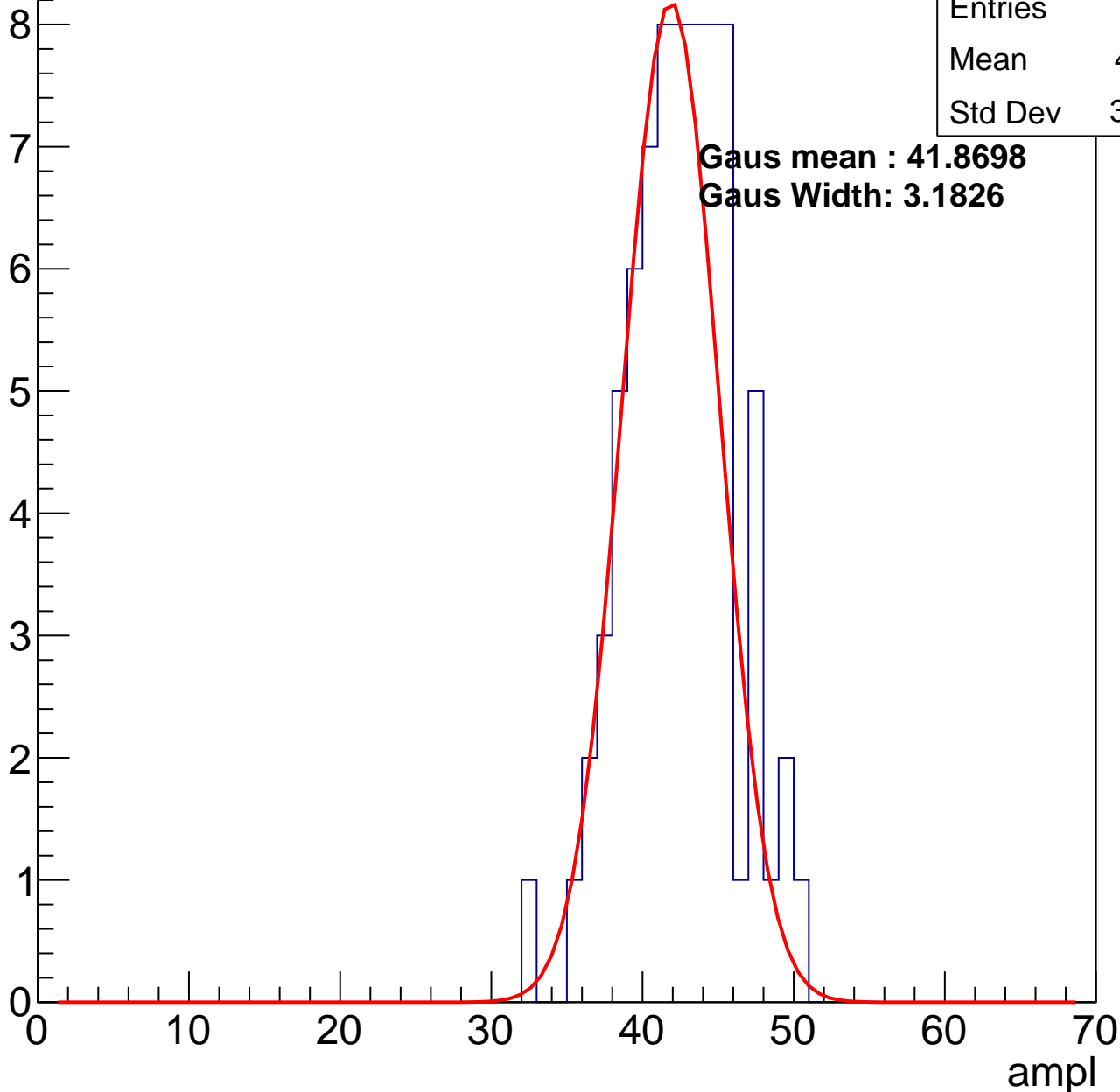
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.01
Std Dev	3.497

Gaus mean : 41.8698

Gaus Width: 3.1826

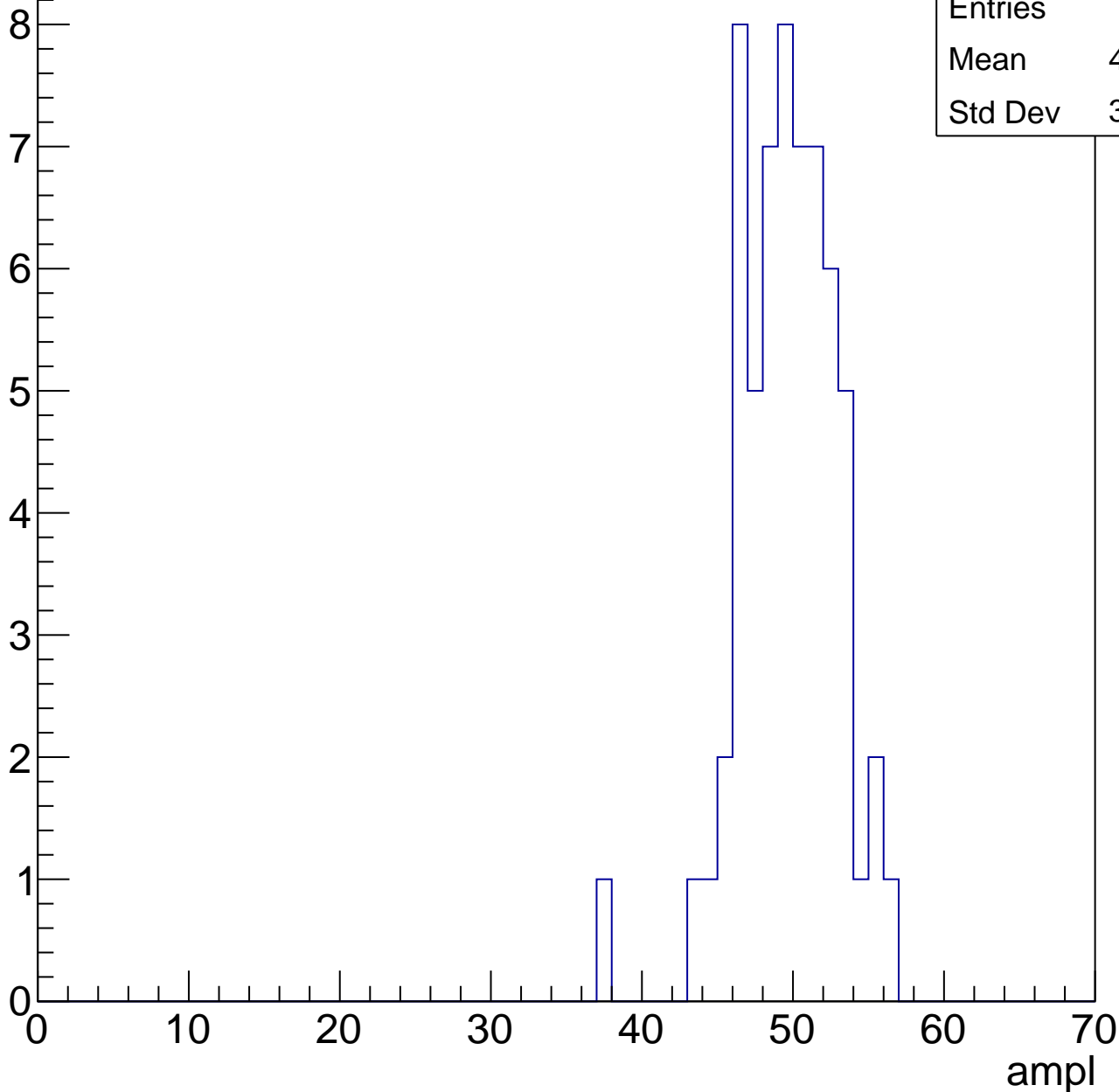


B1L103S, U24-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

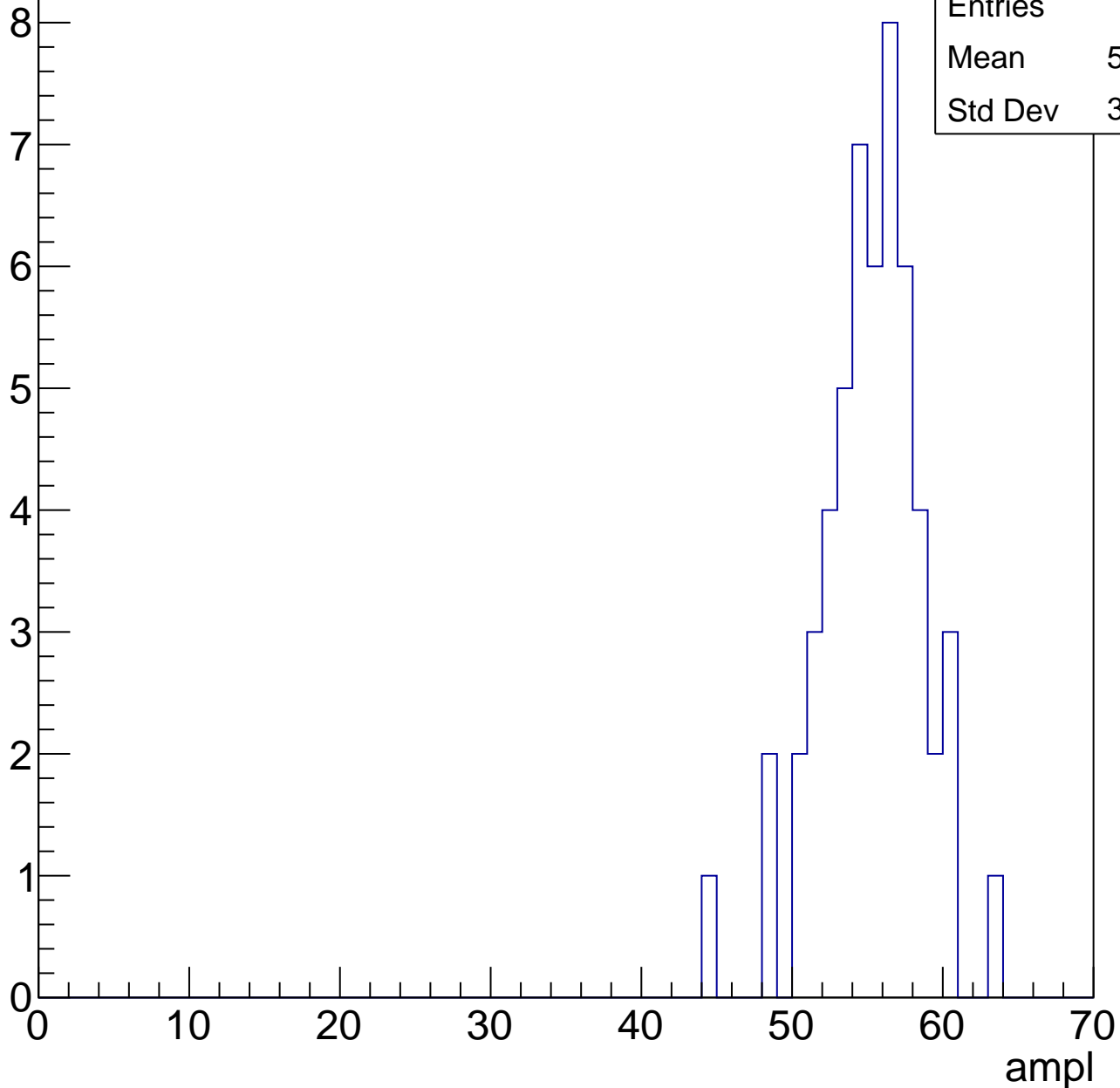
Entries	62
Mean	49.18
Std Dev	3.236



B1L103S, U24-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

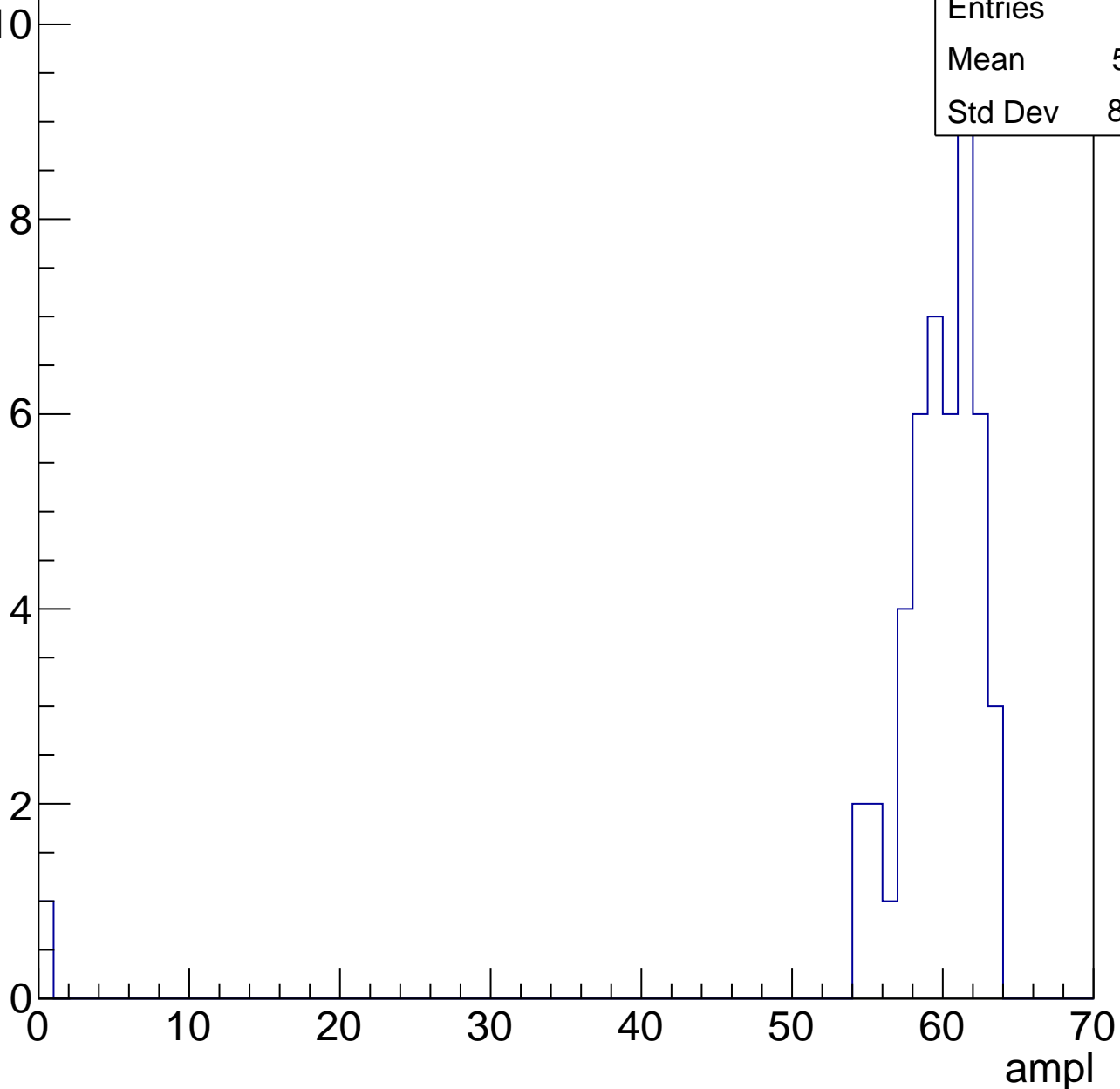


B1L103S, U24-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.21
Std Dev	8.794

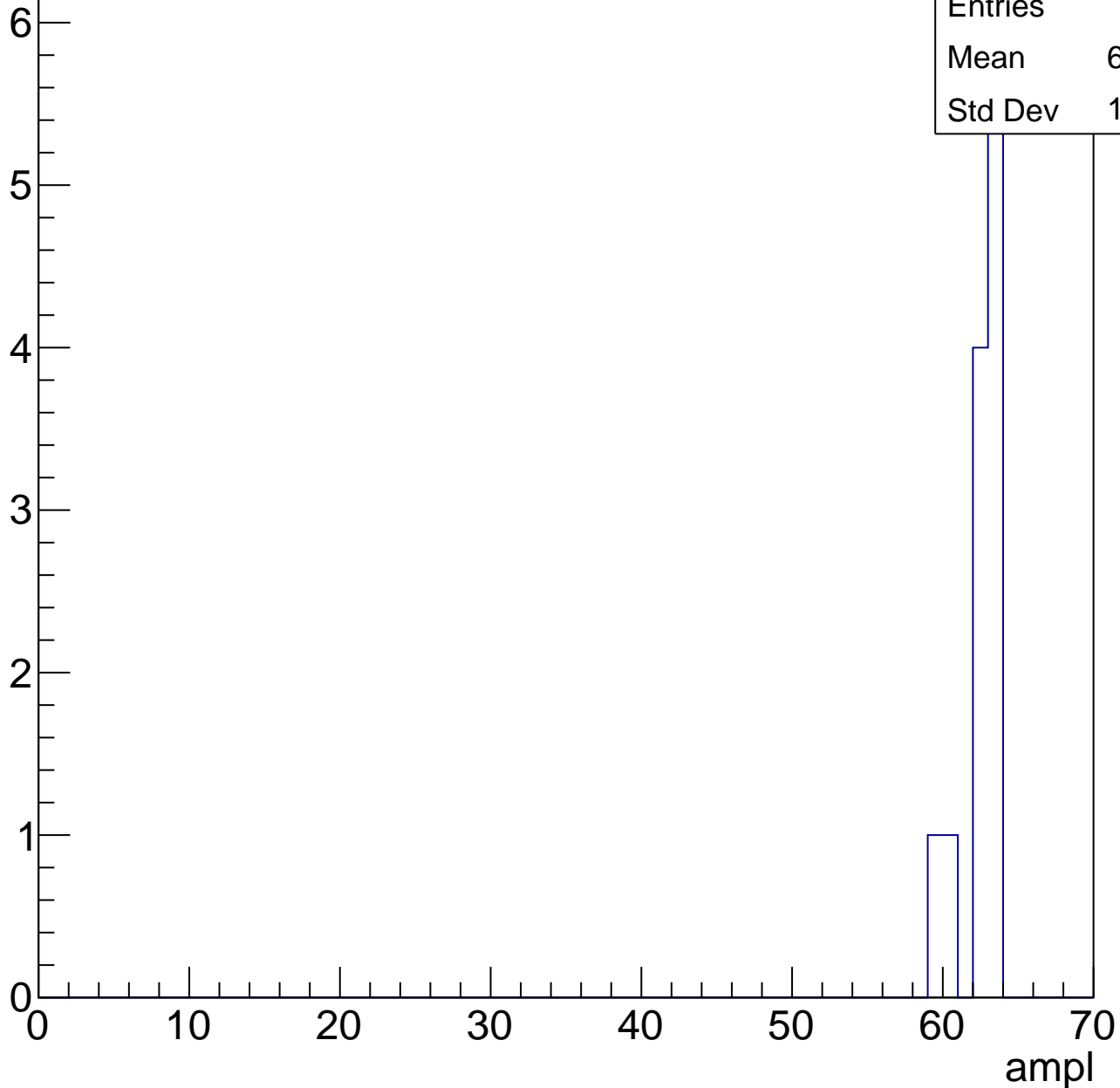


B1L103S, U24-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	62.08
Std Dev	1.256



B1L103S, U24-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch80, adc0

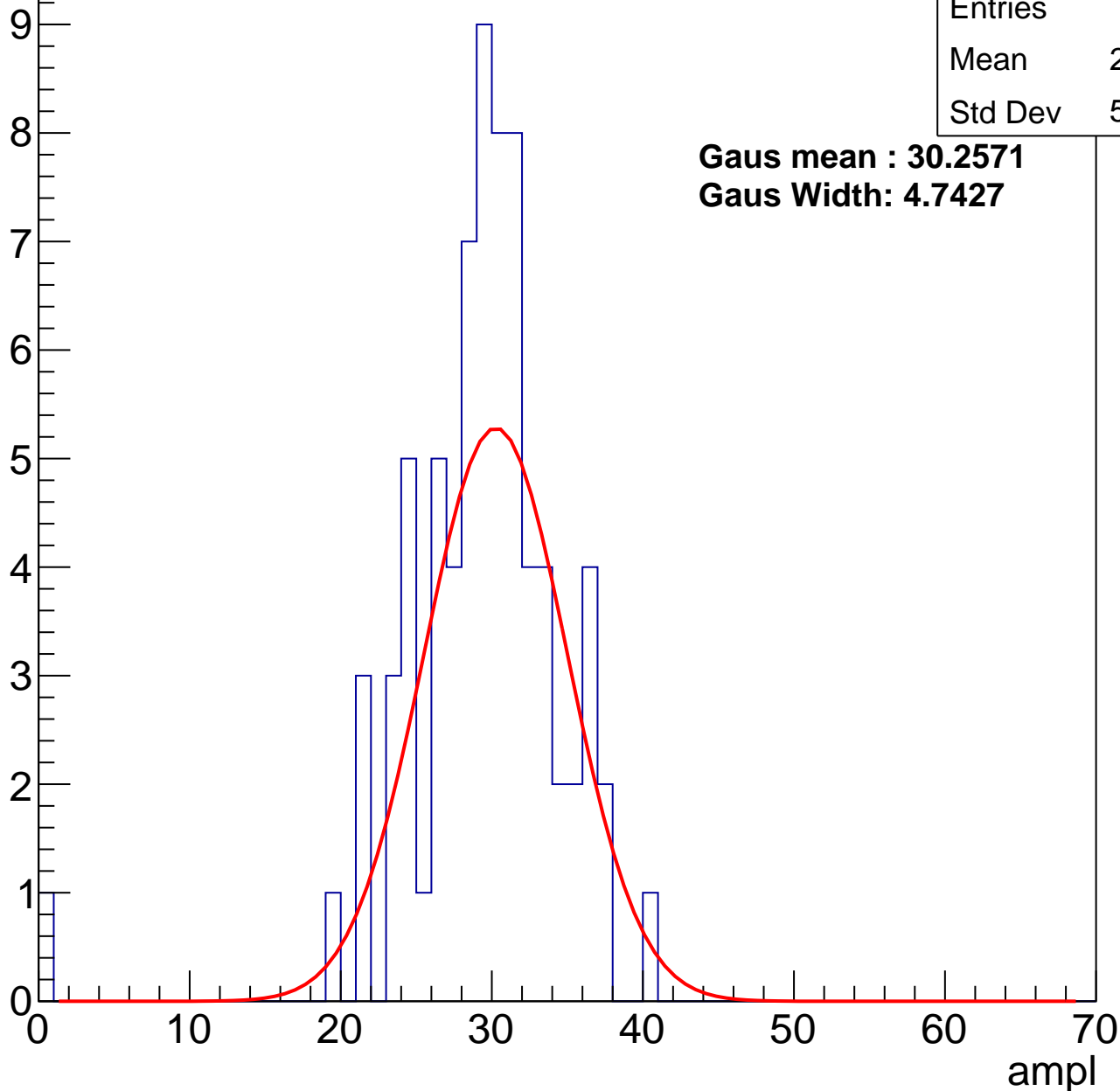
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.85
Std Dev	5.397

Gaus mean : 30.2571

Gaus Width: 4.7427



B1L103S, U24-ch80, adc1

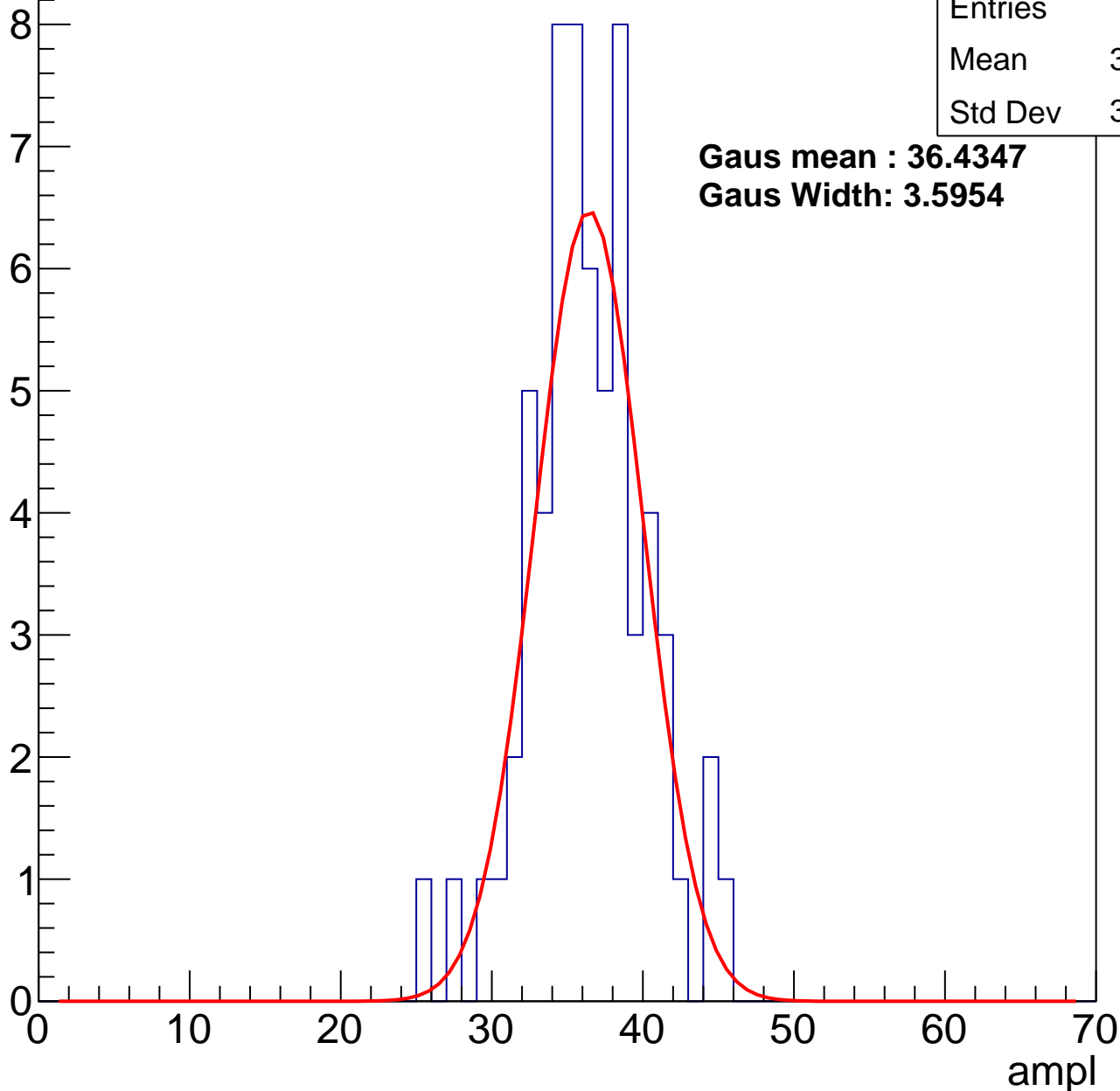
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.89
Std Dev	3.833

Gaus mean : 36.4347

Gaus Width: 3.5954



B1L103S, U24-ch80, adc2

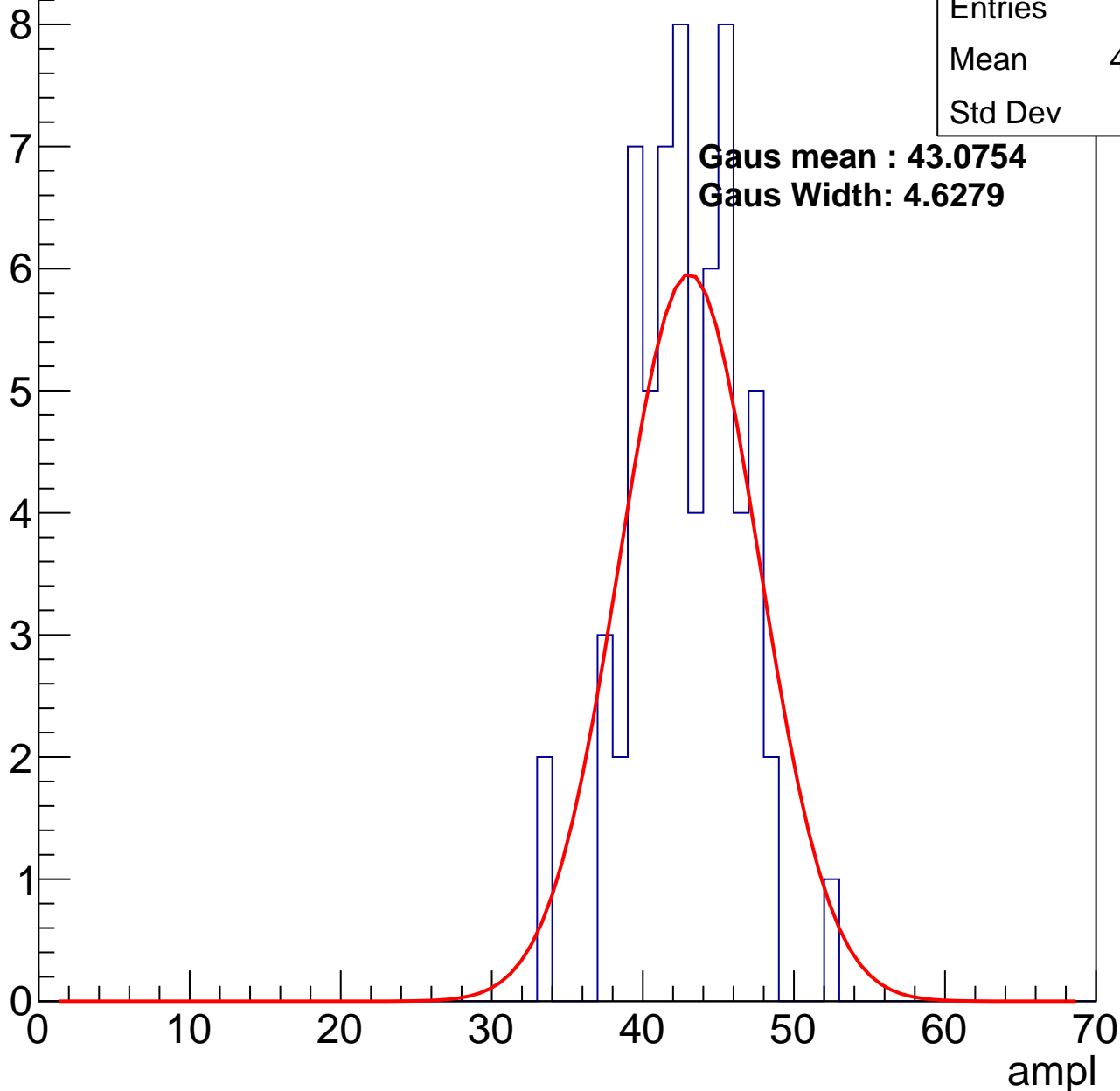
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.38
Std Dev	3.56

Gaus mean : 43.0754

Gaus Width: 4.6279

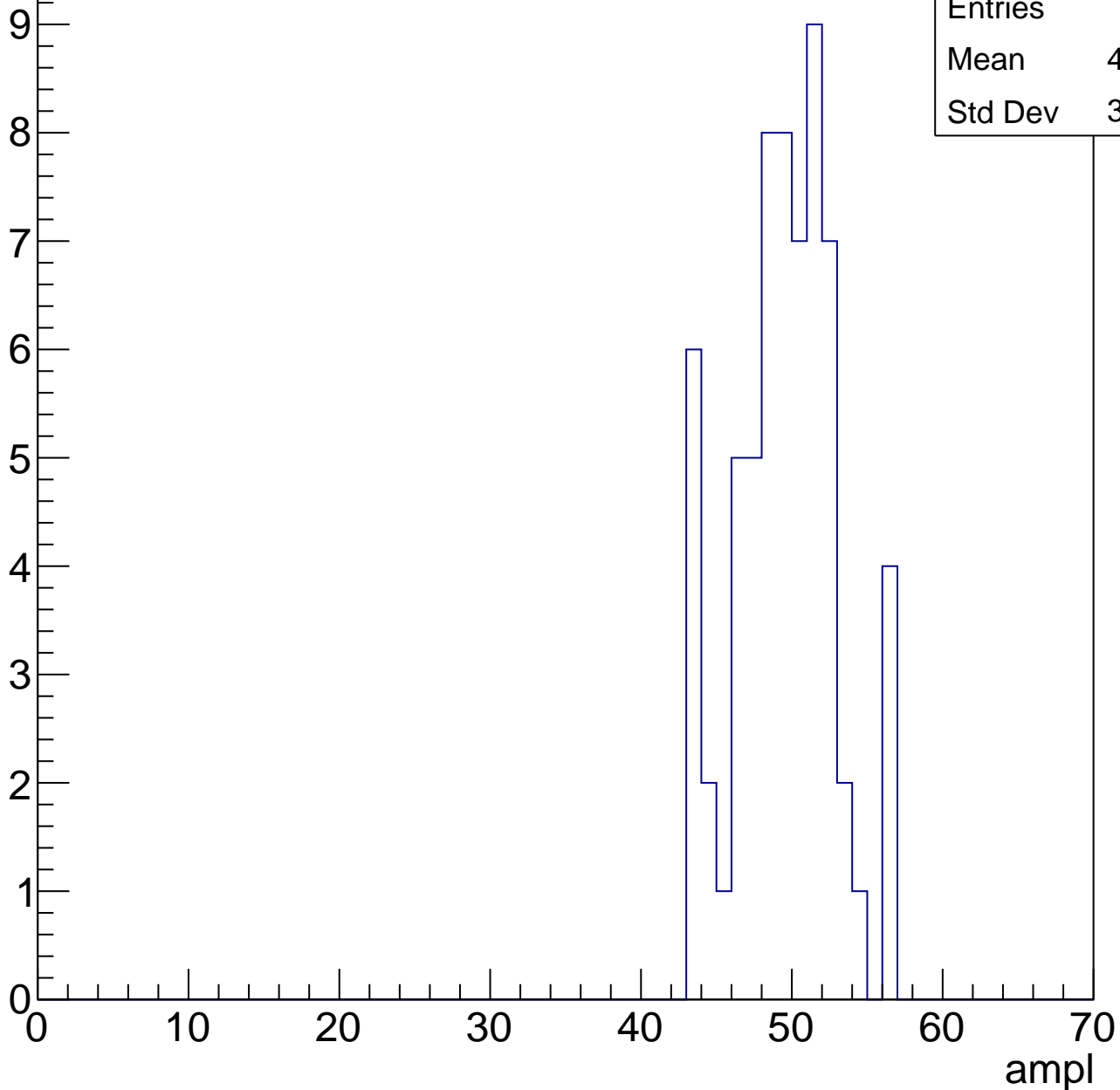


B1L103S, U24-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.06
Std Dev	3.314

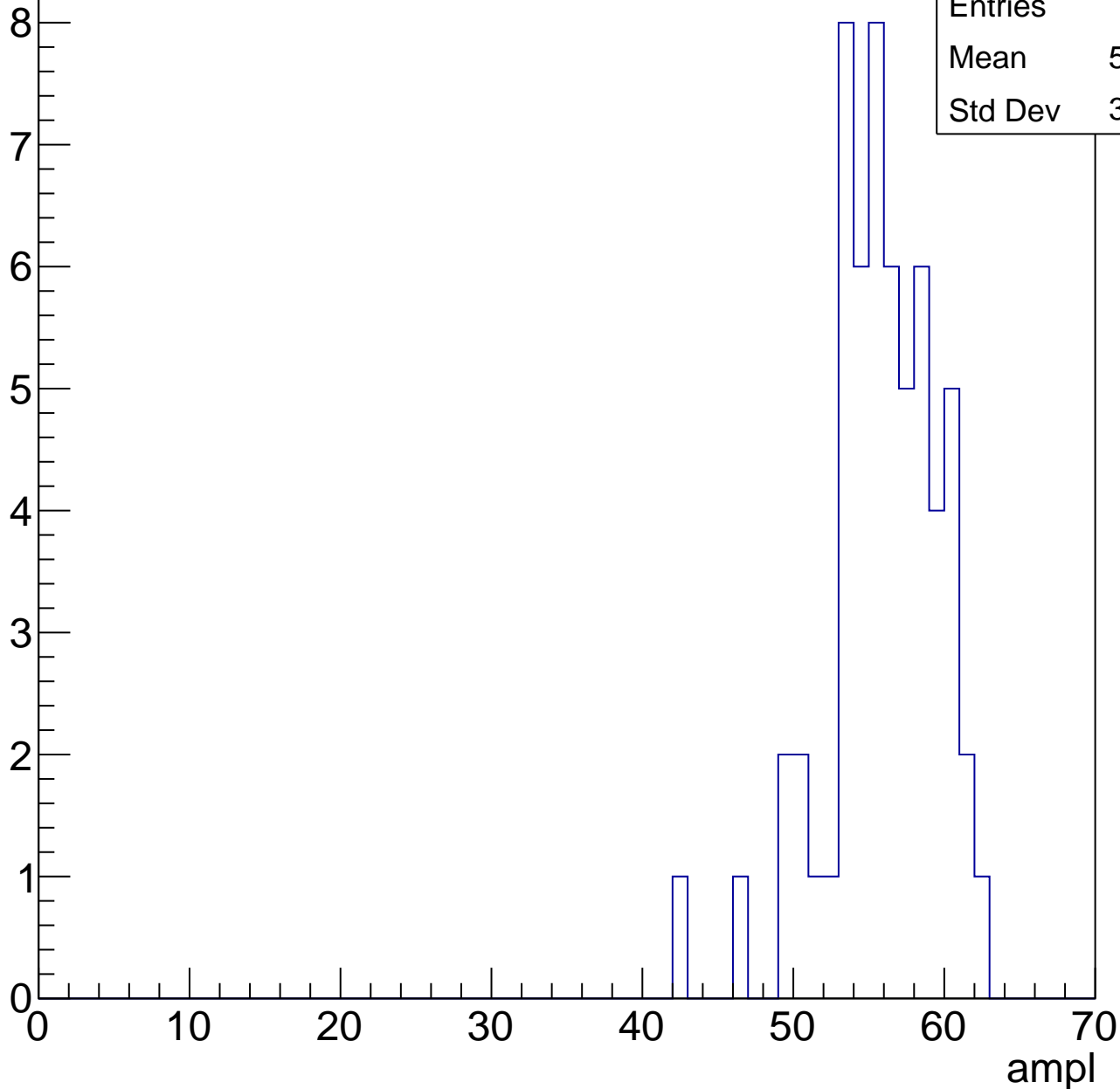


B1L103S, U24-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.36
Std Dev	3.722

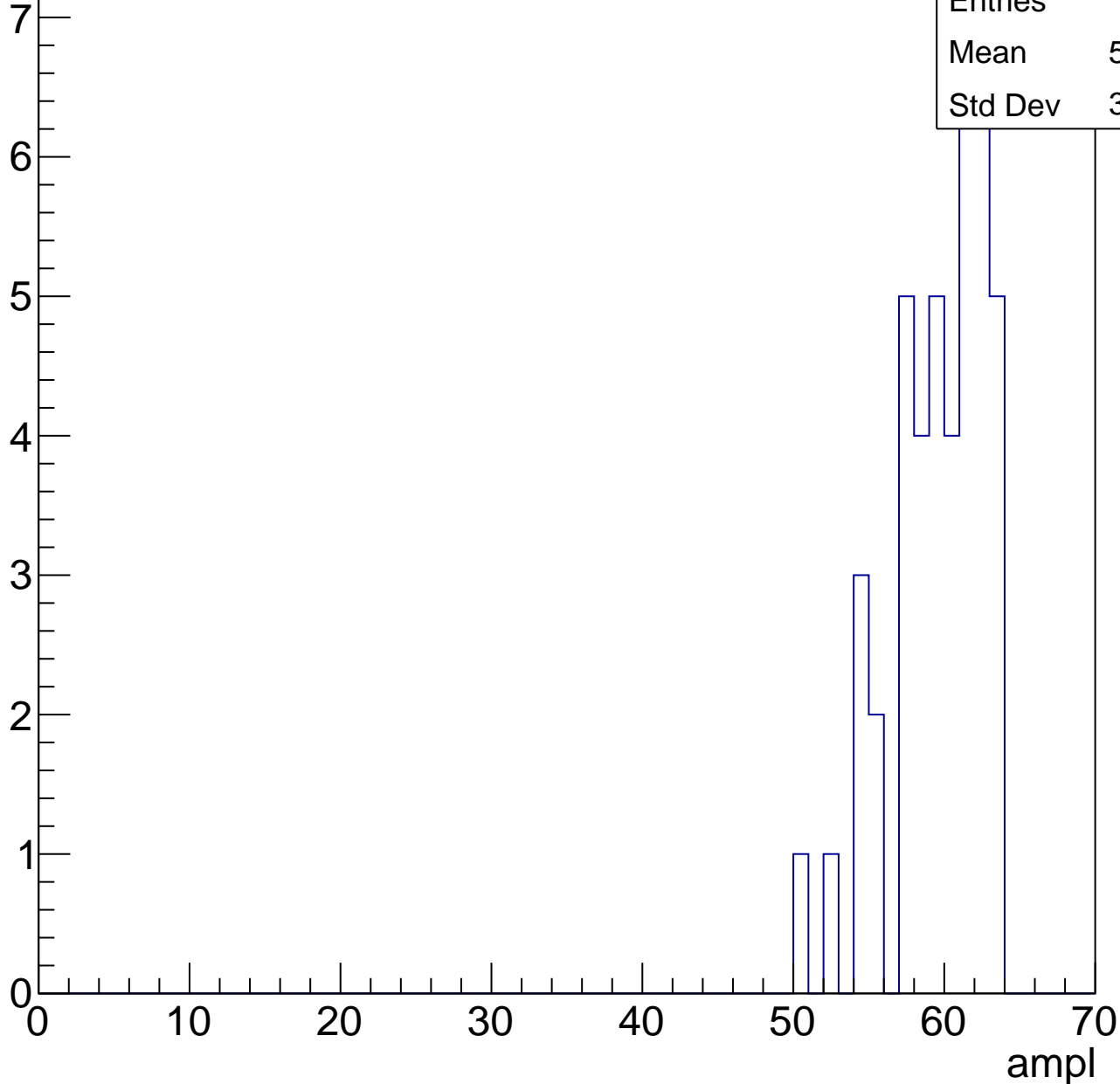


B1L103S, U24-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.14
Std Dev	3.145

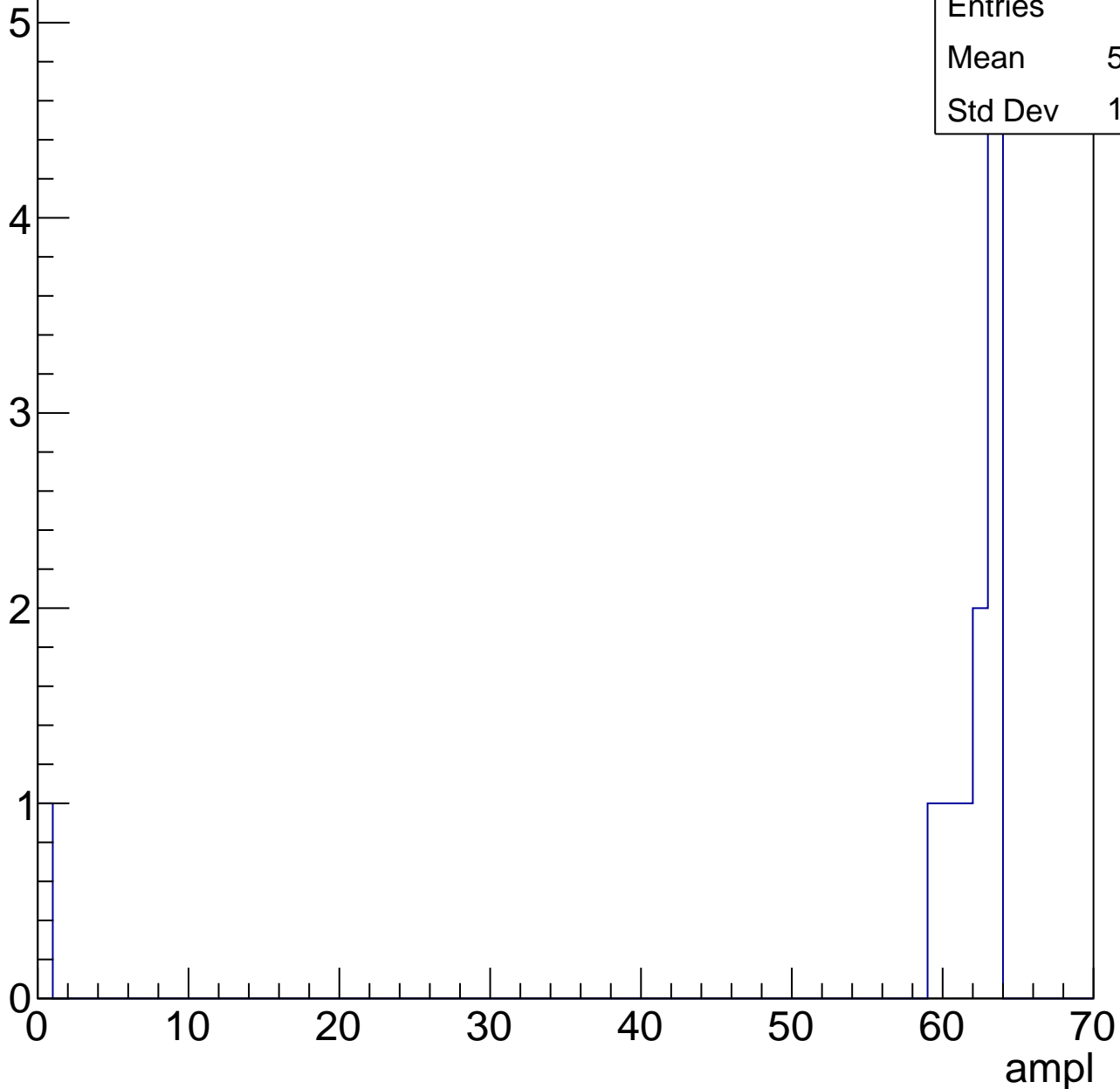


B1L103S, U24-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	56.27
Std Dev	17.84



B1L103S, U24-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch81, adc0

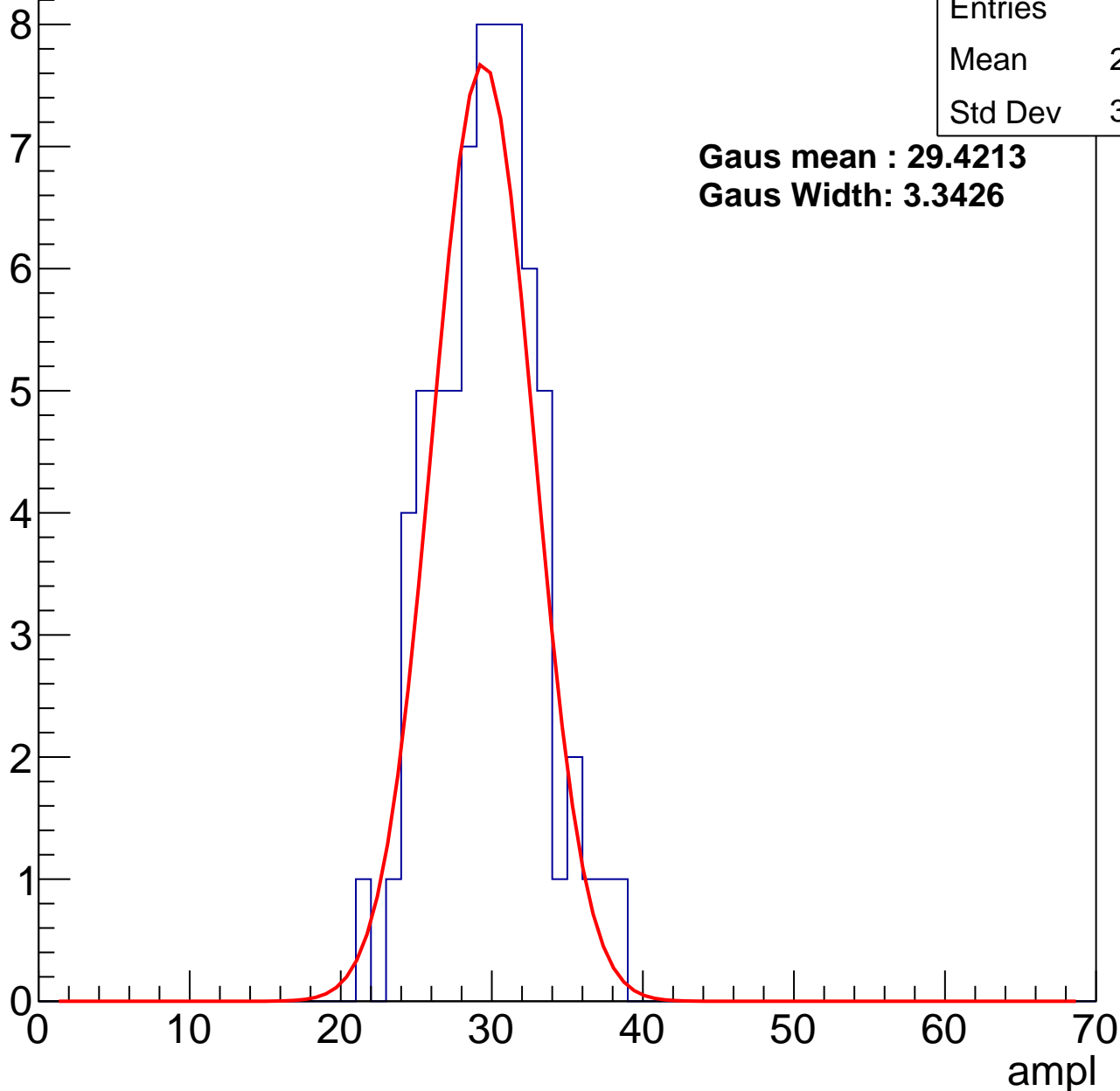
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.25
Std Dev	3.432

Gaus mean : 29.4213

Gaus Width: 3.3426



B1L103S, U24-ch81, adc1

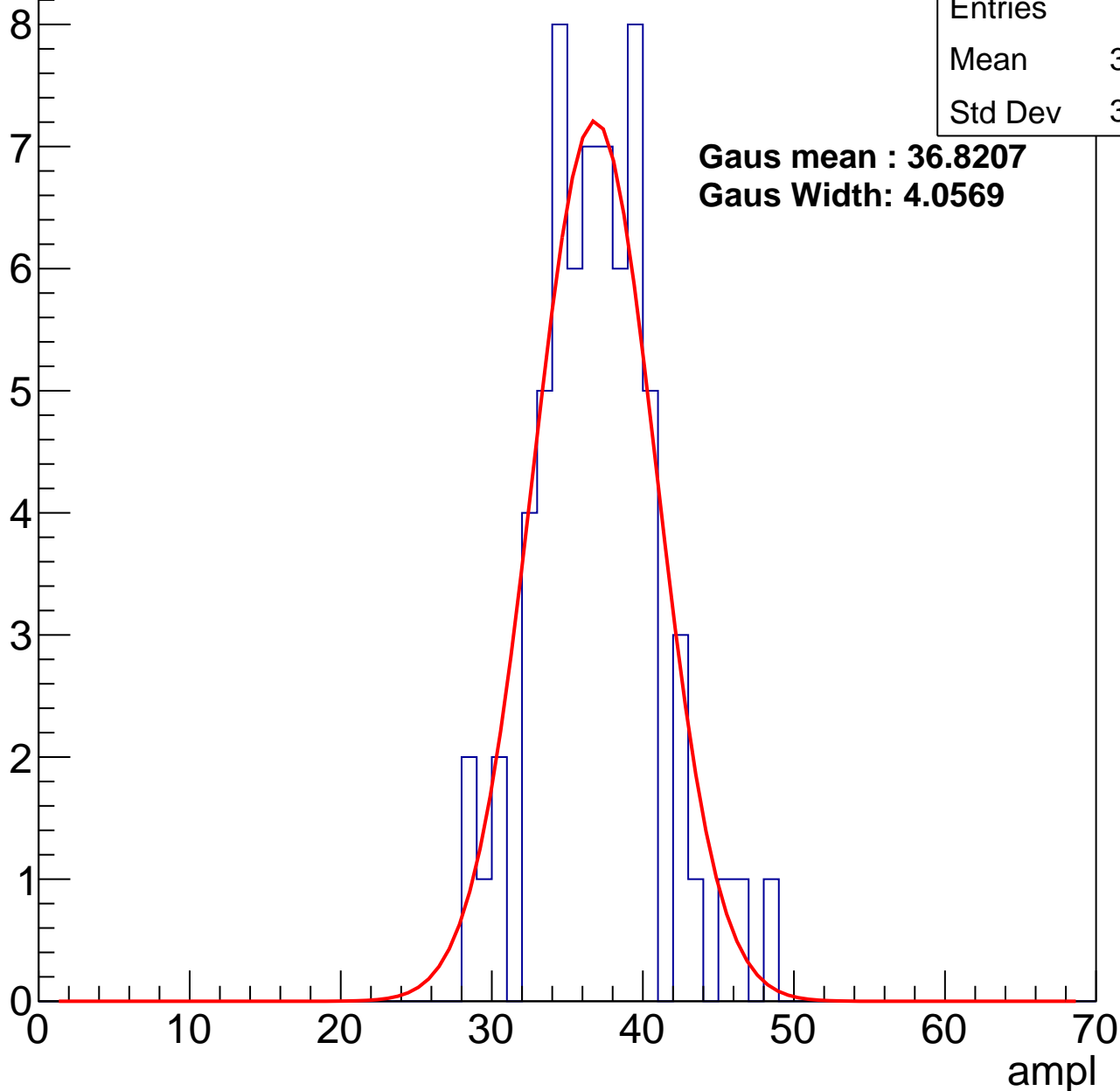
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.46
Std Dev	3.924

Gaus mean : 36.8207

Gaus Width: 4.0569



B1L103S, U24-ch81, adc2

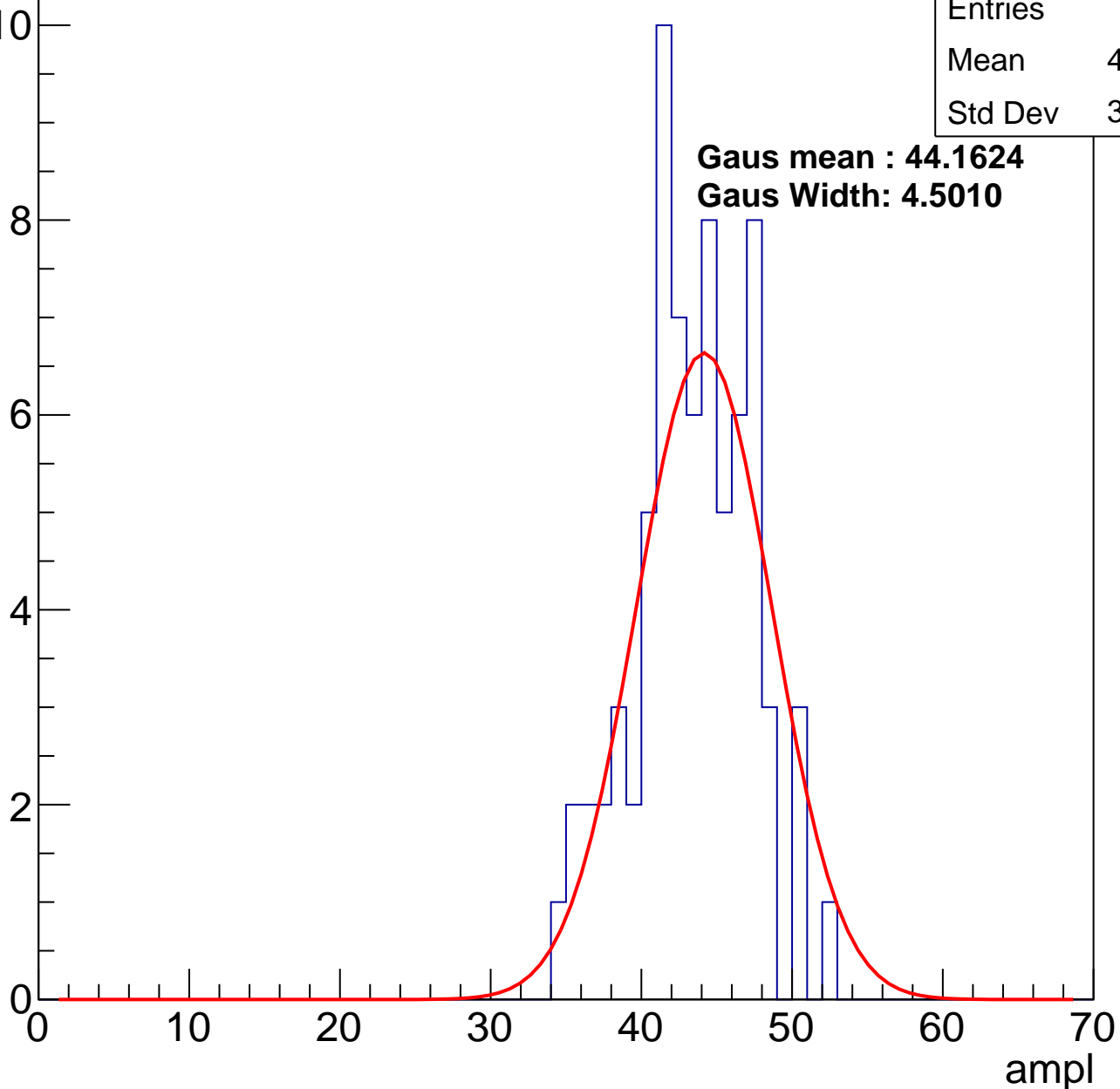
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.96
Std Dev	3.839

Gaus mean : 44.1624

Gaus Width: 4.5010



B1L103S, U24-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

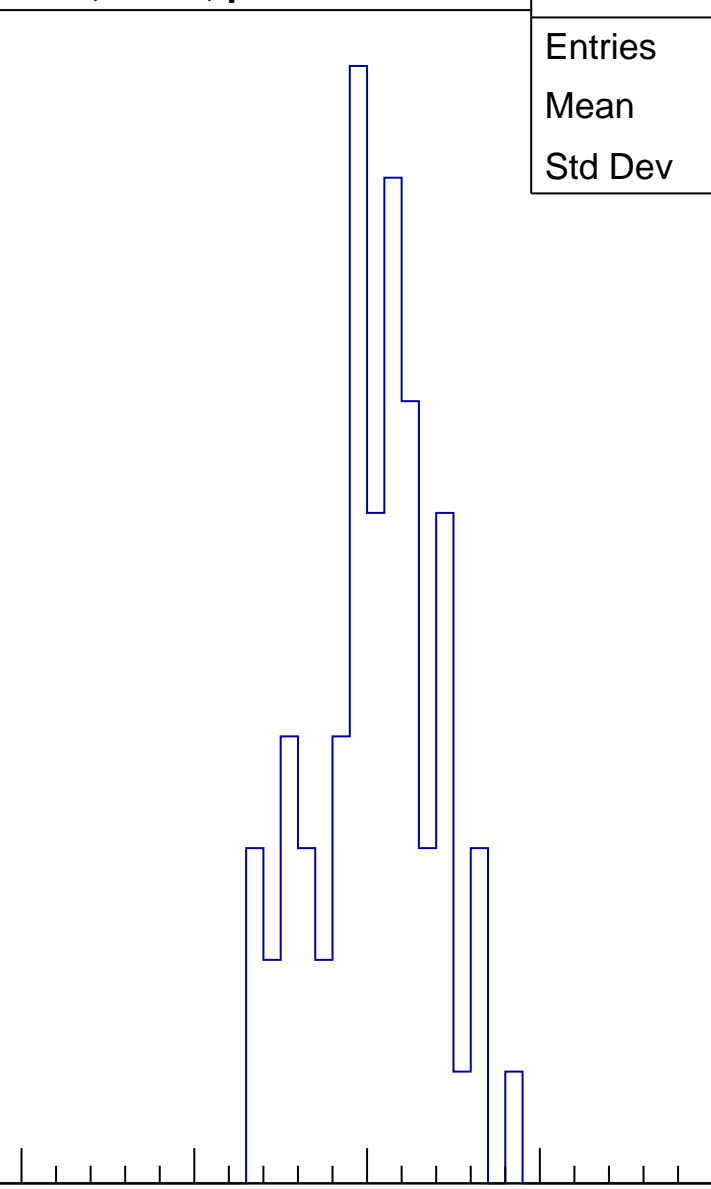
Entries	64
Mean	49.97
Std Dev	3.464

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

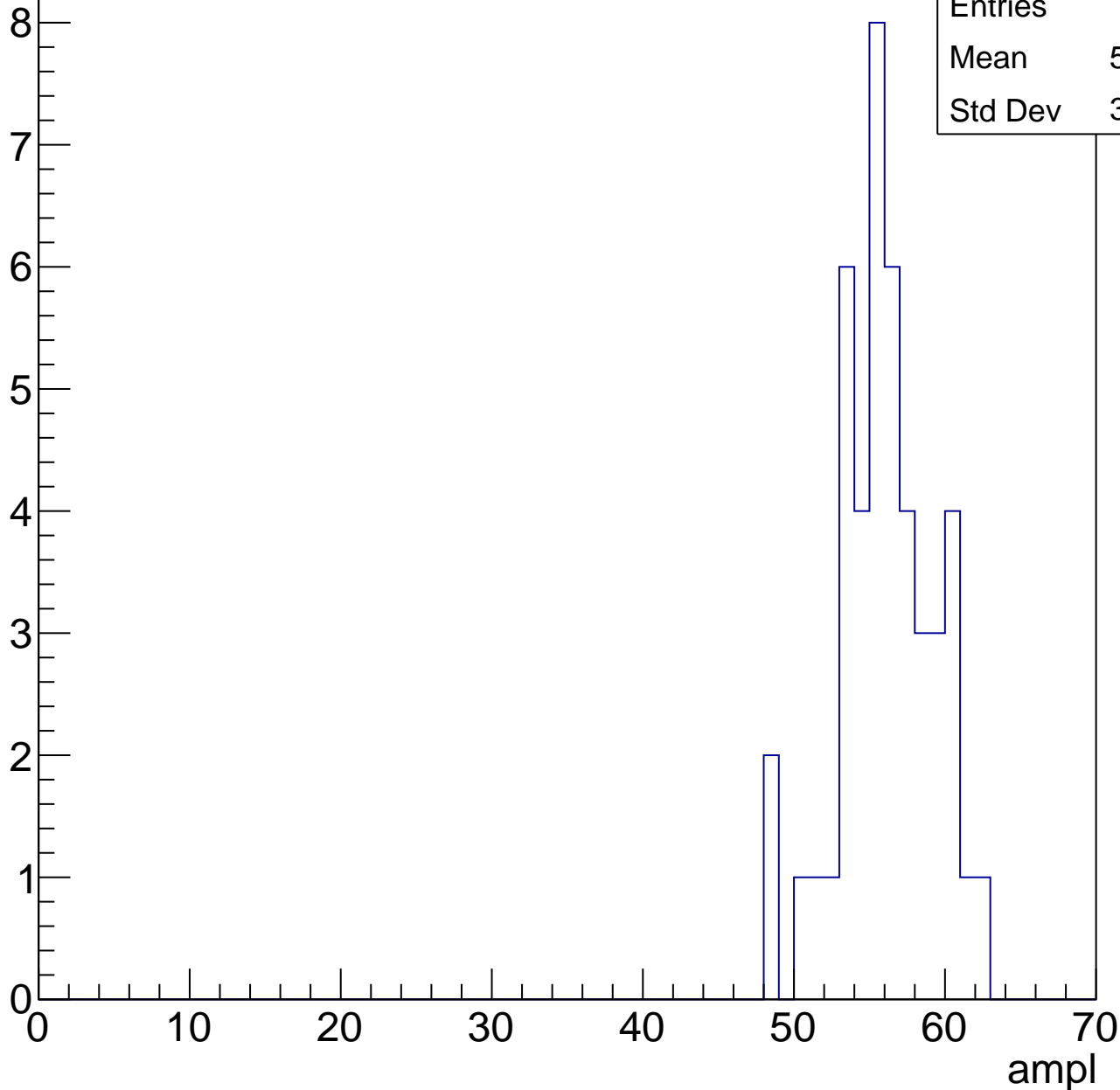


B1L103S, U24-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.58
Std Dev	3.138

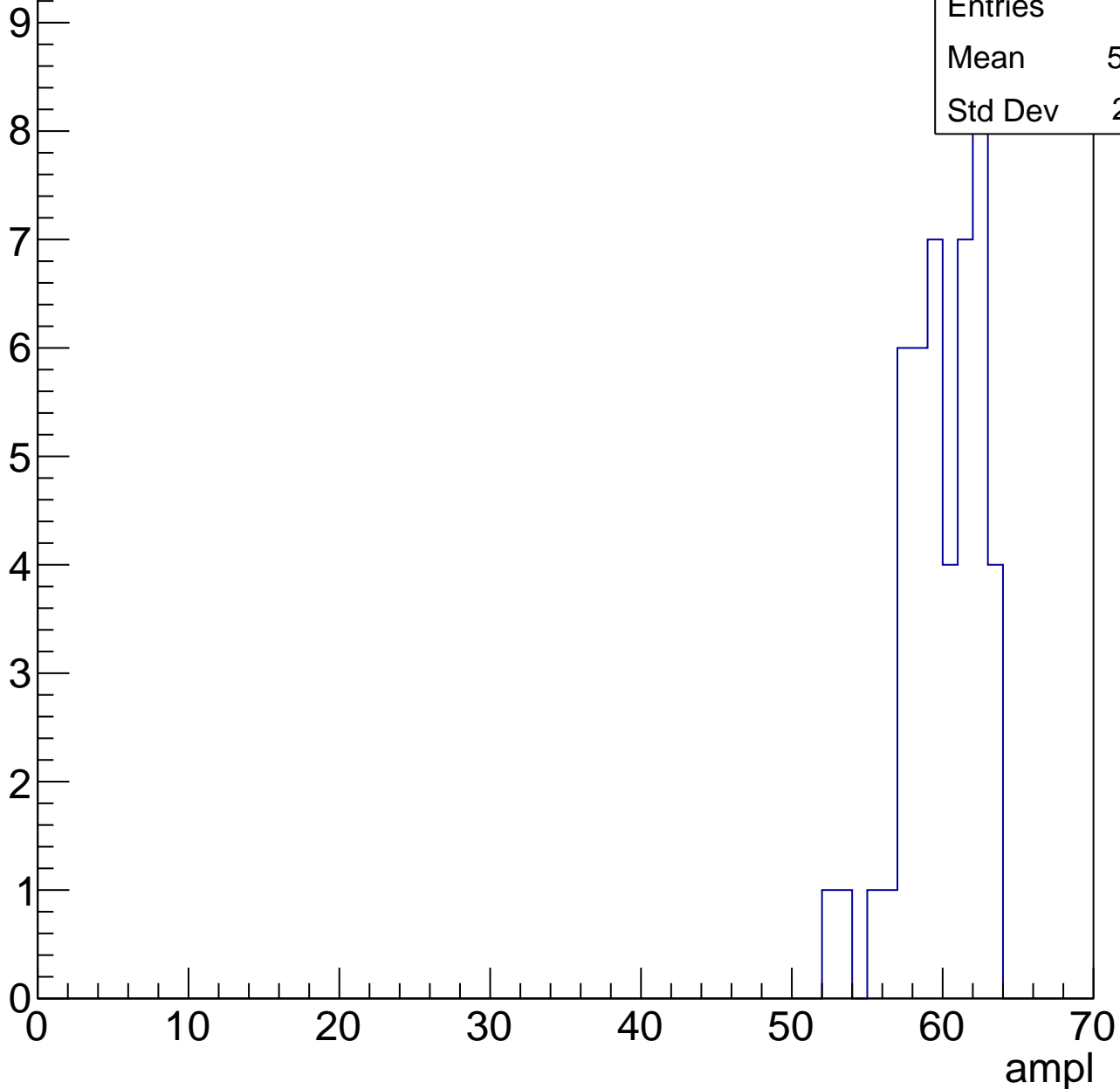


B1L103S, U24-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	59.49
Std Dev	2.551

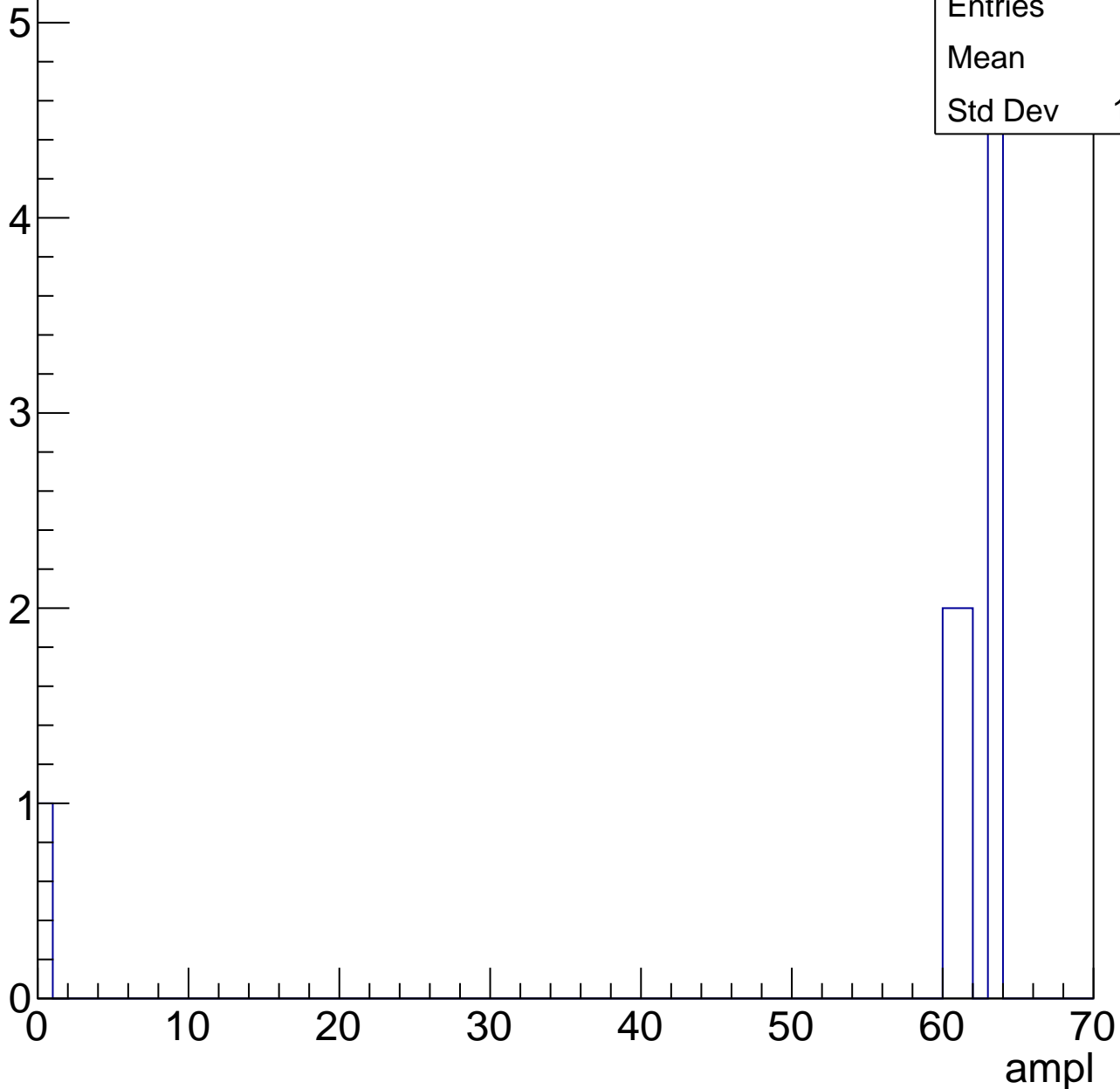


B1L103S, U24-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	55.7
Std Dev	18.61



B1L103S, U24-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U24-ch82, adc0

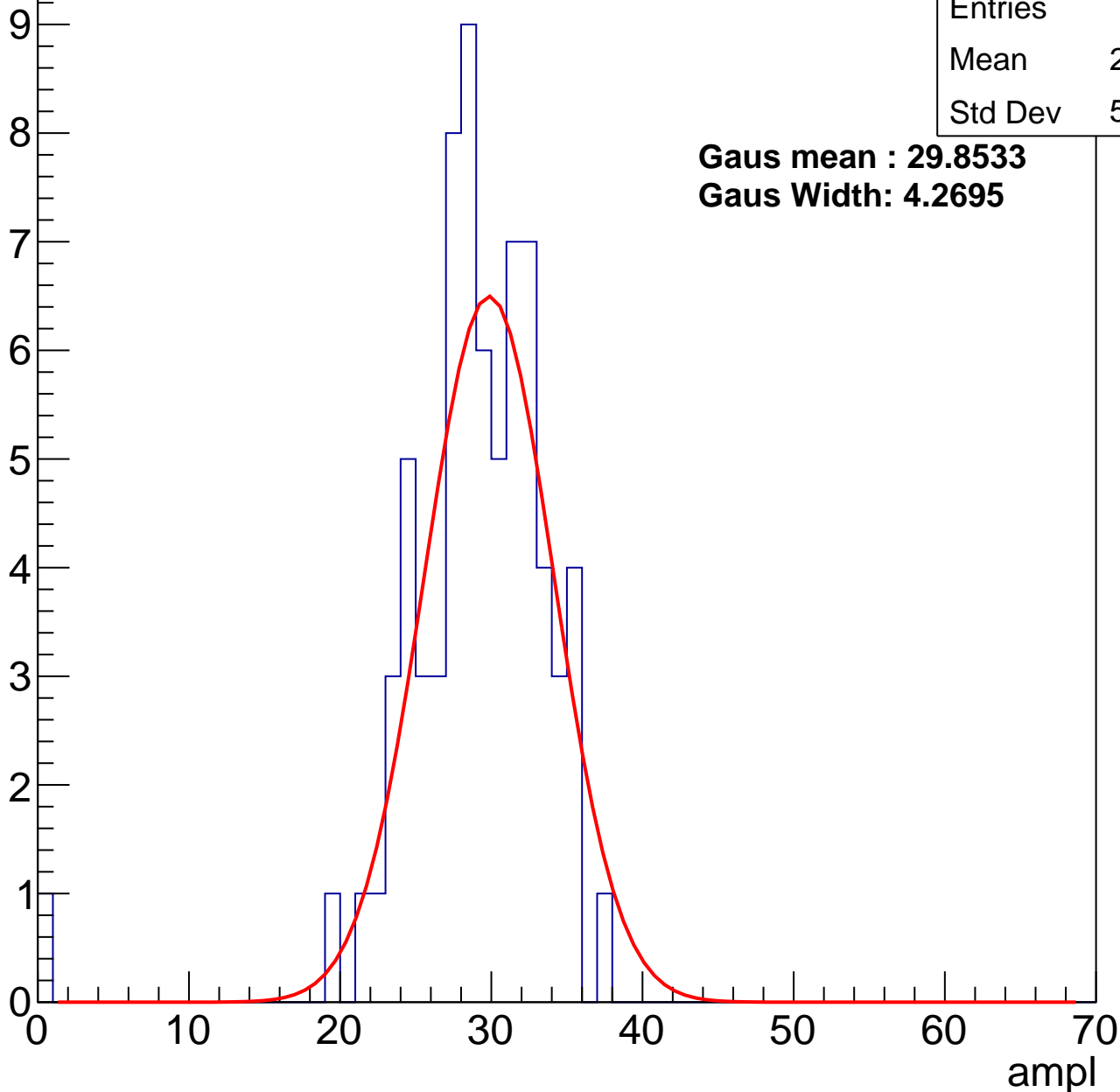
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.44
Std Dev	5.033

Gaus mean : 29.8533

Gaus Width: 4.2695



B1L103S, U24-ch82, adc1

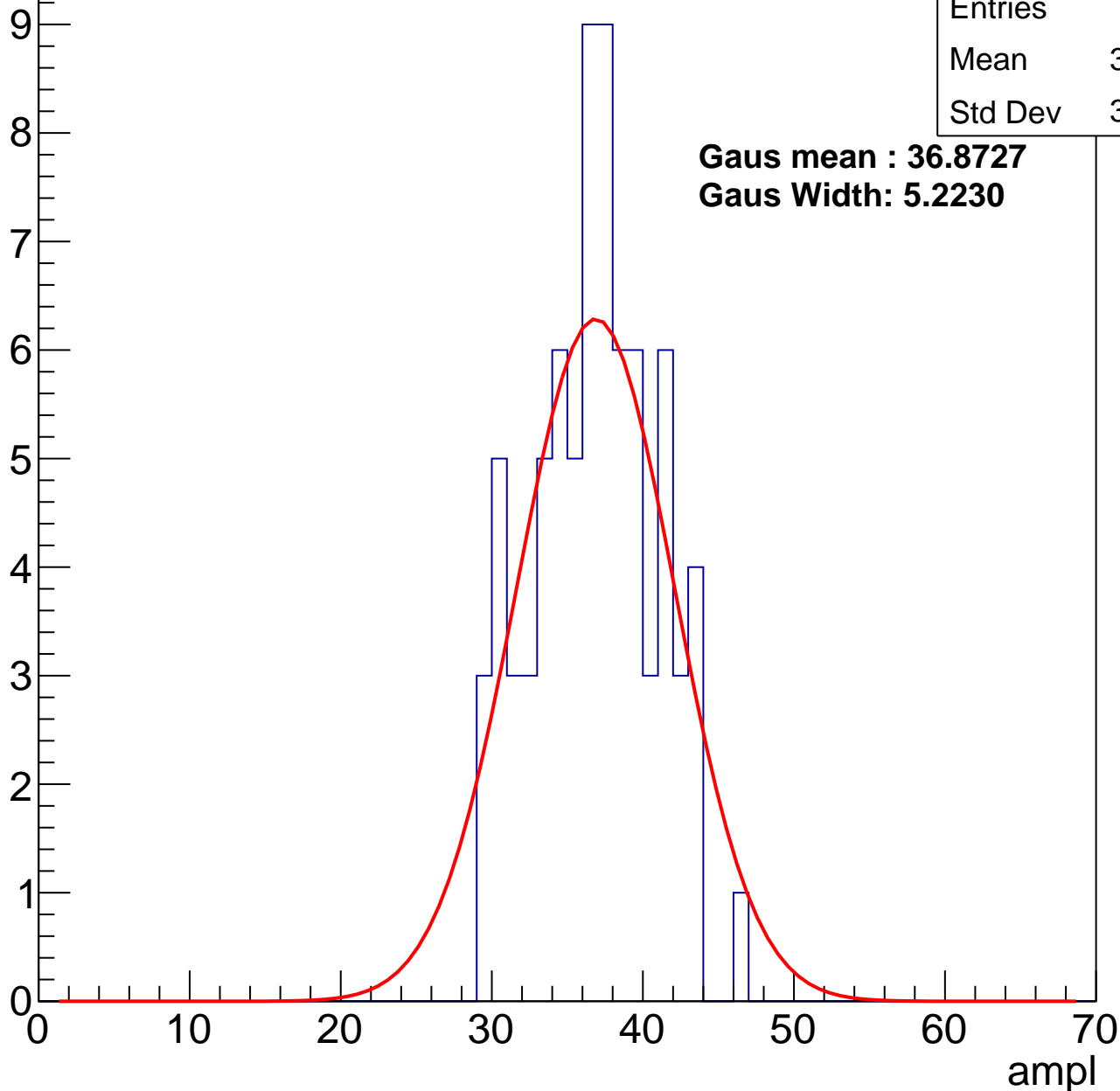
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.35
Std Dev	3.954

Gaus mean : 36.8727

Gaus Width: 5.2230



B1L103S, U24-ch82, adc2

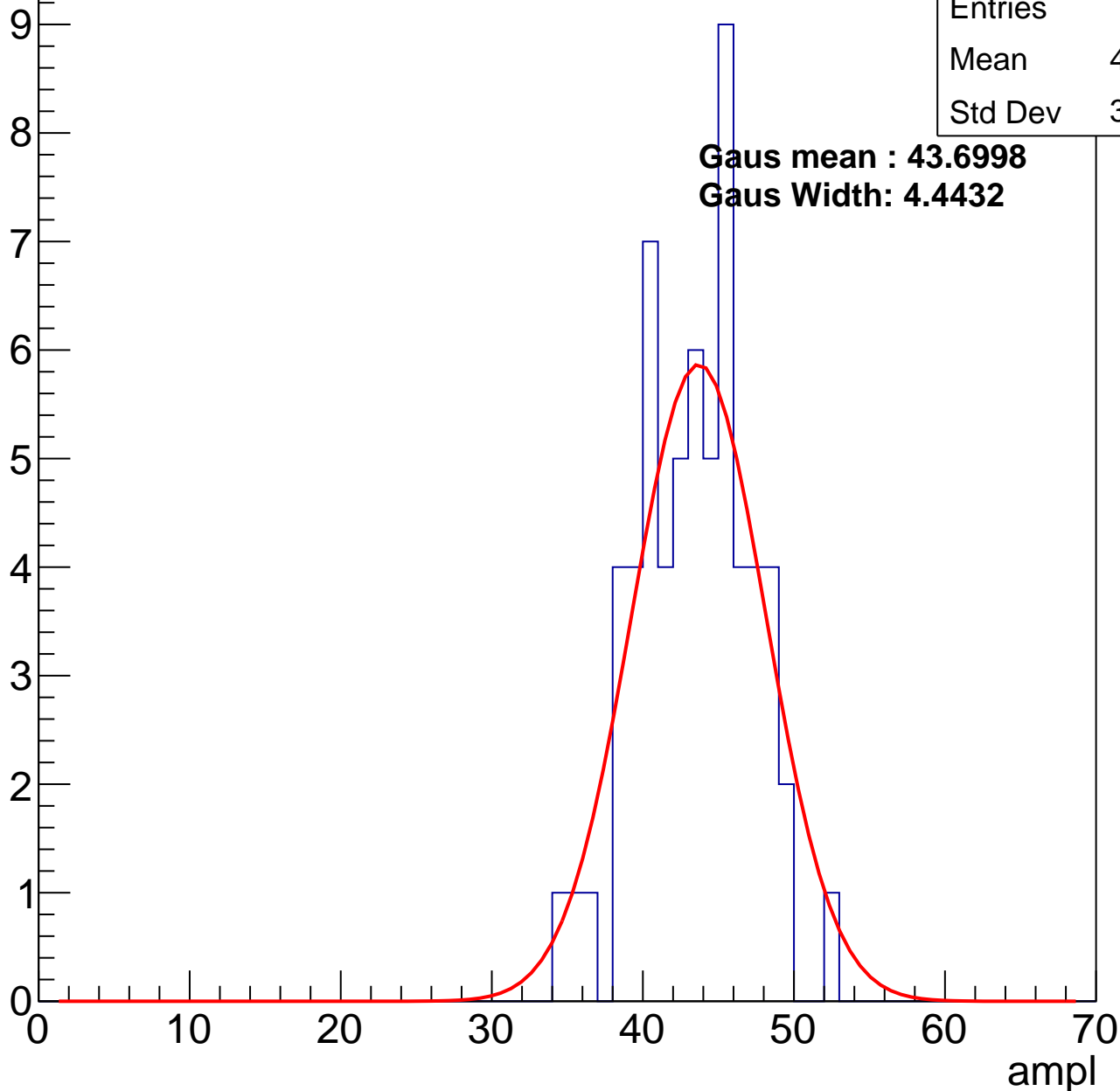
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.97
Std Dev	3.672

Gaus mean : 43.6998

Gaus Width: 4.4432

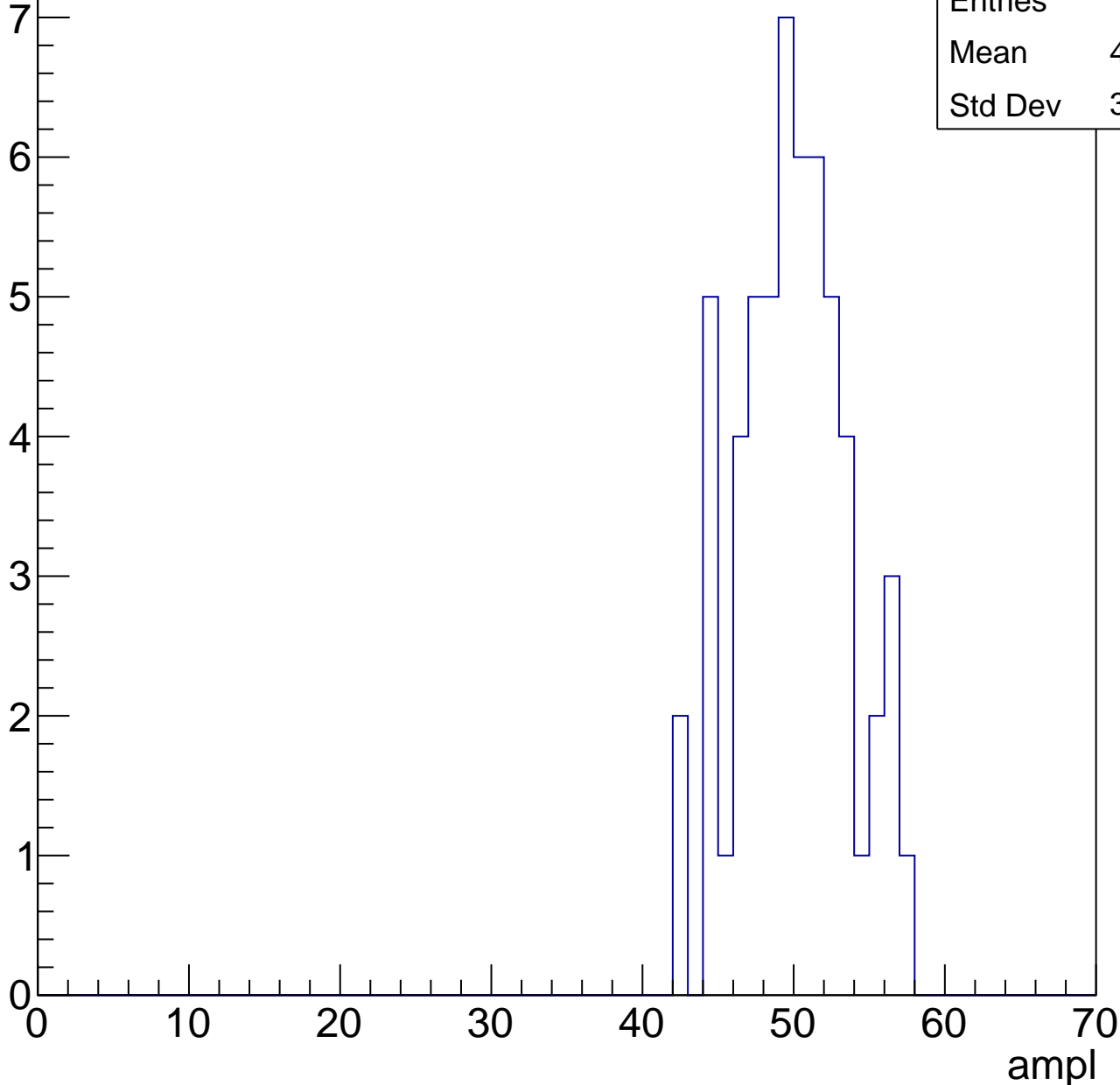


B1L103S, U24-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	49.44
Std Dev	3.593

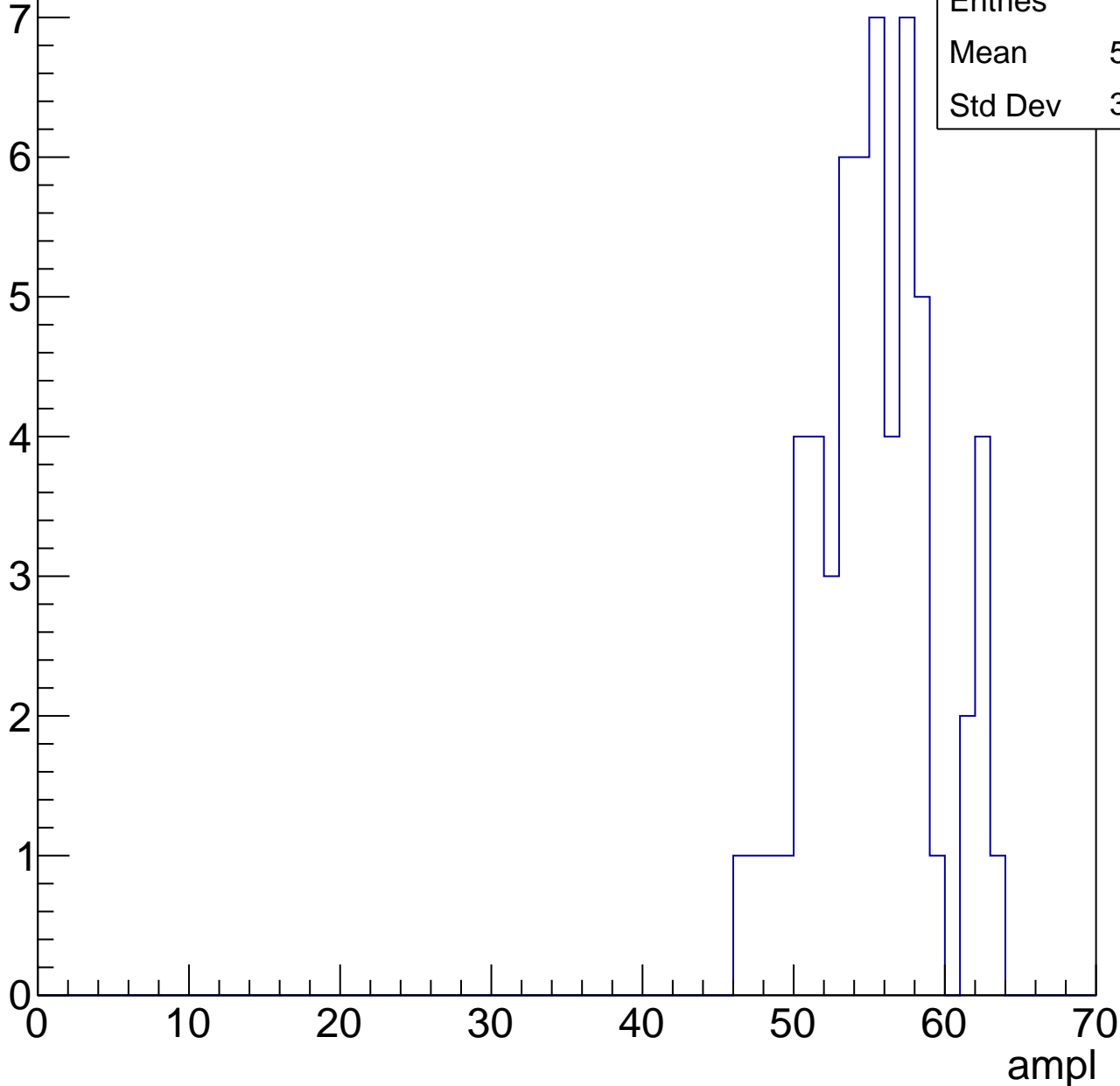


B1L103S, U24-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.86
Std Dev	3.884

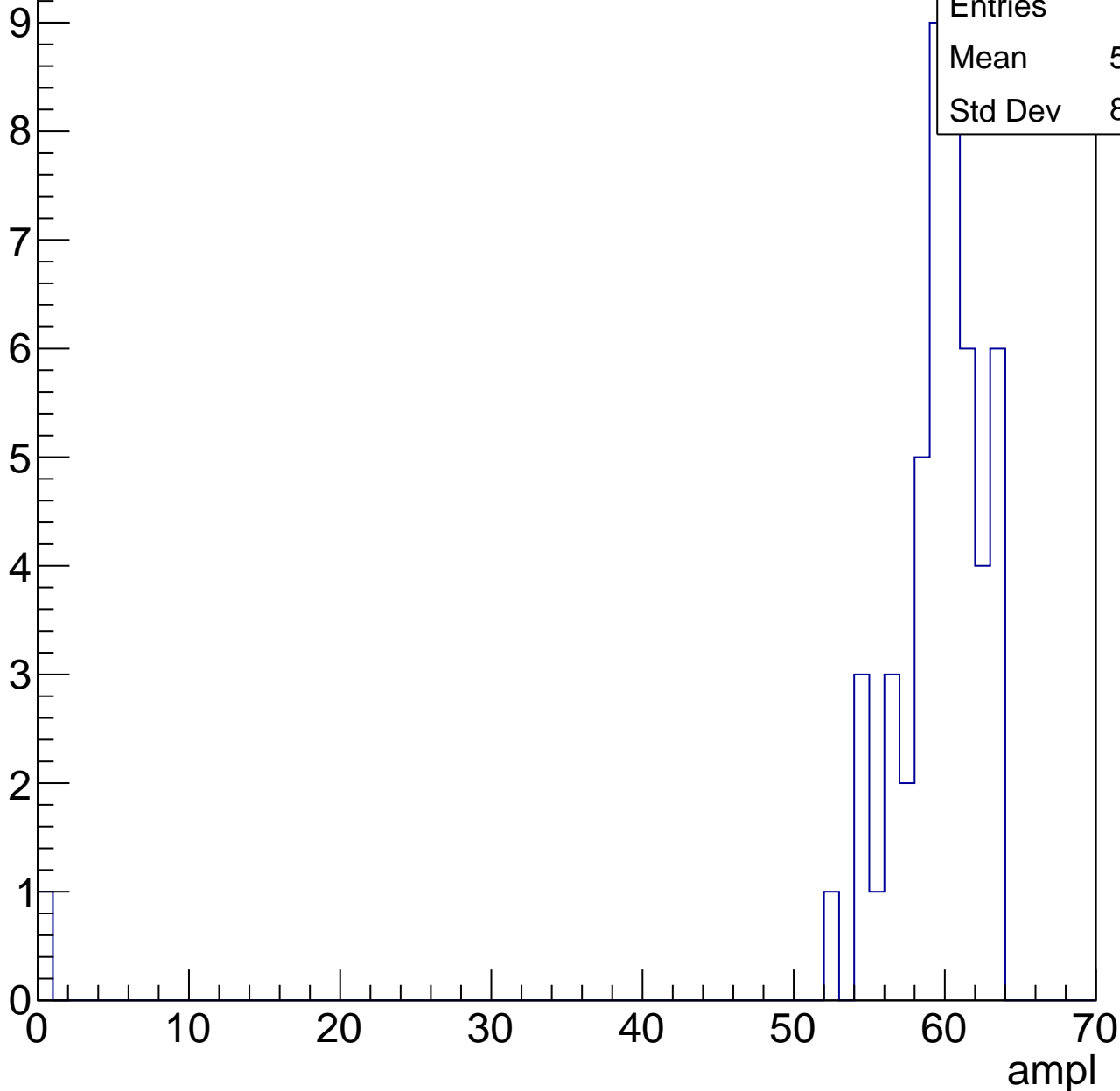


B1L103S, U24-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.08
Std Dev	8.699



B1L103S, U24-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

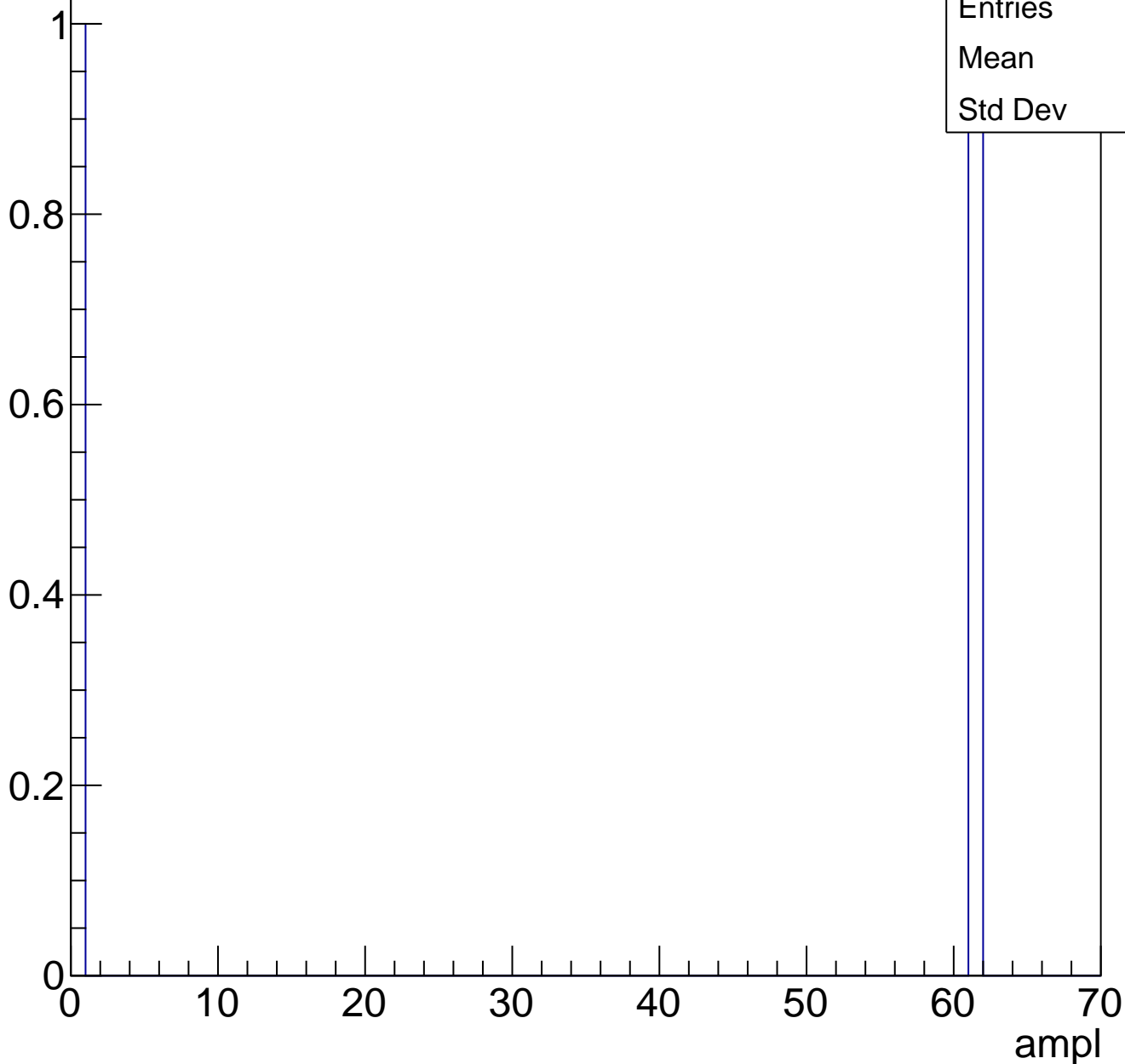
Entry



B1L103S, U24-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch83, adc0

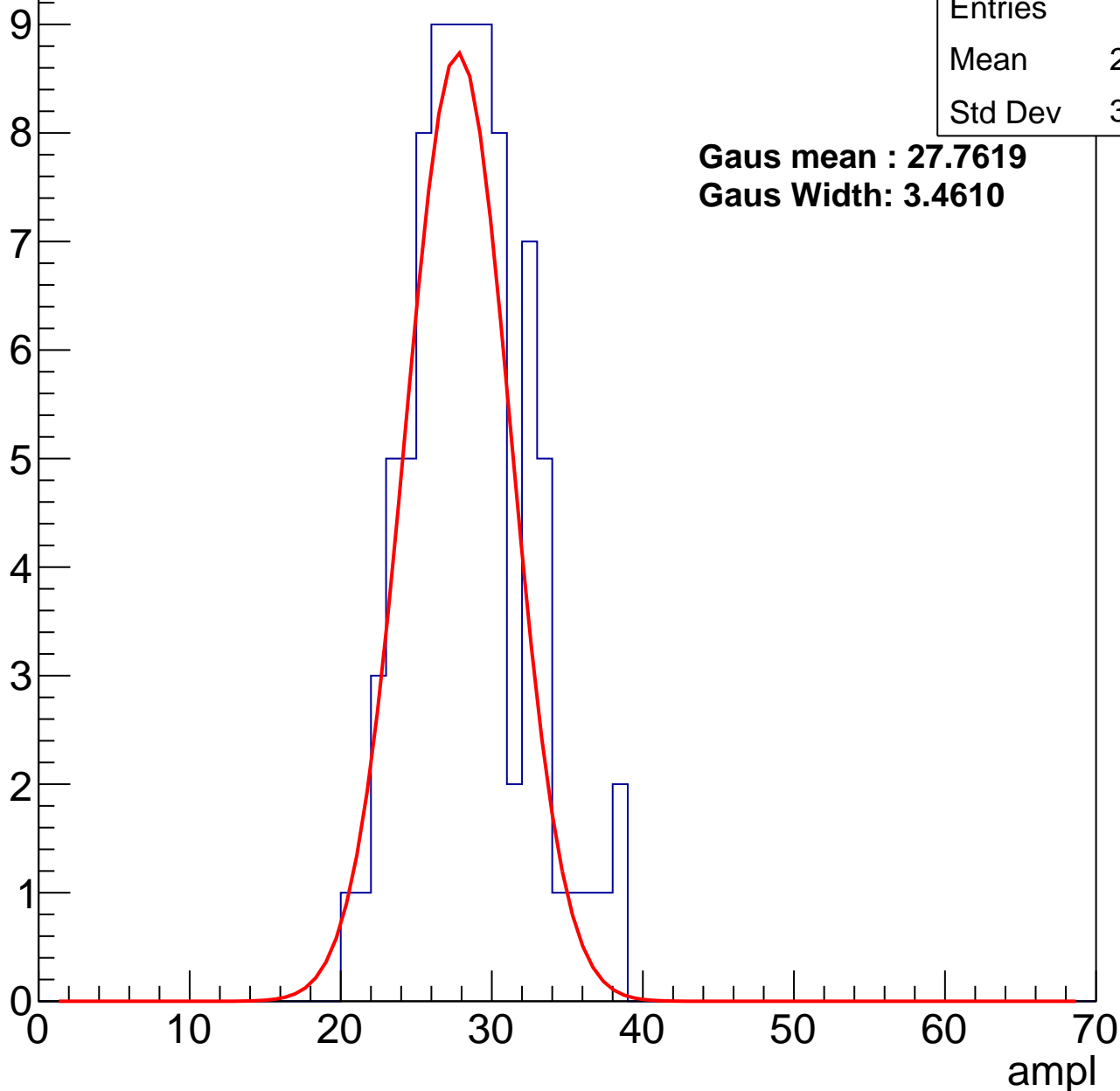
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	28.06
Std Dev	3.819

Gaus mean : 27.7619

Gaus Width: 3.4610



B1L103S, U24-ch83, adc1

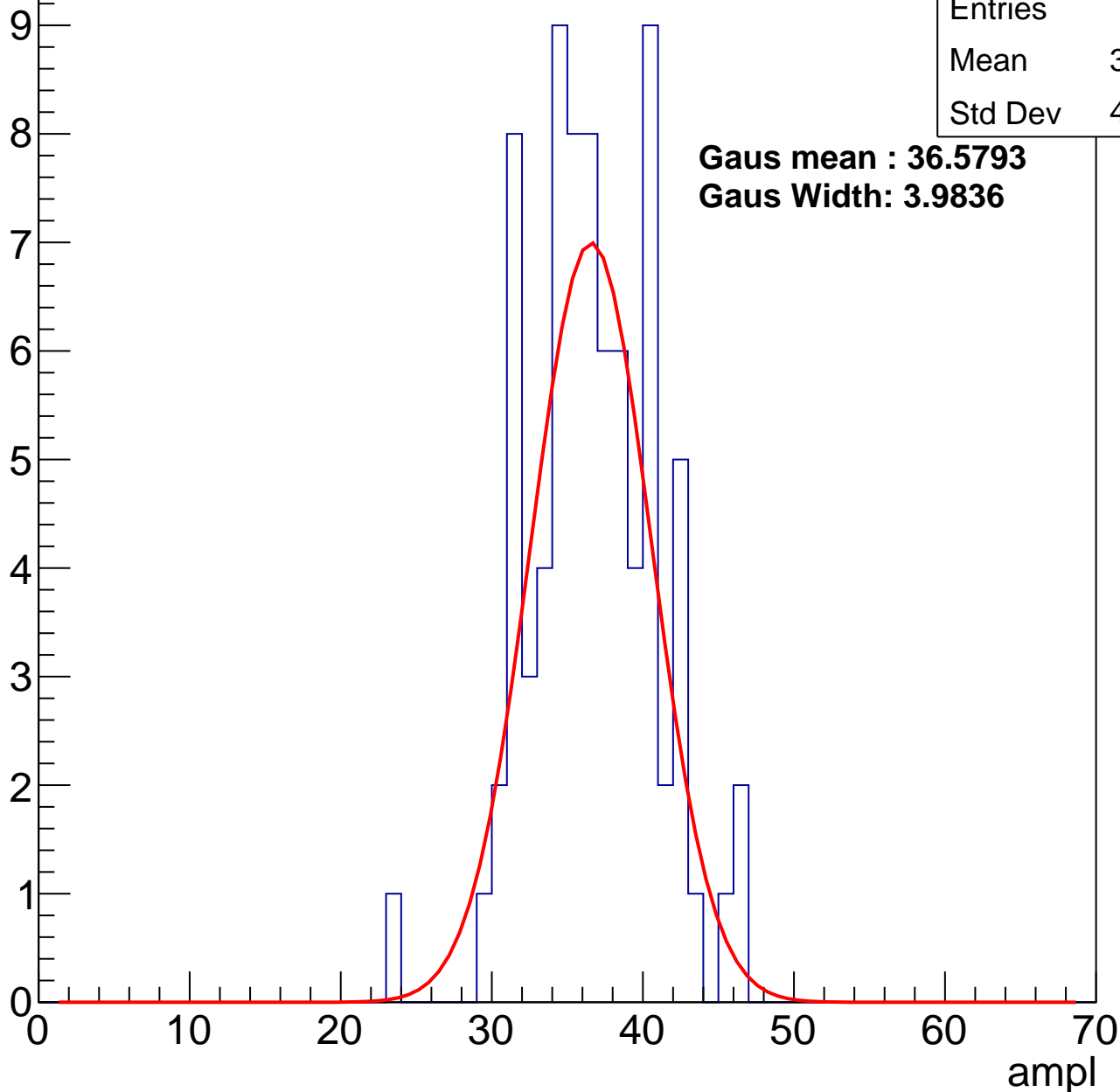
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	36.25
Std Dev	4.158

Gaus mean : 36.5793

Gaus Width: 3.9836

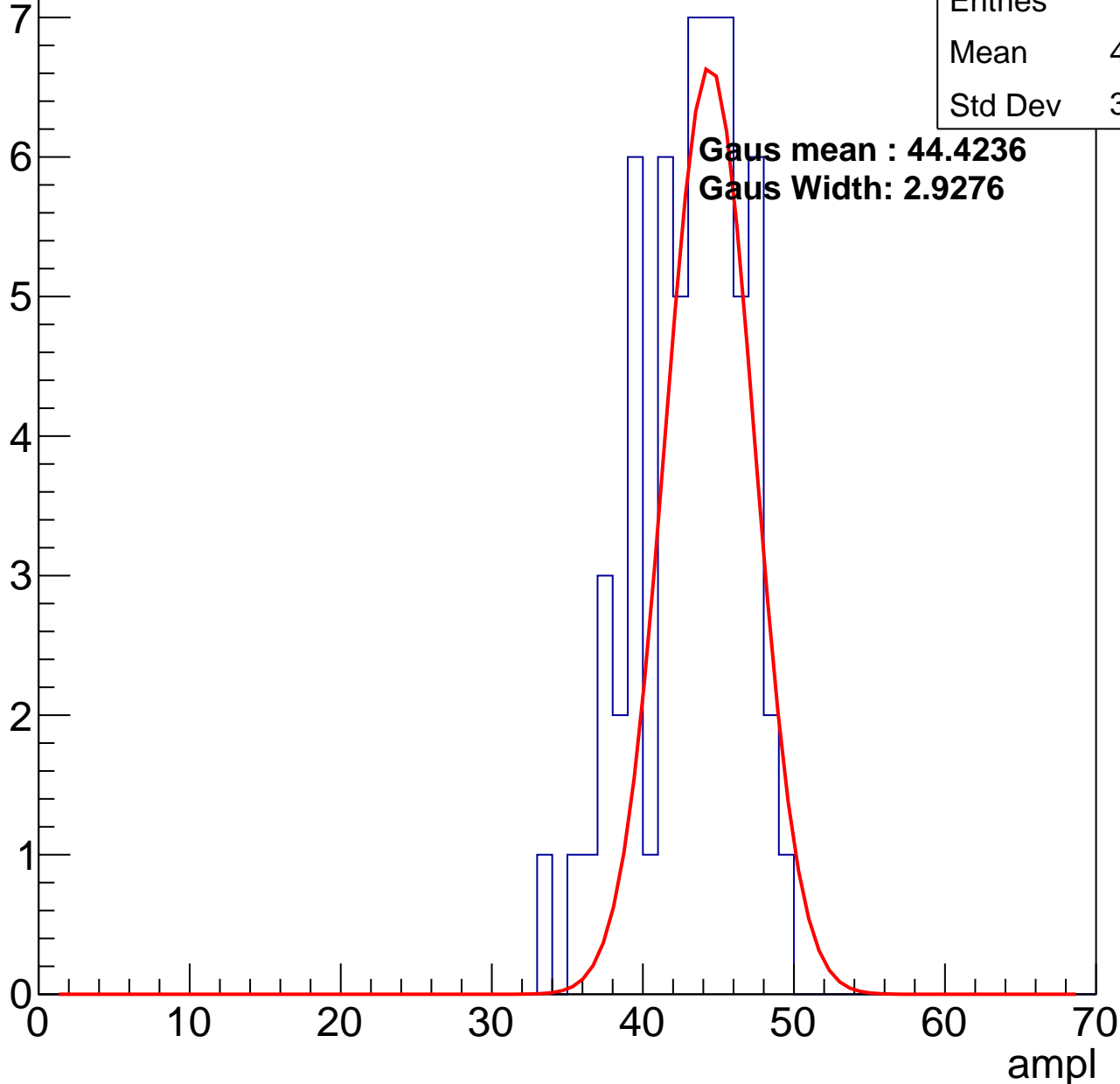


B1L103S, U24-ch83, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.66
Std Dev	3.529

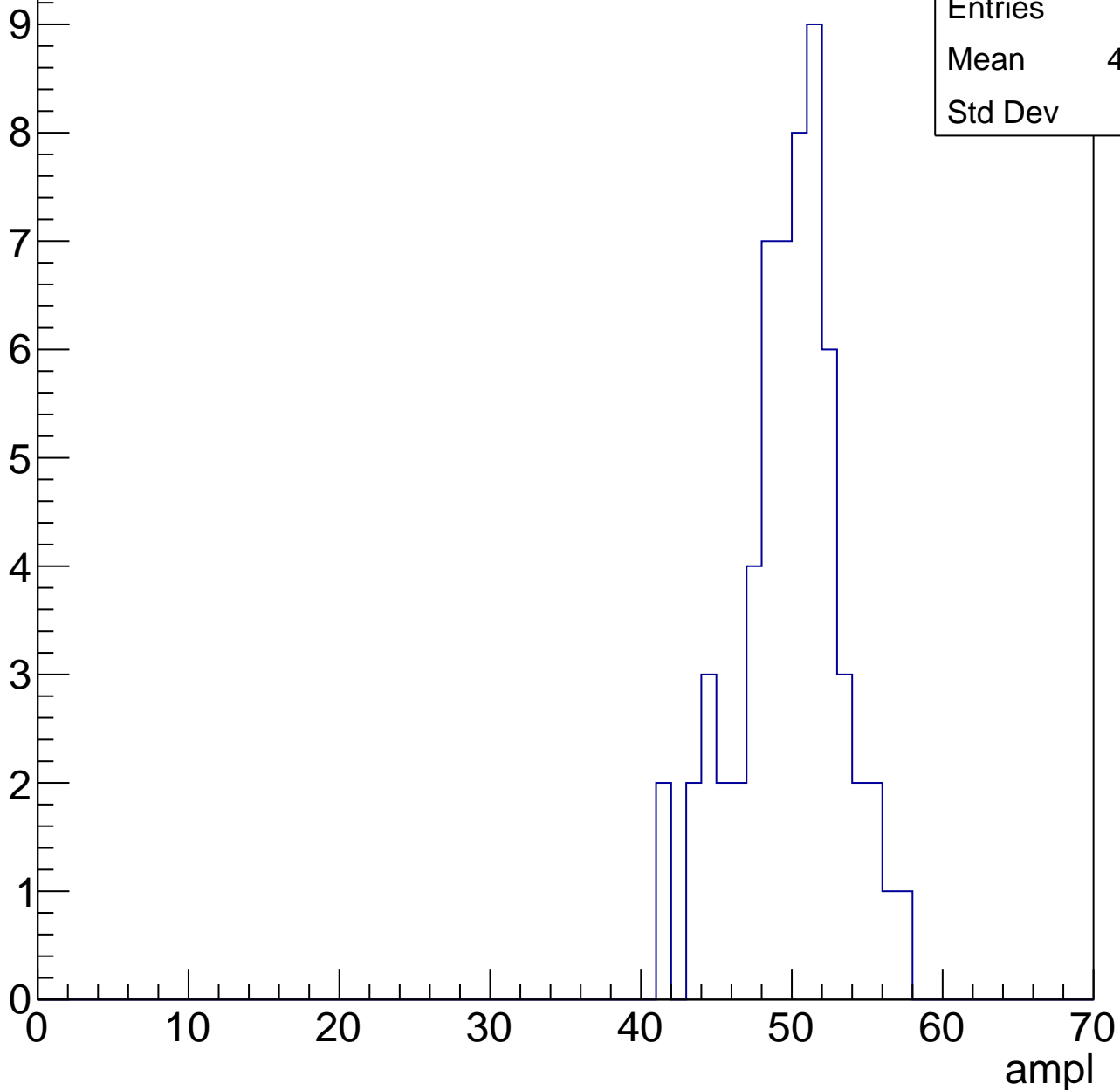


B1L103S, U24-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.34
Std Dev	3.44

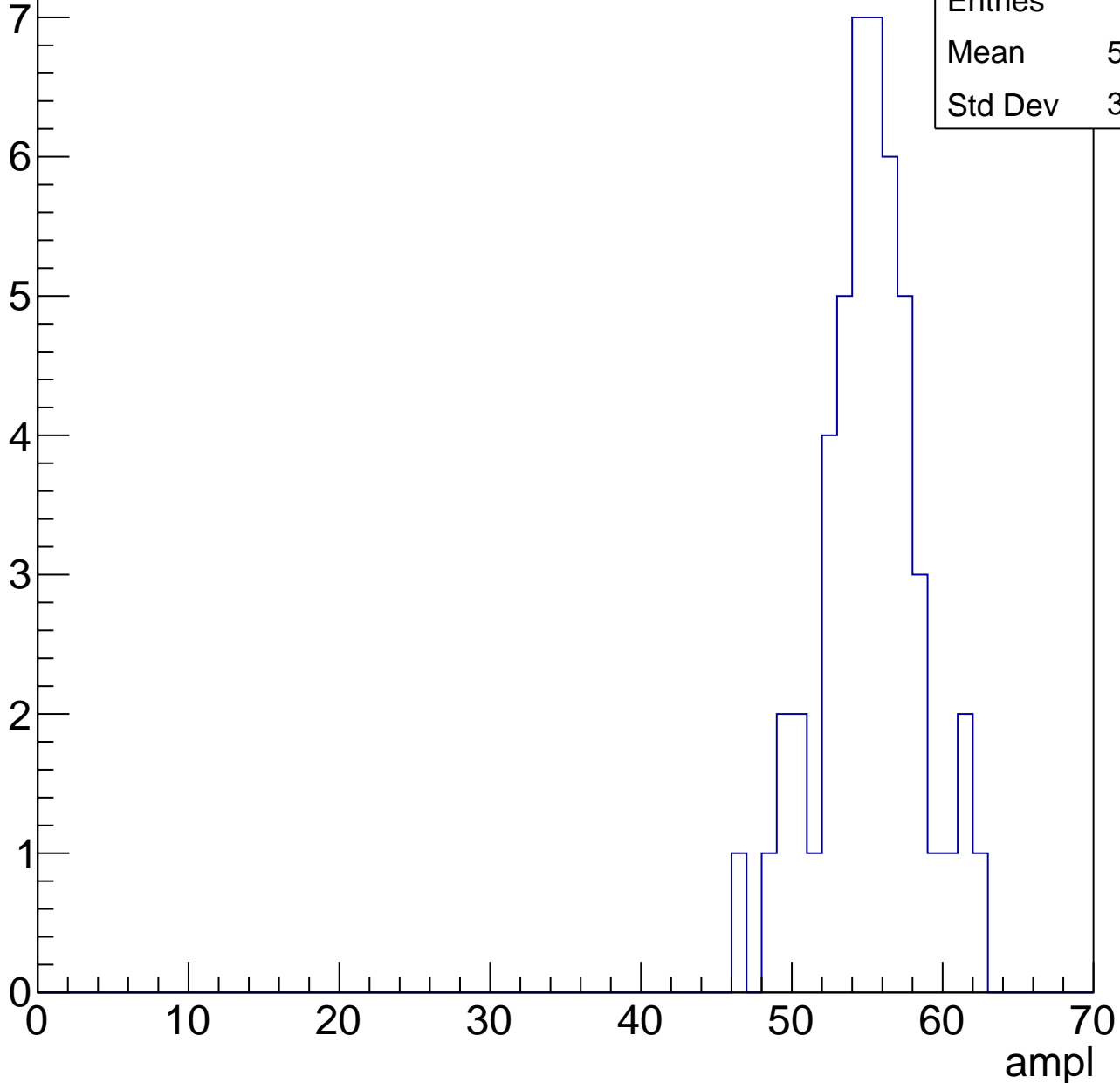


B1L103S, U24-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.63
Std Dev	3.318



B1L103S, U24-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.45
Std Dev	8.291

ampl

0

10

20

30

40

50

60

70

B1L103S, U24-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

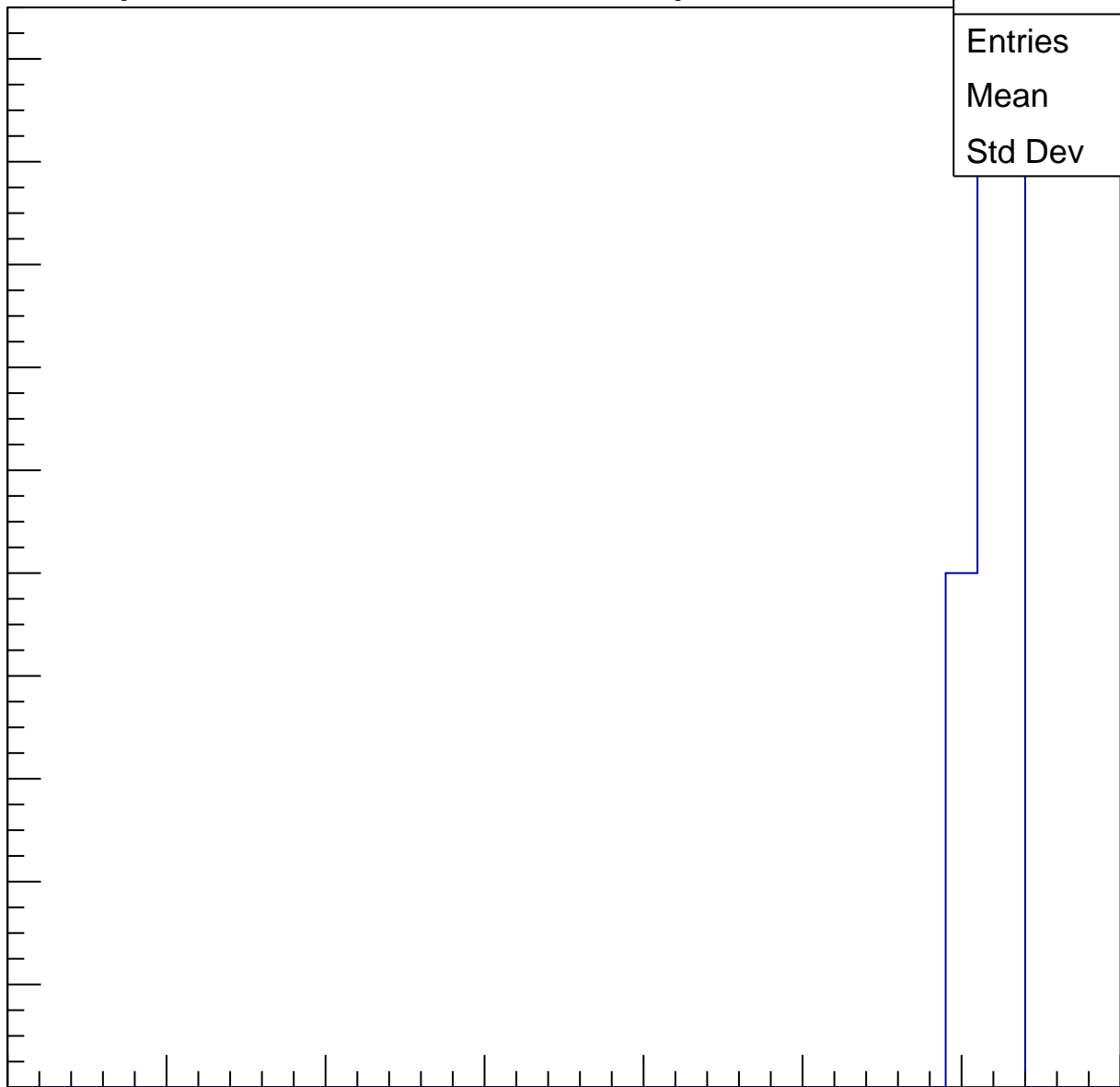
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.38
Std Dev	1.317

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch84, adc0

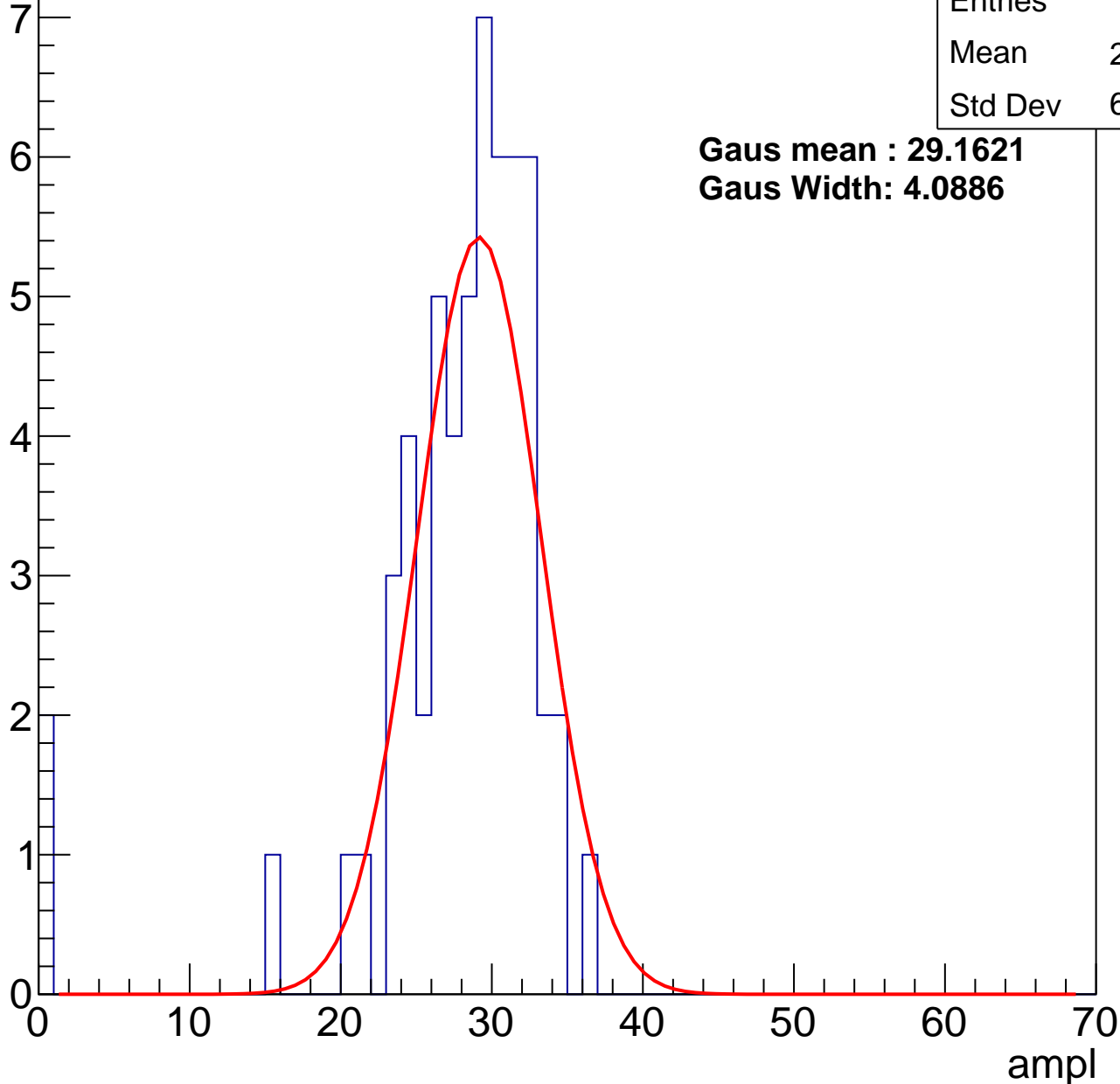
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	27.24
Std Dev	6.385

Gaus mean : 29.1621

Gaus Width: 4.0886



B1L103S, U24-ch84, adc1

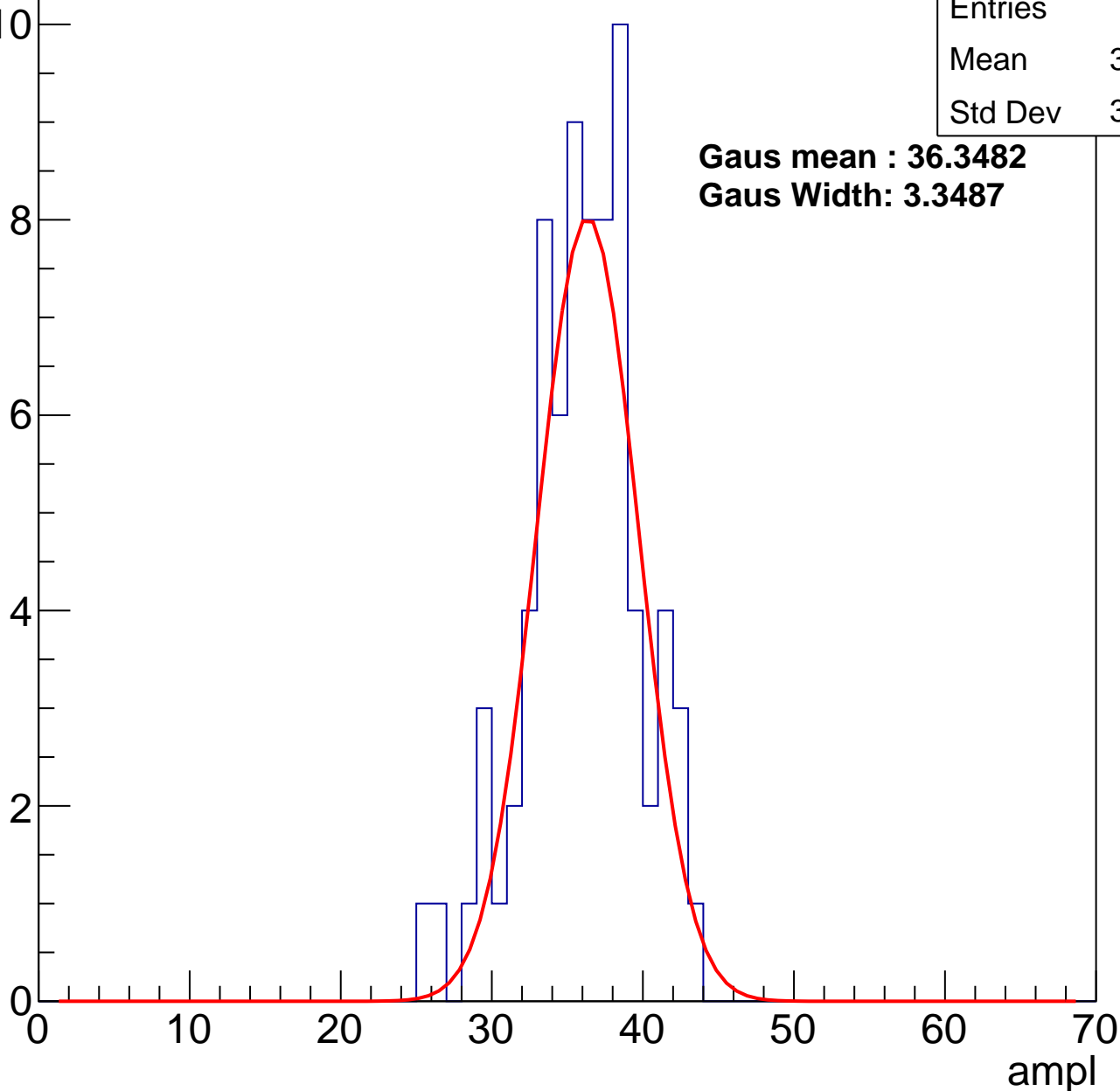
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.55
Std Dev	3.708

Gaus mean : 36.3482

Gaus Width: 3.3487



B1L103S, U24-ch84, adc2

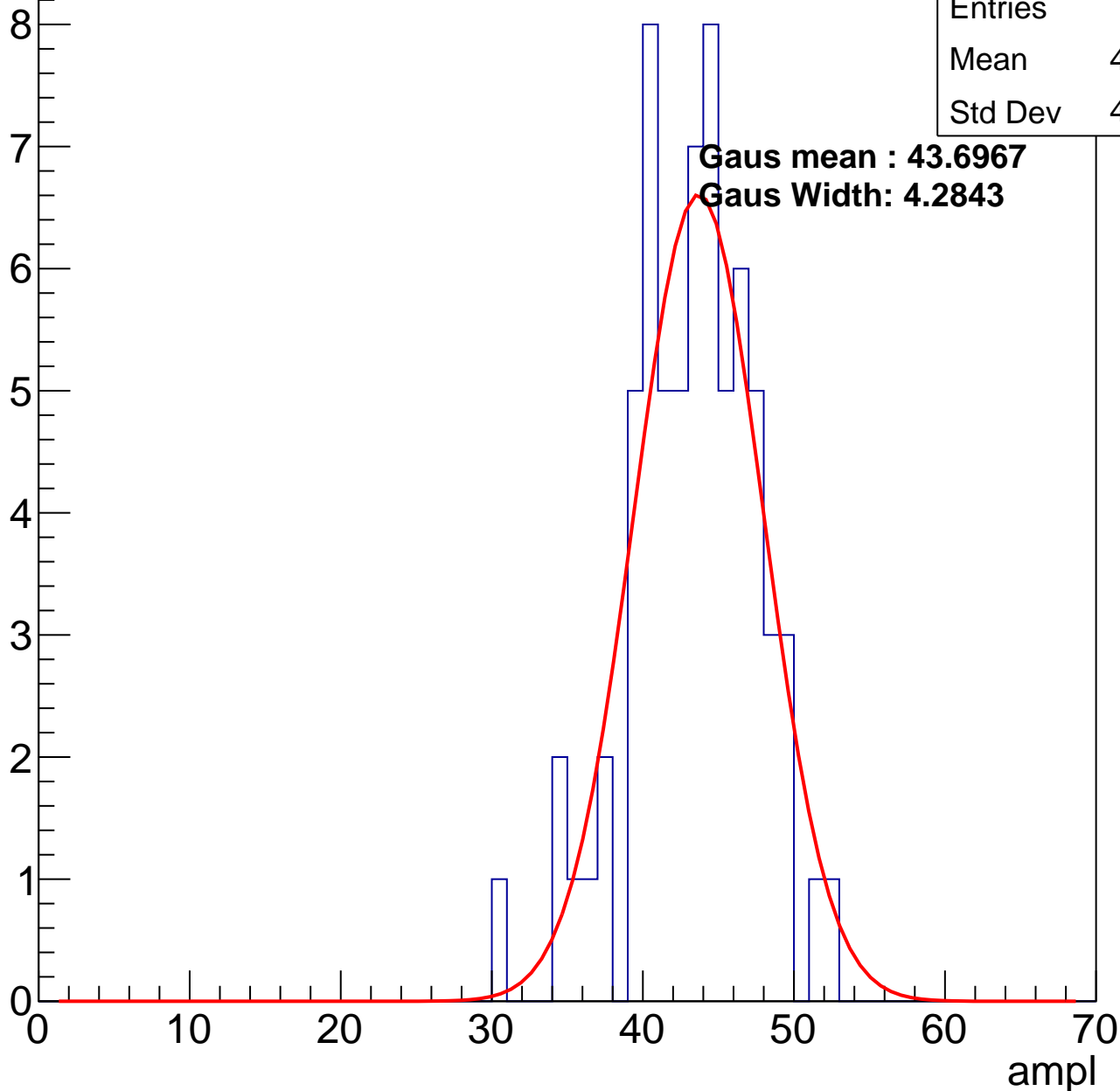
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42.84
Std Dev	4.138

Gaus mean : 43.6967

Gaus Width: 4.2843

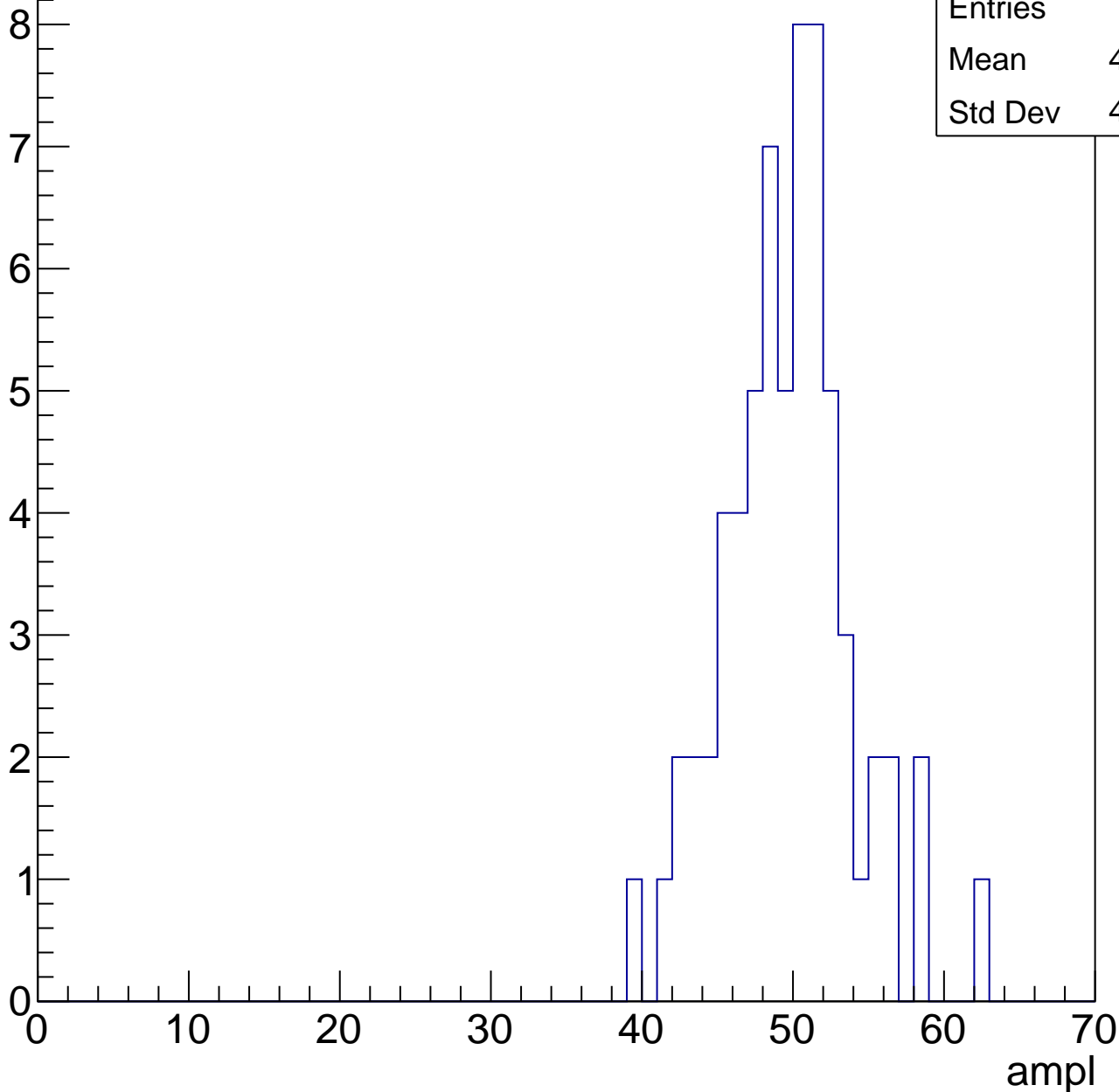


B1L103S, U24-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.22
Std Dev	4.237

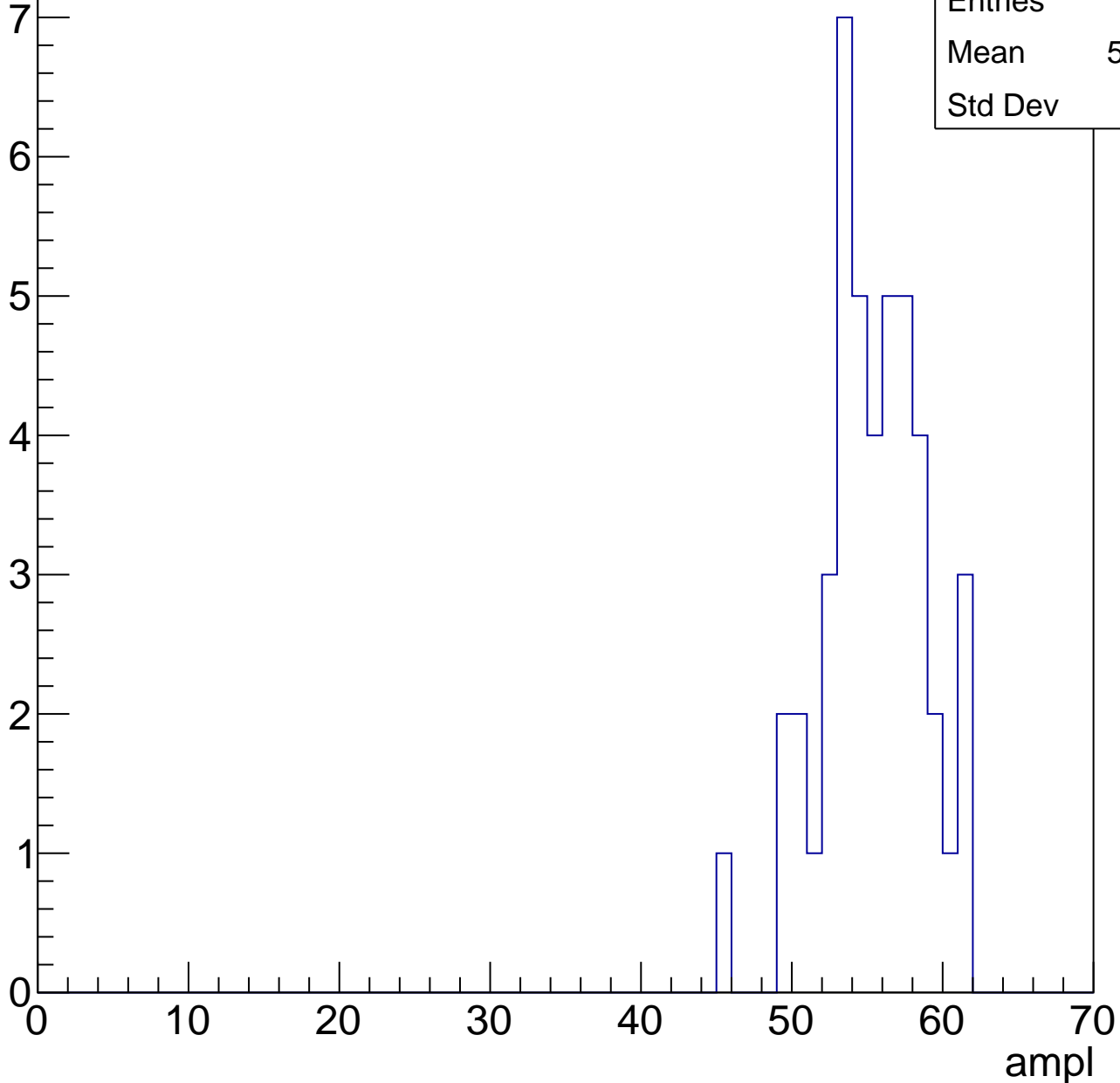


B1L103S, U24-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	54.87
Std Dev	3.41

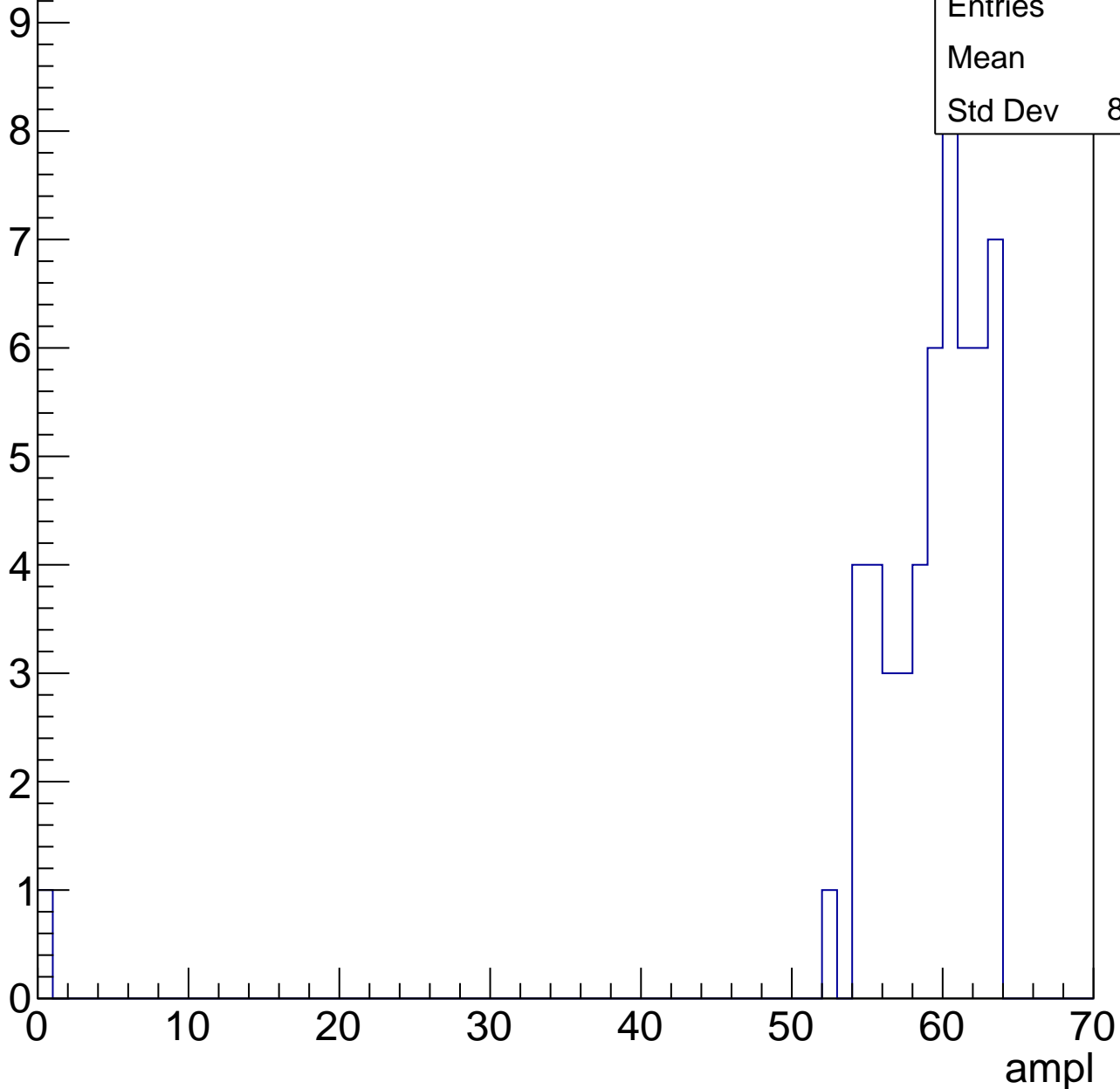


B1L103S, U24-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58
Std Dev	8.479



B1L103S, U24-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries	11
Mean	60.73
Std Dev	2.219

B1L103S, U24-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch85, adc0

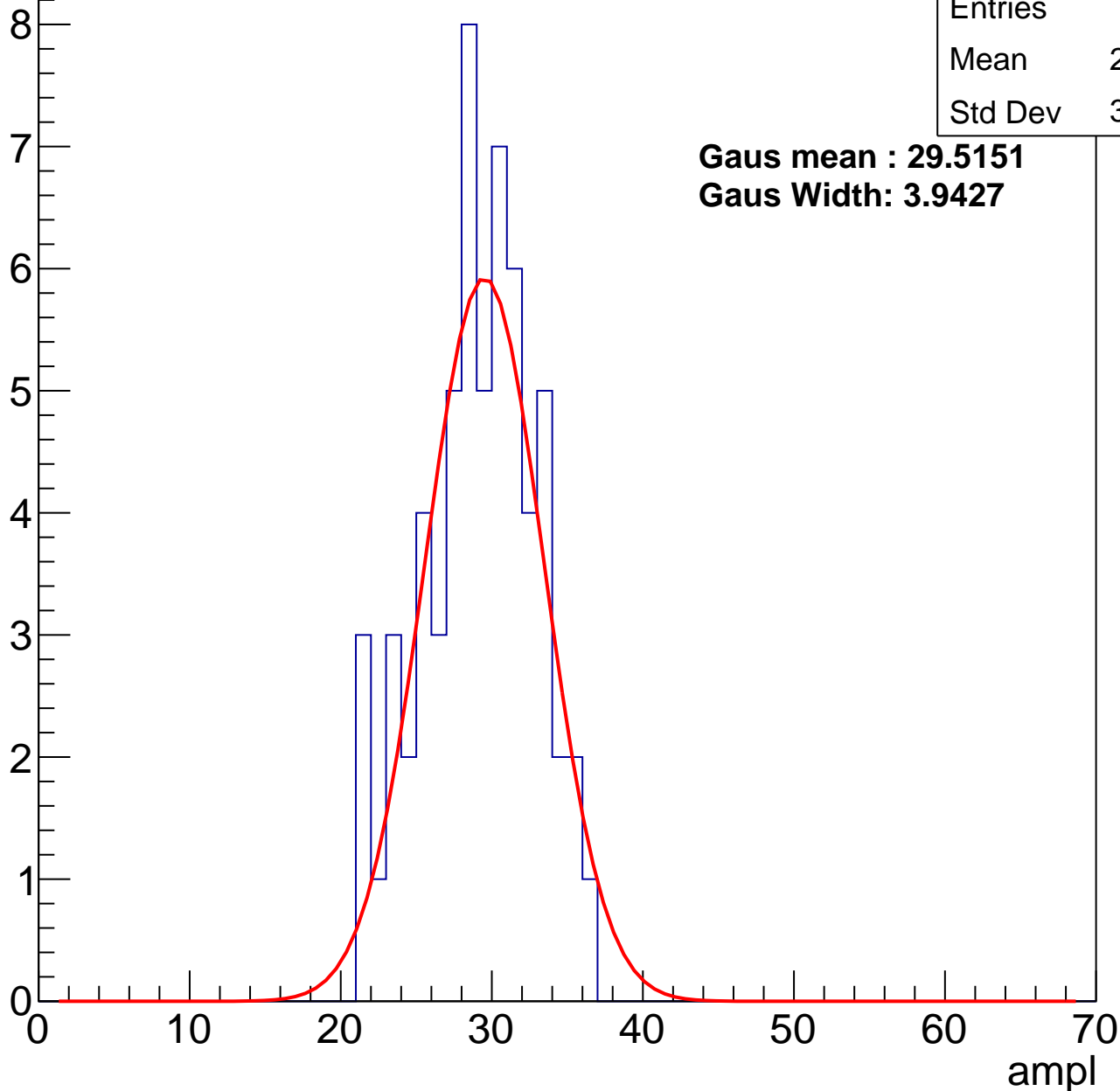
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.64
Std Dev	3.684

Gaus mean : 29.5151

Gaus Width: 3.9427



B1L103S, U24-ch85, adc1

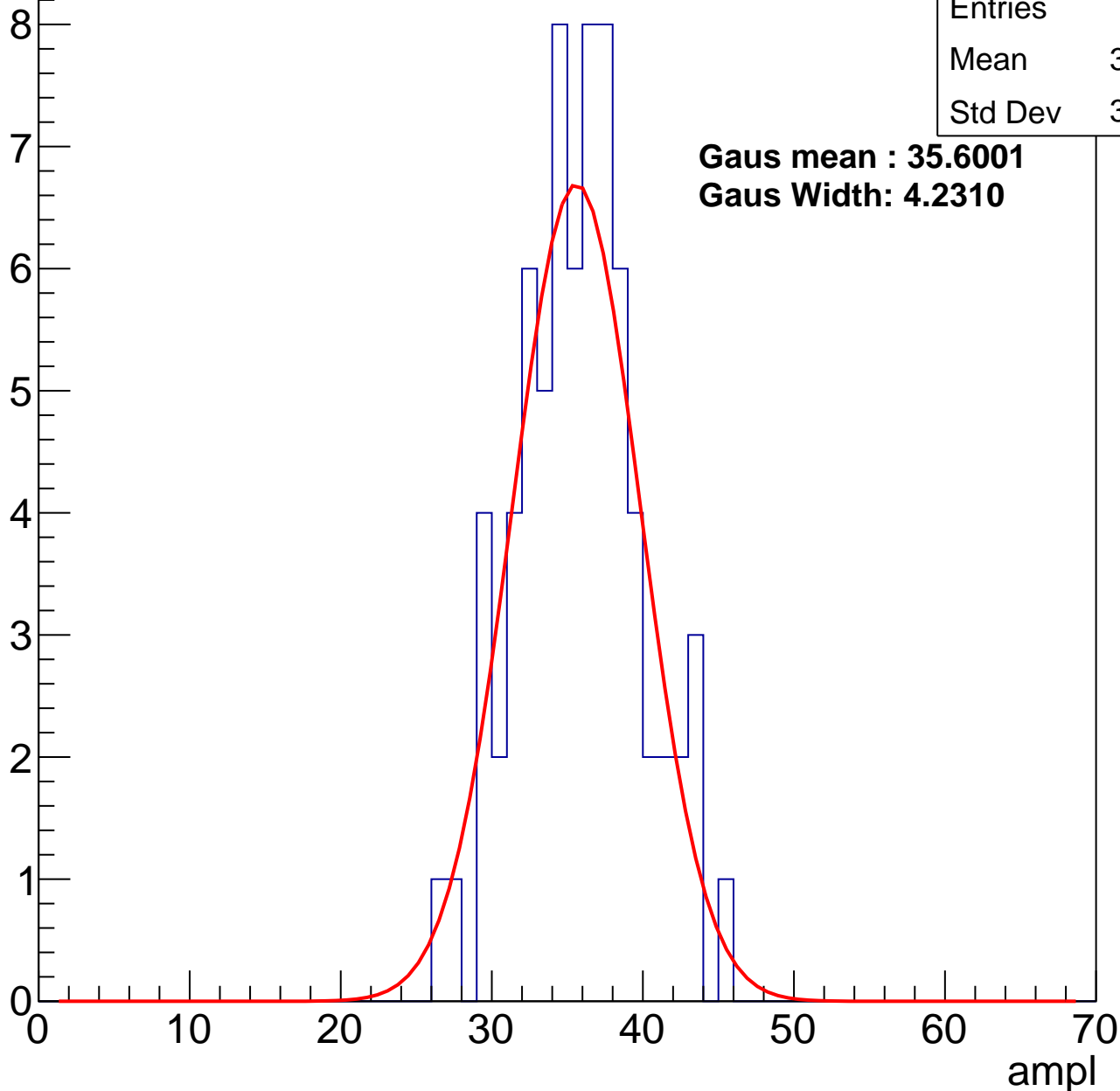
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.34
Std Dev	3.963

Gaus mean : 35.6001

Gaus Width: 4.2310



B1L103S, U24-ch85, adc2

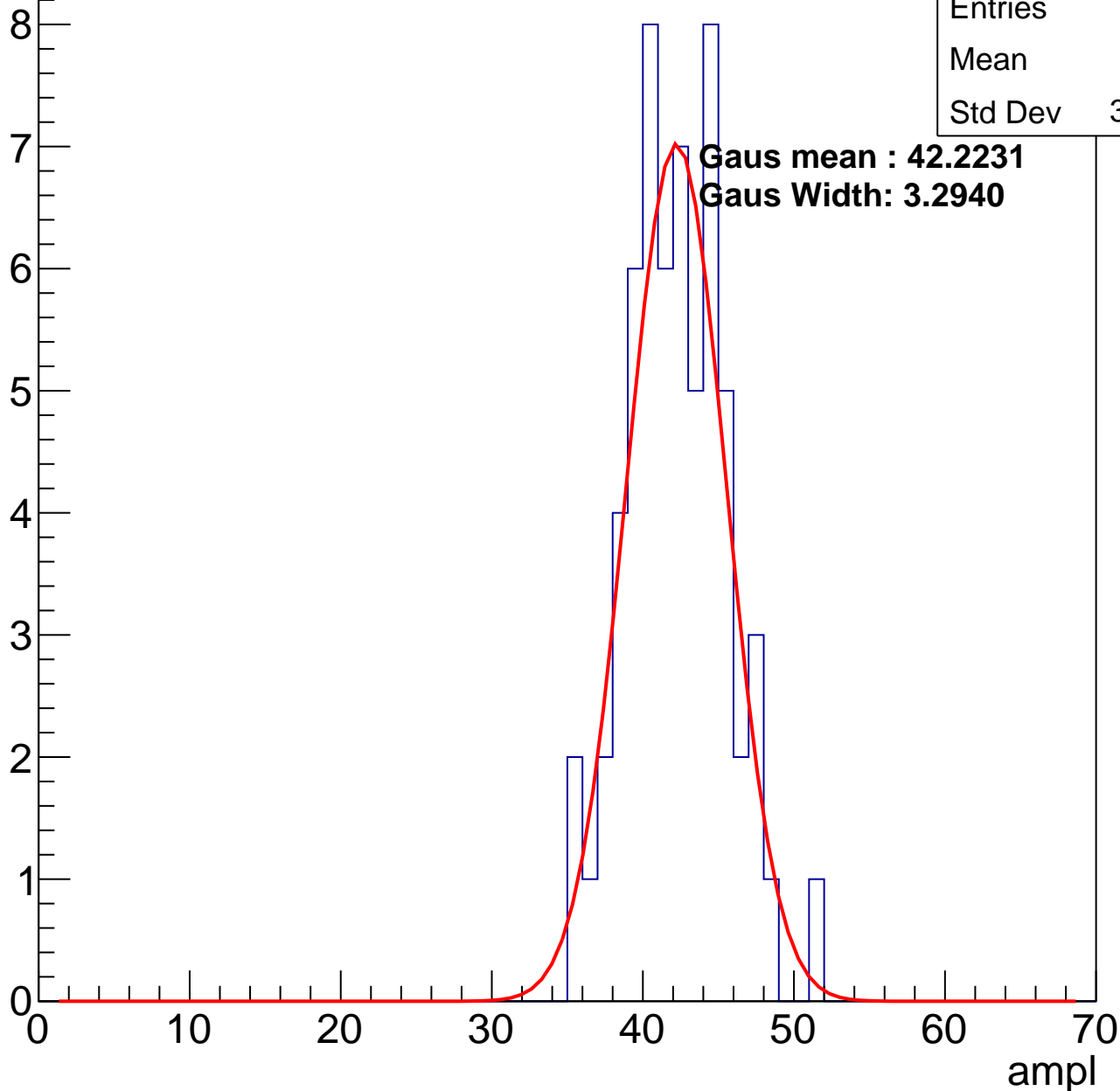
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.8
Std Dev	3.248

Gaus mean : 42.2231

Gaus Width: 3.2940



B1L103S, U24-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	49.07
Std Dev	3.573

Entry

10

8

6

4

2

0

0

10

20

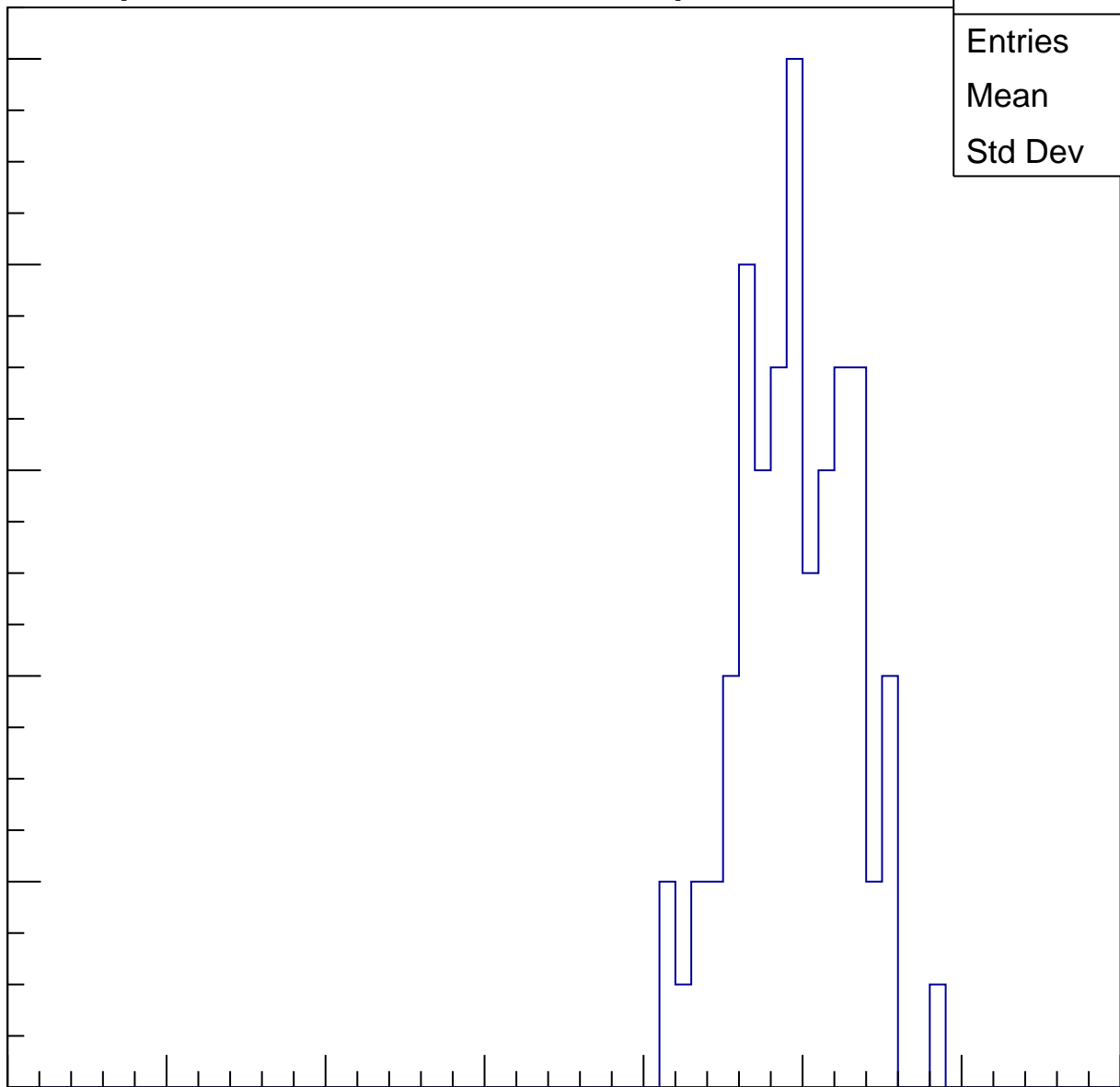
30

40

50

60

ampl

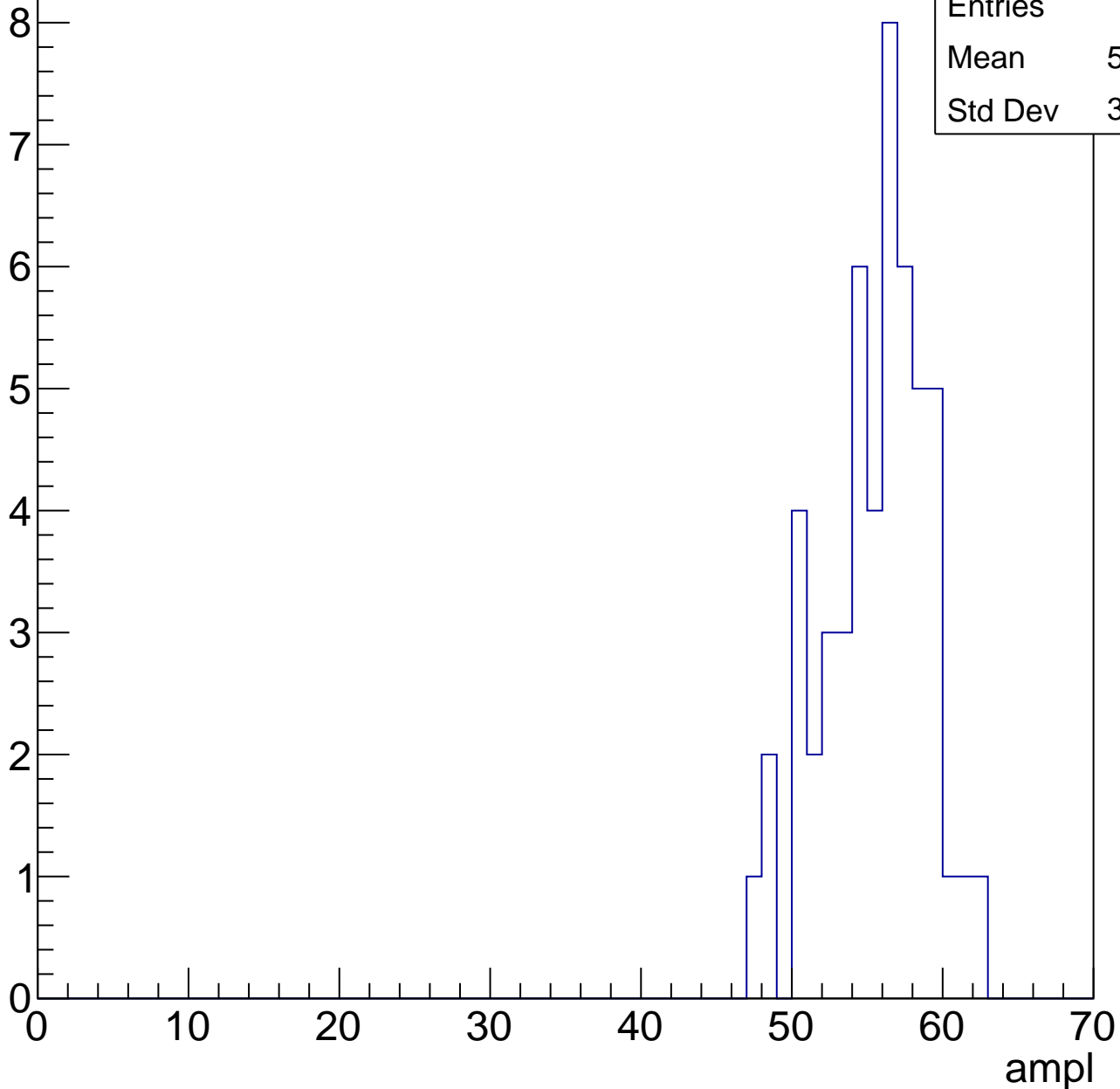


B1L103S, U24-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.04
Std Dev	3.414

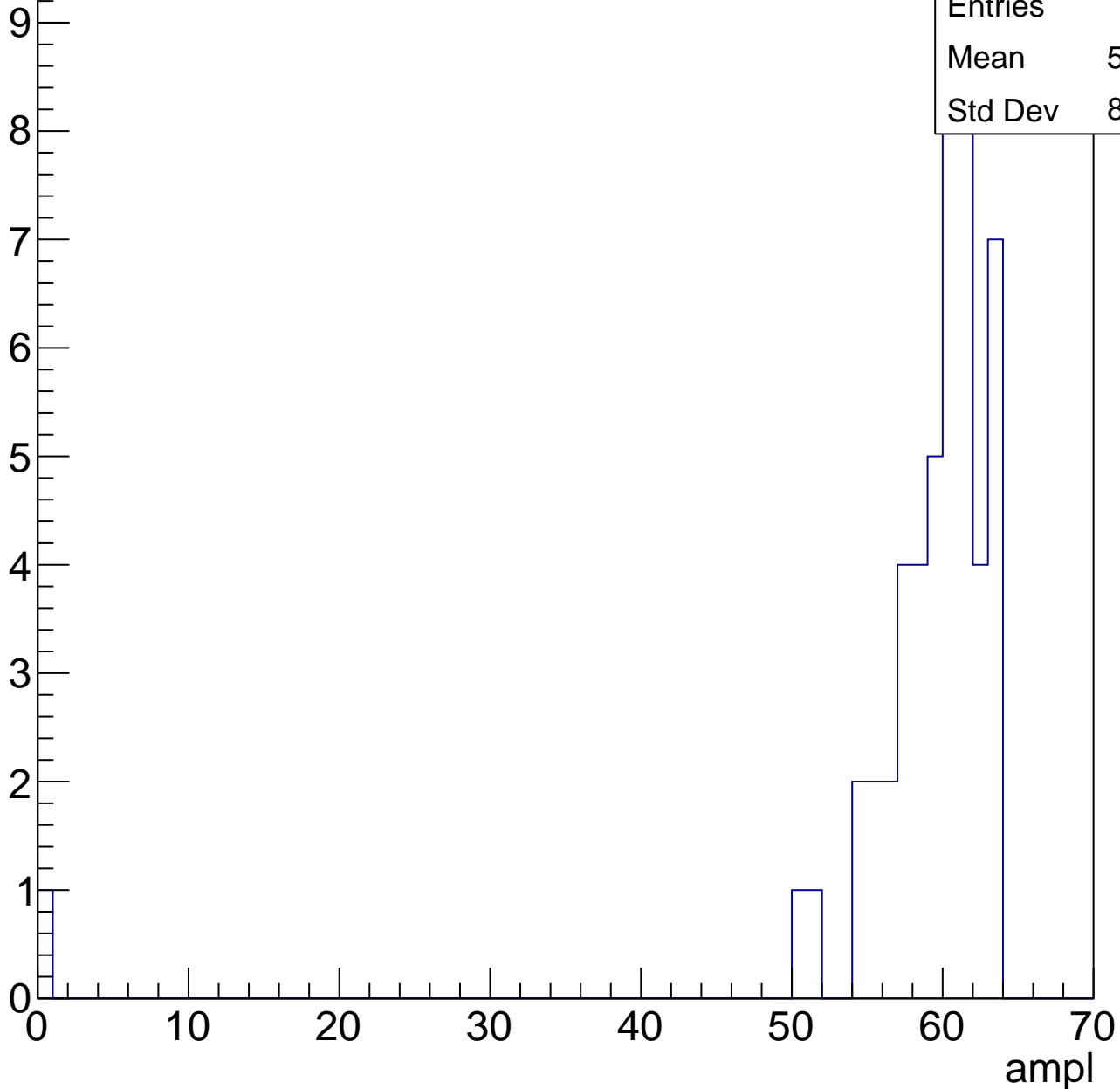


B1L103S, U24-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.12
Std Dev	8.744

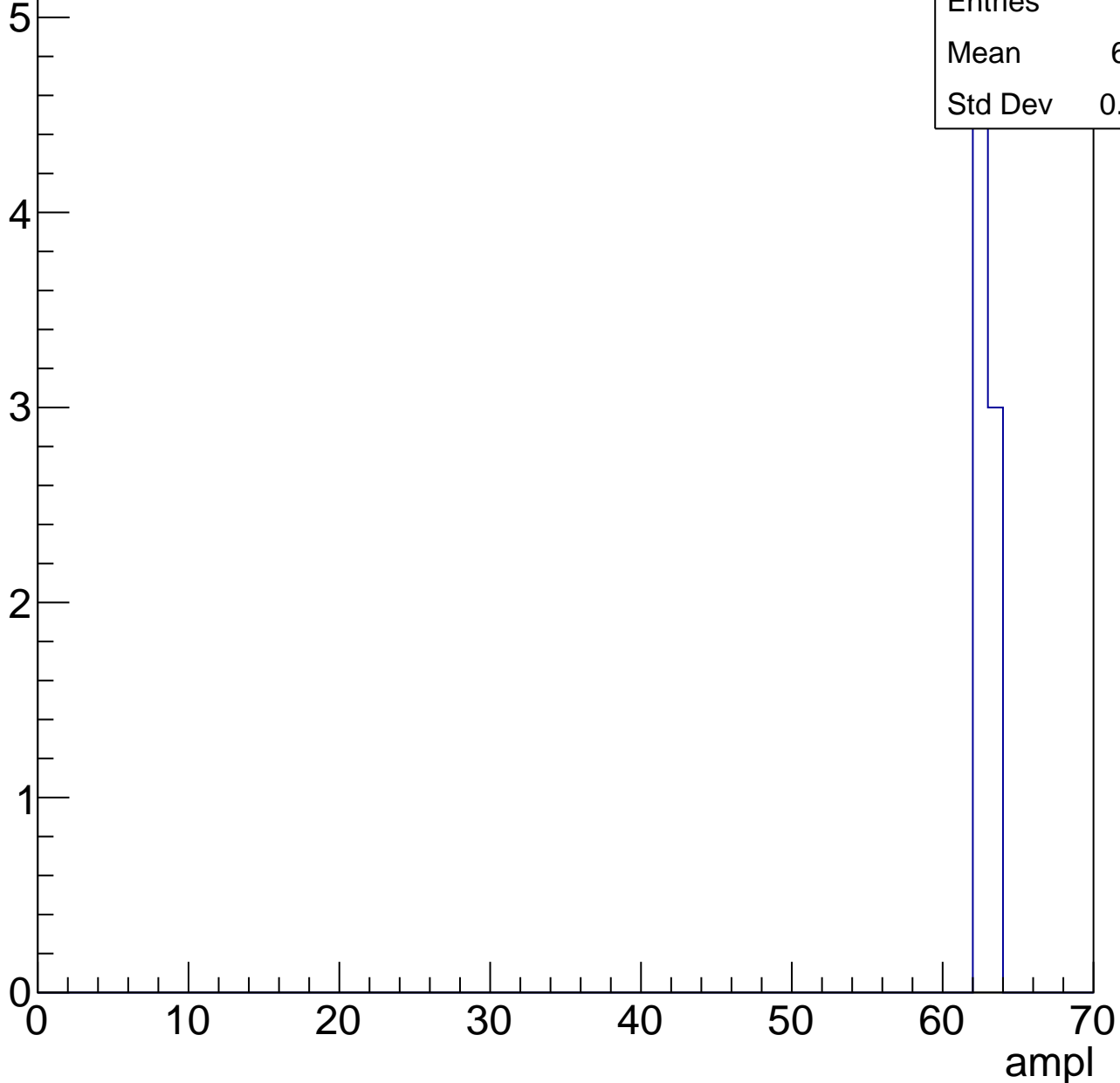


B1L103S, U24-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.38
Std Dev	0.4841



B1L103S, U24-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch86, adc0

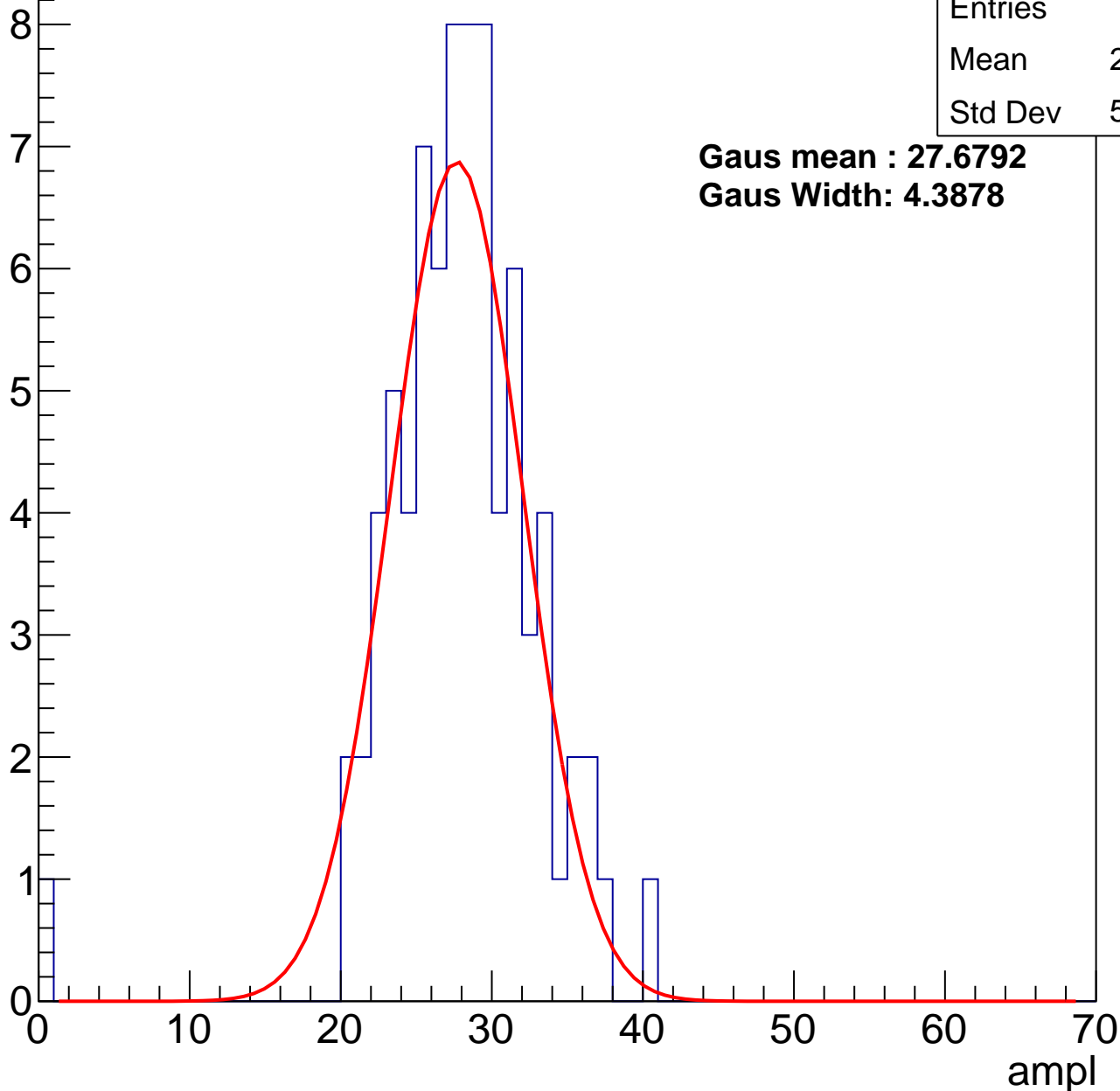
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	27.48
Std Dev	5.192

Gaus mean : 27.6792

Gaus Width: 4.3878



B1L103S, U24-ch86, adc1

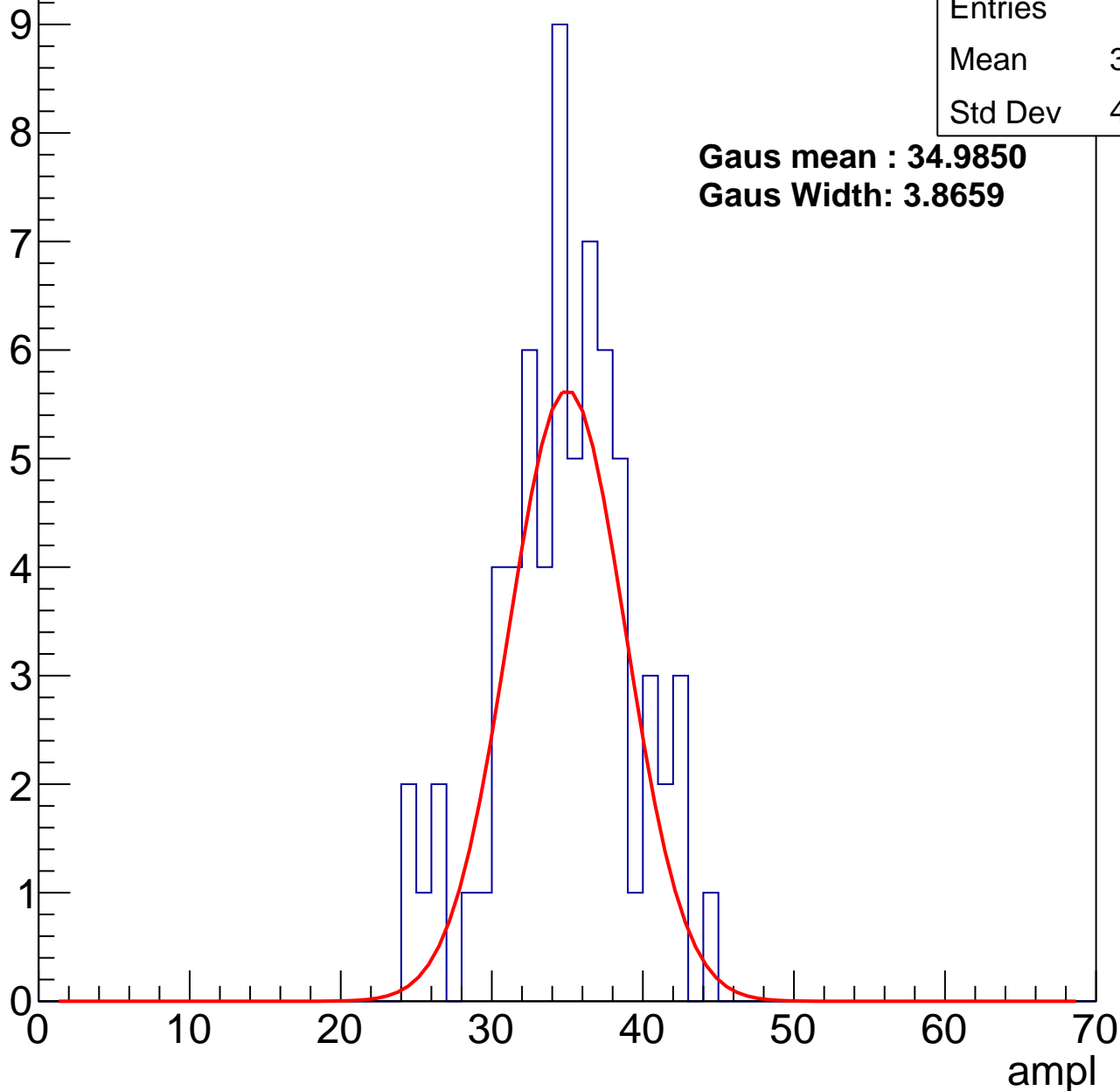
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	34.42
Std Dev	4.365

Gaus mean : 34.9850

Gaus Width: 3.8659



B1L103S, U24-ch86, adc2

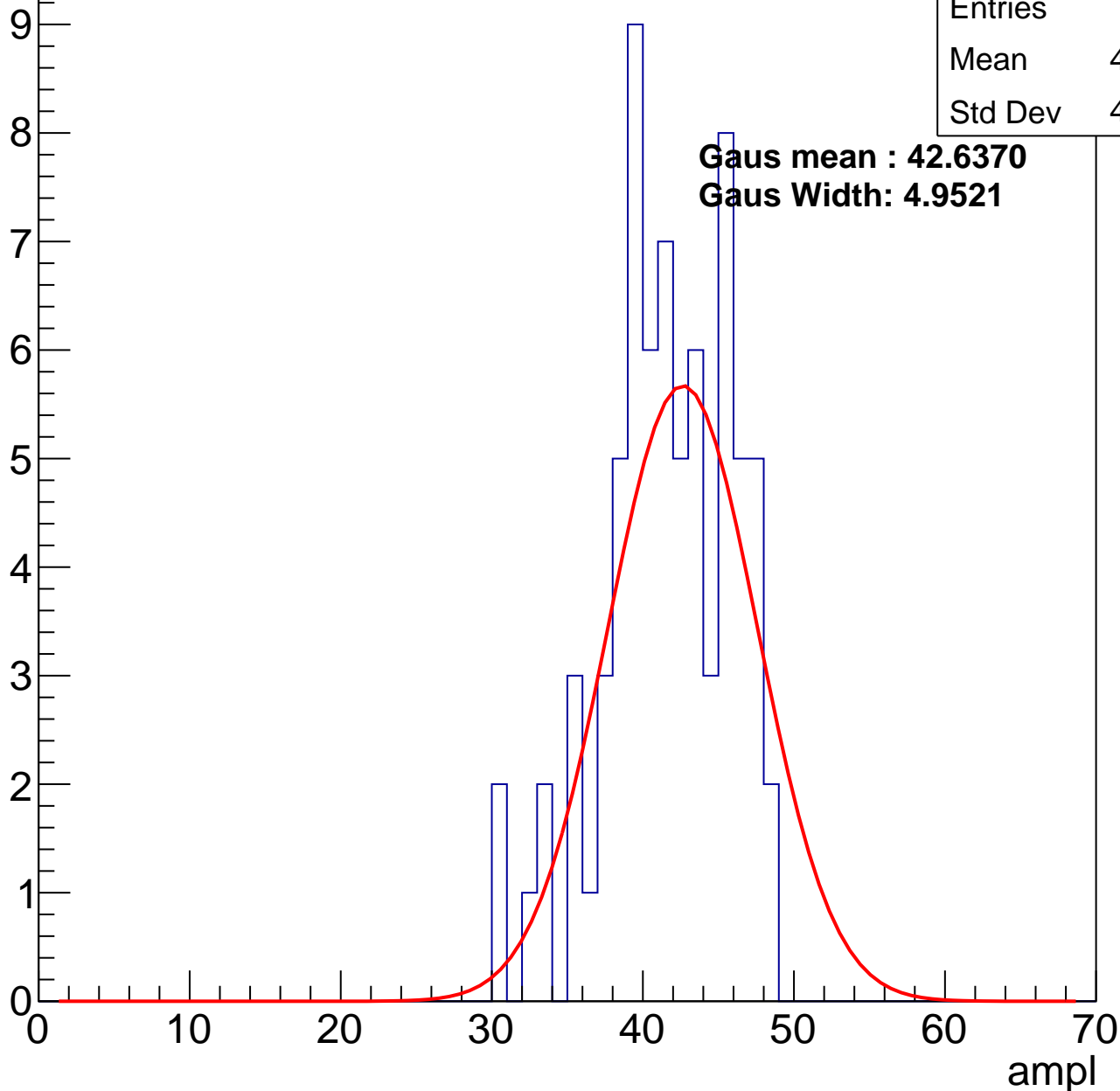
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	41.08
Std Dev	4.229

Gaus mean : 42.6370

Gaus Width: 4.9521

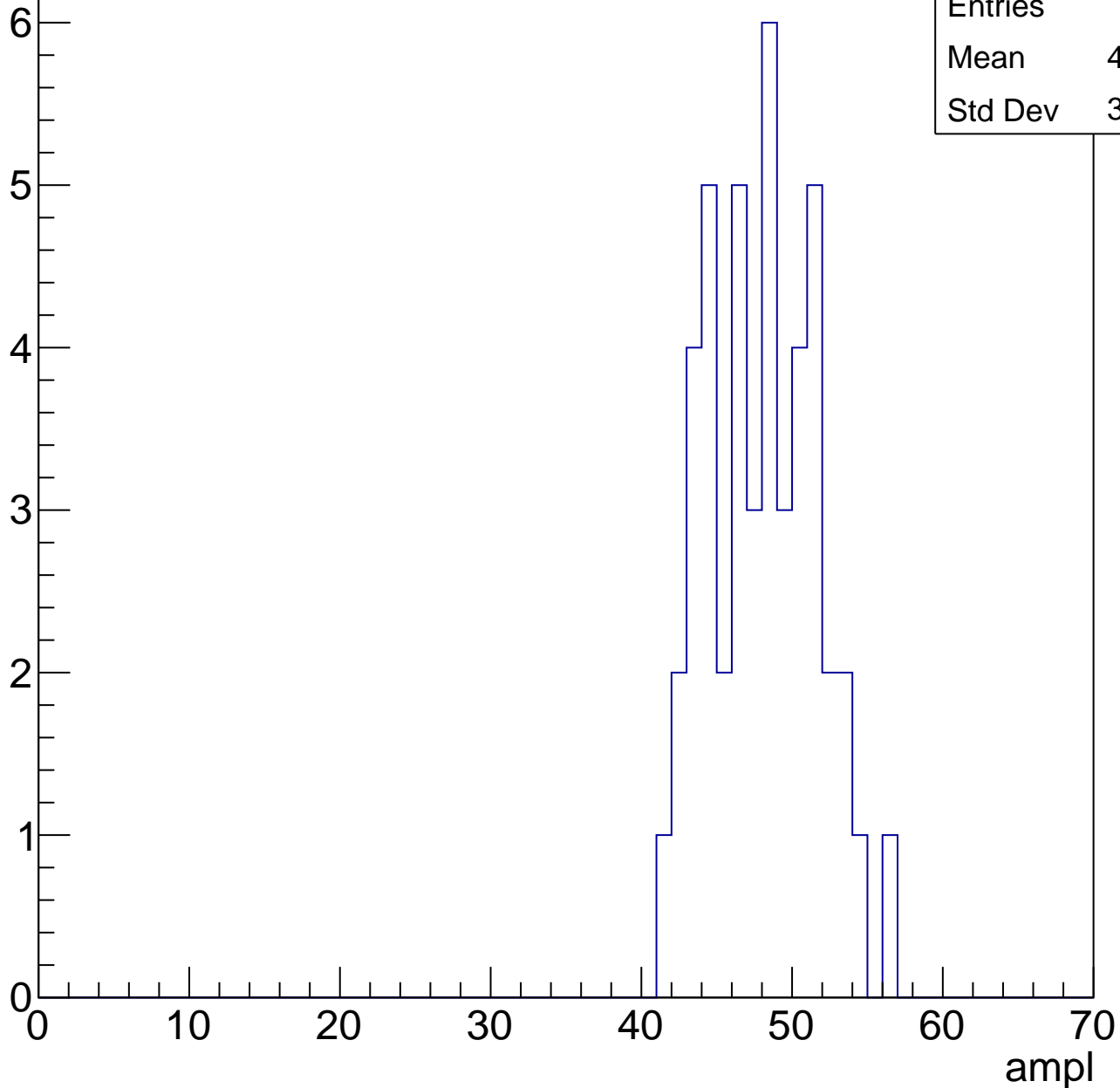


B1L103S, U24-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

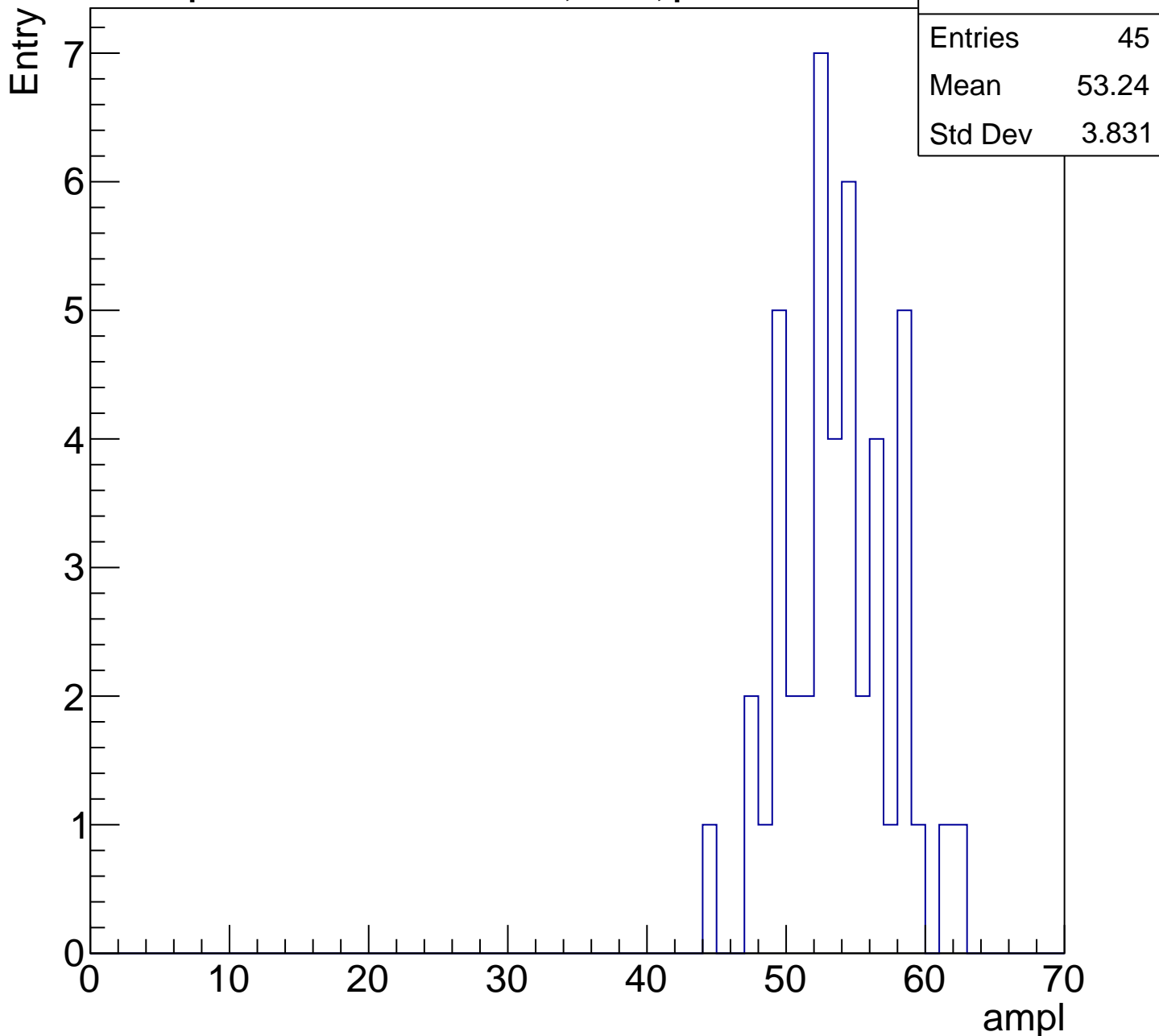
Entry

Entries	46
Mean	47.57
Std Dev	3.549



B1L103S, U24-ch86, adc4

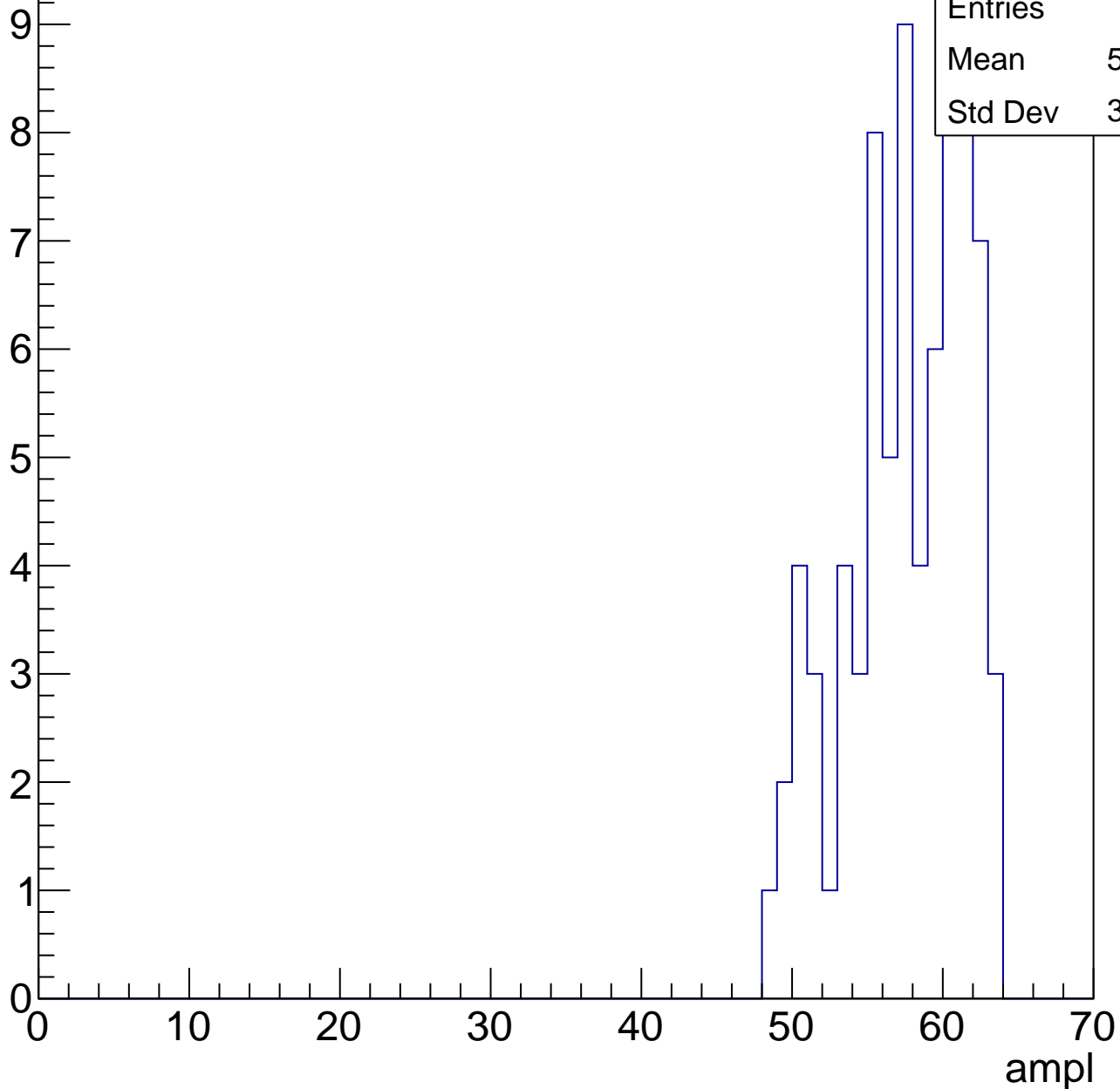
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



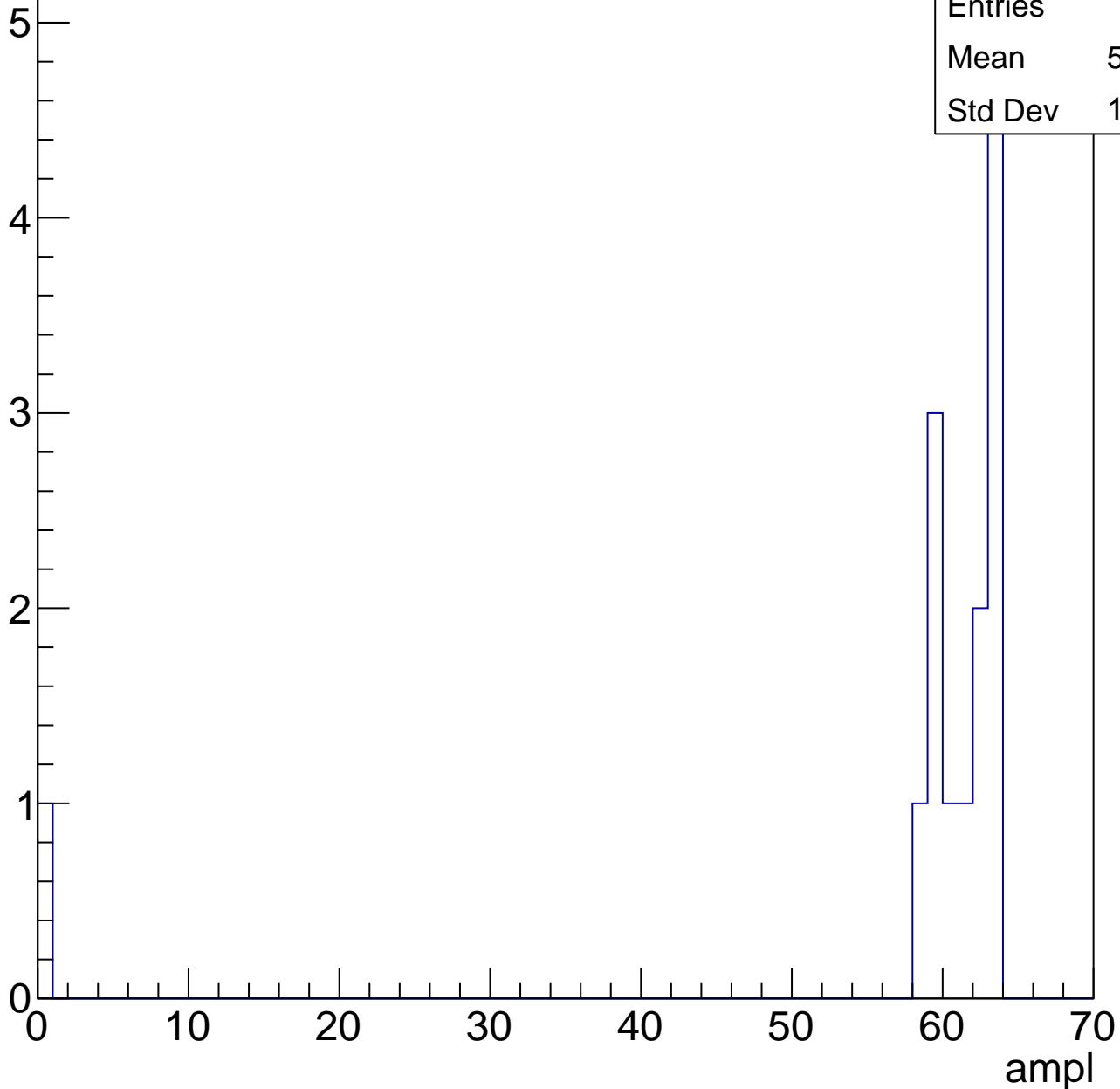
Entries	77
Mean	57.08
Std Dev	3.924

B1L103S, U24-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	56.79
Std Dev	15.85



B1L103S, U24-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch87, adc0

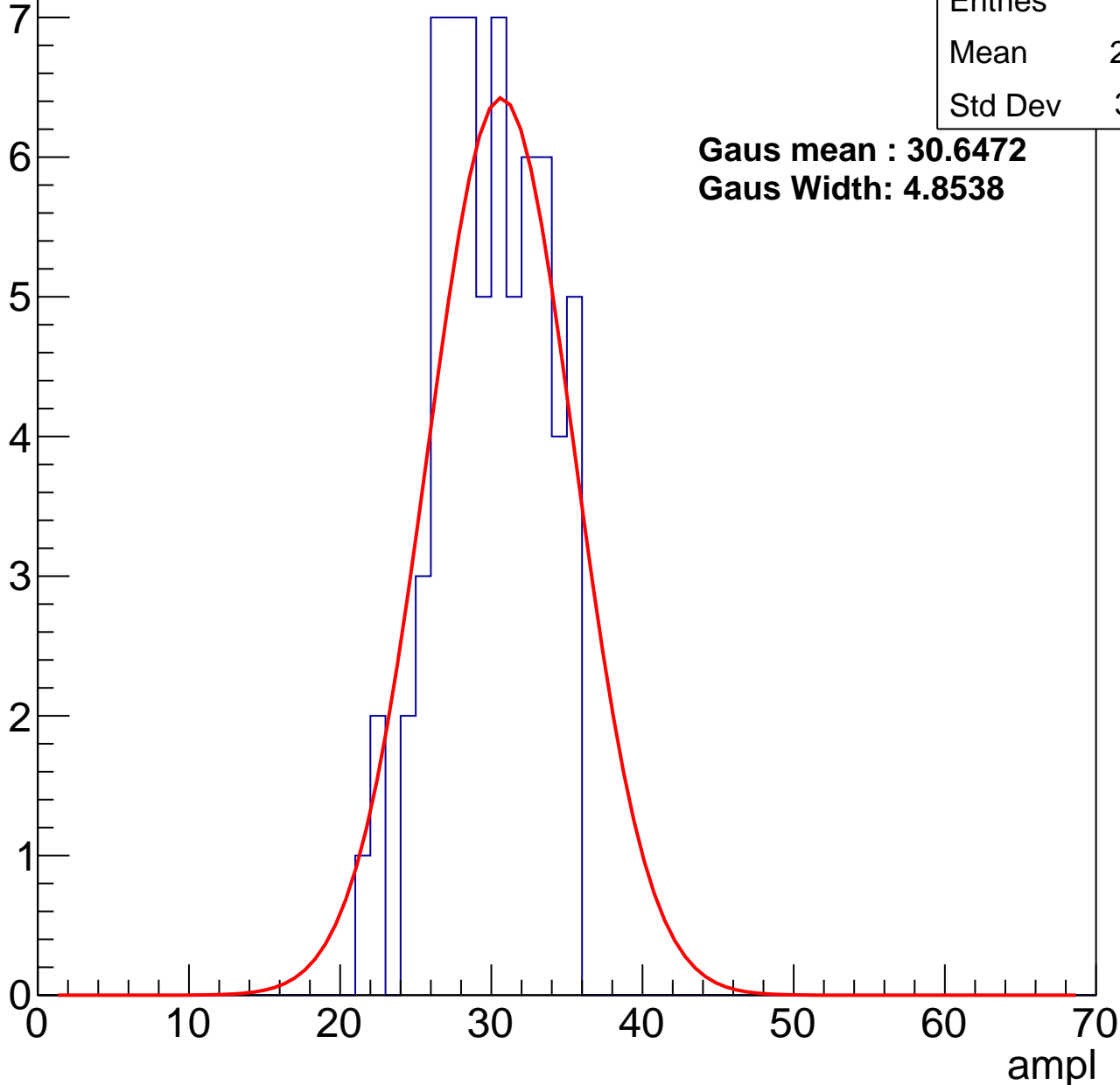
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.34
Std Dev	3.471

Gaus mean : 30.6472

Gaus Width: 4.8538



B1L103S, U24-ch87, adc1

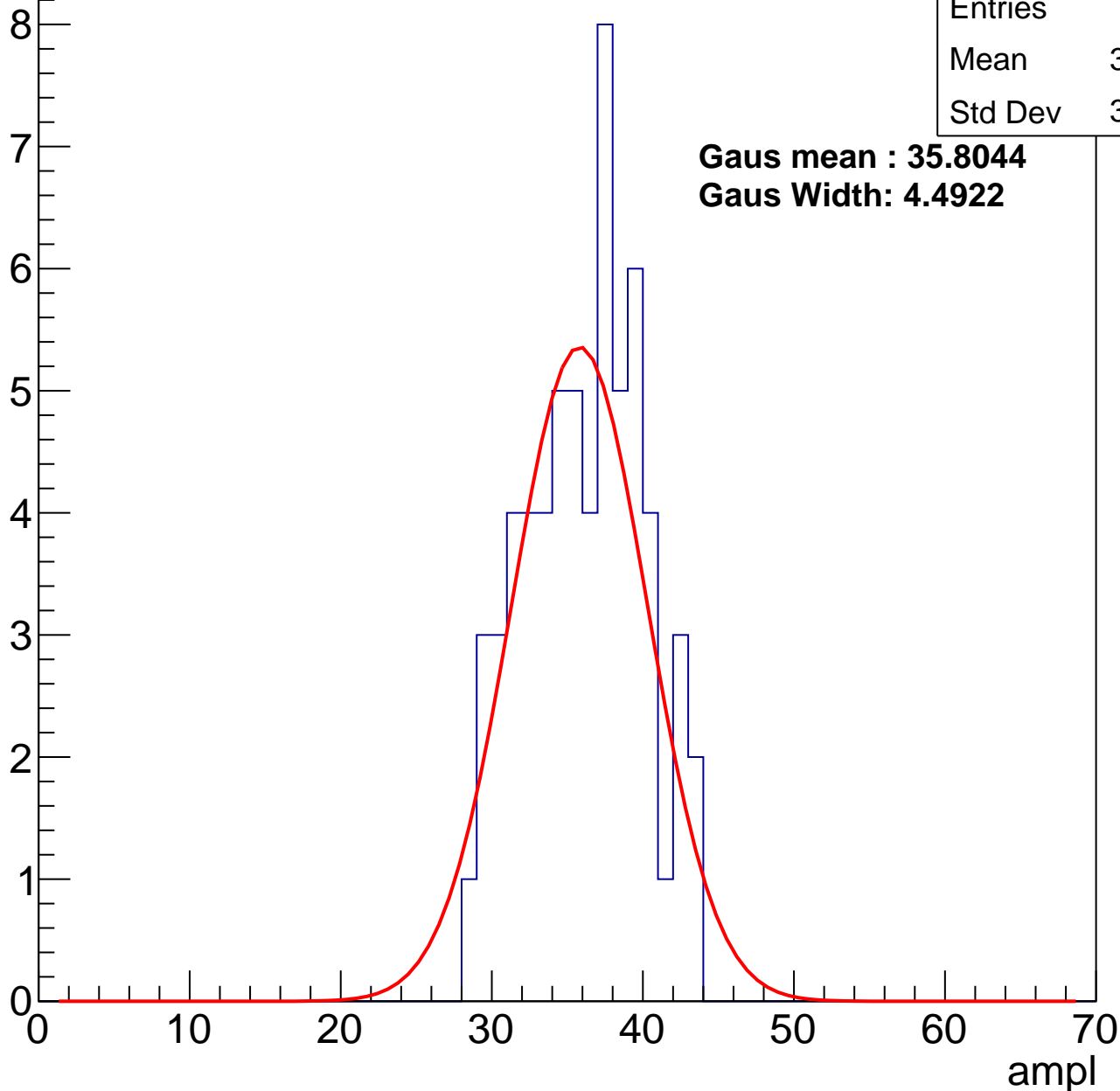
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.66
Std Dev	3.839

Gaus mean : 35.8044

Gaus Width: 4.4922



B1L103S, U24-ch87, adc2

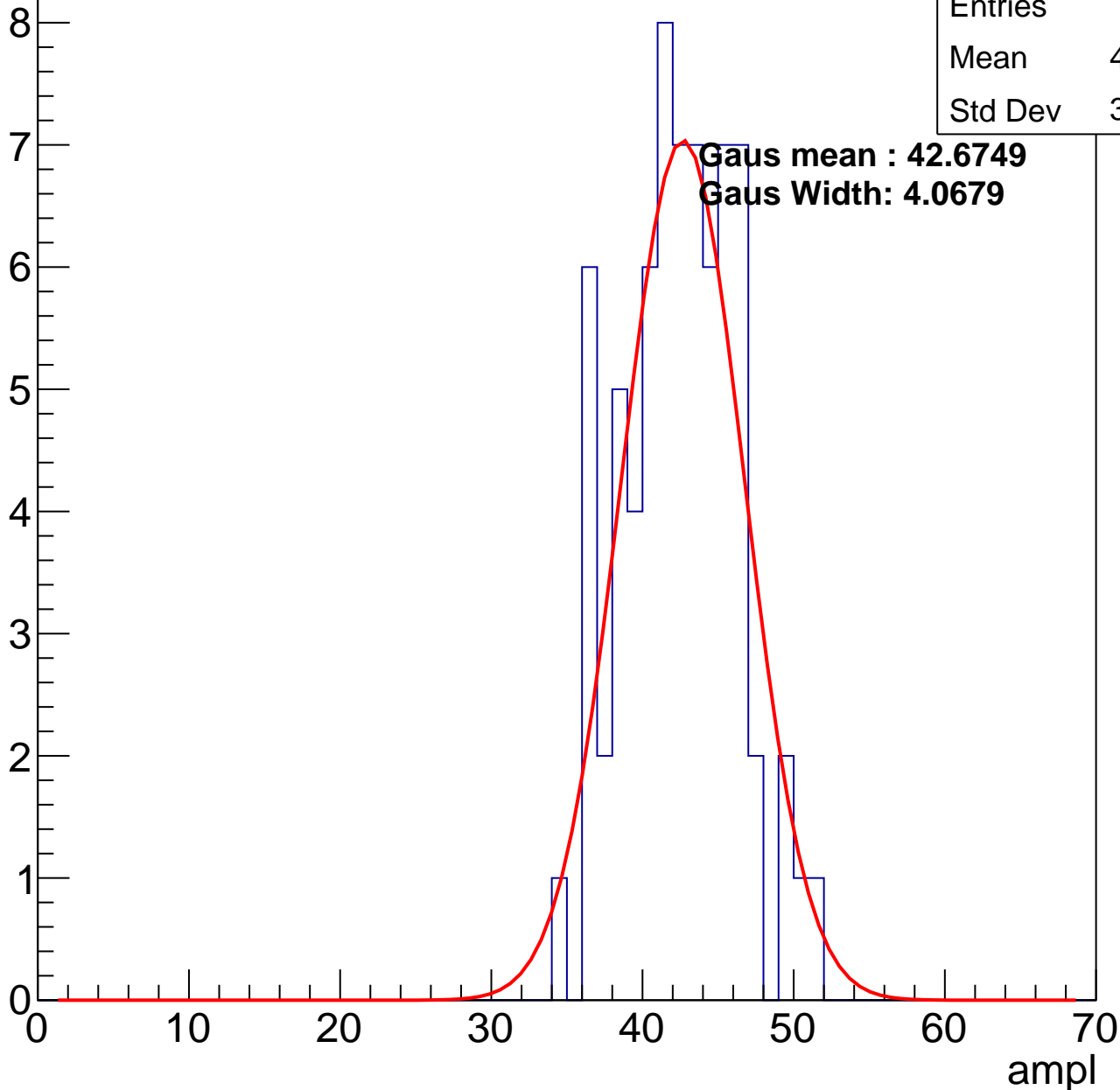
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.04
Std Dev	3.683

Gaus mean : 42.6749

Gaus Width: 4.0679



B1L103S, U24-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	50.02
Std Dev	3.617

Entry

10

8

6

4

2

0

0

10

20

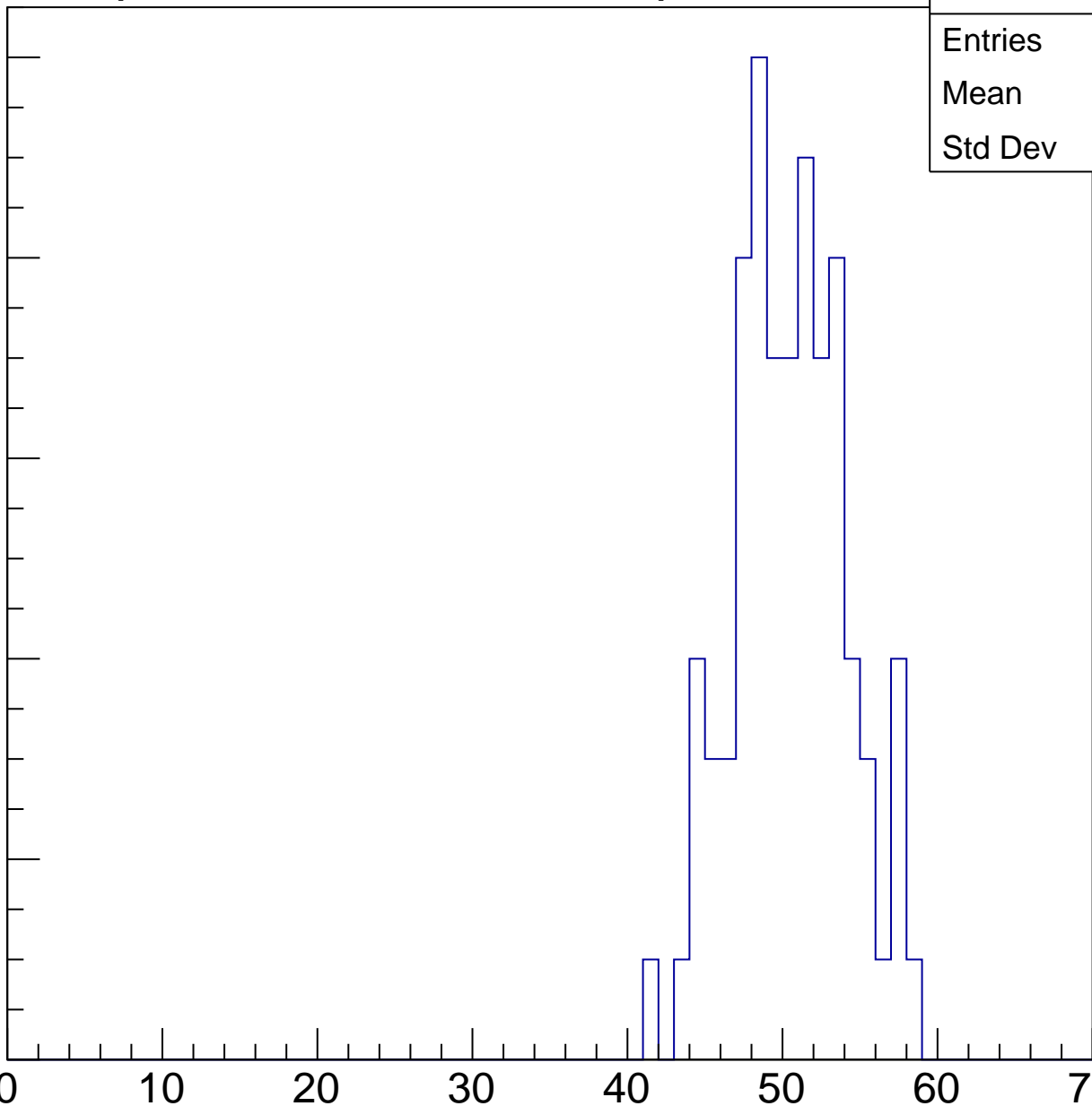
30

40

50

60

ampl

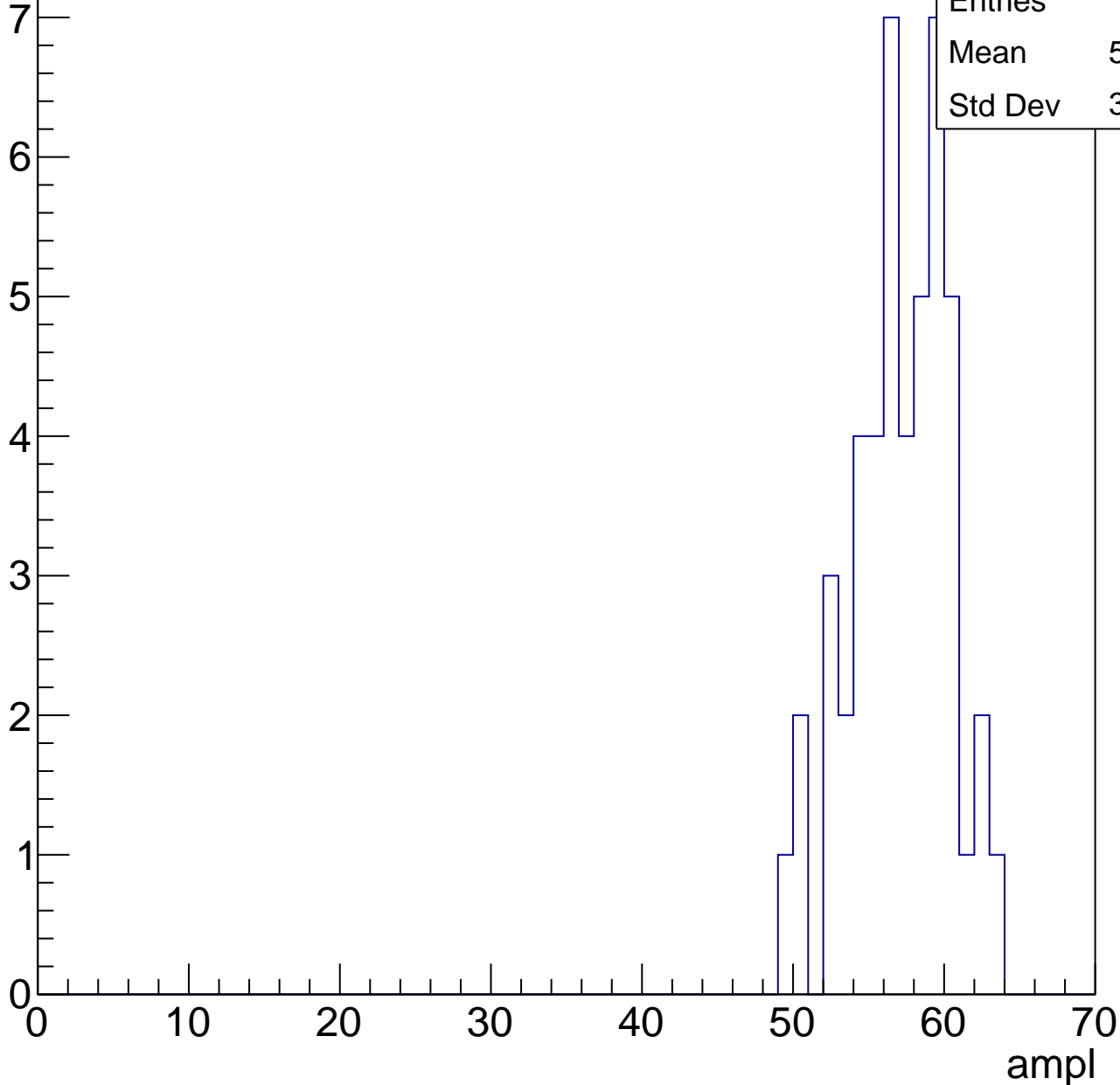


B1L103S, U24-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	56.62
Std Dev	3.232

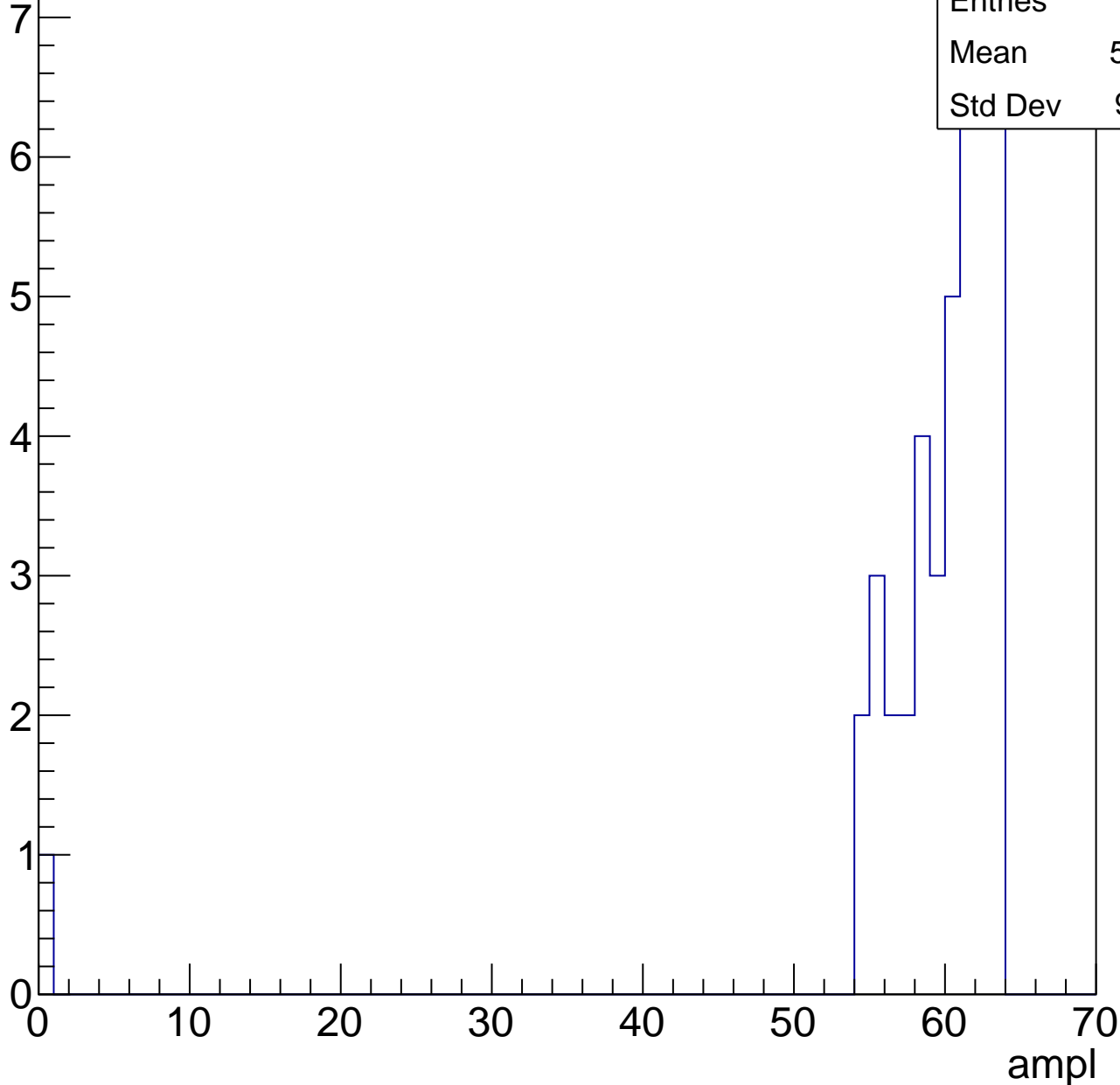


B1L103S, U24-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.37
Std Dev	9.401



B1L103S, U24-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch88, adc0

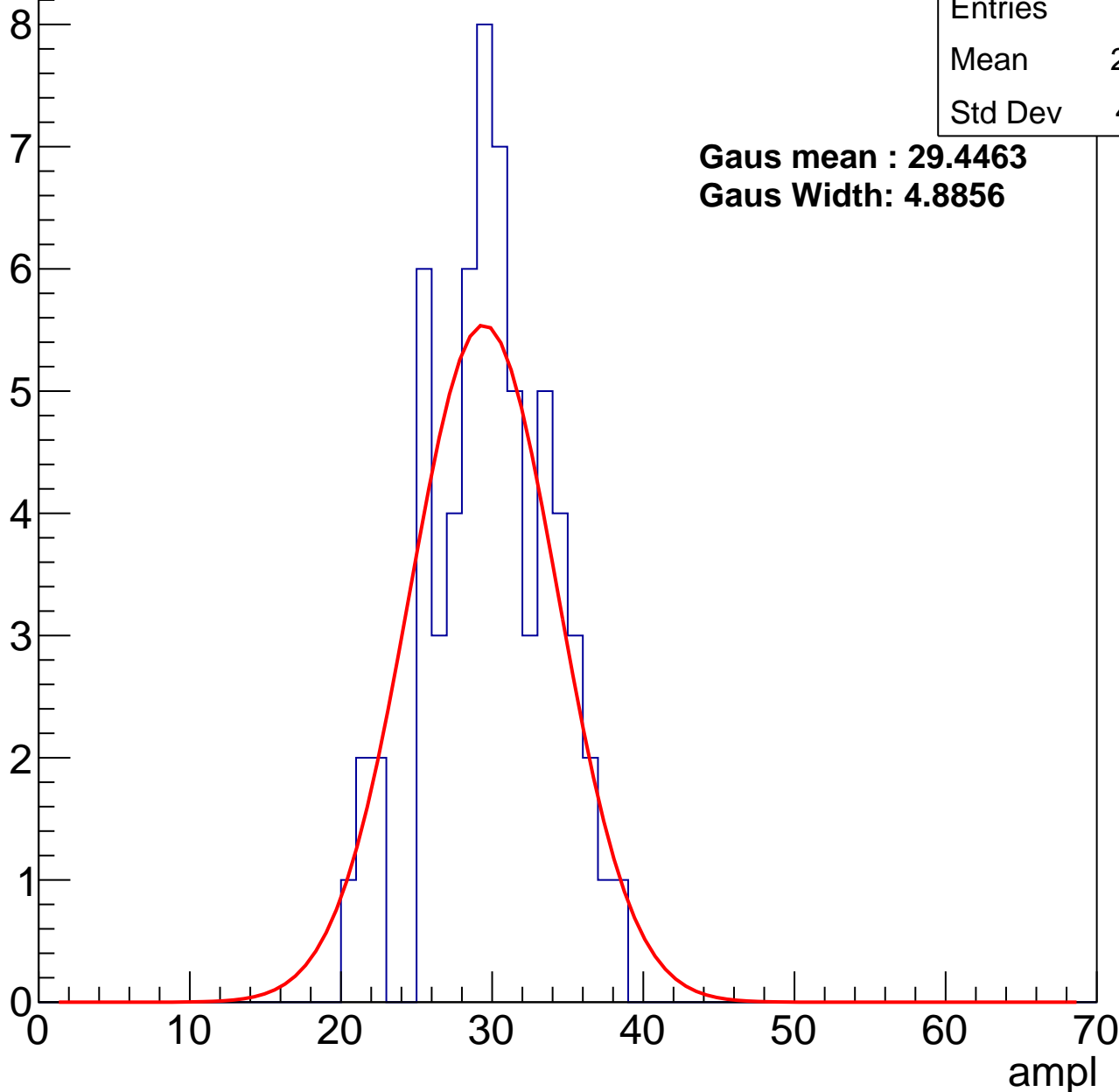
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.46
Std Dev	4.031

Gaus mean : 29.4463

Gaus Width: 4.8856



B1L103S, U24-ch88, adc1

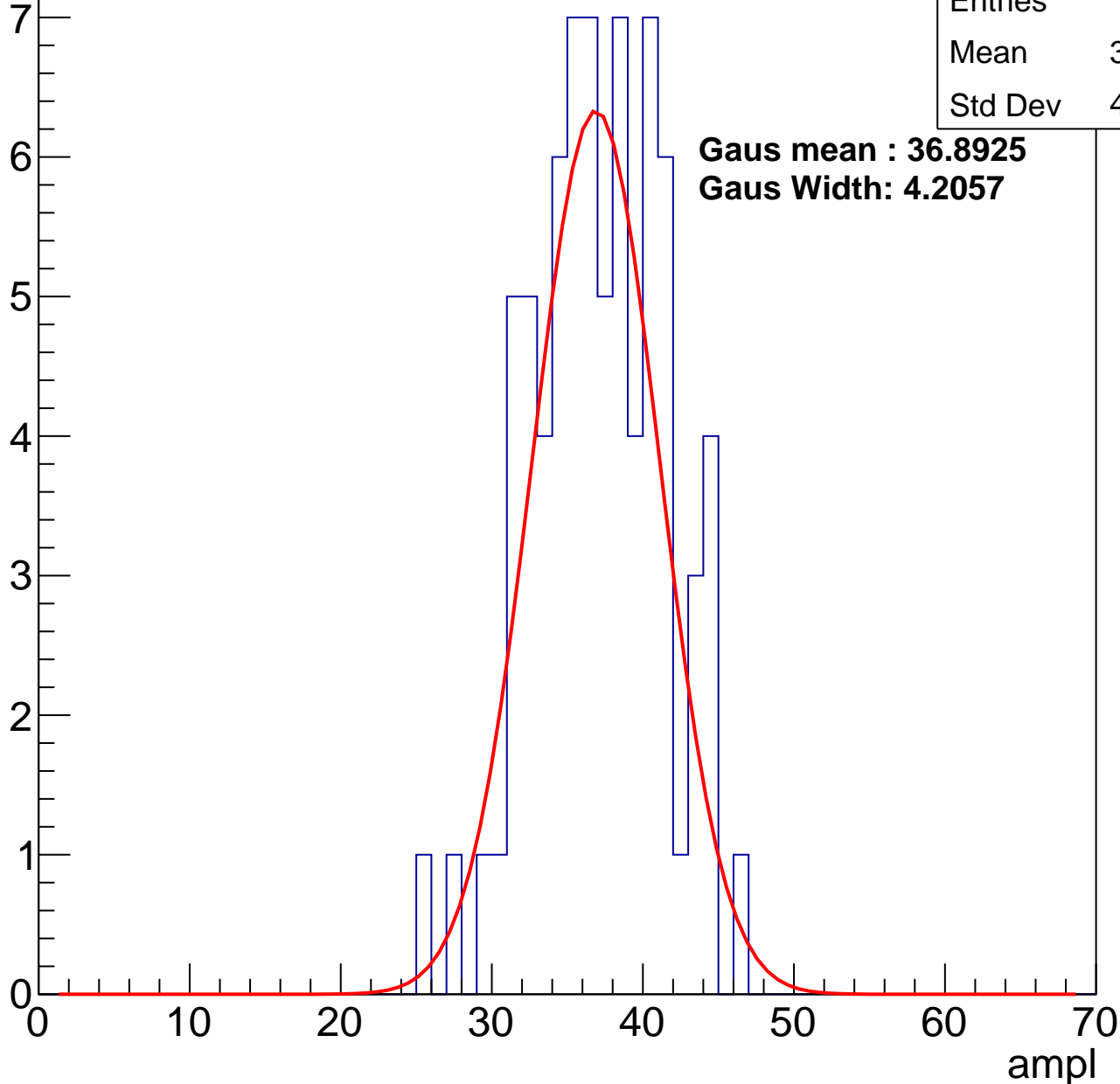
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.64
Std Dev	4.288

Gaus mean : 36.8925

Gaus Width: 4.2057



B1L103S, U24-ch88, adc2

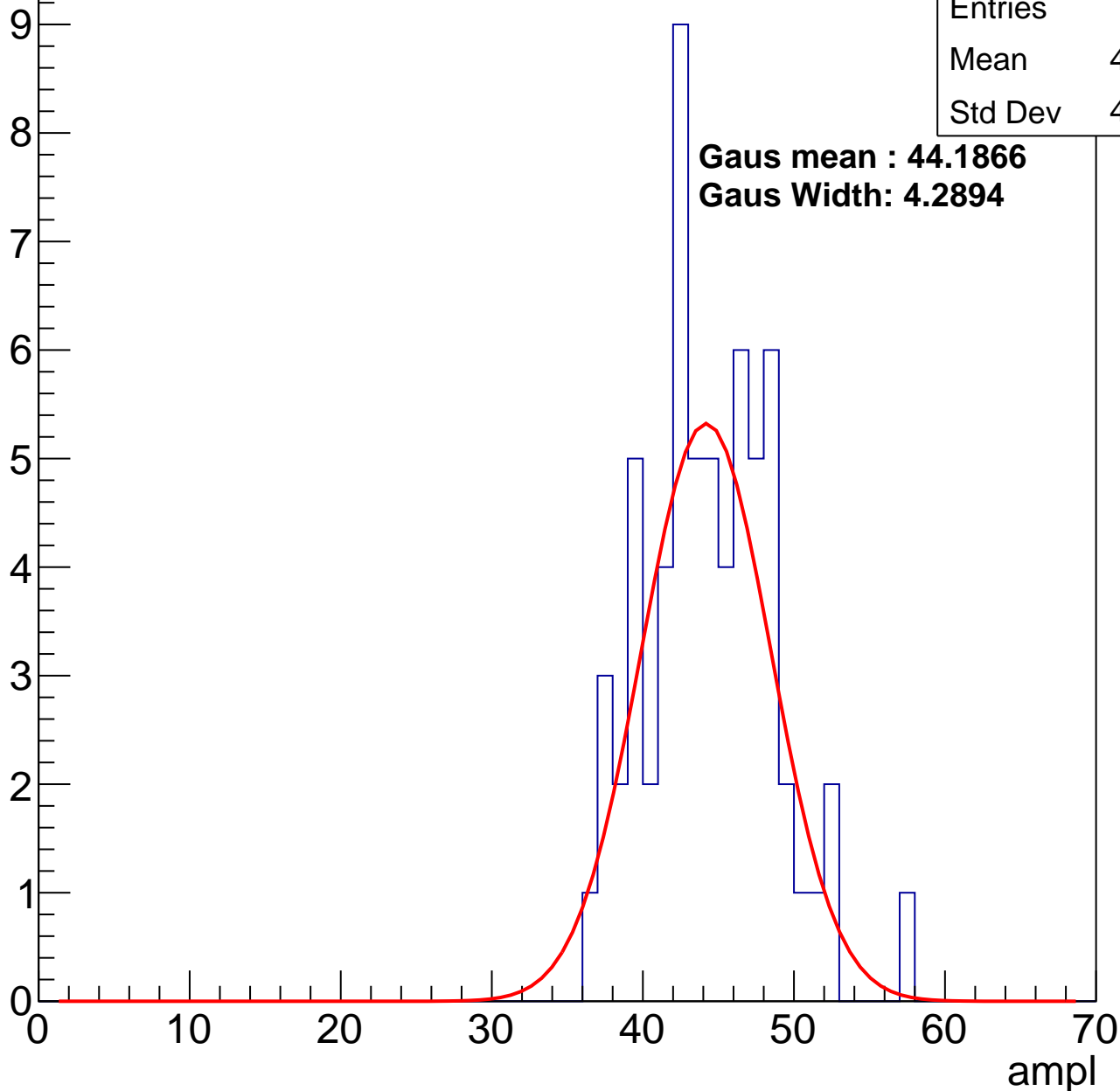
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.97
Std Dev	4.179

Gaus mean : 44.1866

Gaus Width: 4.2894

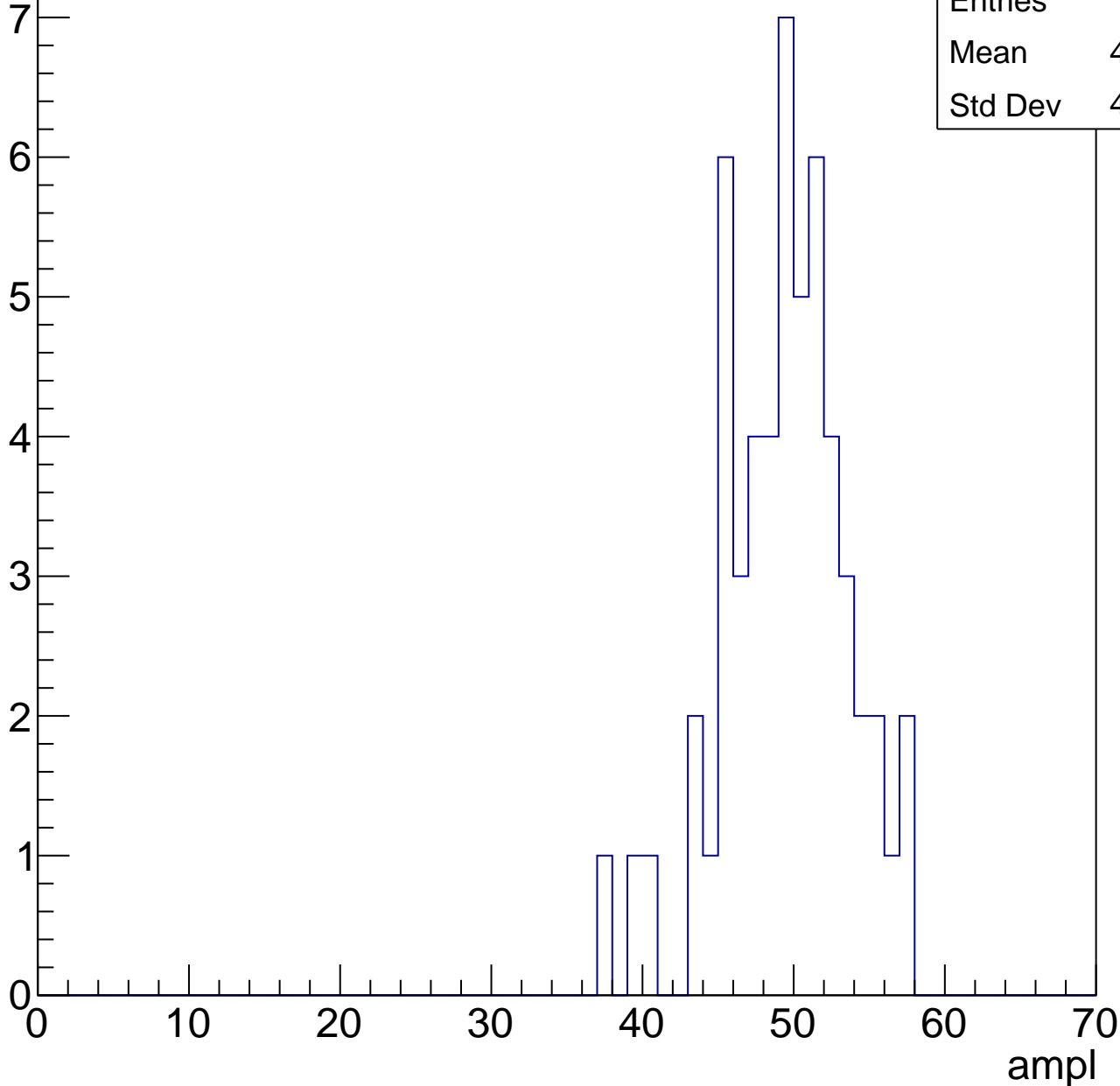


B1L103S, U24-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	48.87
Std Dev	4.213

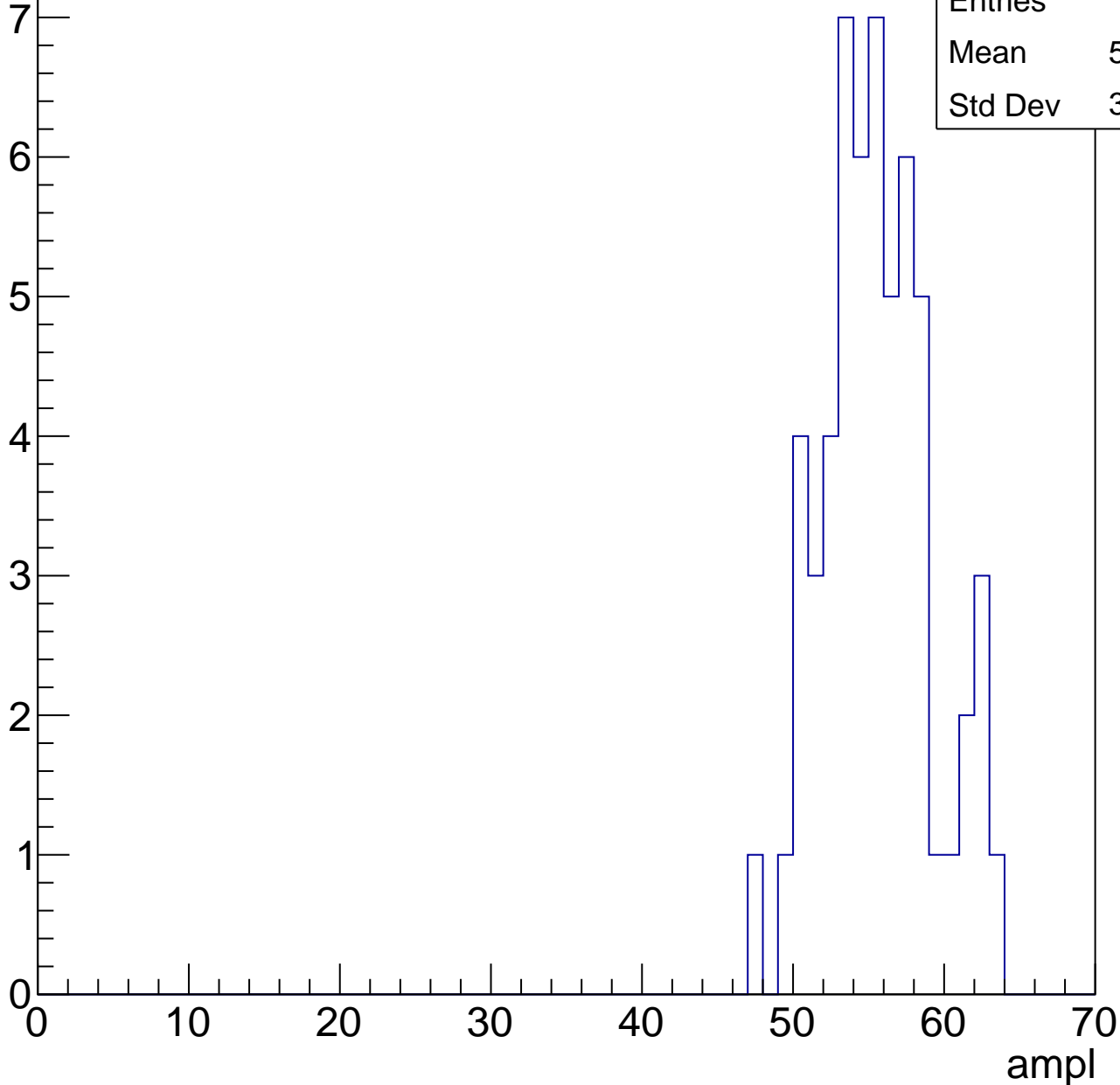


B1L103S, U24-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

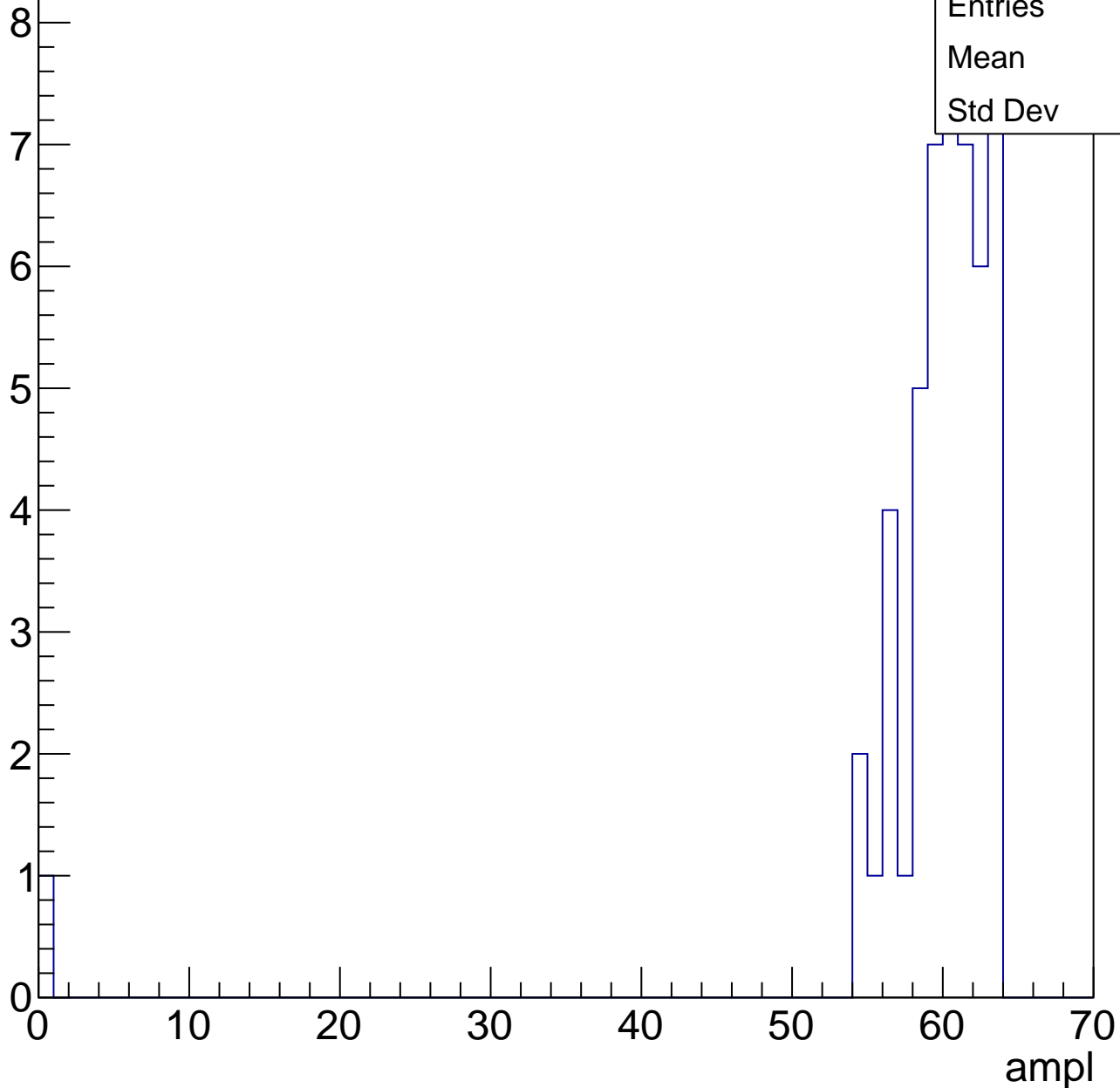
Entries	57
Mean	55.07
Std Dev	3.544



B1L103S, U24-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

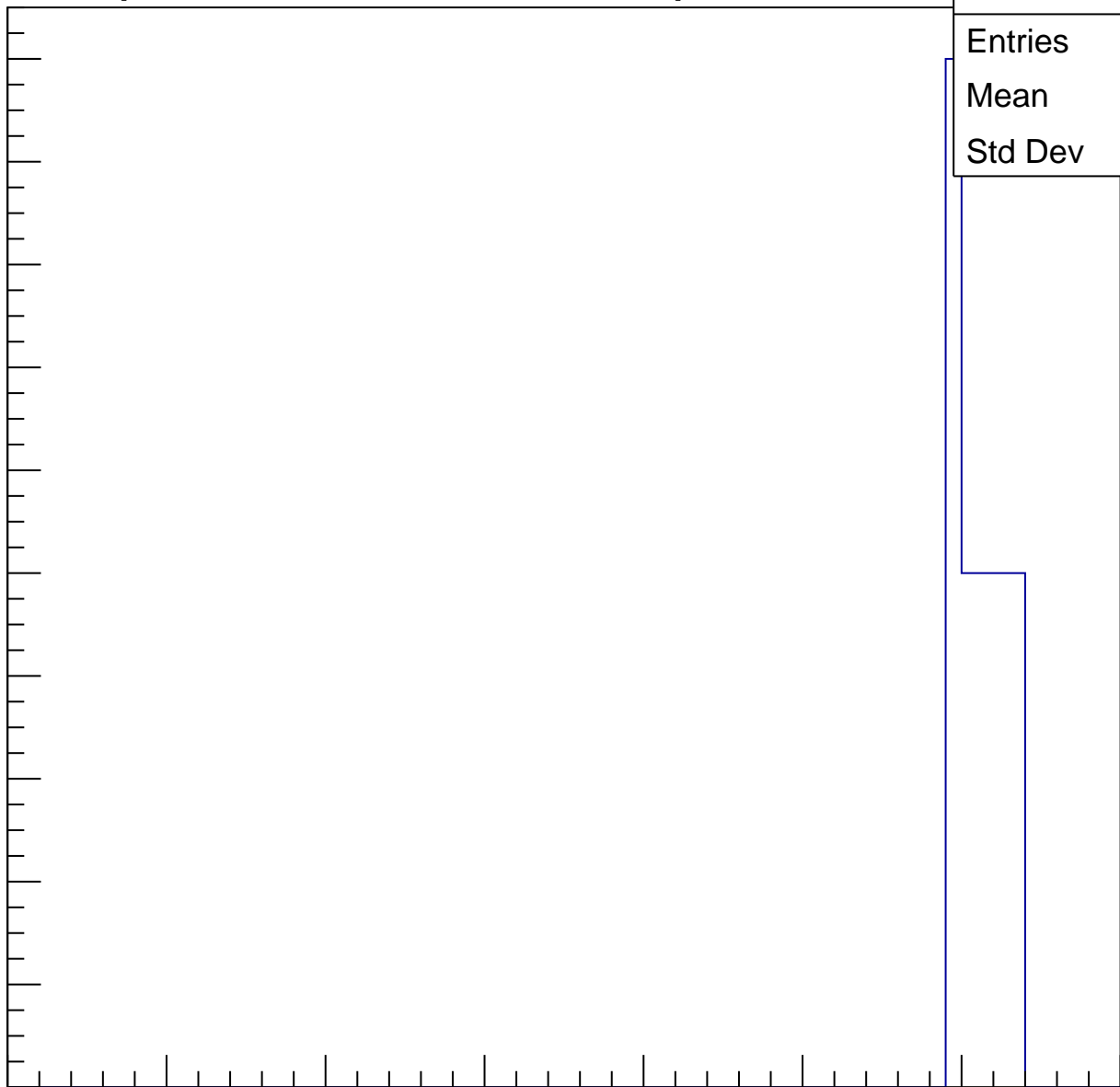
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch89, adc0

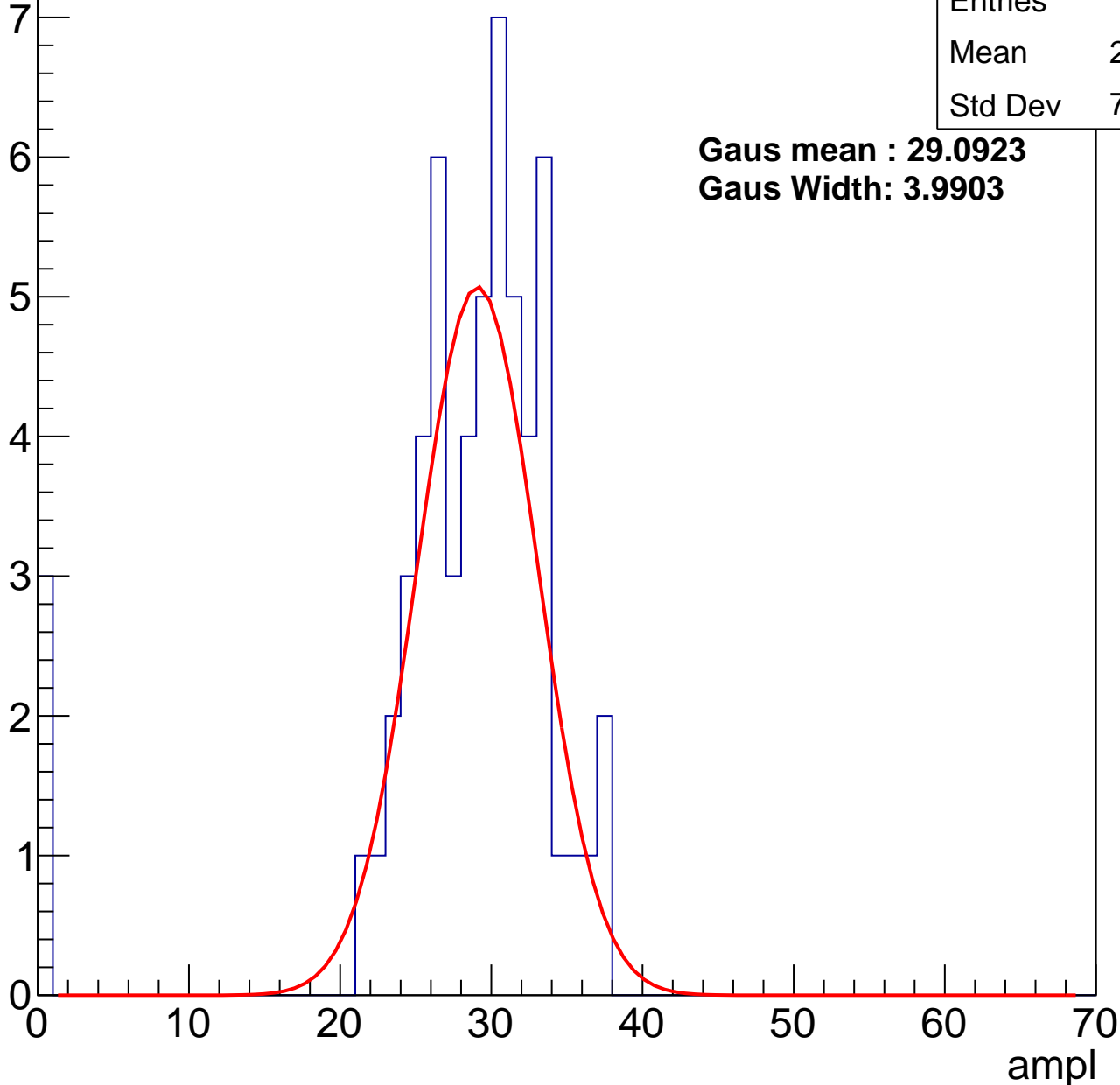
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	27.54
Std Dev	7.352

Gaus mean : 29.0923

Gaus Width: 3.9903



B1L103S, U24-ch89, adc1

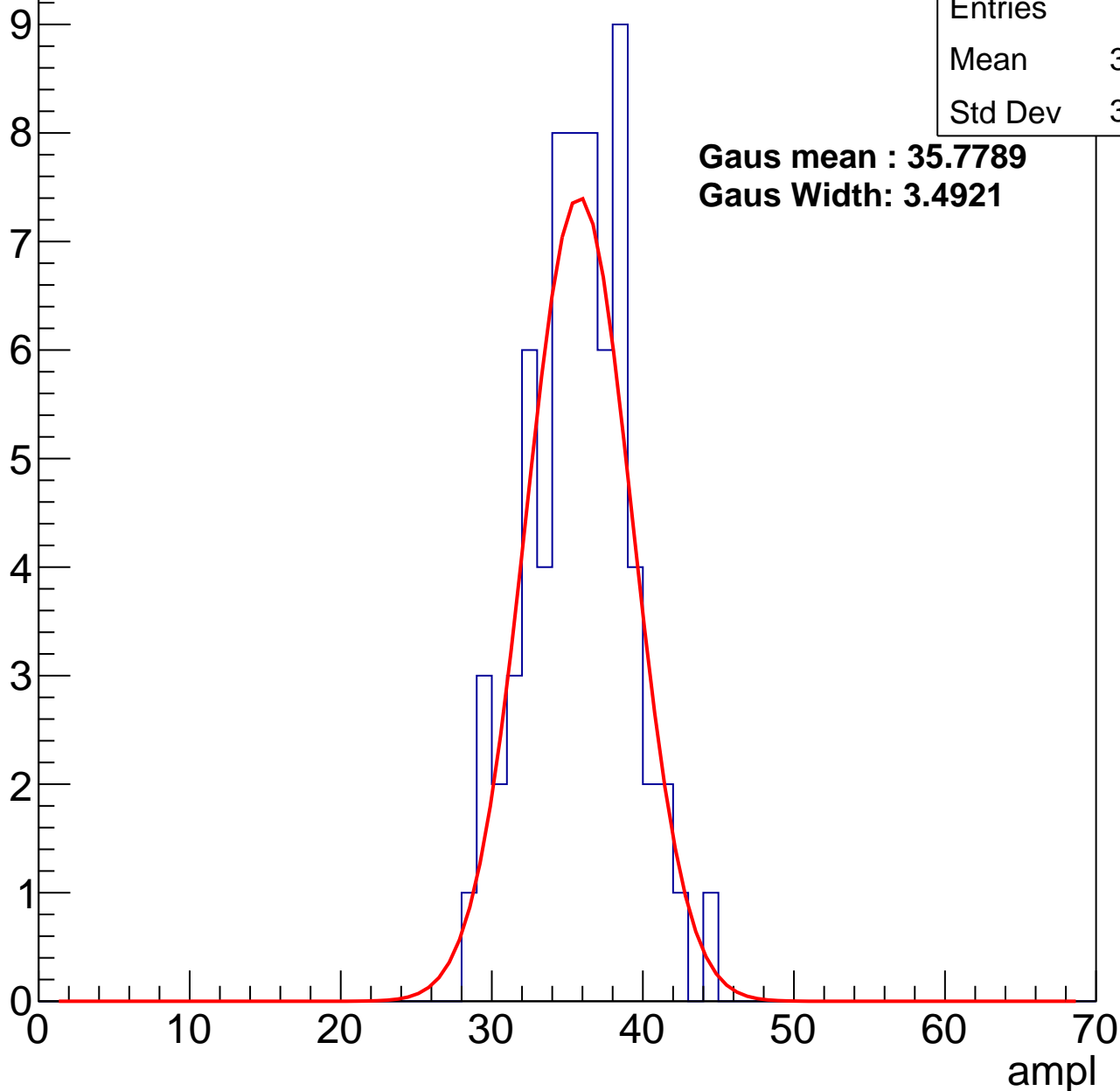
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.29
Std Dev	3.335

Gaus mean : 35.7789

Gaus Width: 3.4921



B1L103S, U24-ch89, adc2

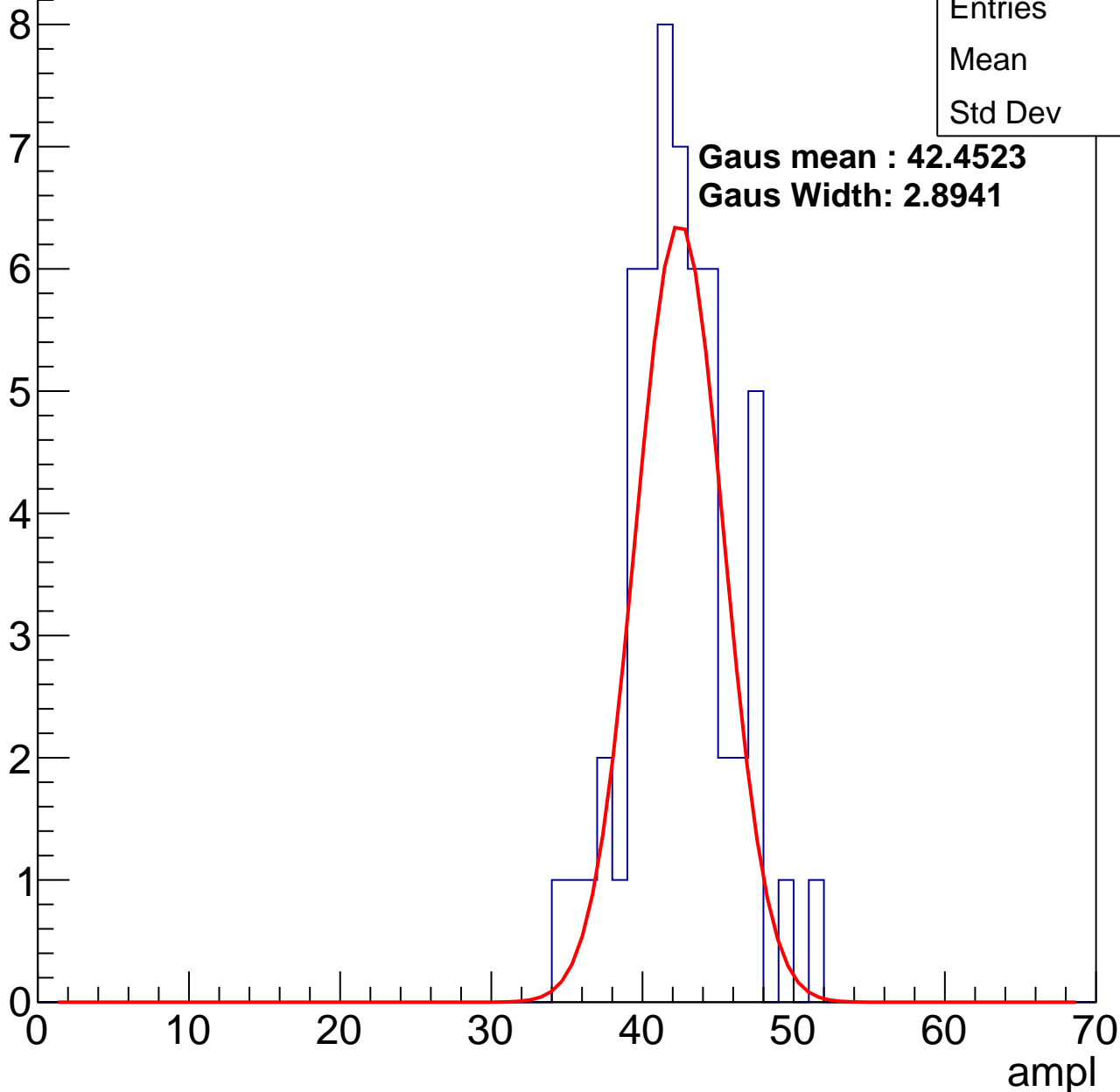
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42
Std Dev	3.37

Gaus mean : 42.4523

Gaus Width: 2.8941



B1L103S, U24-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	48.29
Std Dev	3.594

Entry

10

8

6

4

2

0

0

10

20

30

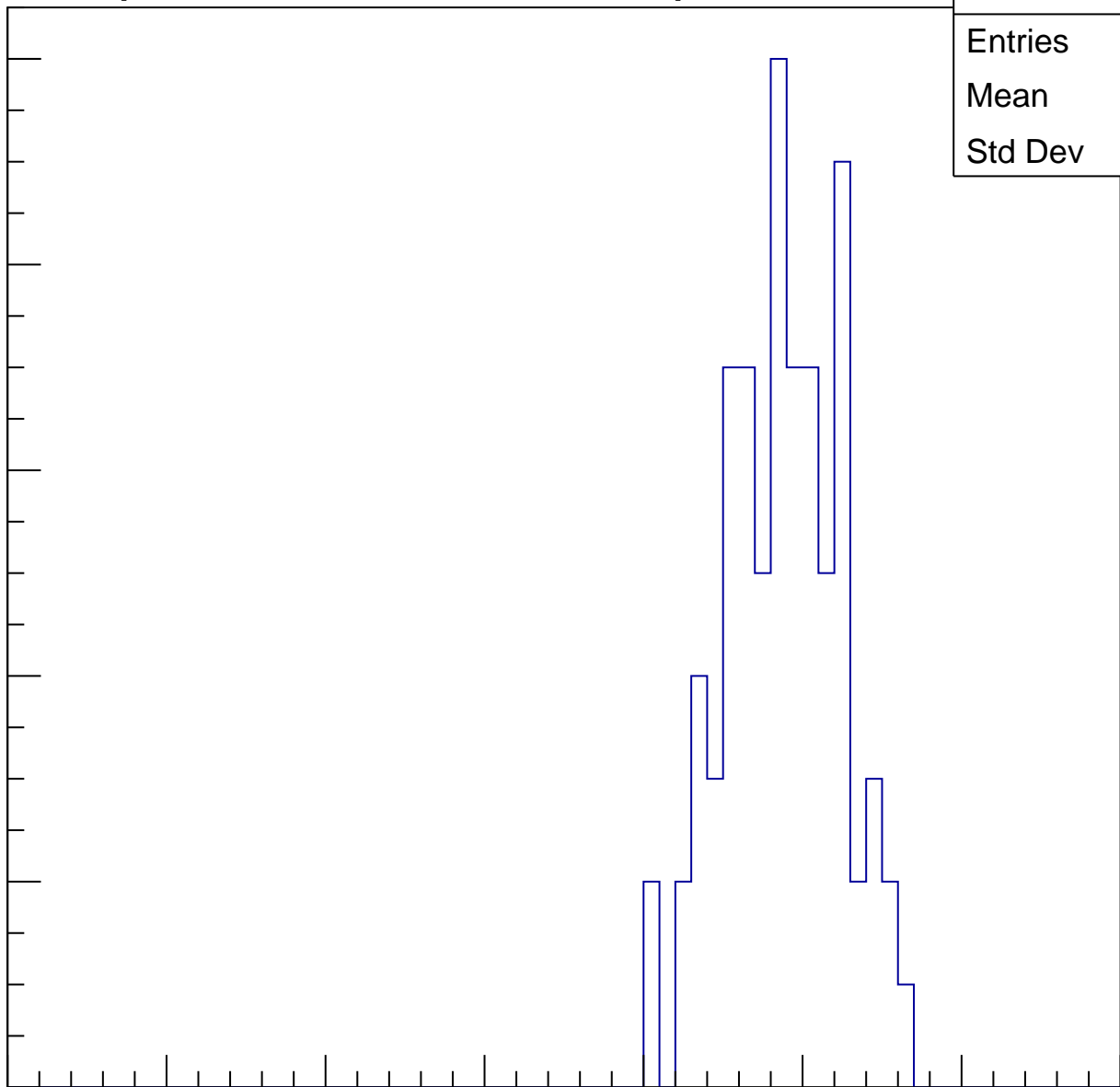
40

50

60

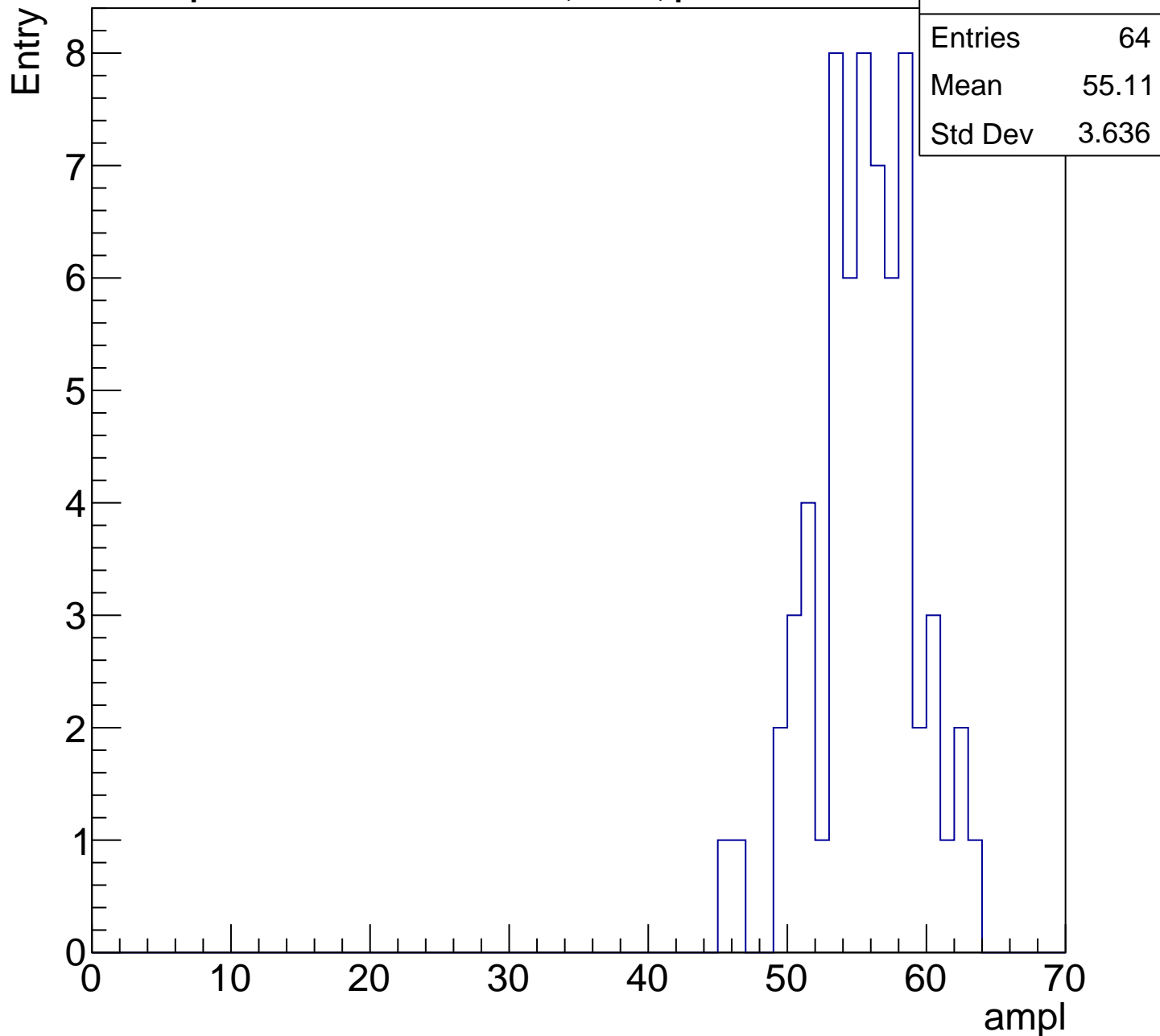
70

ampl



B1L103S, U24-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

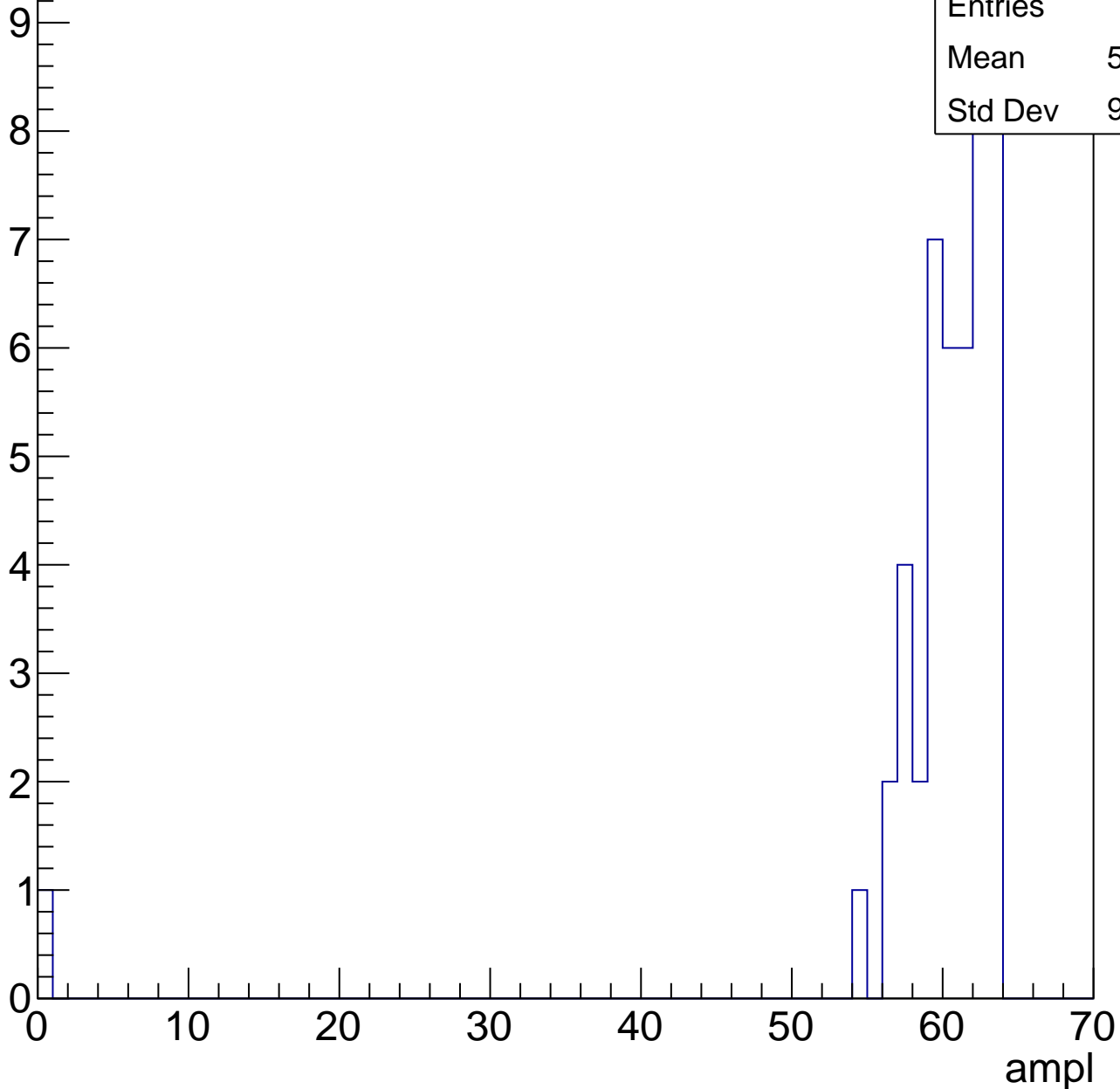


B1L103S, U24-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

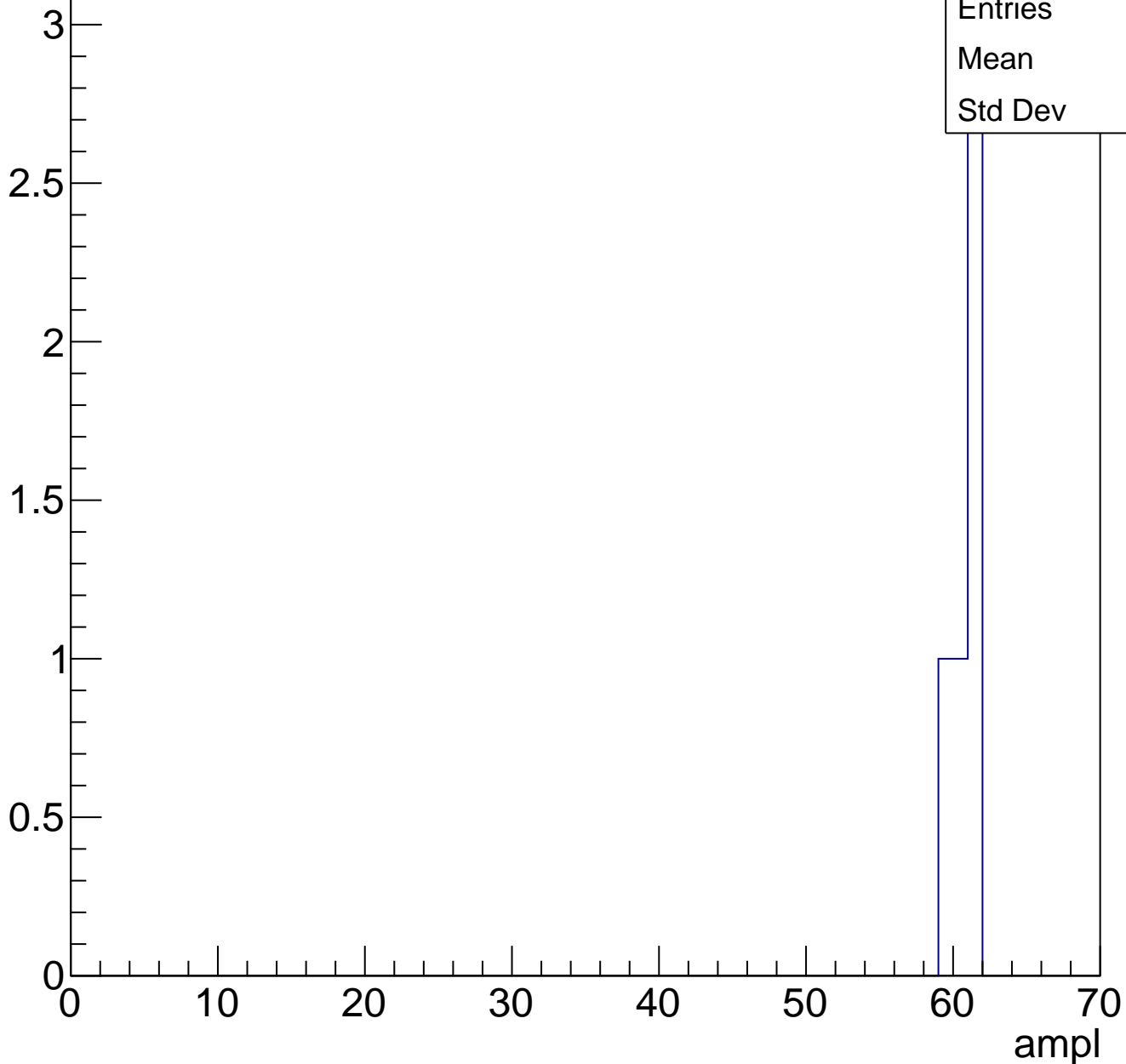
Entries	46
Mean	58.96
Std Dev	9.074



B1L103S, U24-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch90, adc0

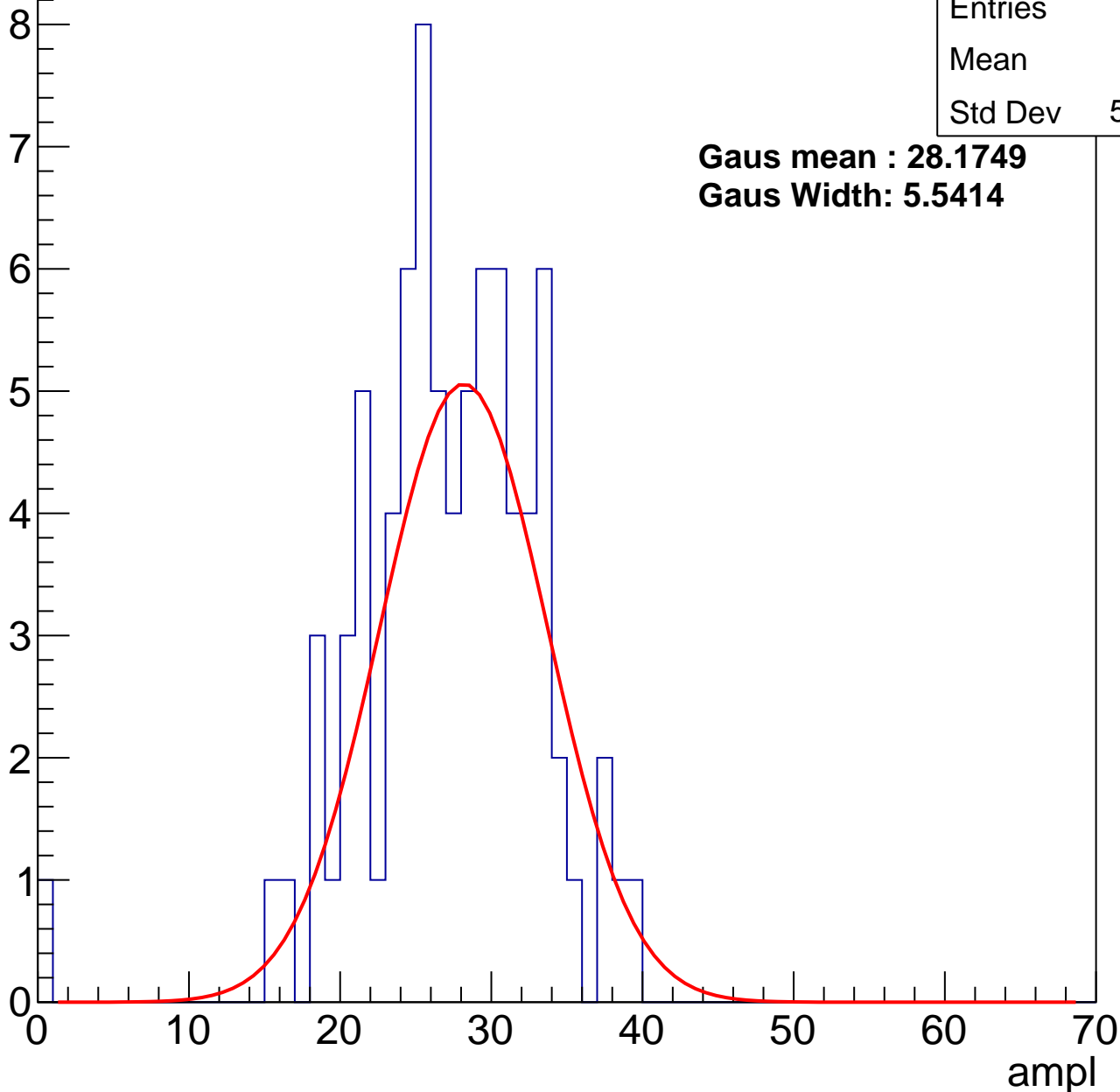
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	26.7
Std Dev	5.974

Gaus mean : 28.1749

Gaus Width: 5.5414



B1L103S, U24-ch90, adc1

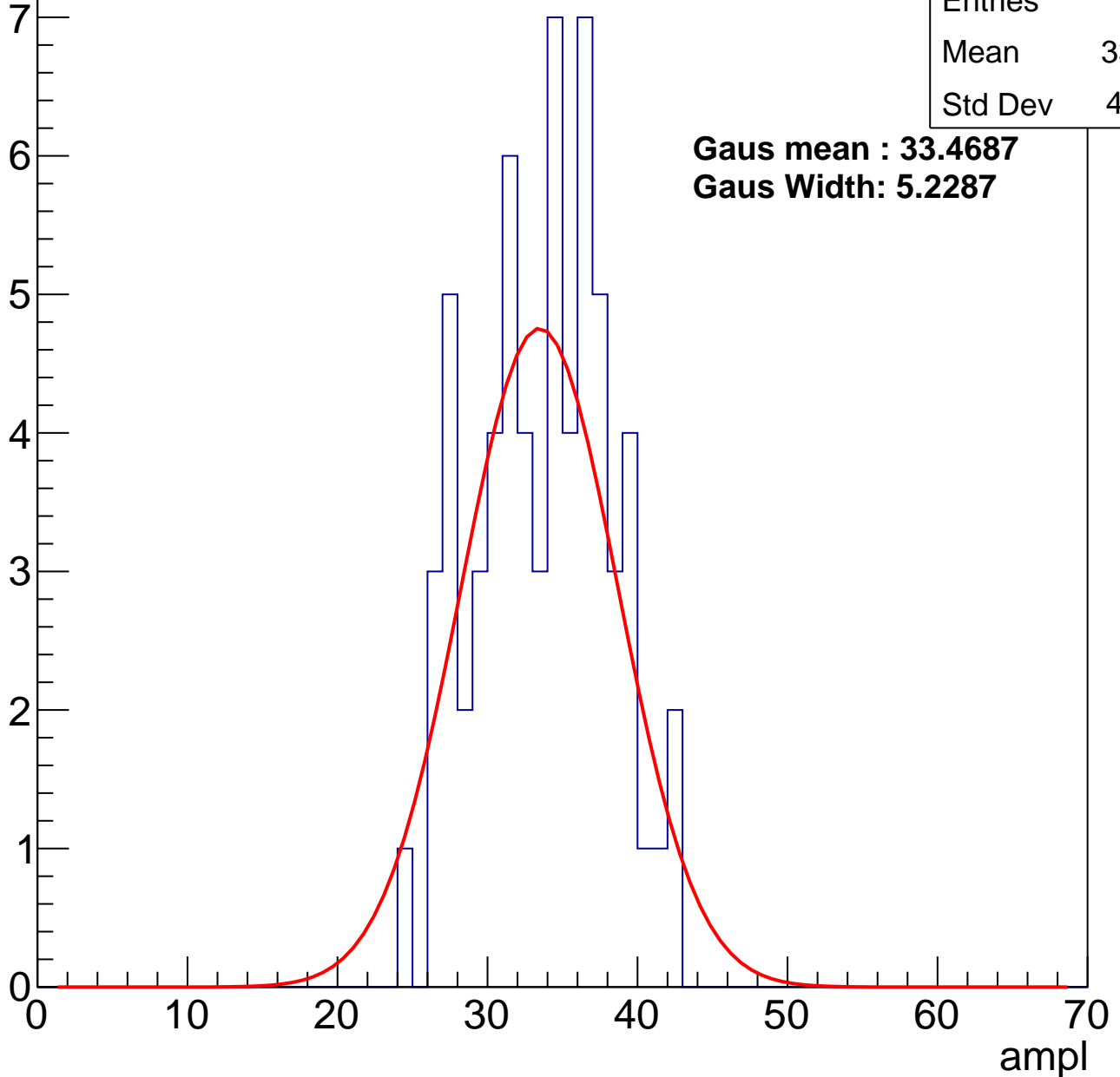
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	33.28
Std Dev	4.341

Gaus mean : 33.4687

Gaus Width: 5.2287



B1L103S, U24-ch90, adc2

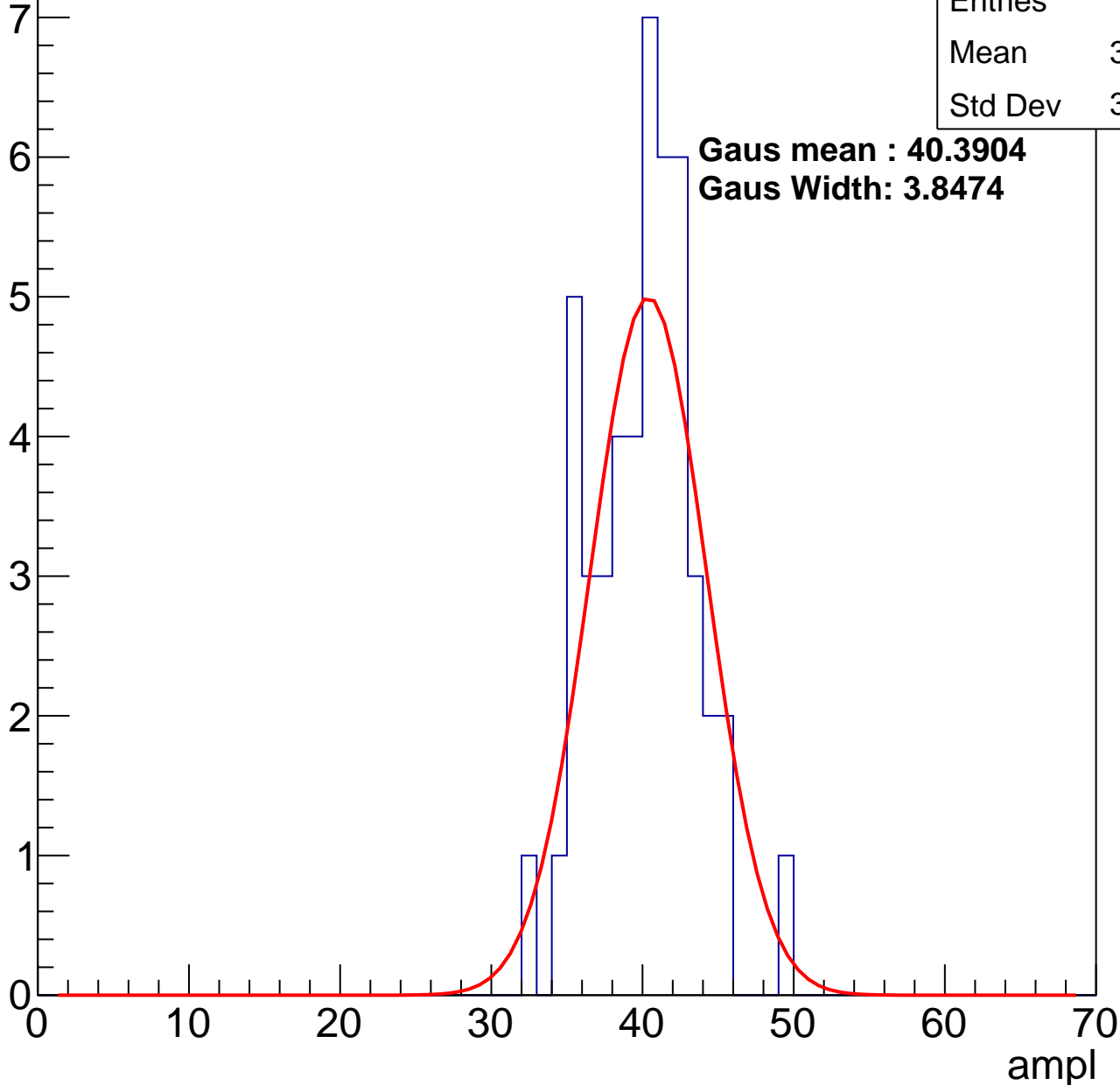
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	39.62
Std Dev	3.333

Gaus mean : 40.3904

Gaus Width: 3.8474

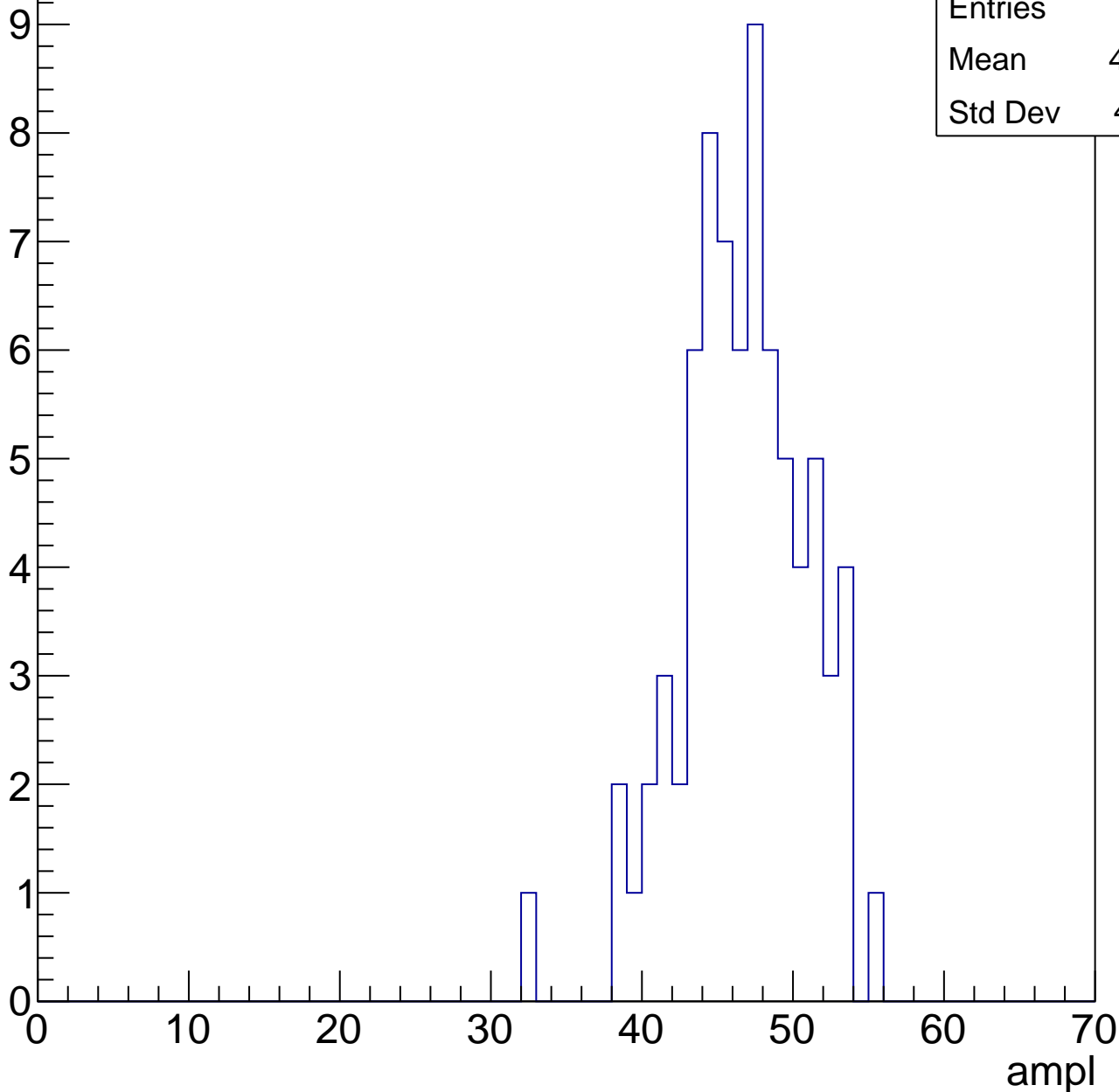


B1L103S, U24-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	46.25
Std Dev	4.151

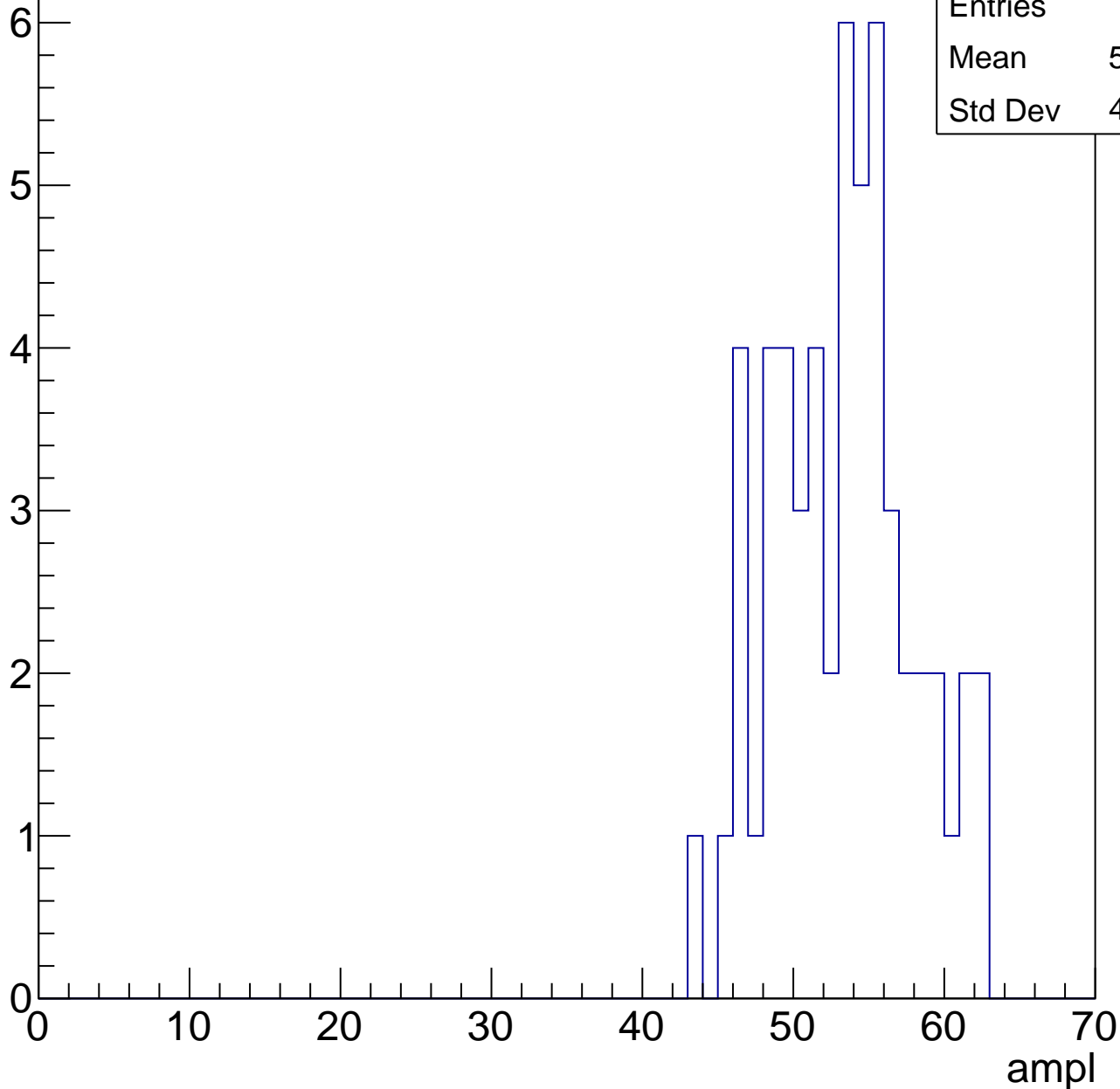


B1L103S, U24-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	52.82
Std Dev	4.577

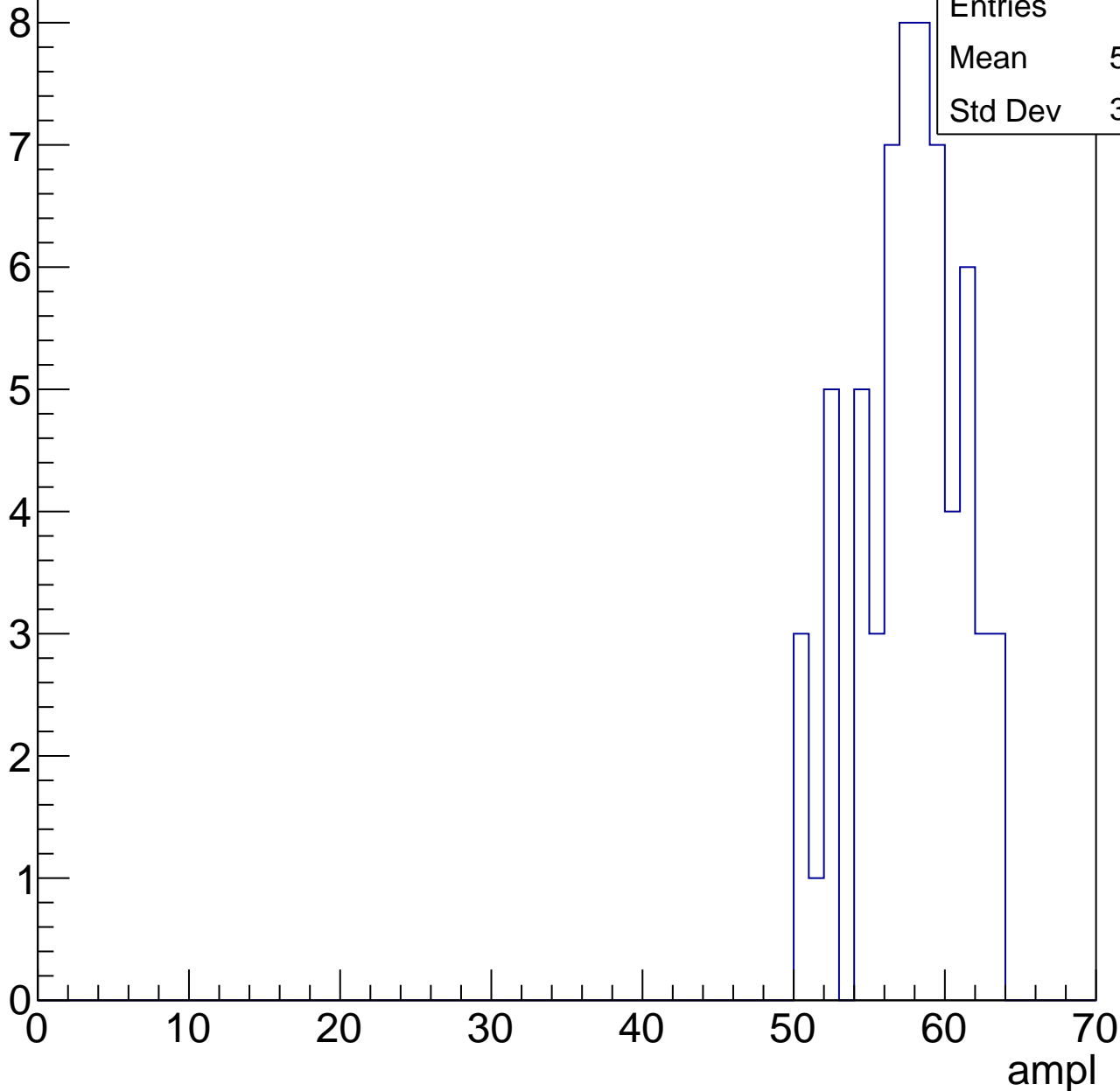


B1L103S, U24-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	57.17
Std Dev	3.383

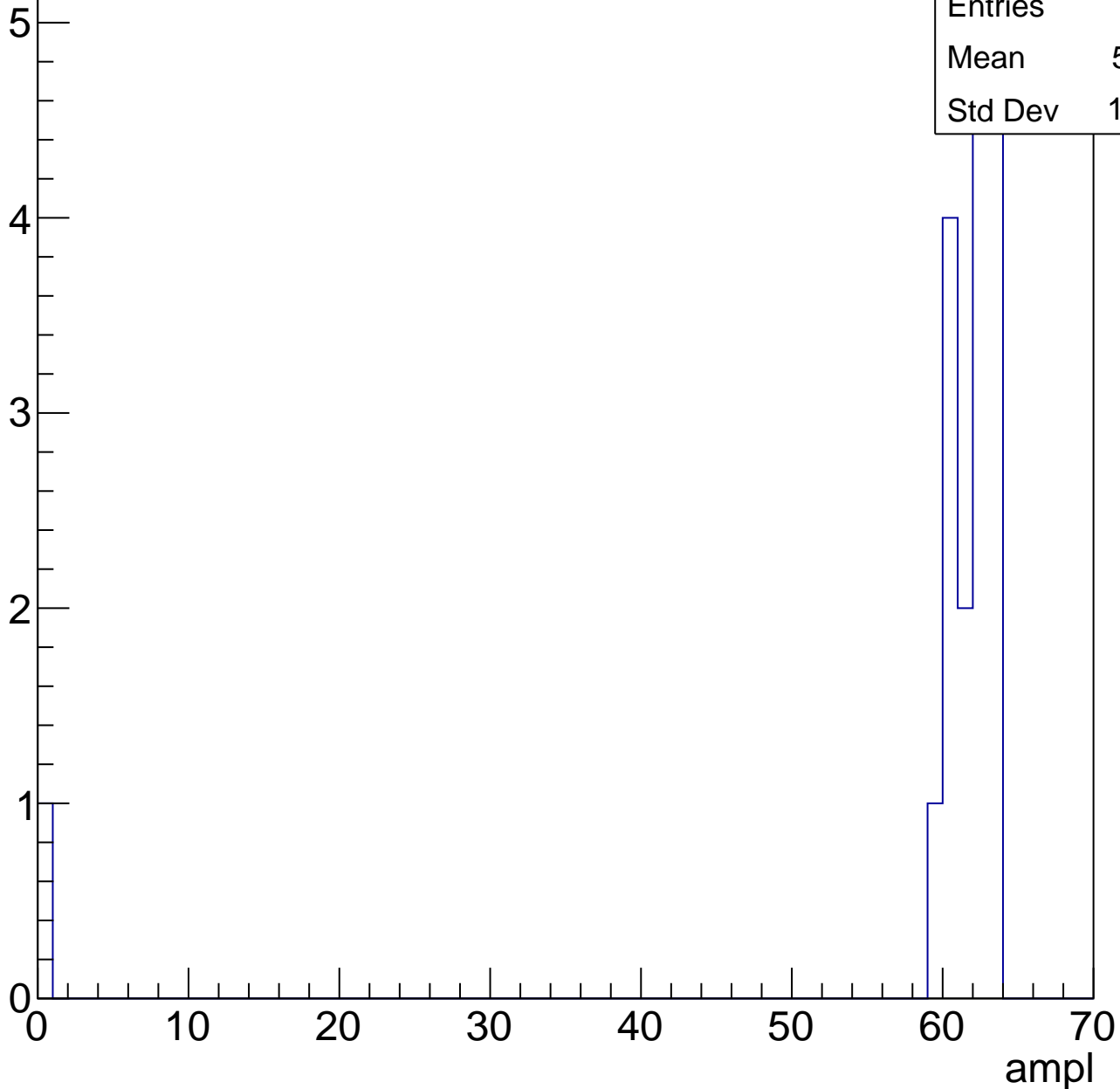


B1L103S, U24-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

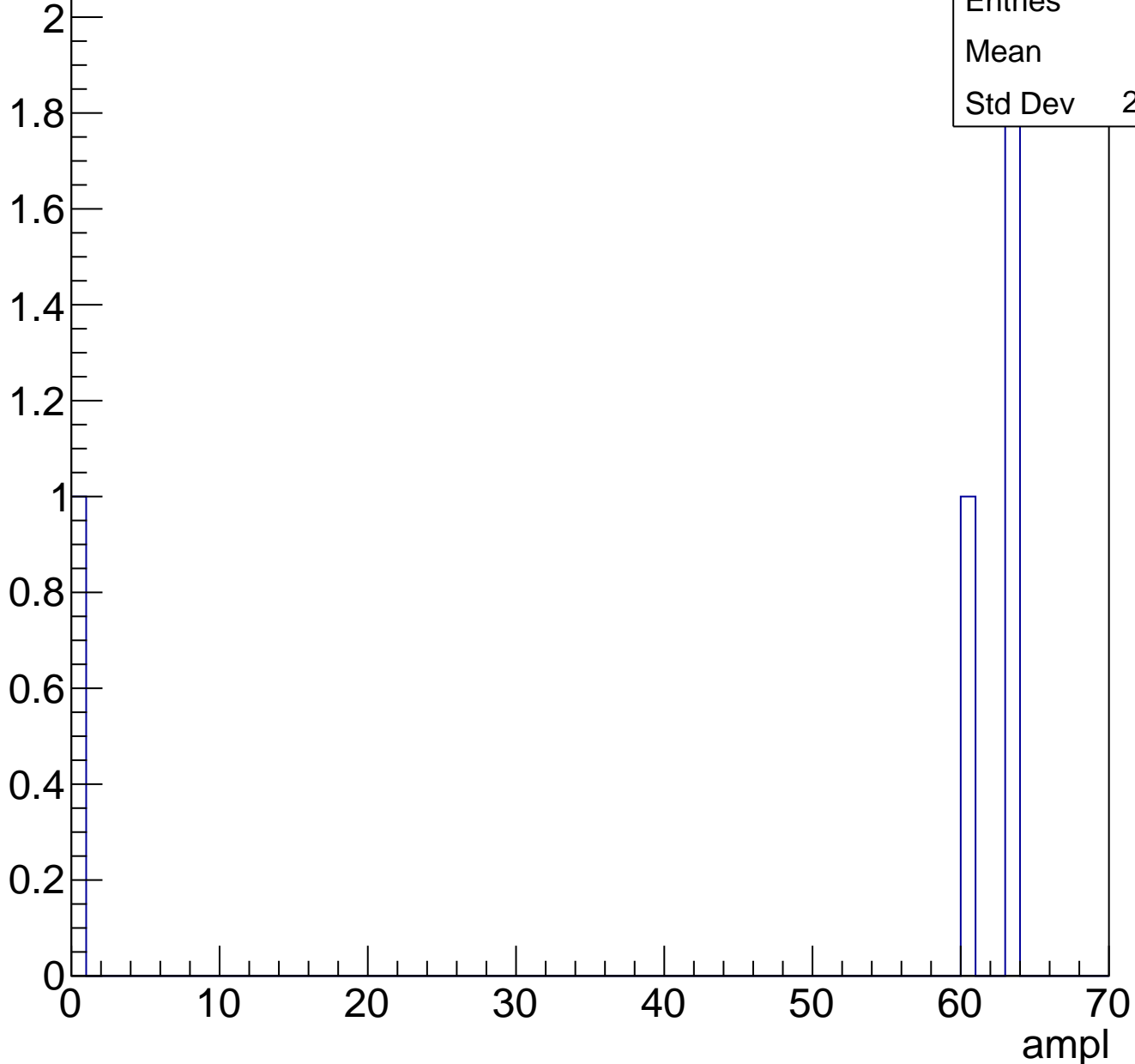
Entries	18
Mean	58.11
Std Dev	14.15



B1L103S, U24-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch91, adc0

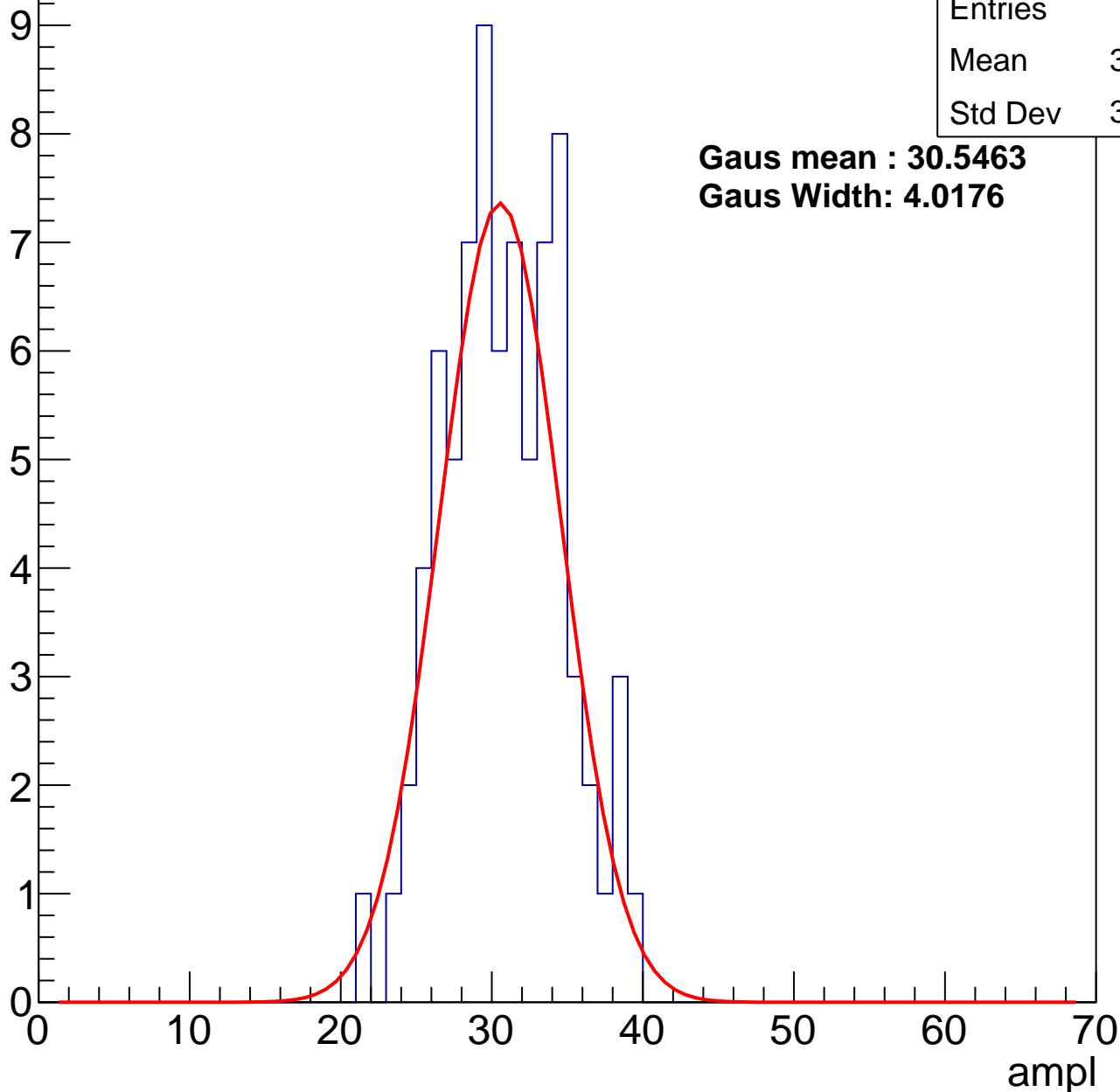
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	30.35
Std Dev	3.852

Gaus mean : 30.5463

Gaus Width: 4.0176



B1L103S, U24-ch91, adc1

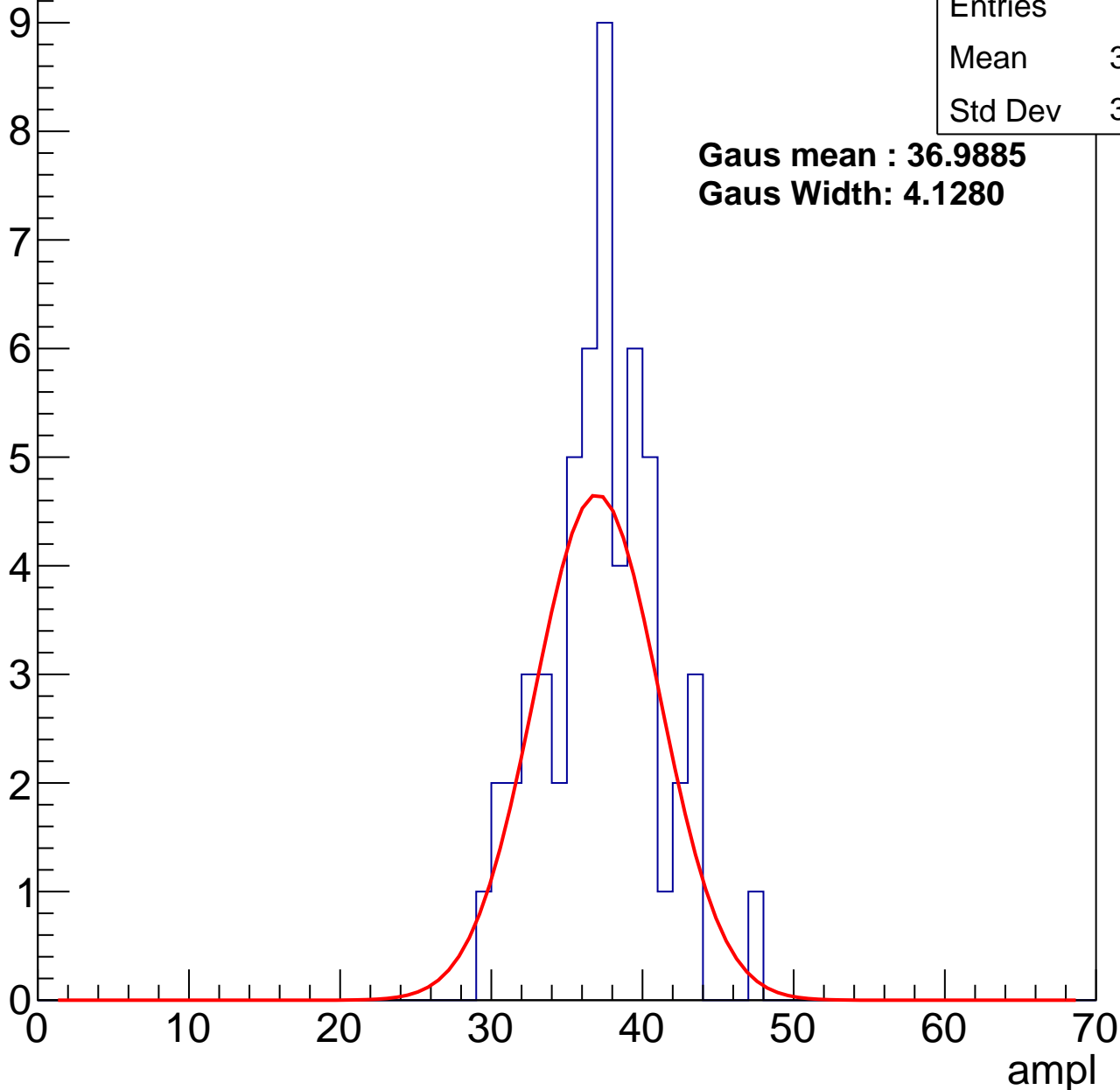
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	36.82
Std Dev	3.683

Gaus mean : 36.9885

Gaus Width: 4.1280



B1L103S, U24-ch91, adc2

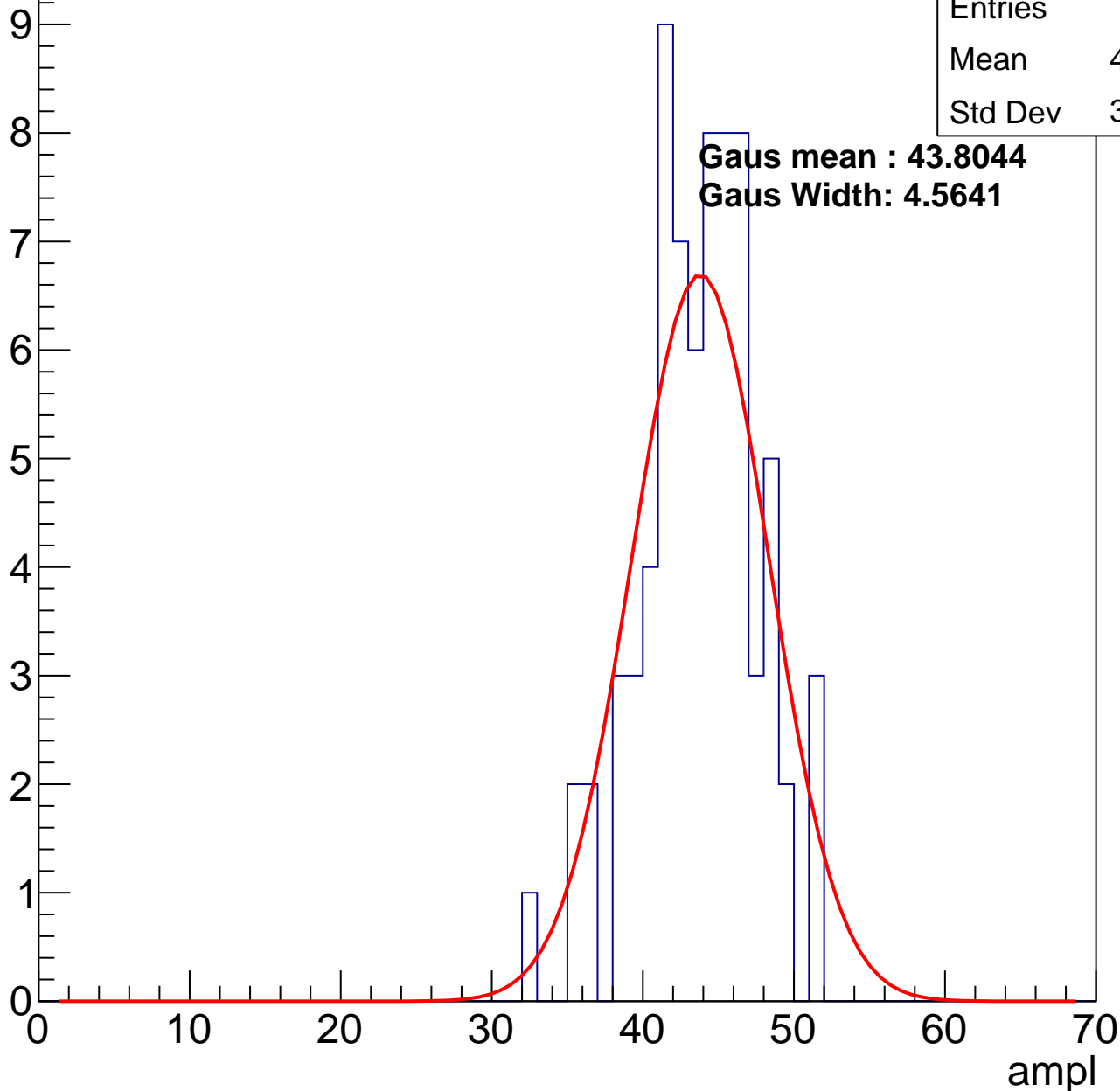
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	43.22
Std Dev	3.846

Gaus mean : 43.8044

Gaus Width: 4.5641

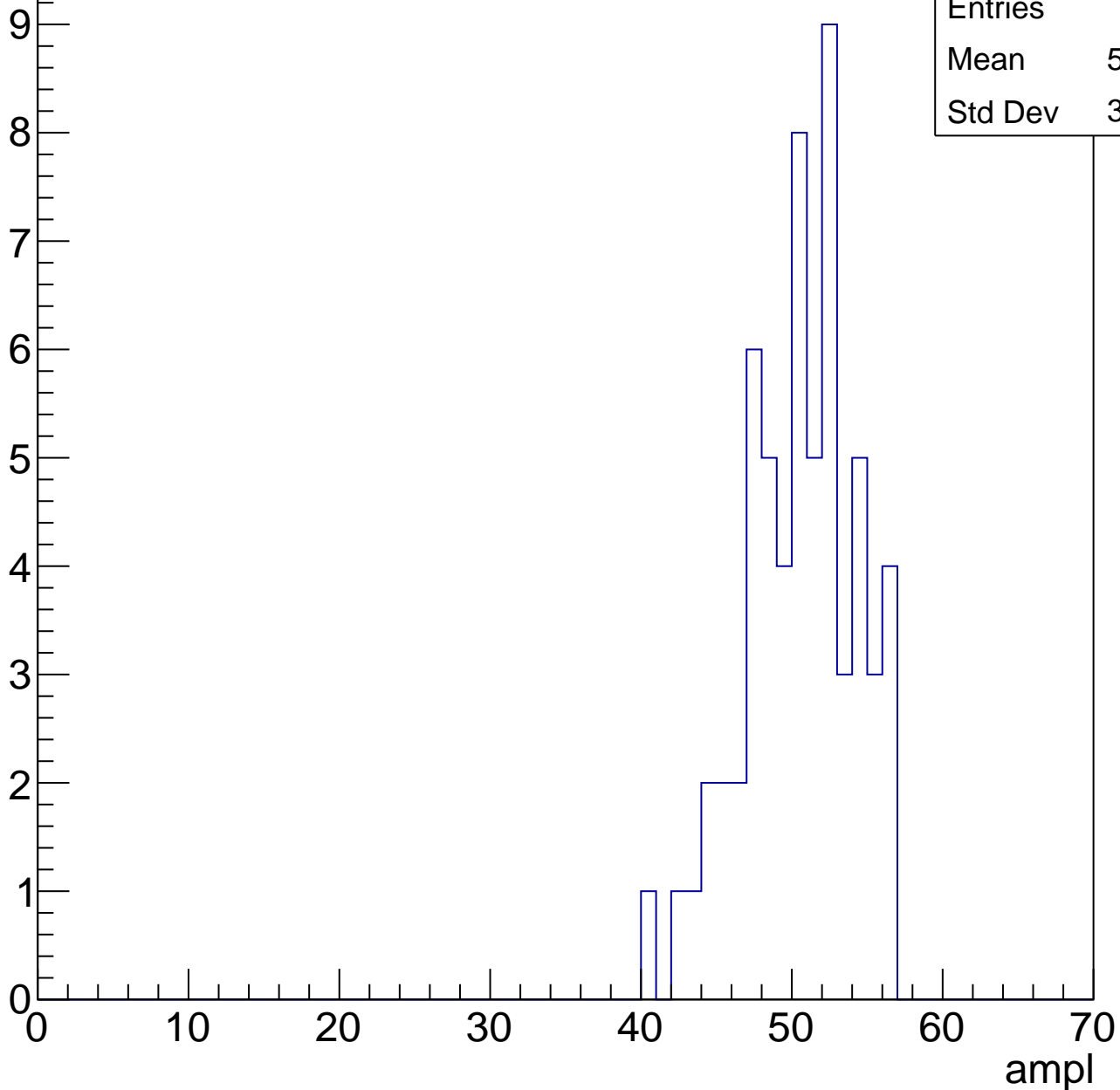


B1L103S, U24-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.07
Std Dev	3.648

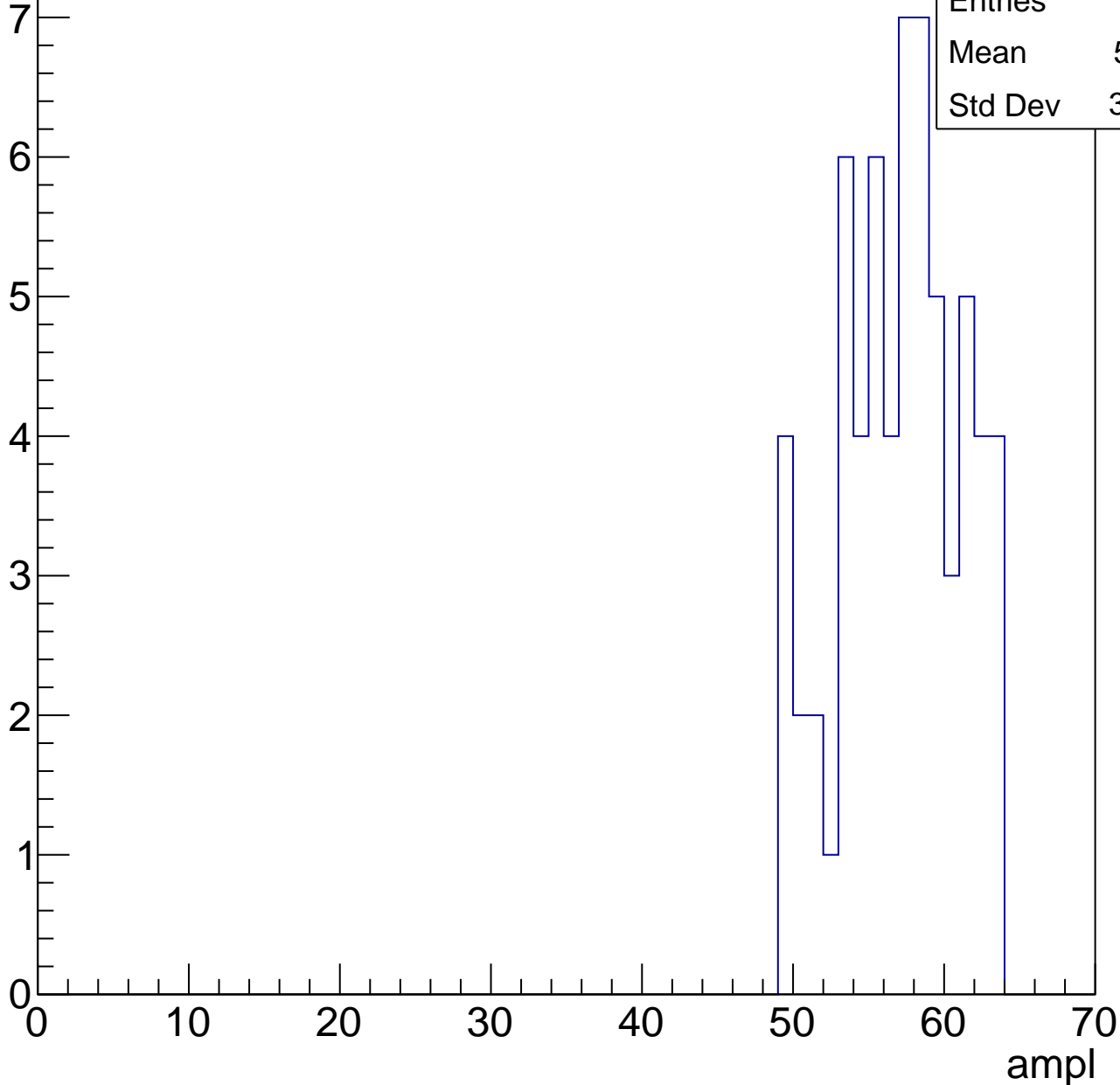


B1L103S, U24-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

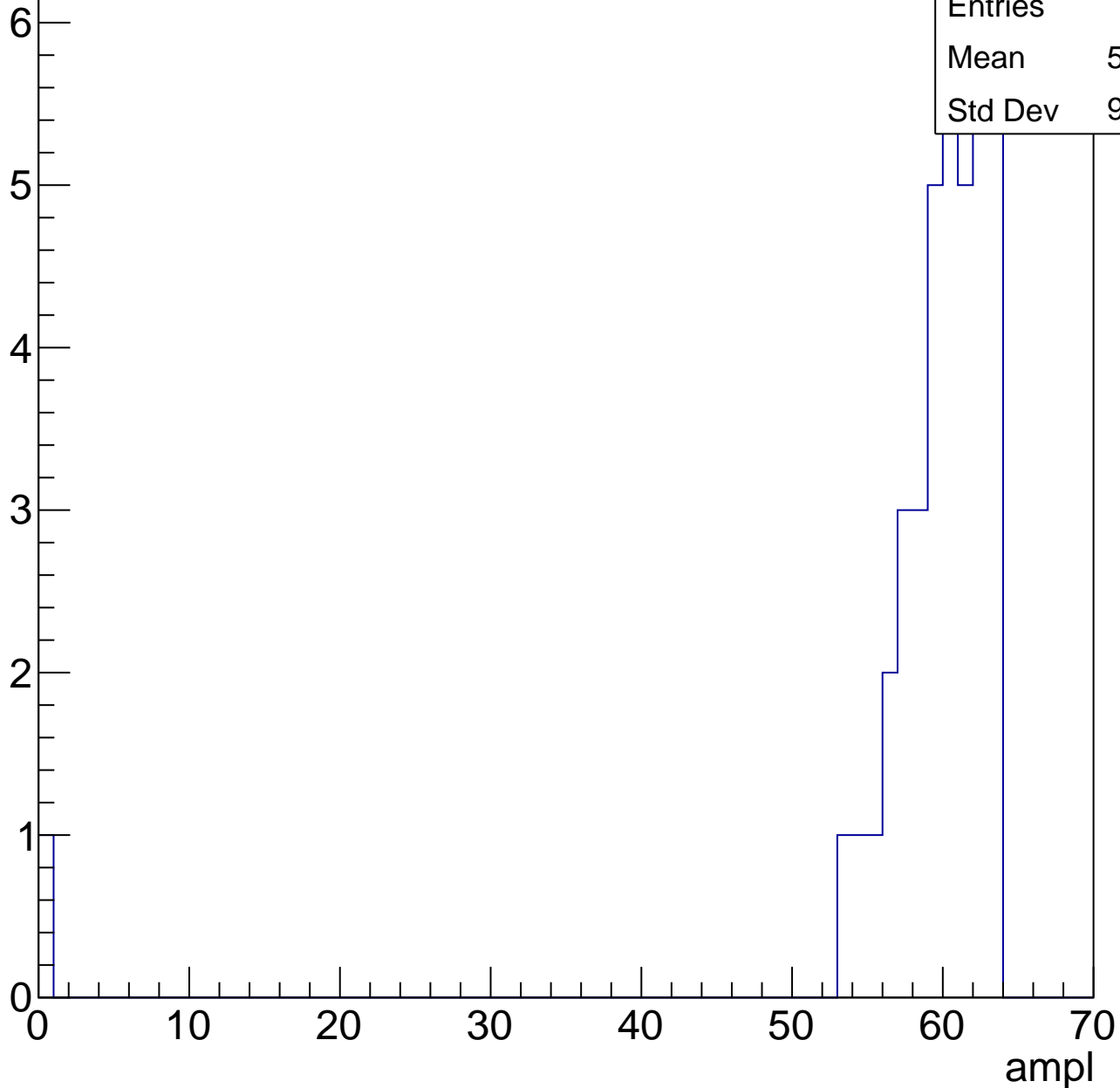
Entries	64
Mean	56.61
Std Dev	3.912



B1L103S, U24-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch92, adc0

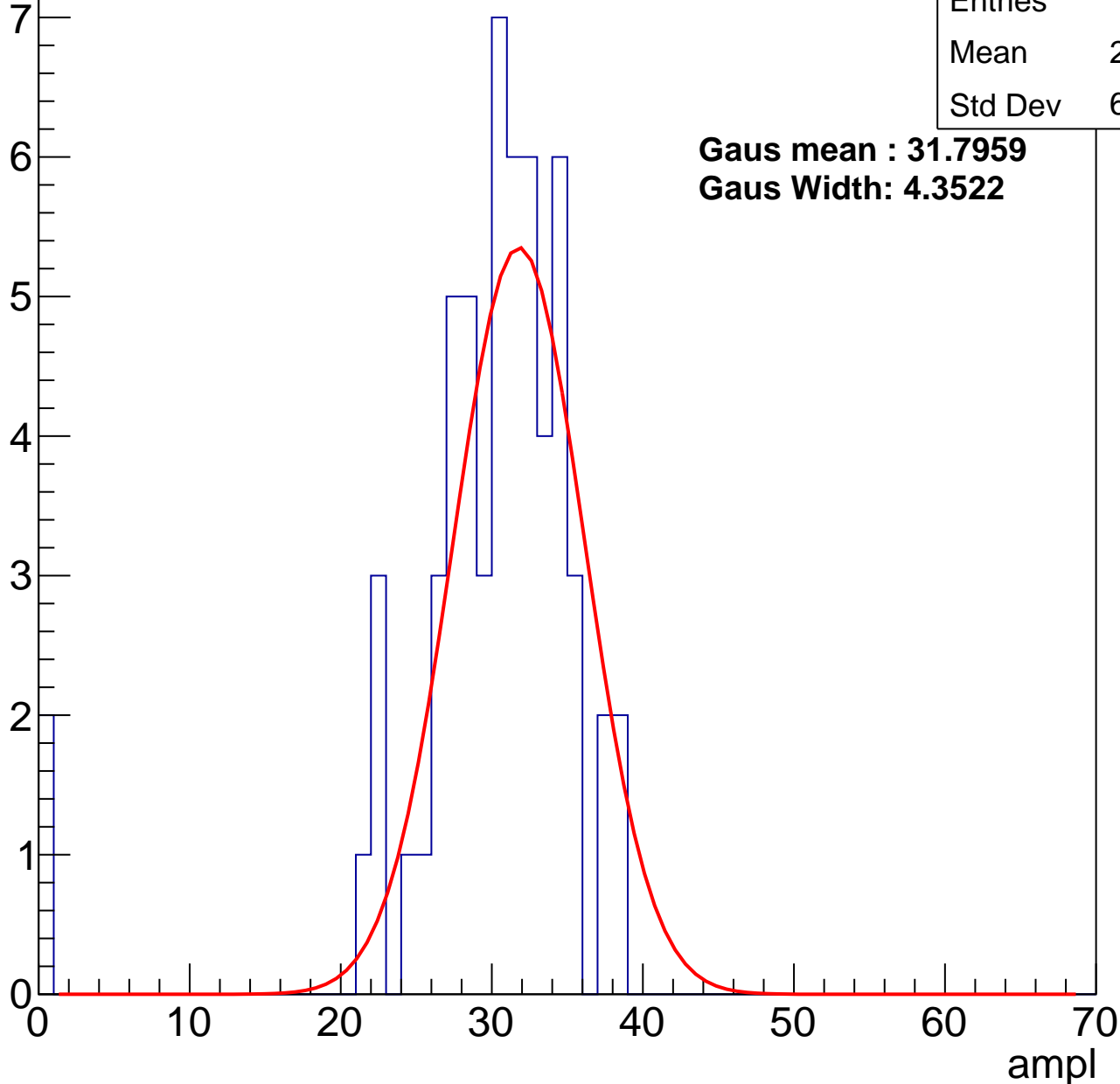
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	29.25
Std Dev	6.682

Gaus mean : 31.7959

Gaus Width: 4.3522



B1L103S, U24-ch92, adc1

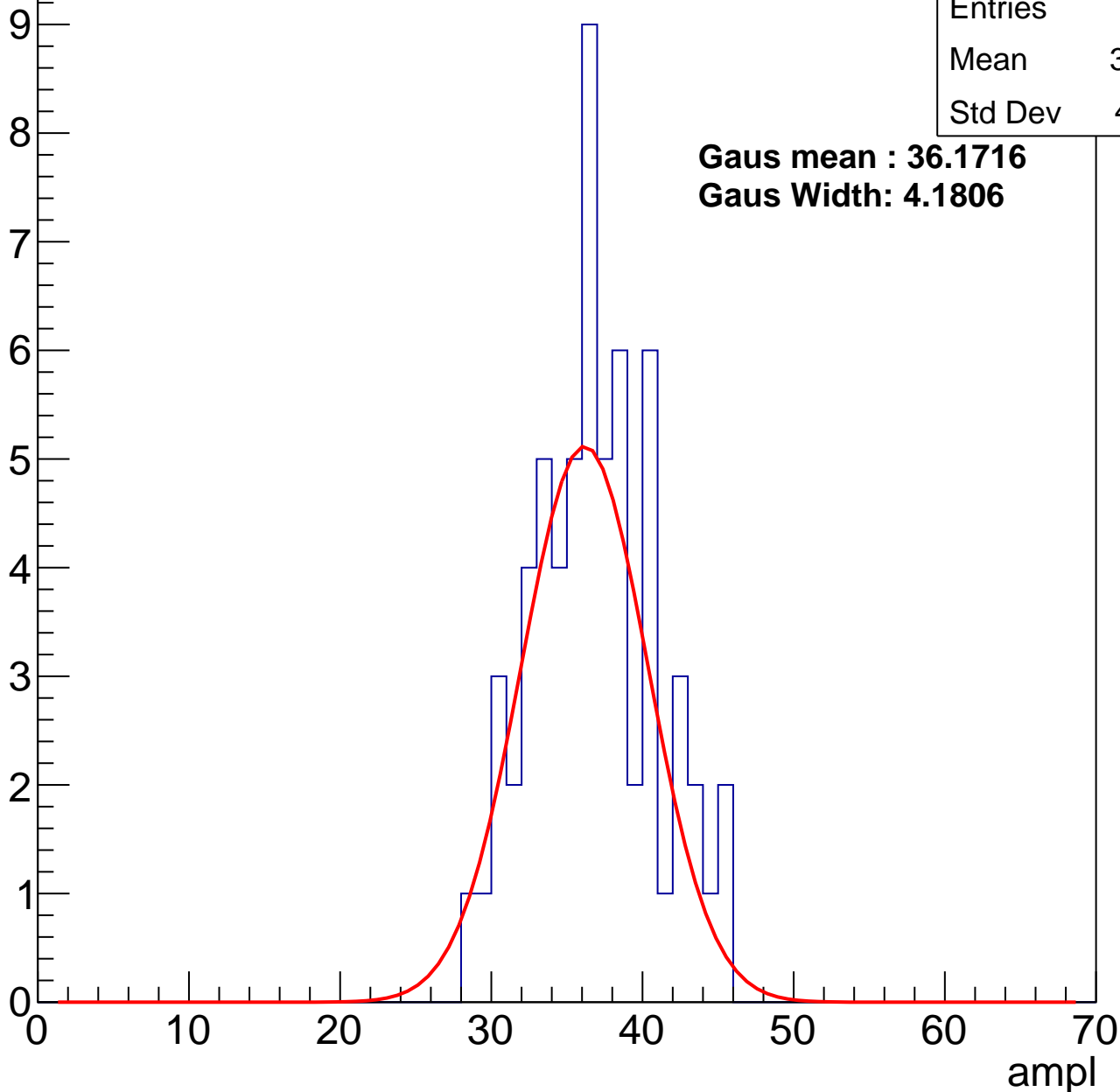
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.37
Std Dev	4.001

Gaus mean : 36.1716

Gaus Width: 4.1806



B1L103S, U24-ch92, adc2

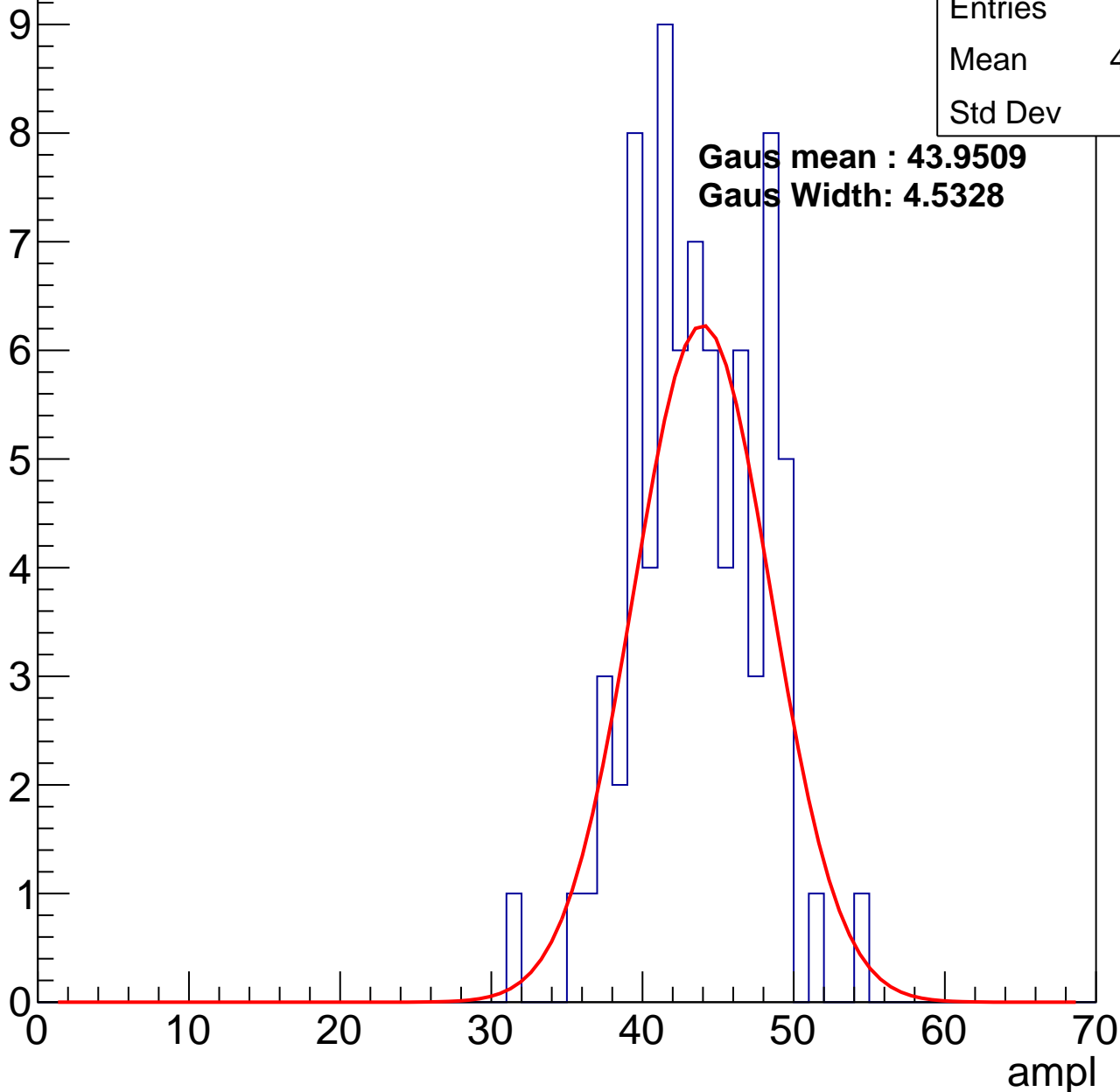
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.13
Std Dev	4.15

Gaus mean : 43.9509

Gaus Width: 4.5328



B1L103S, U24-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

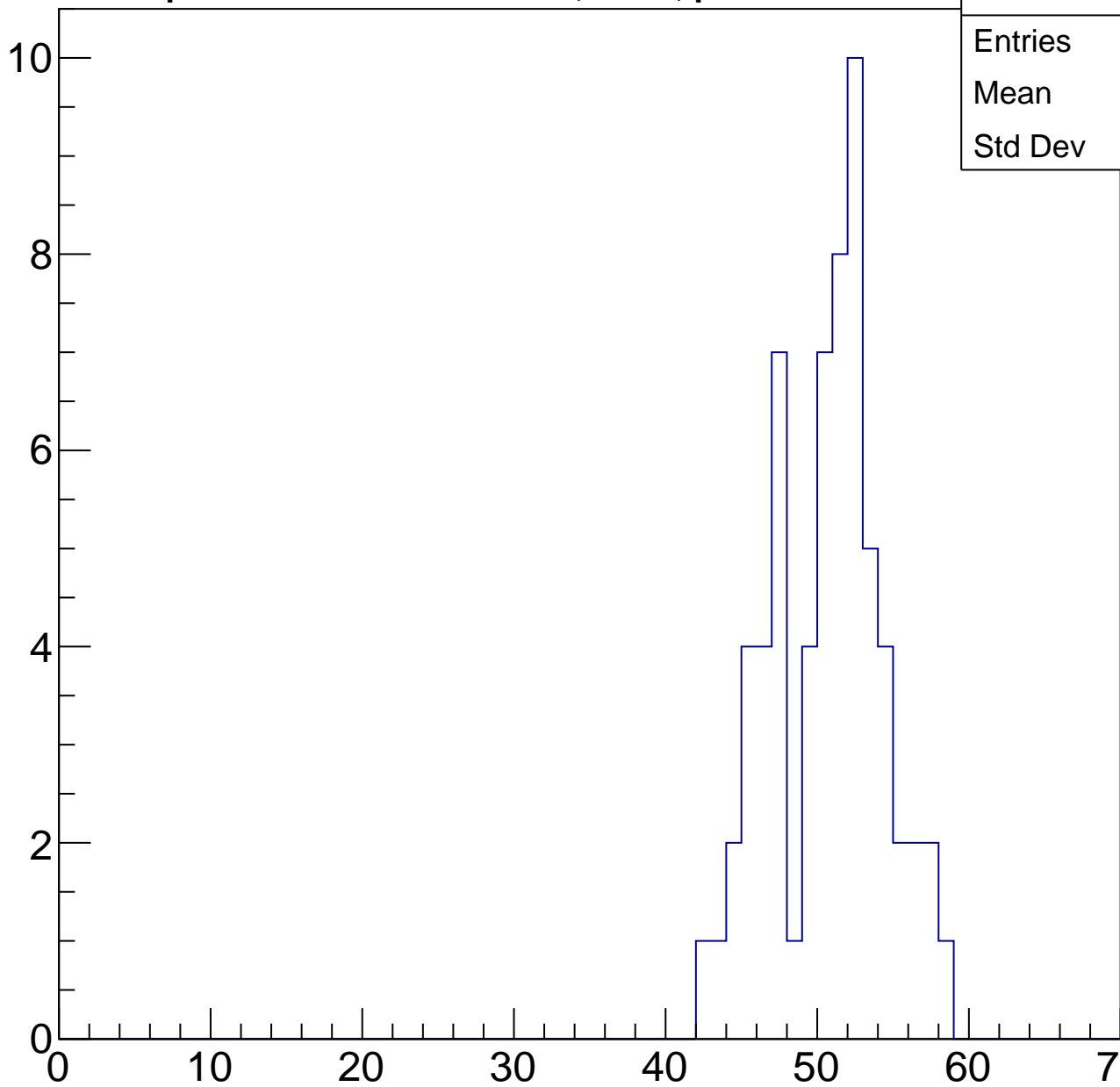
Entries	65
Mean	50.2
Std Dev	3.634

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

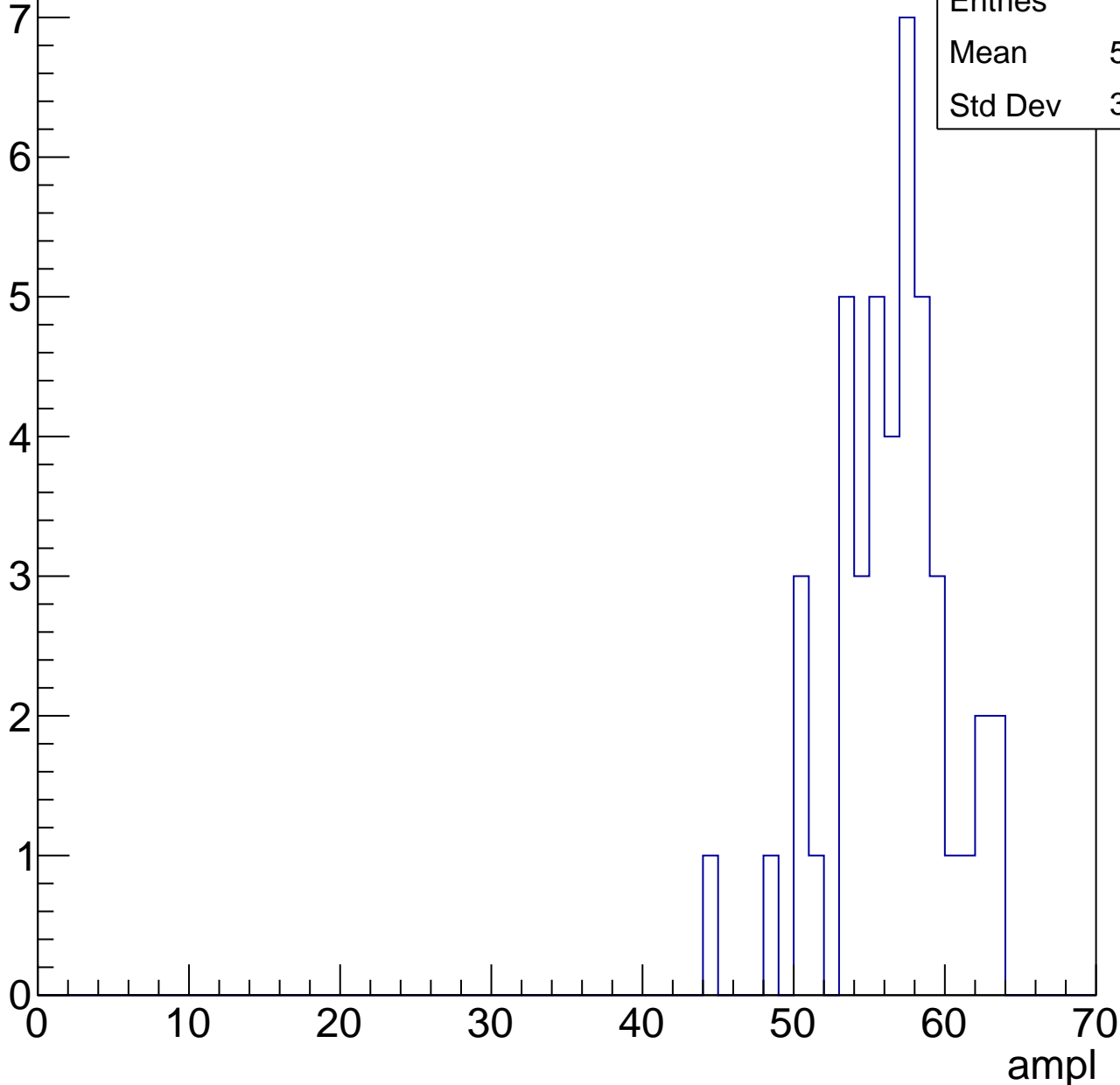


B1L103S, U24-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.82
Std Dev	3.892

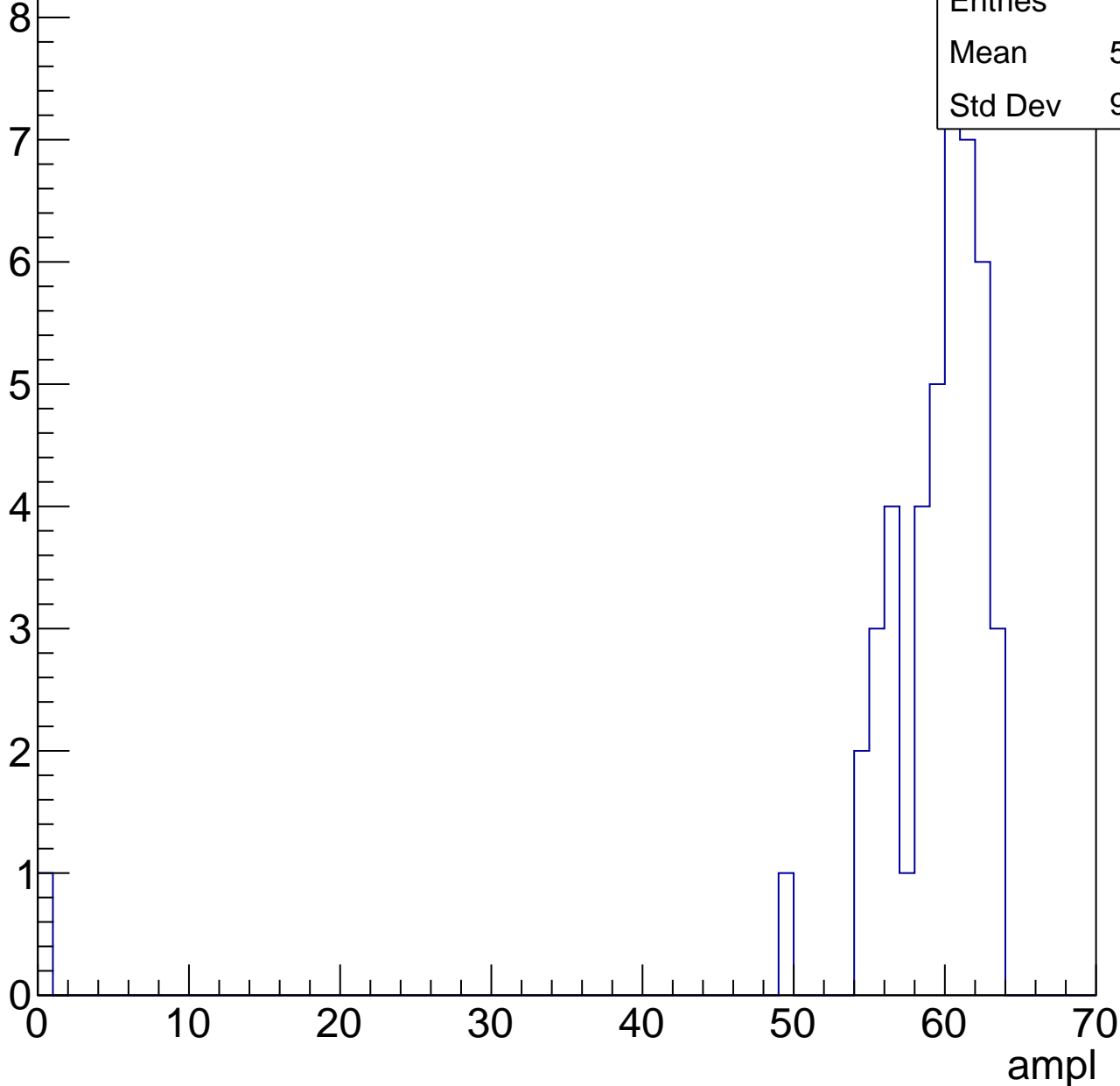


B1L103S, U24-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	57.73
Std Dev	9.176

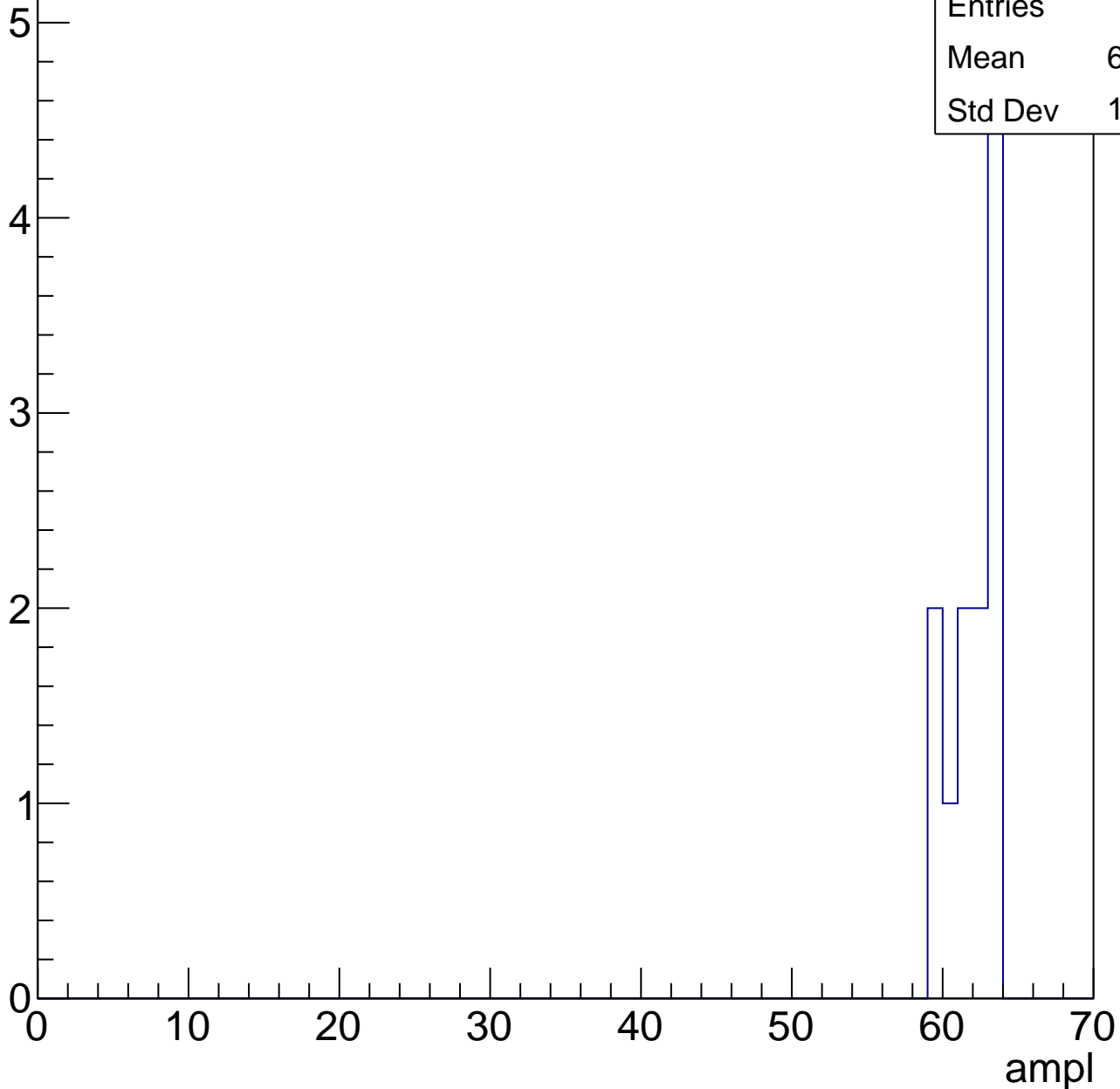


B1L103S, U24-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.498



B1L103S, U24-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch93, adc0

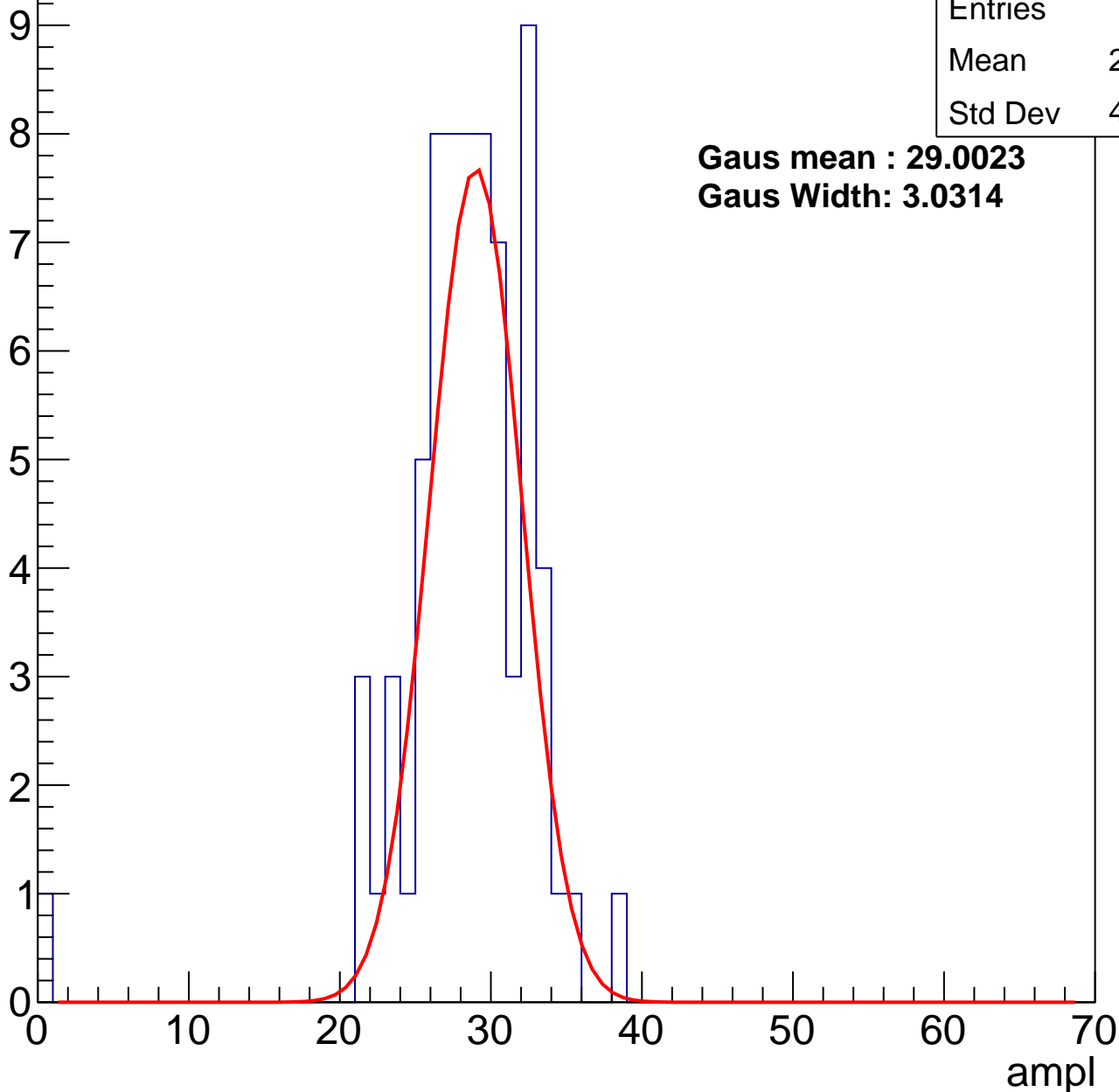
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.96
Std Dev	4.765

Gaus mean : 29.0023

Gaus Width: 3.0314



B1L103S, U24-ch93, adc1

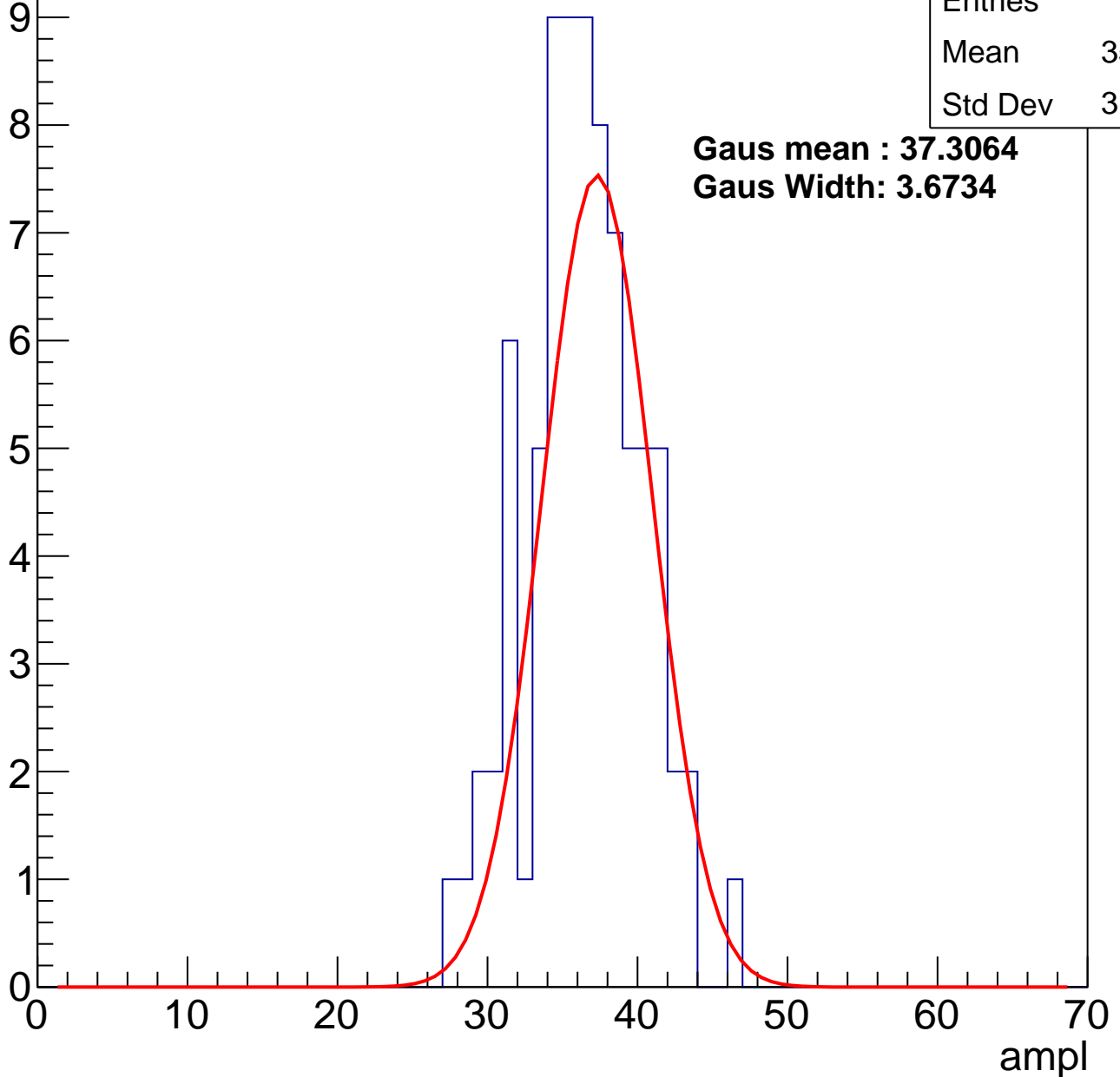
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.99
Std Dev	3.753

Gaus mean : 37.3064

Gaus Width: 3.6734



B1L103S, U24-ch93, adc2

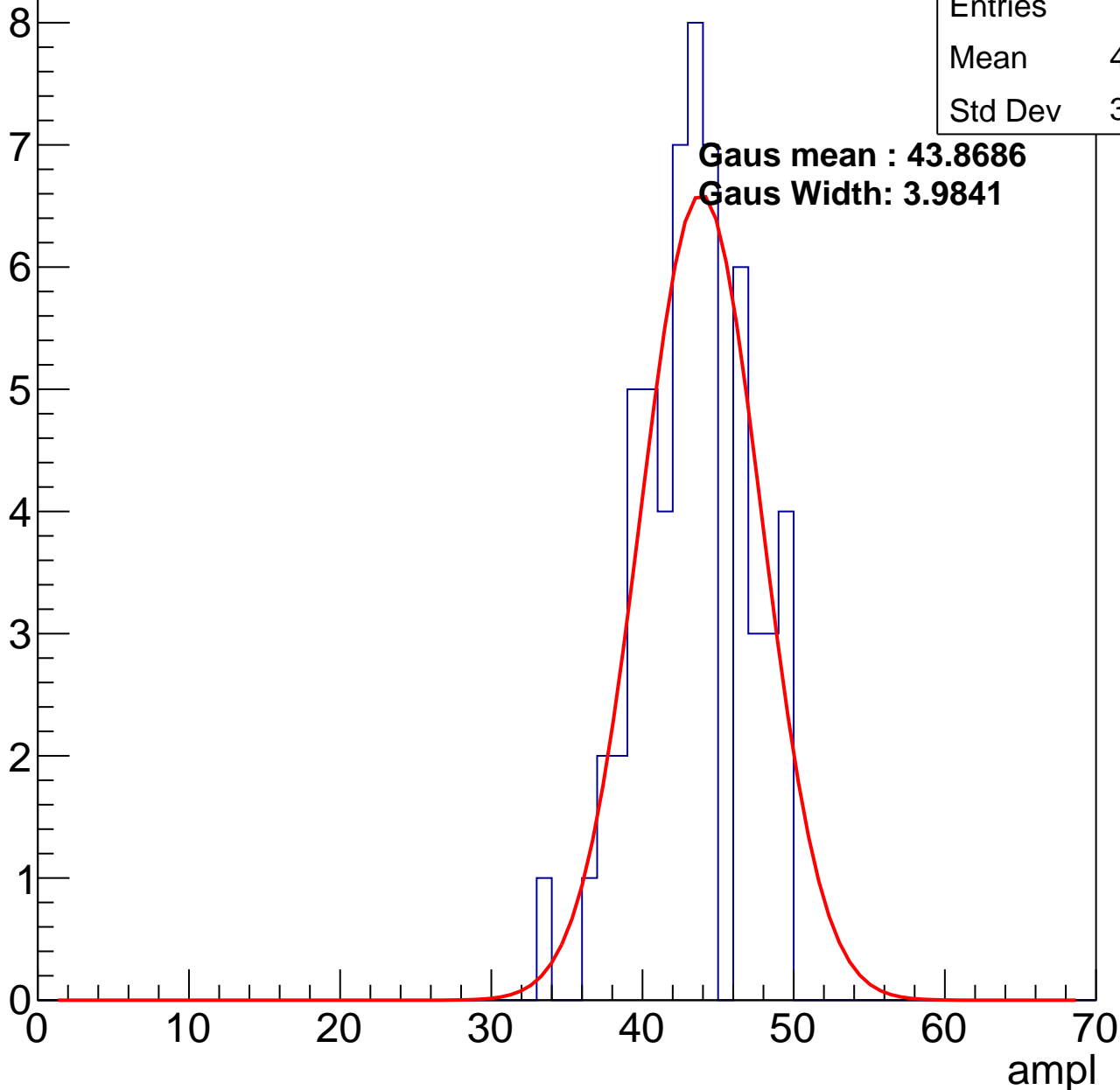
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.78
Std Dev	3.582

Gaus mean : 43.8686

Gaus Width: 3.9841

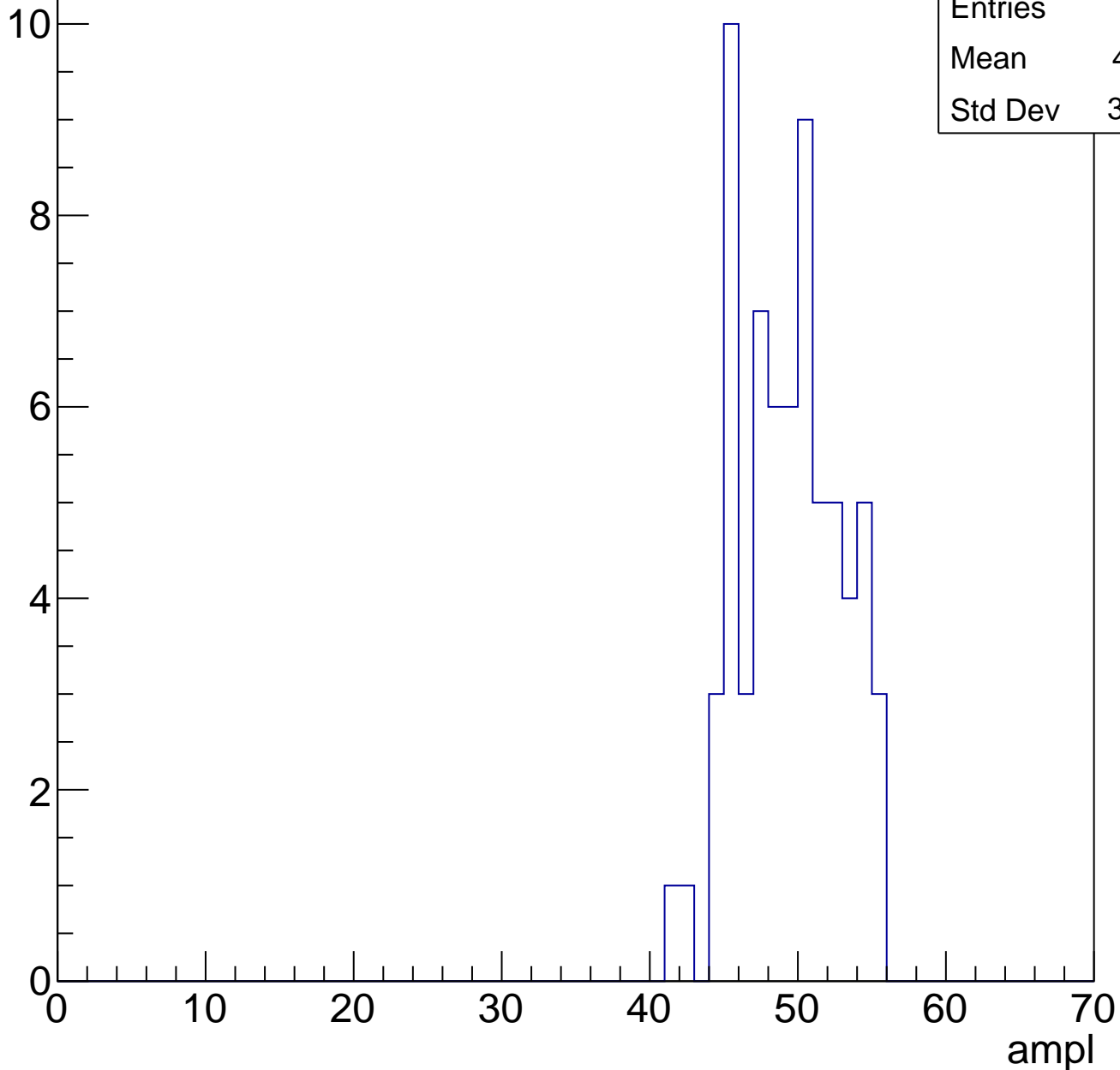


B1L103S, U24-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

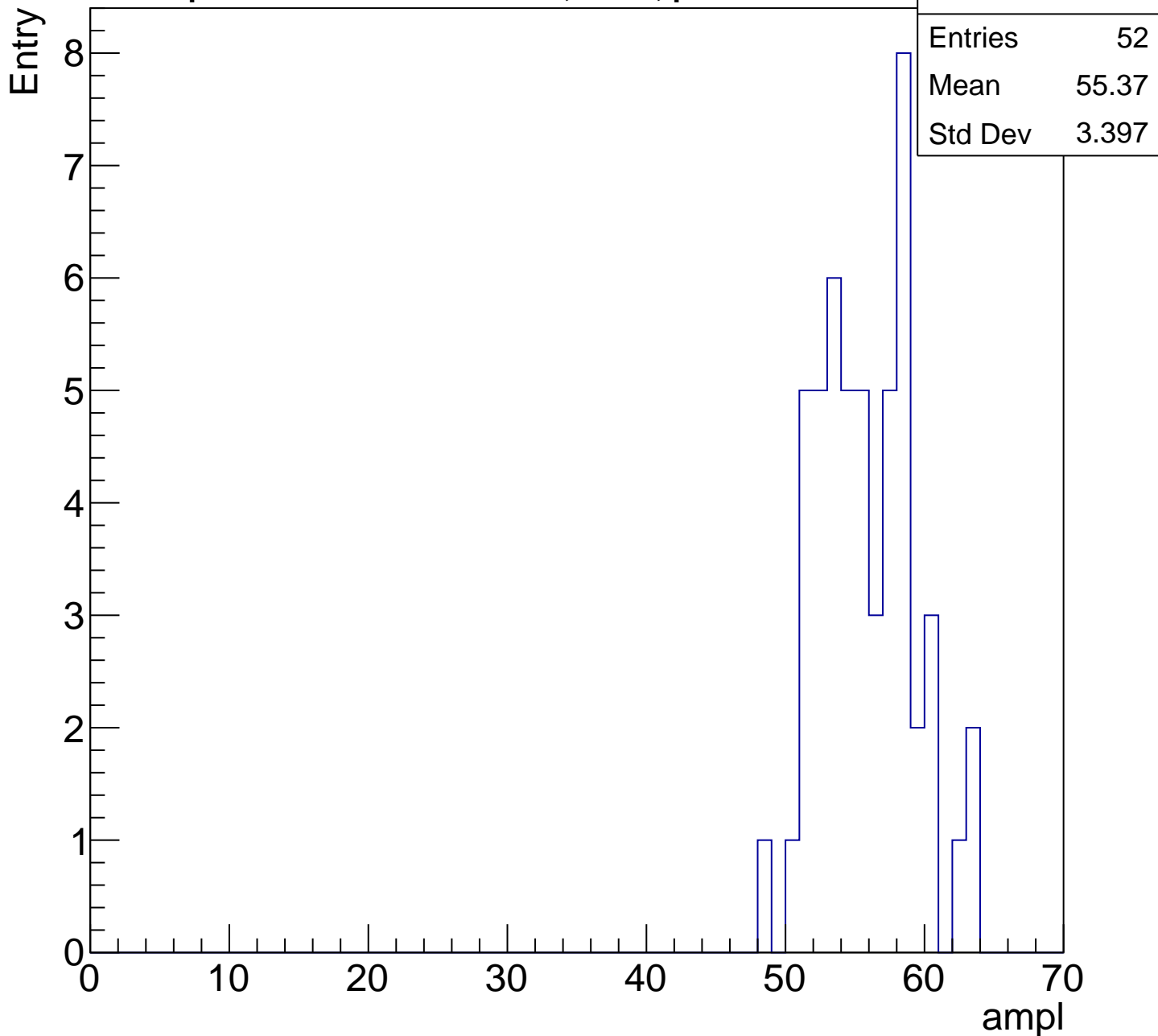
Entries	68
Mean	48.91
Std Dev	3.386

Entry



B1L103S, U24-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U24-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	55
Mean	58.49
Std Dev	8.301

Entry

10

8

6

4

2

0

0

10

20

30

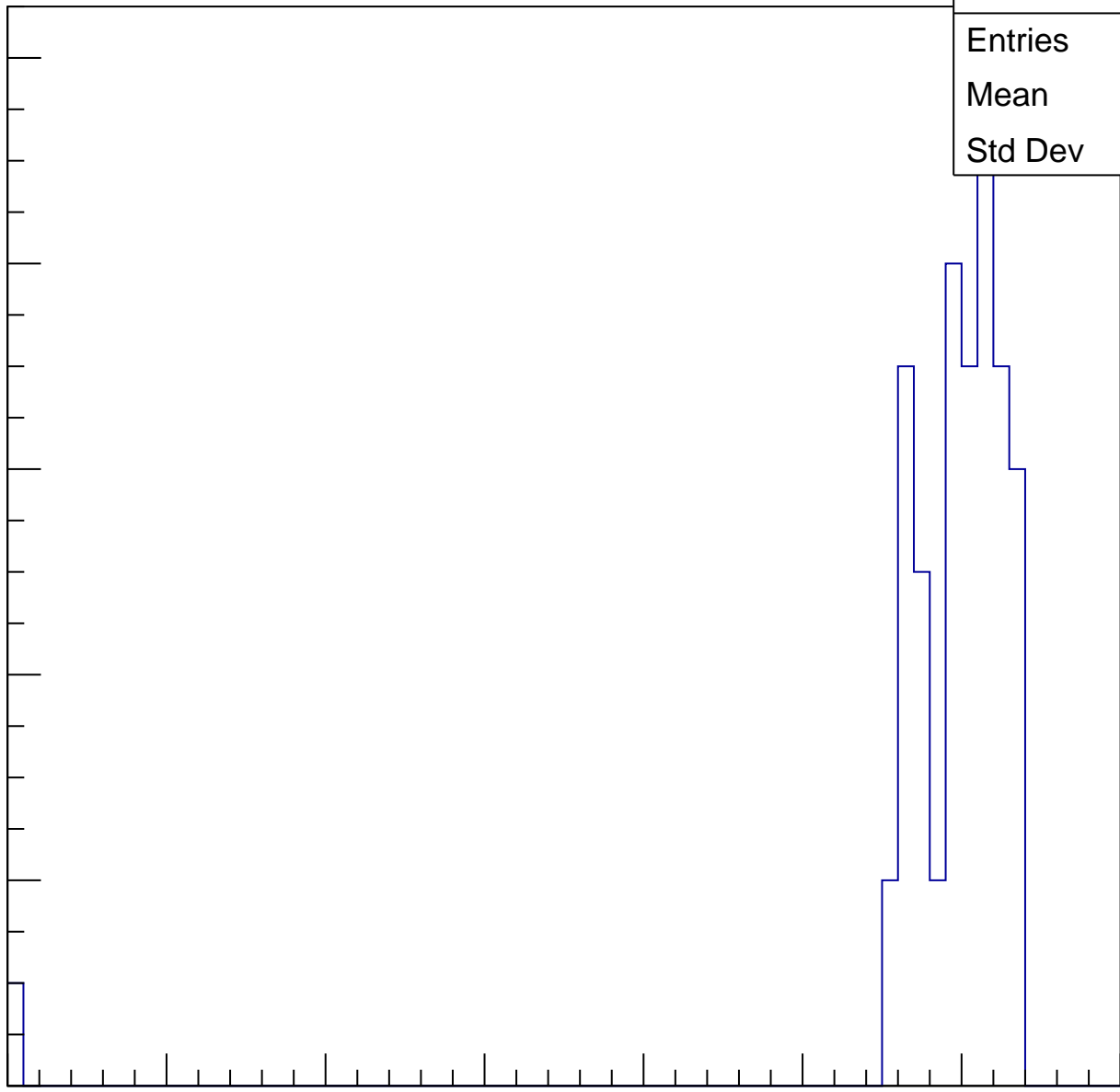
40

50

60

70

ampl



B1L103S, U24-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch94, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	29.53
Std Dev	3.666

Gaus mean : 30.3457

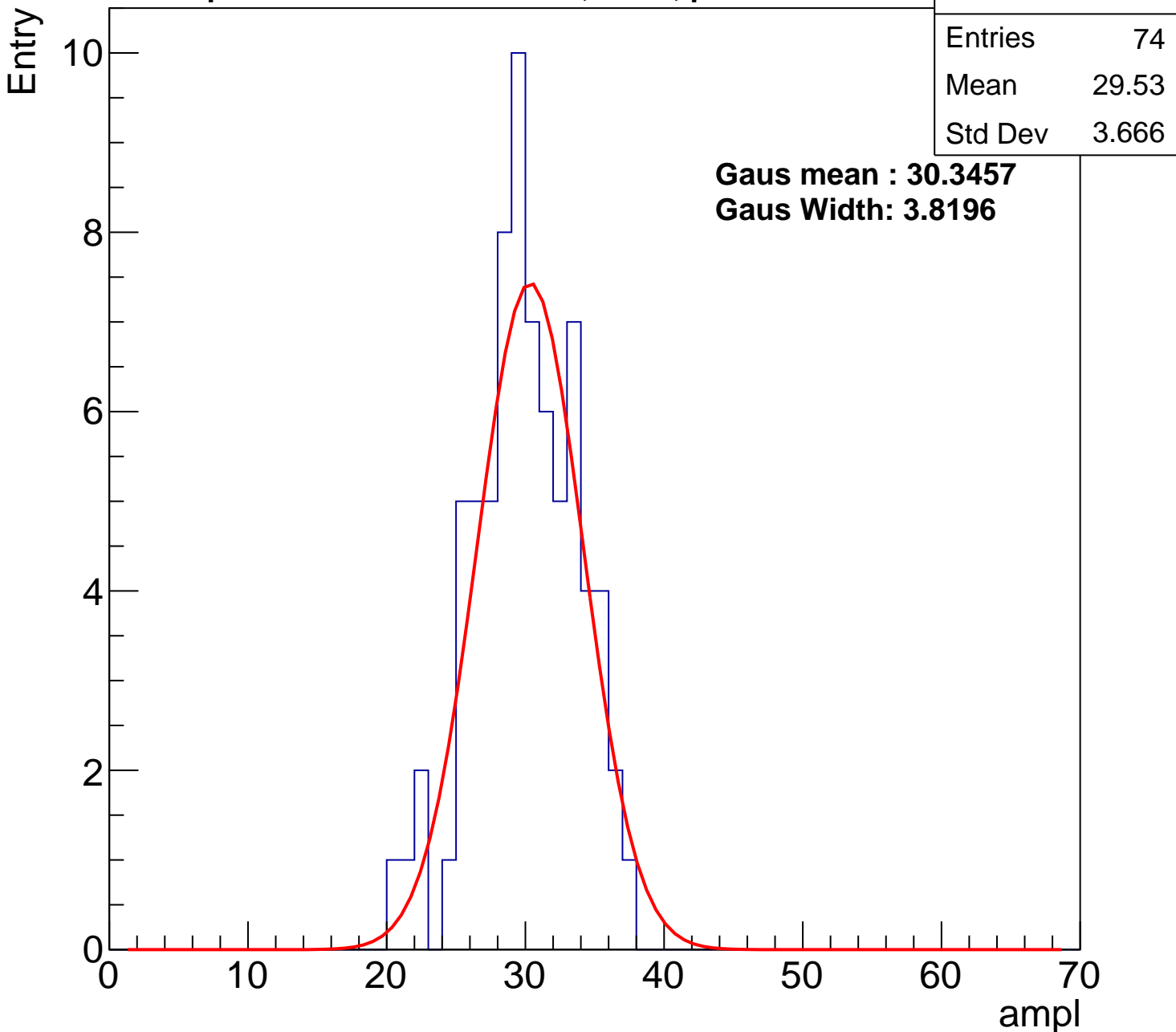
Gaus Width: 3.8196

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch94, adc1

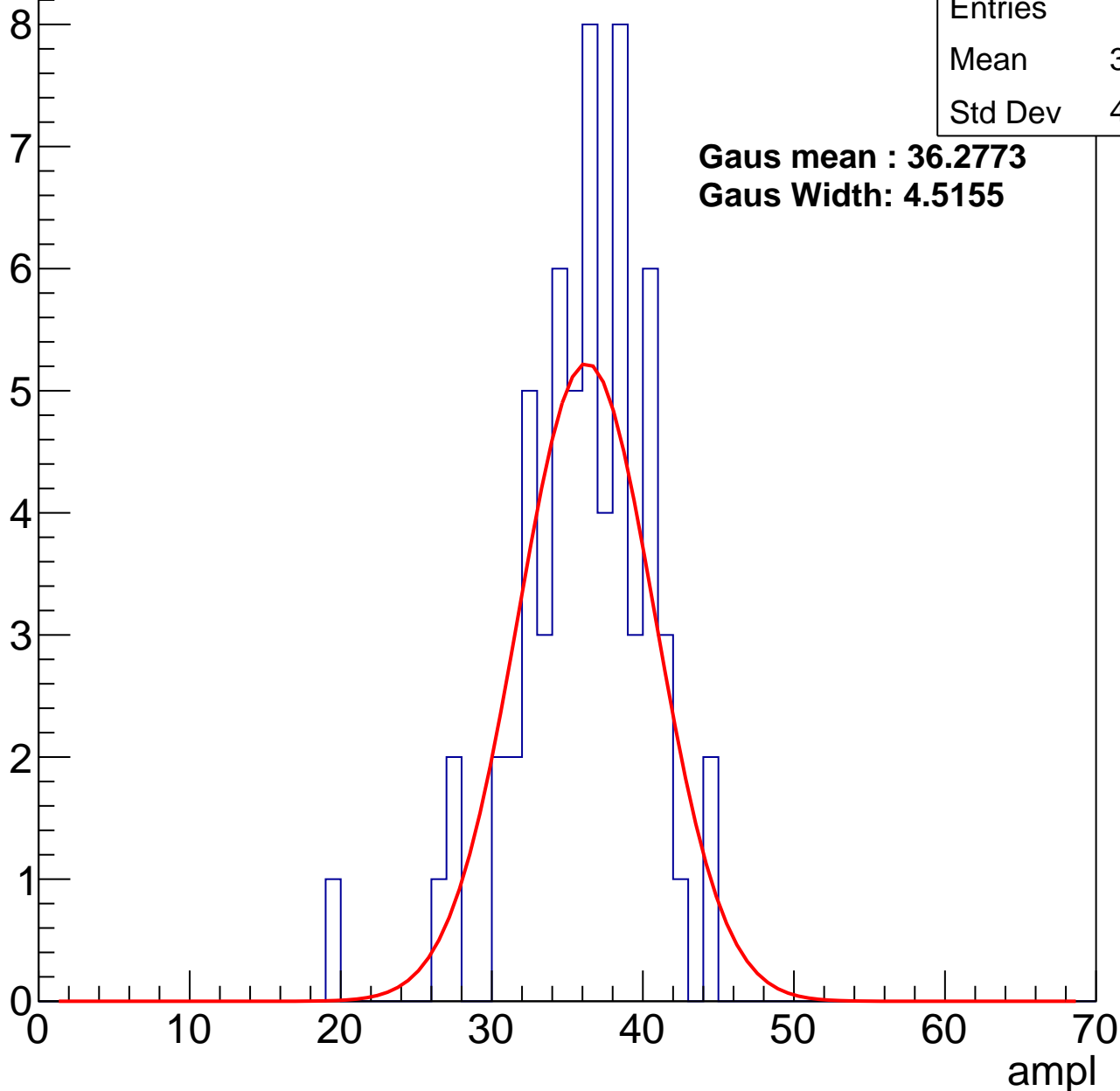
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.63
Std Dev	4.397

Gaus mean : 36.2773

Gaus Width: 4.5155



B1L103S, U24-ch94, adc2

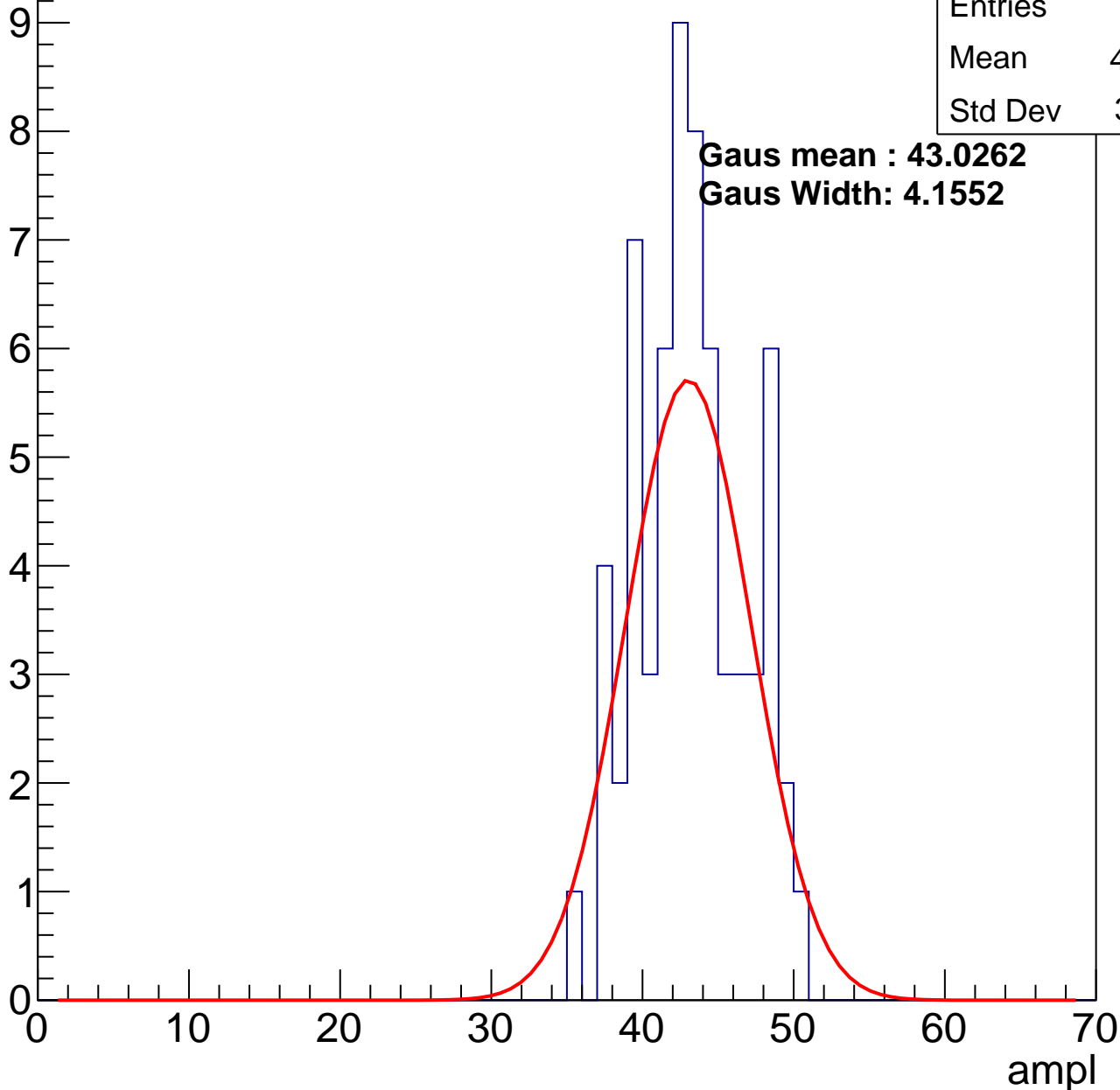
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.72
Std Dev	3.511

Gaus mean : 43.0262

Gaus Width: 4.1552

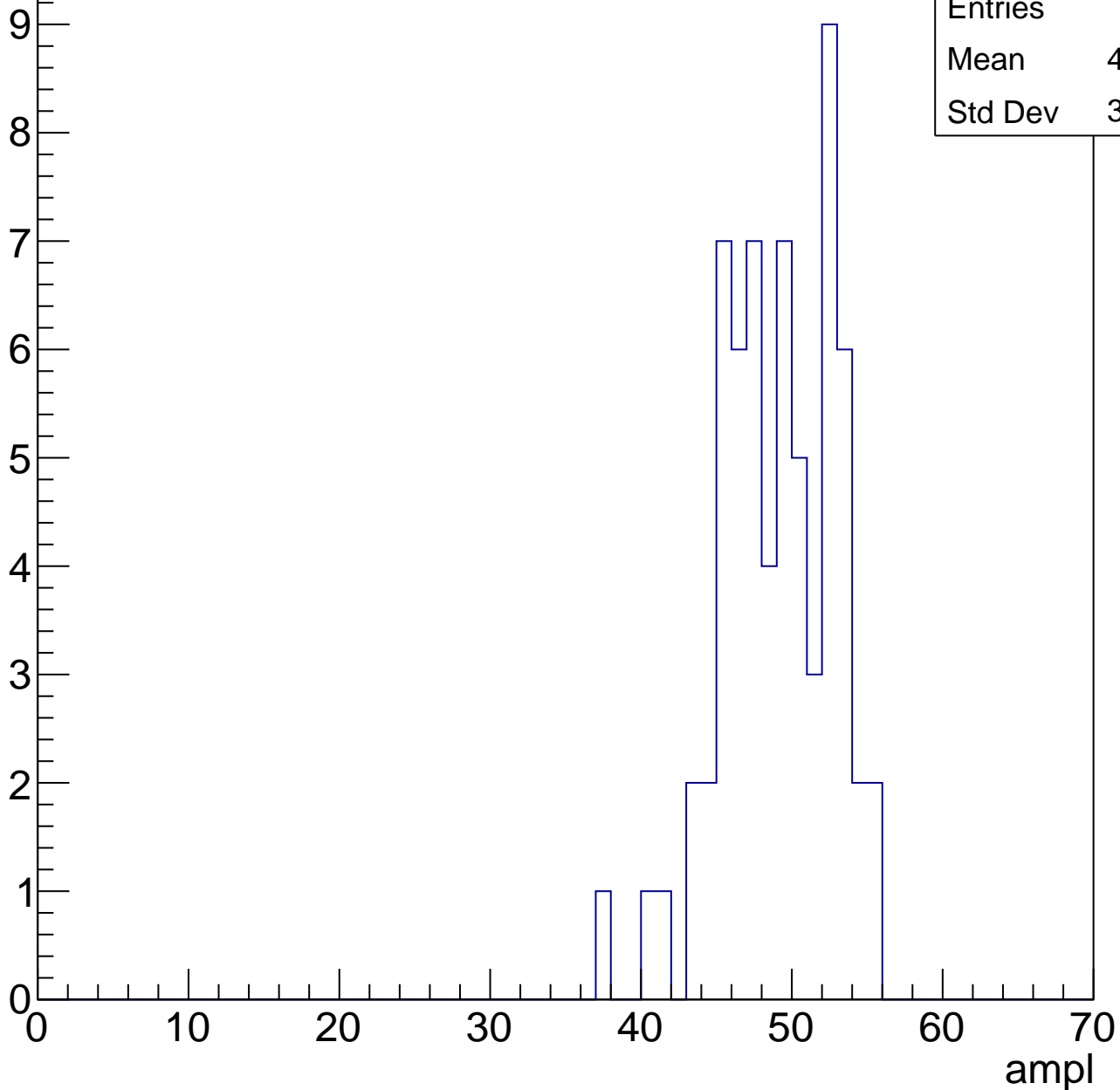


B1L103S, U24-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

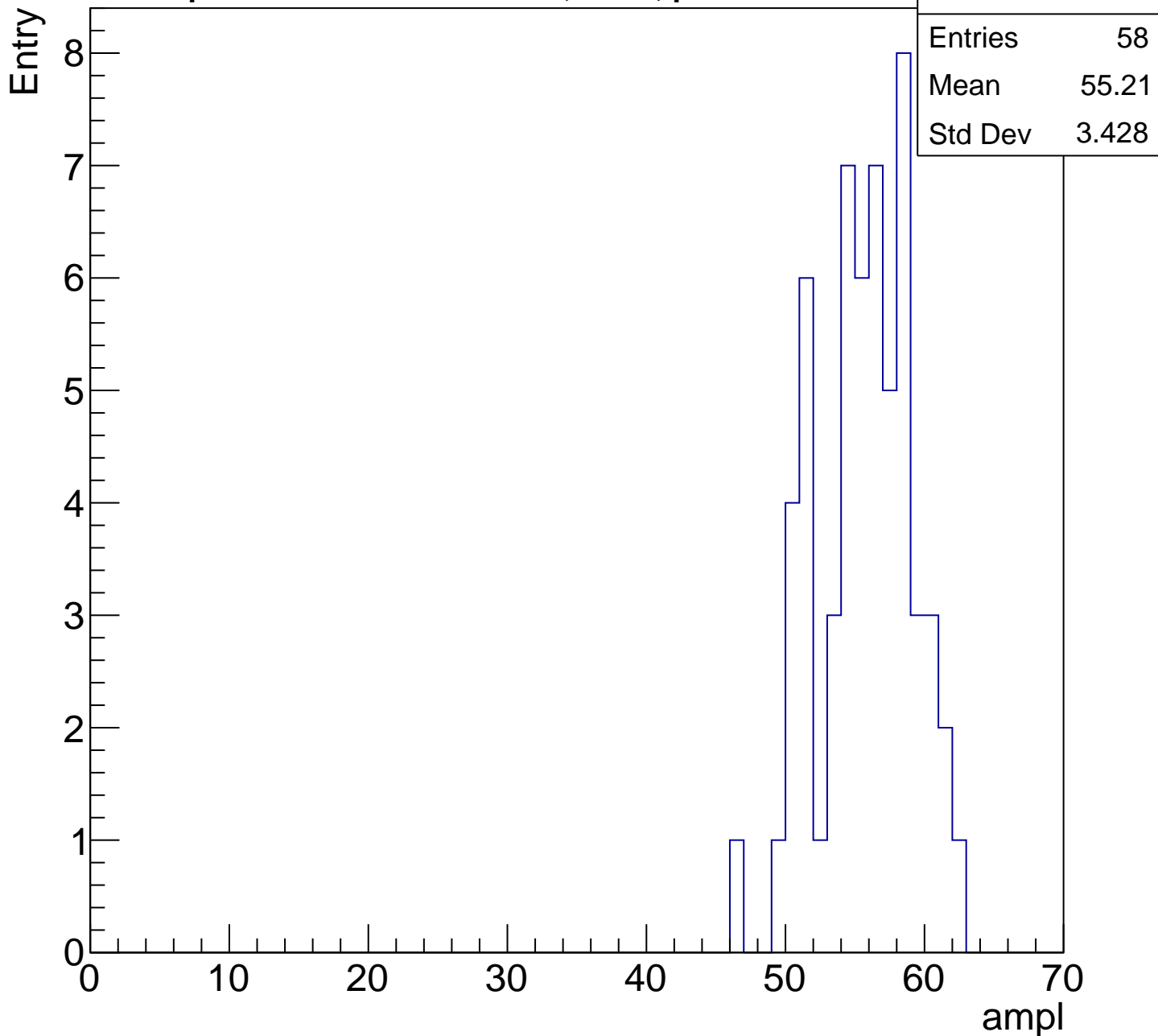
Entry

Entries	65
Mean	48.52
Std Dev	3.742



B1L103S, U24-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

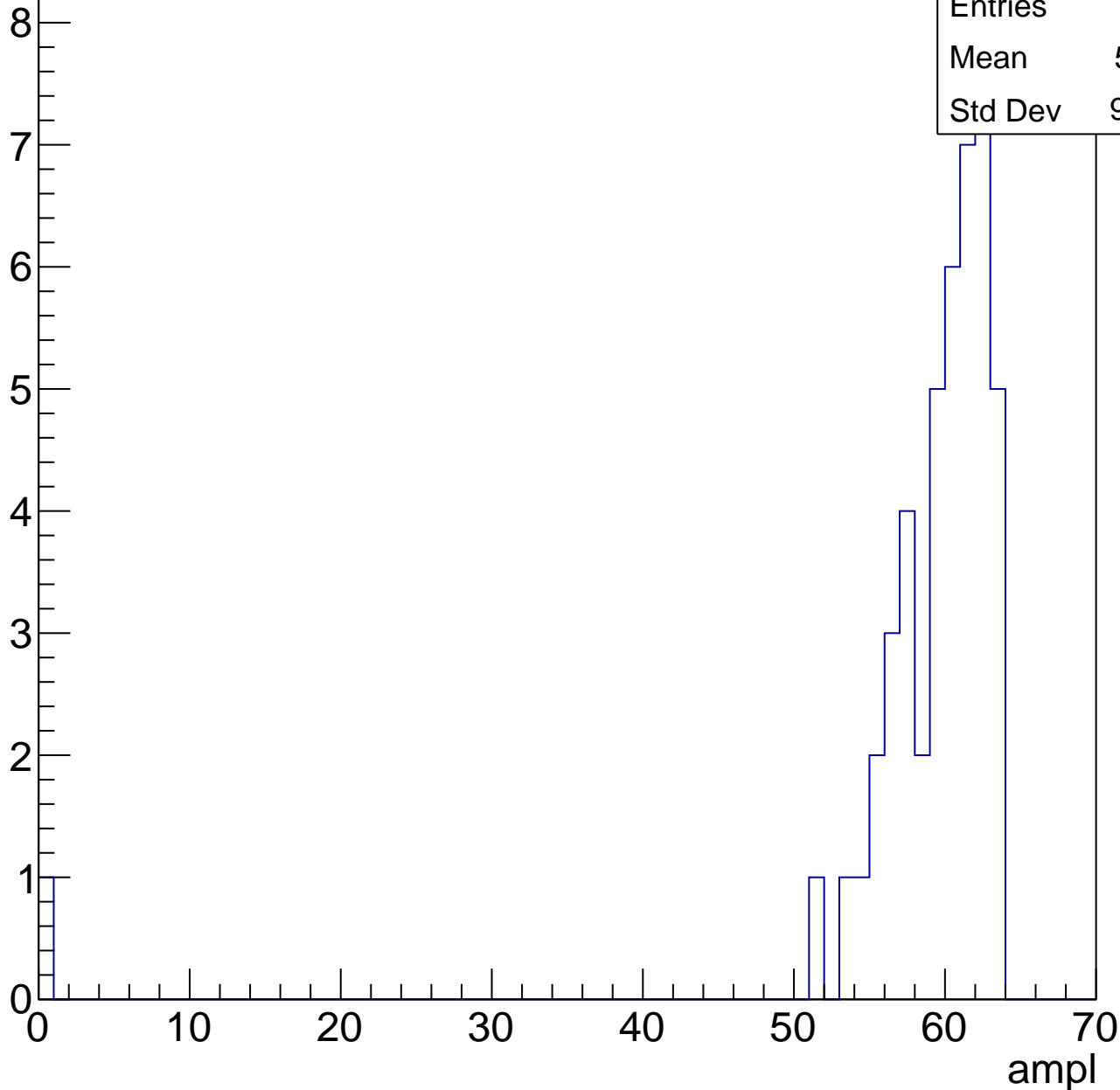


B1L103S, U24-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

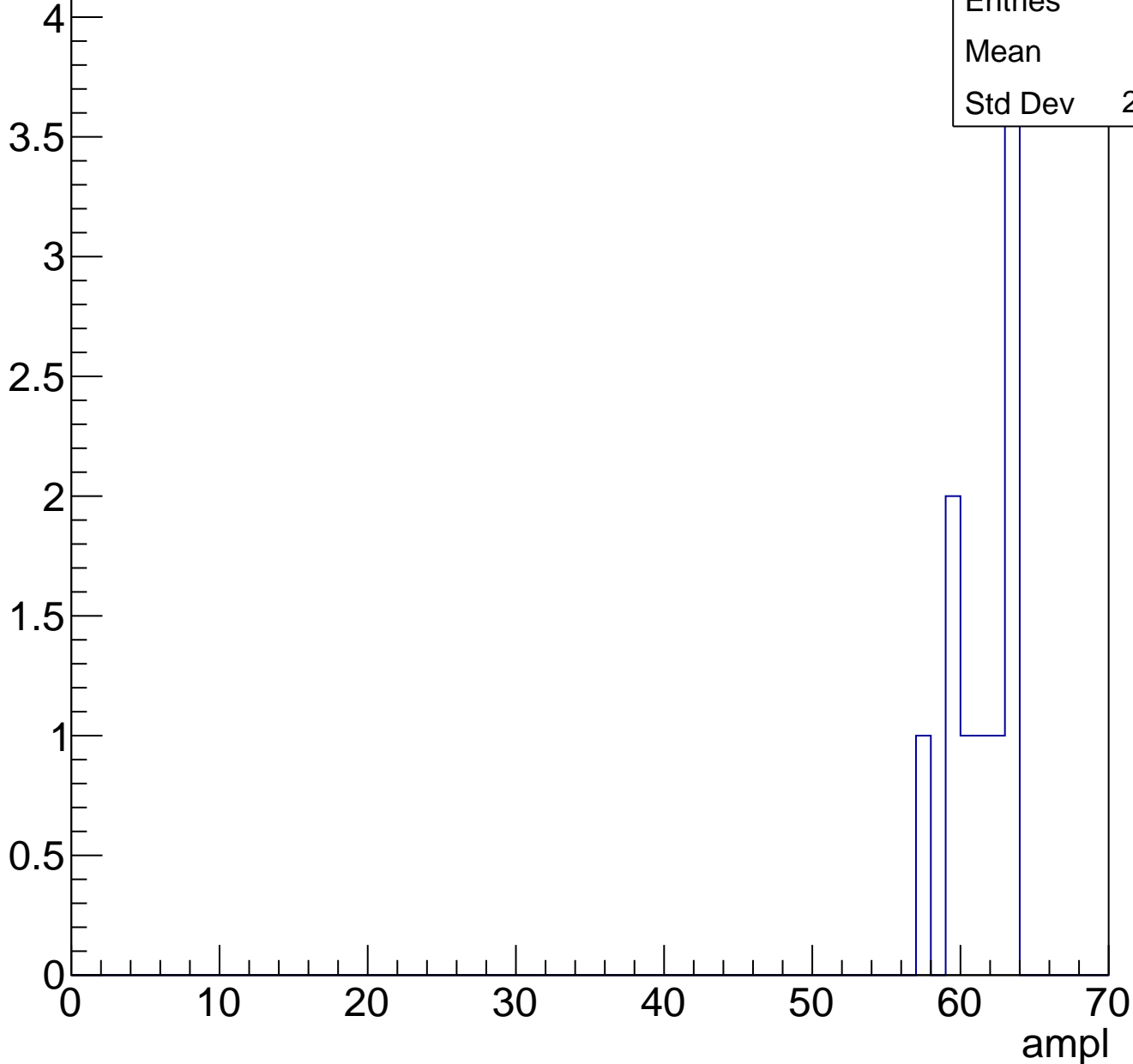
Entries	46
Mean	58.11
Std Dev	9.128



B1L103S, U24-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U24-ch95, adc0

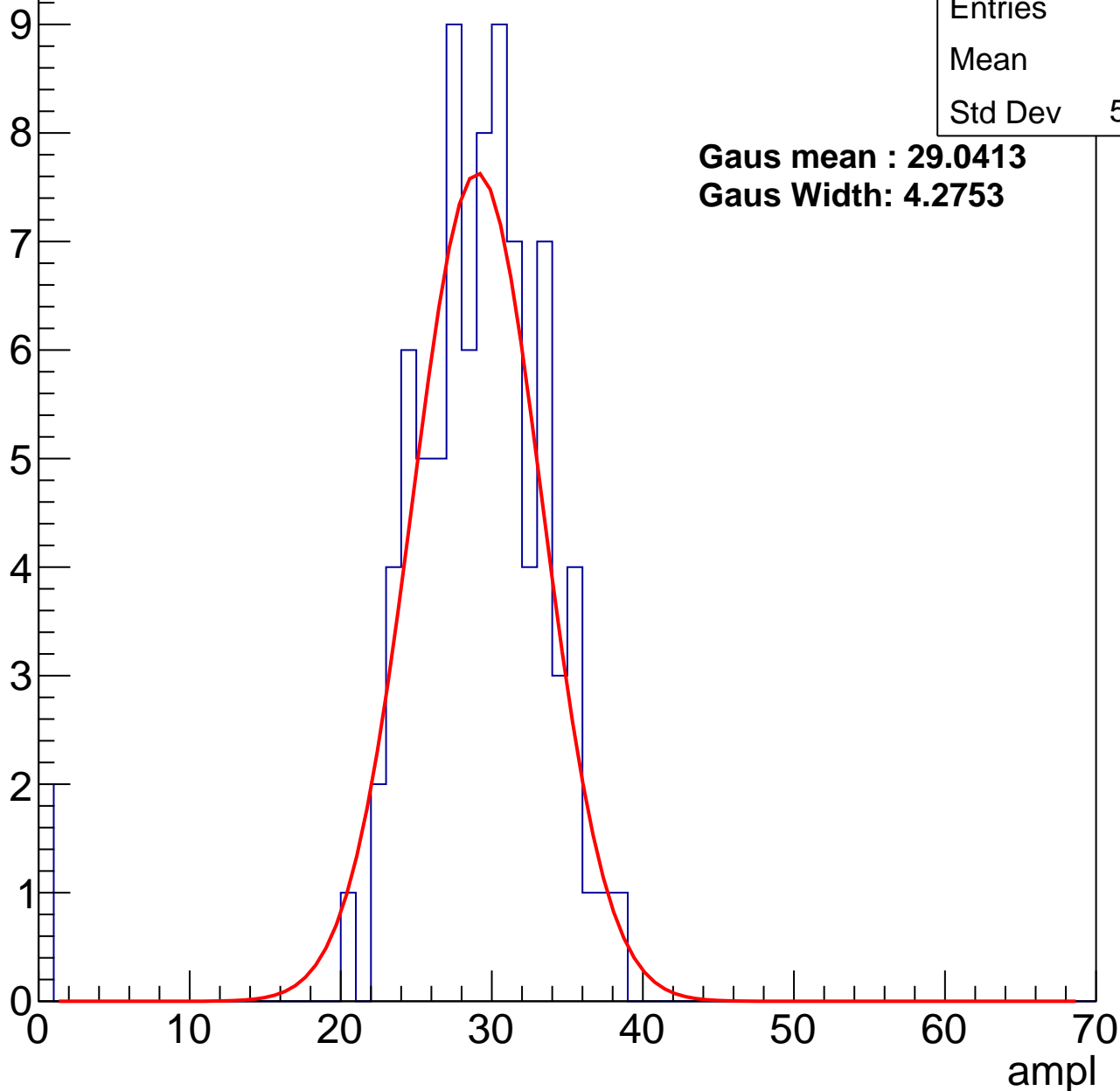
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	28.2
Std Dev	5.806

Gaus mean : 29.0413

Gaus Width: 4.2753



B1L103S, U24-ch95, adc1

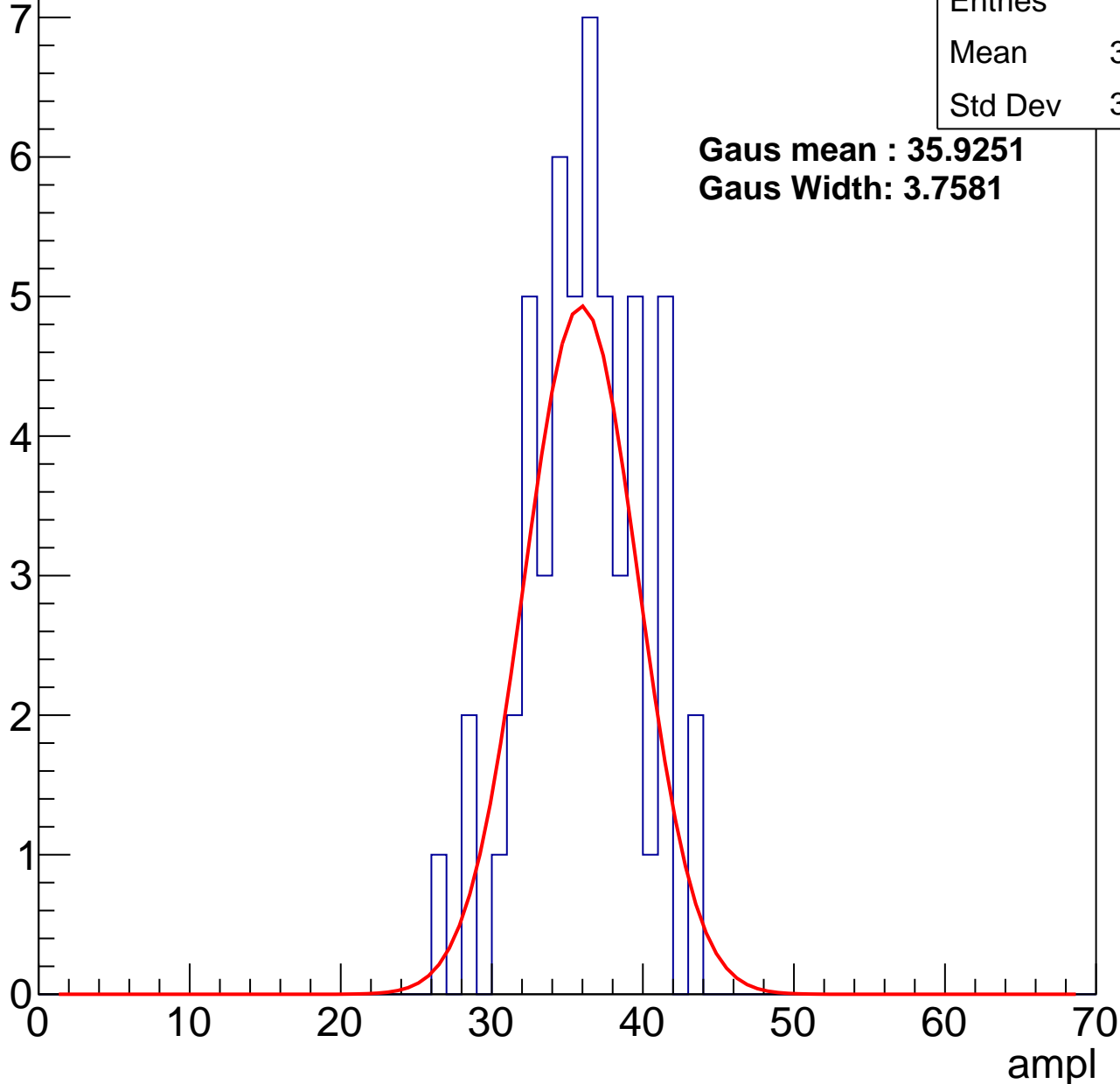
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	35.64
Std Dev	3.747

Gaus mean : 35.9251

Gaus Width: 3.7581



B1L103S, U24-ch95, adc2

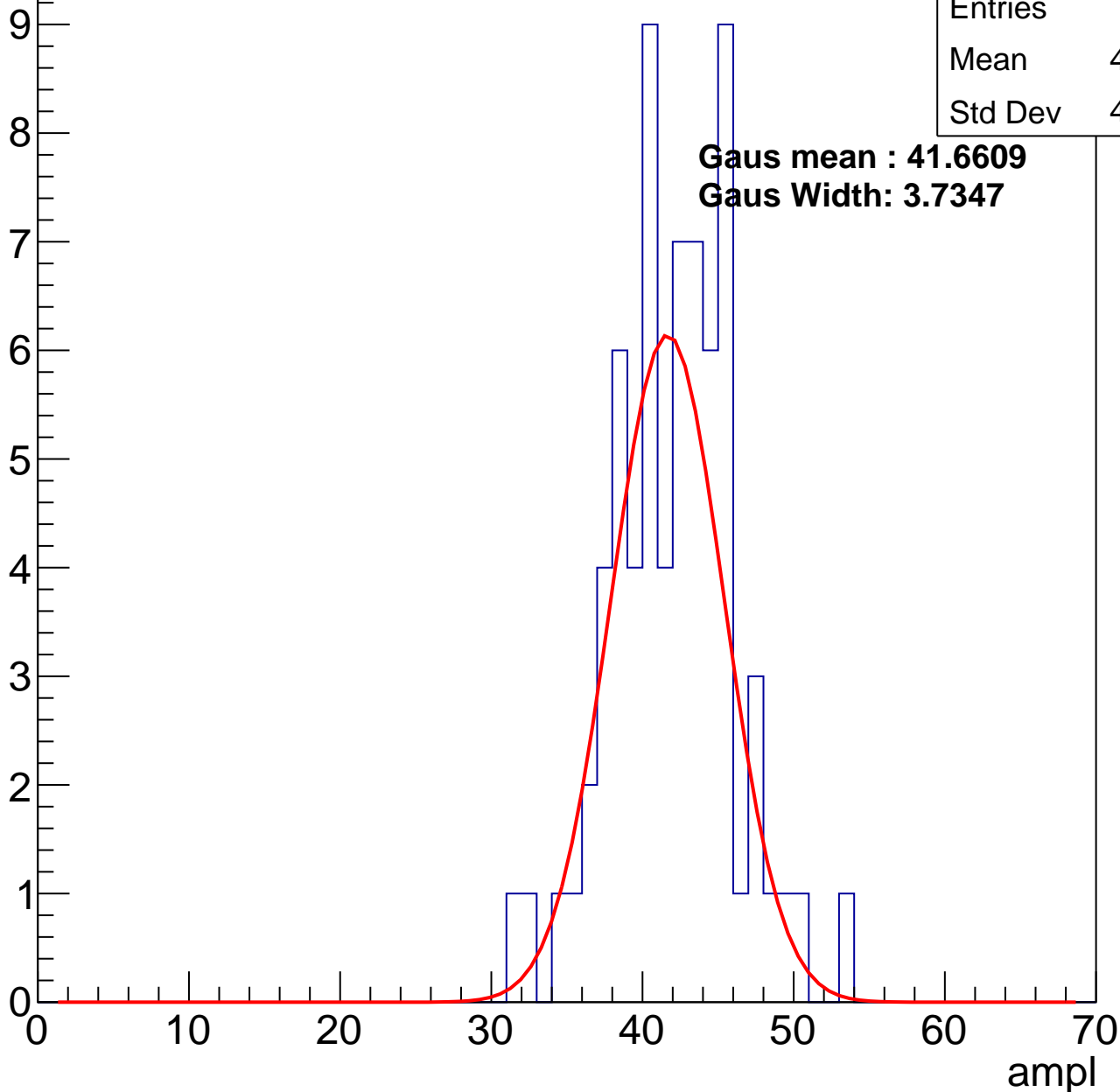
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.59
Std Dev	4.034

Gaus mean : 41.6609

Gaus Width: 3.7347

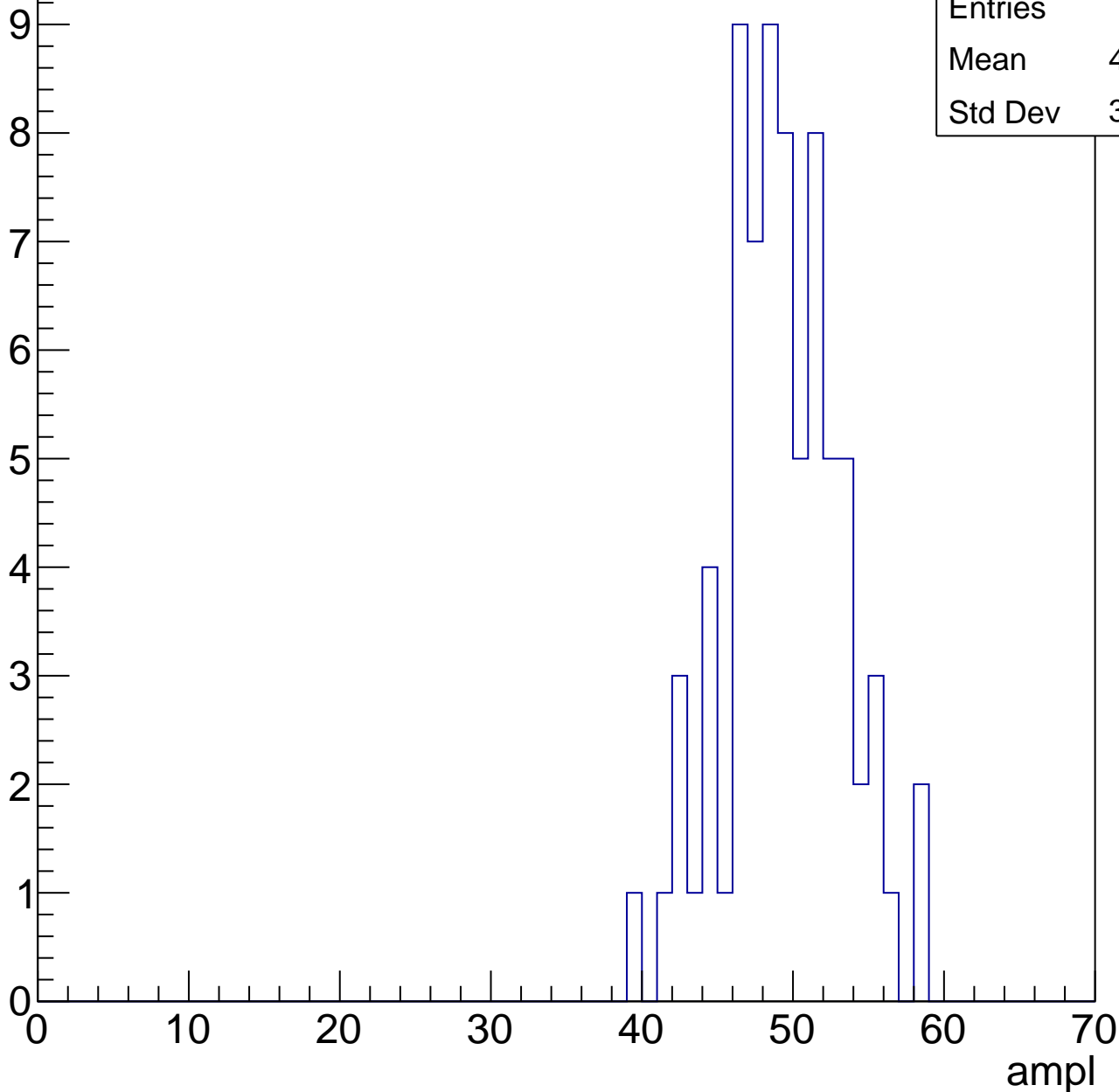


B1L103S, U24-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

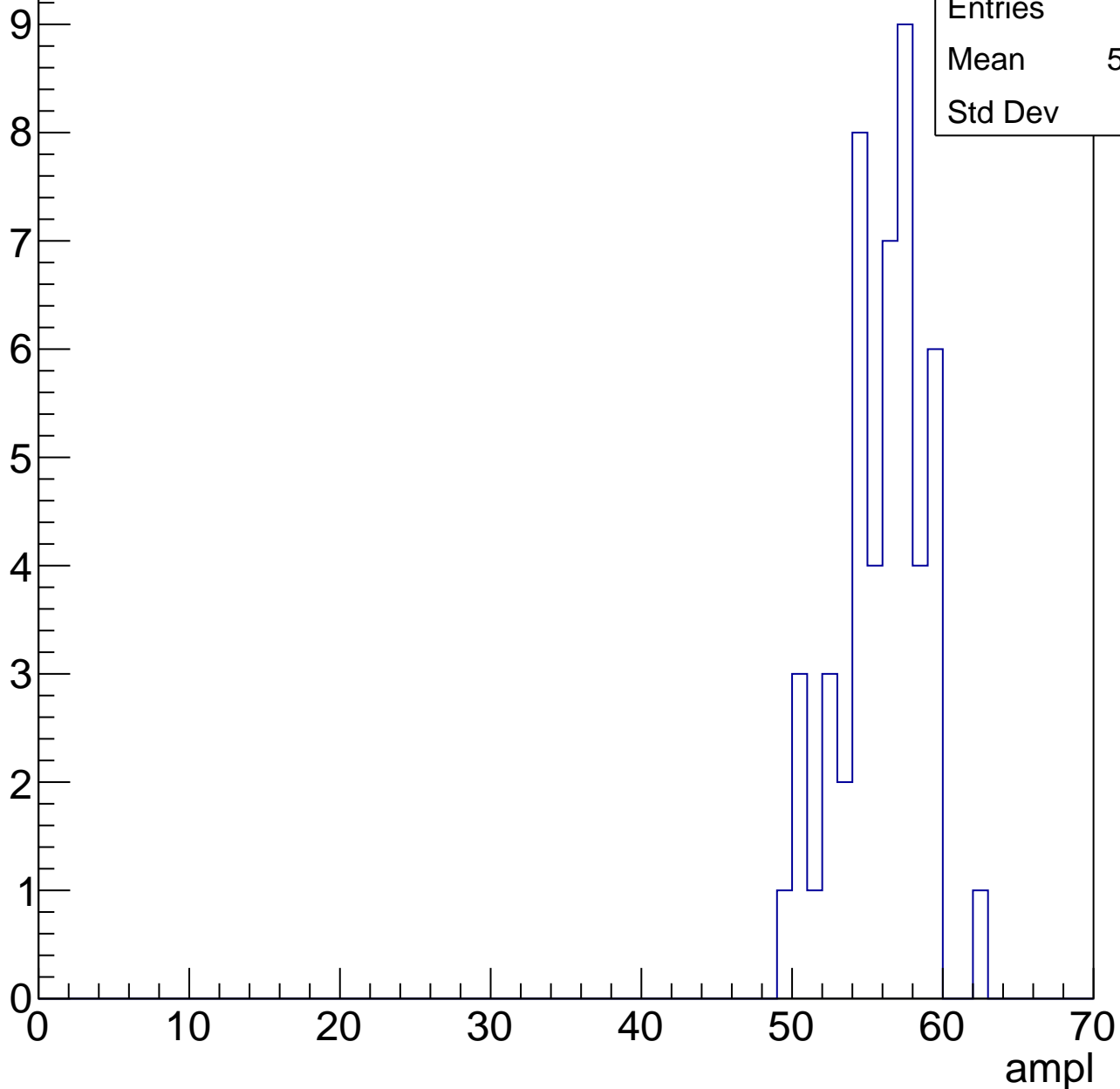
Entries	75
Mean	48.87
Std Dev	3.848



B1L103S, U24-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

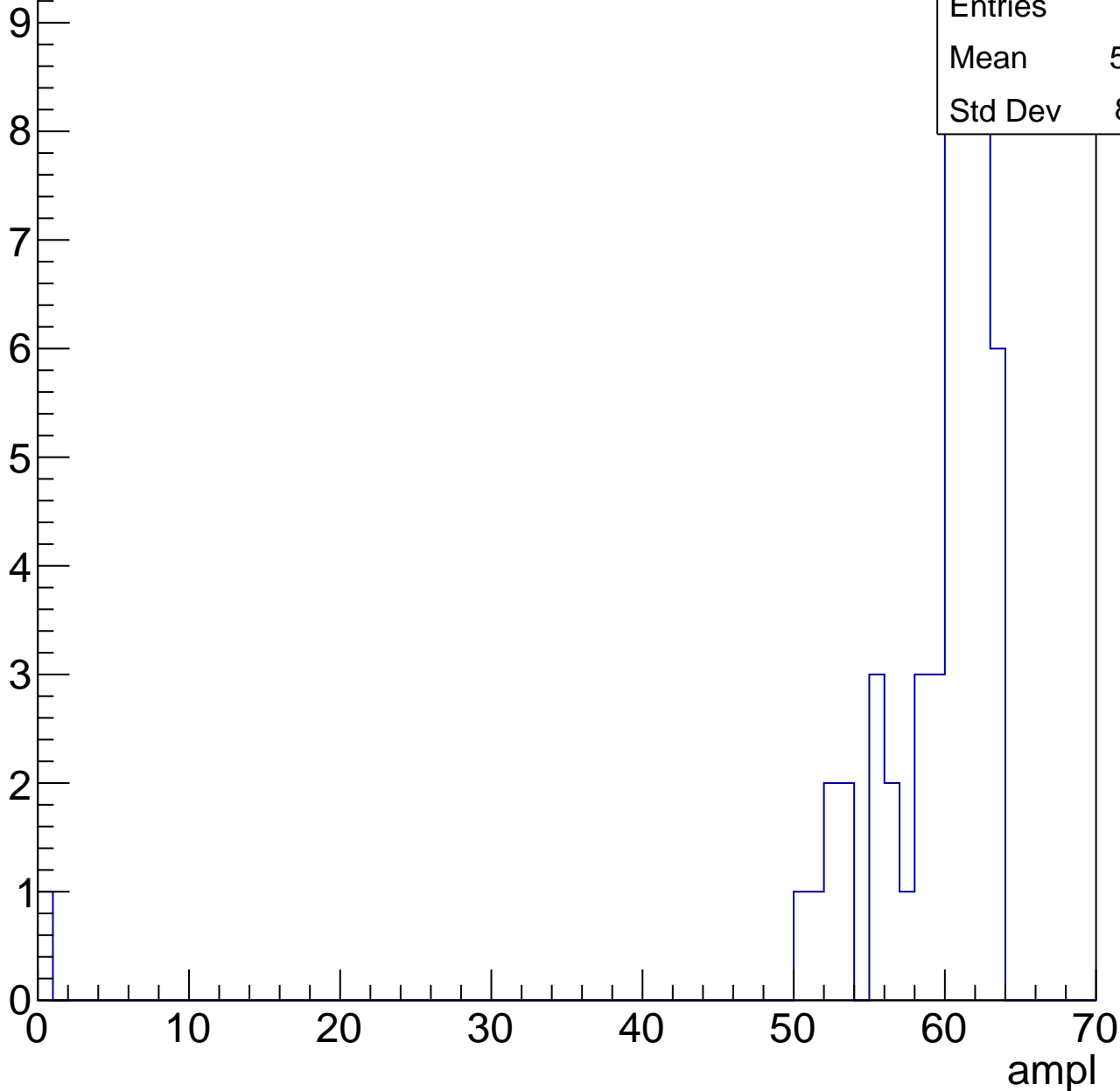


B1L103S, U24-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

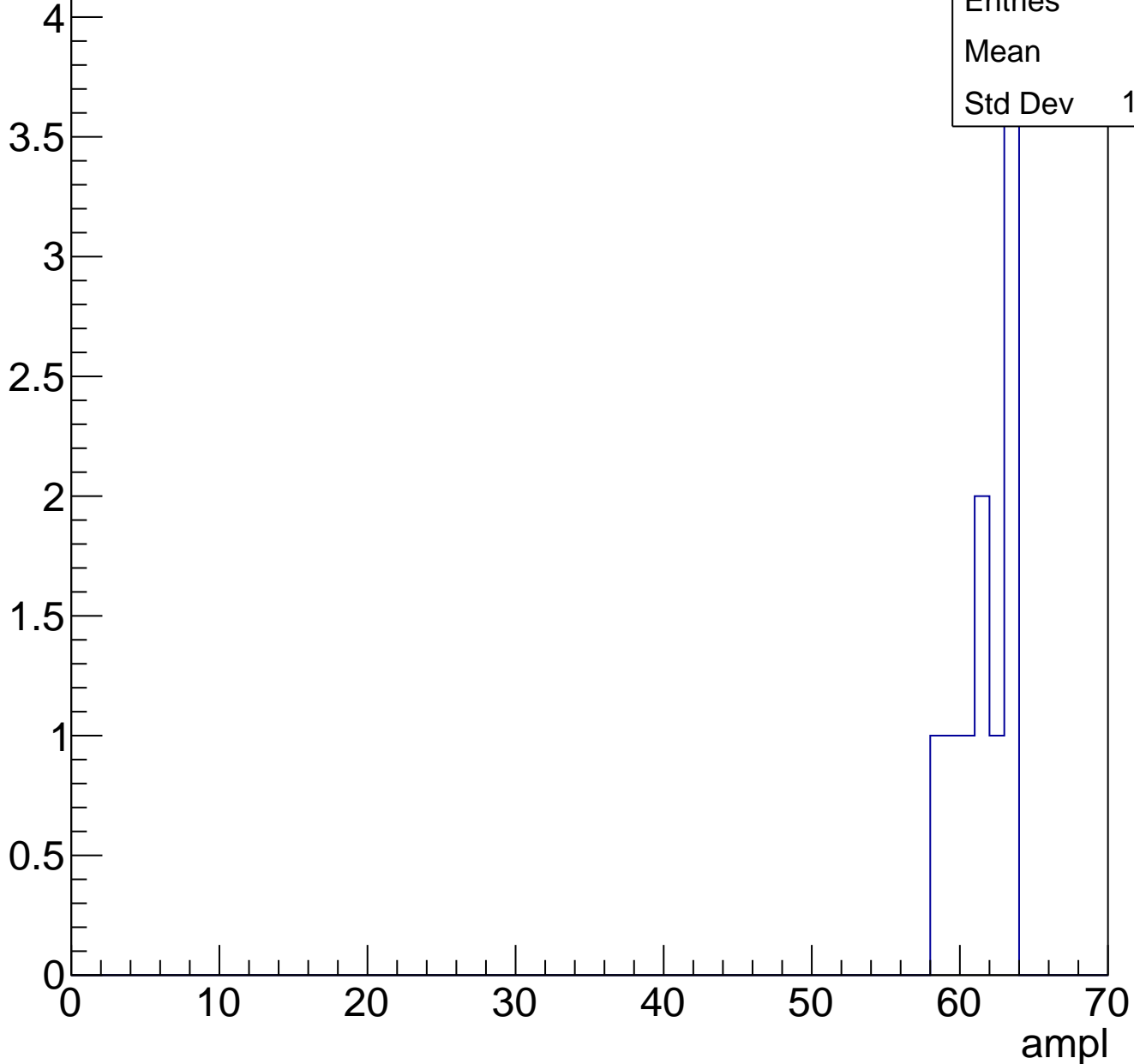
Entries	50
Mean	57.96
Std Dev	8.971



B1L103S, U24-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.3
Std Dev	1.735

B1L103S, U24-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch96, adc0

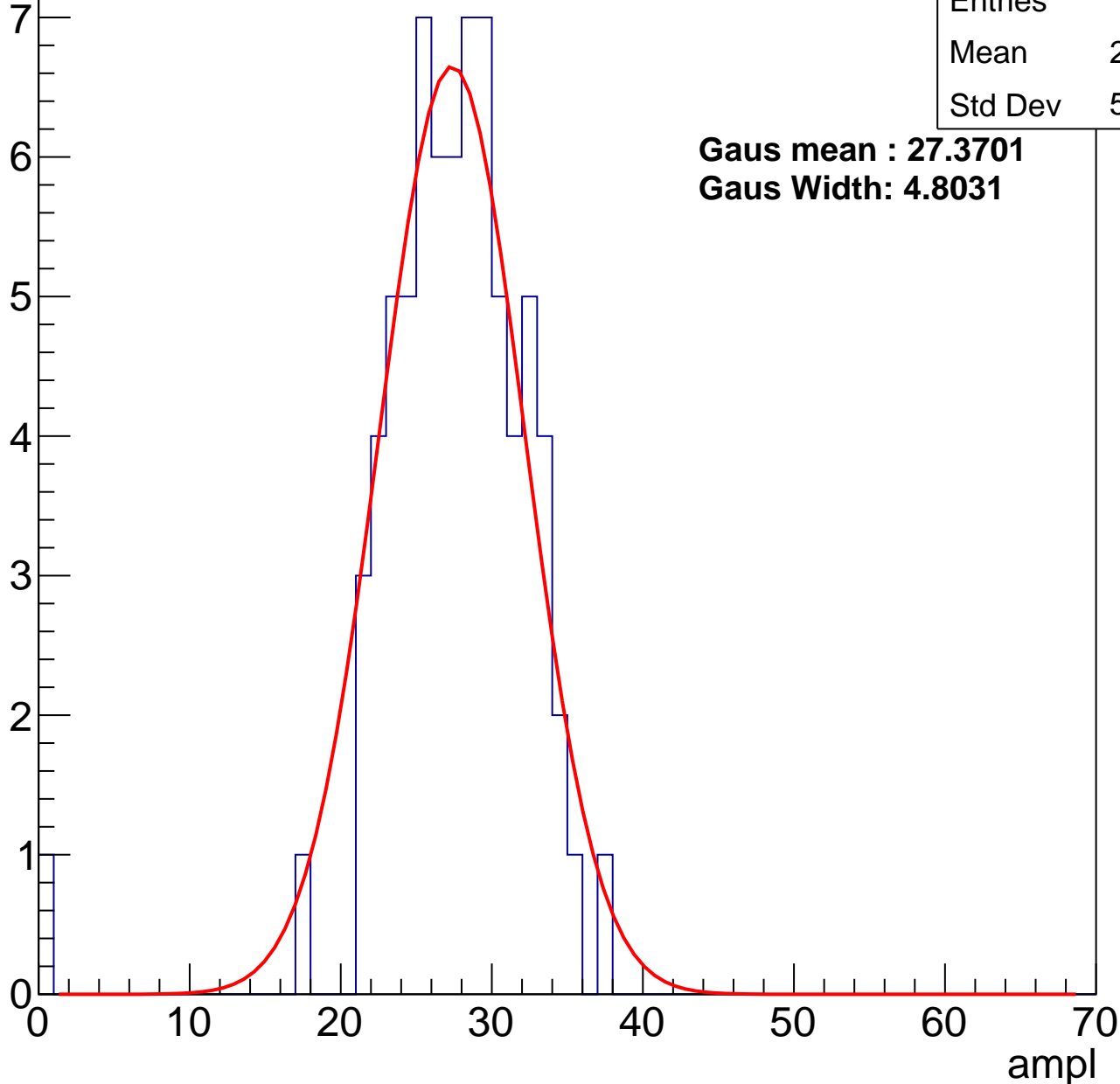
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.04
Std Dev	5.028

Gaus mean : 27.3701

Gaus Width: 4.8031



B1L103S, U24-ch96, adc1

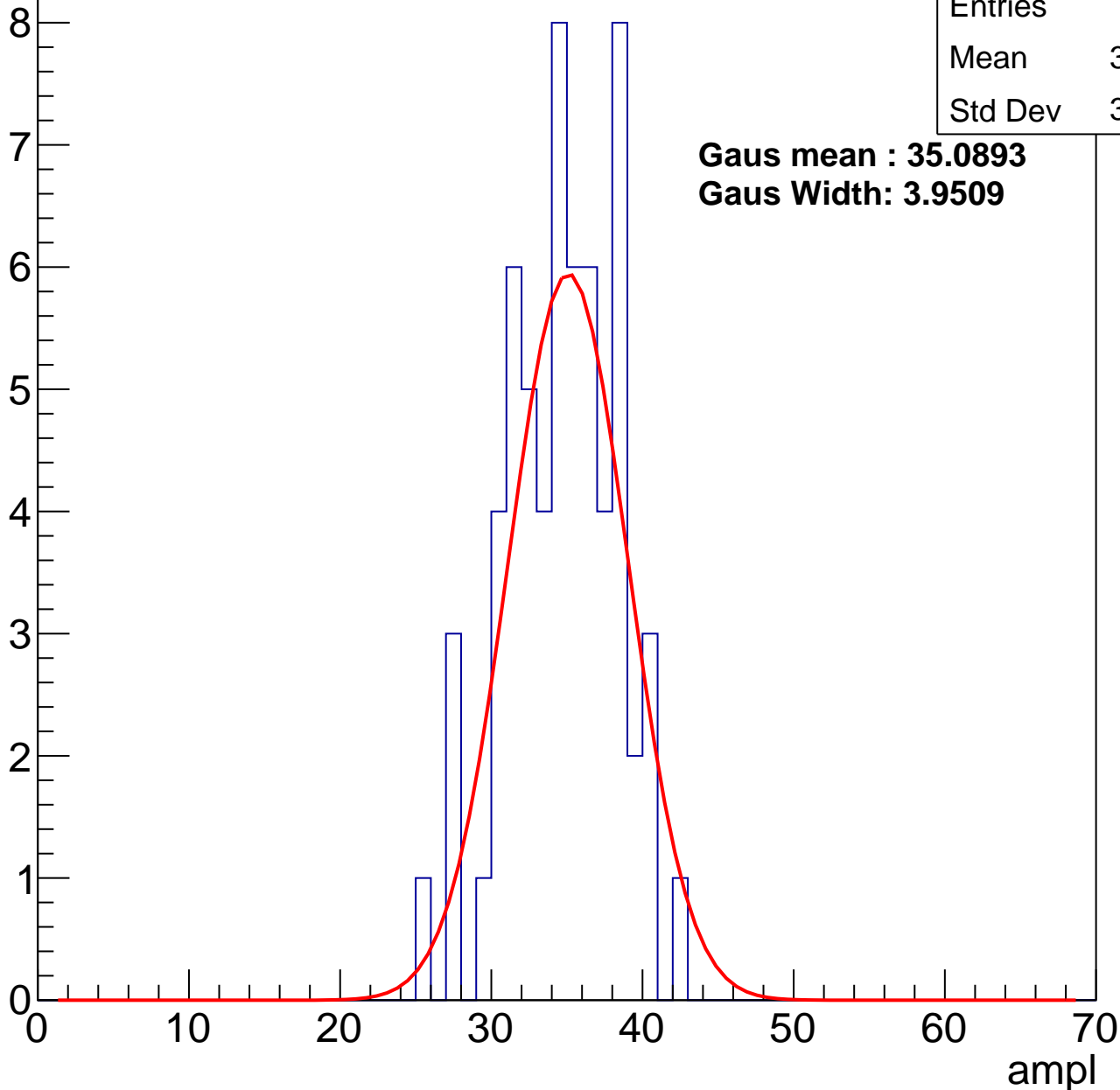
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	34.24
Std Dev	3.609

Gaus mean : 35.0893

Gaus Width: 3.9509



B1L103S, U24-ch96, adc2

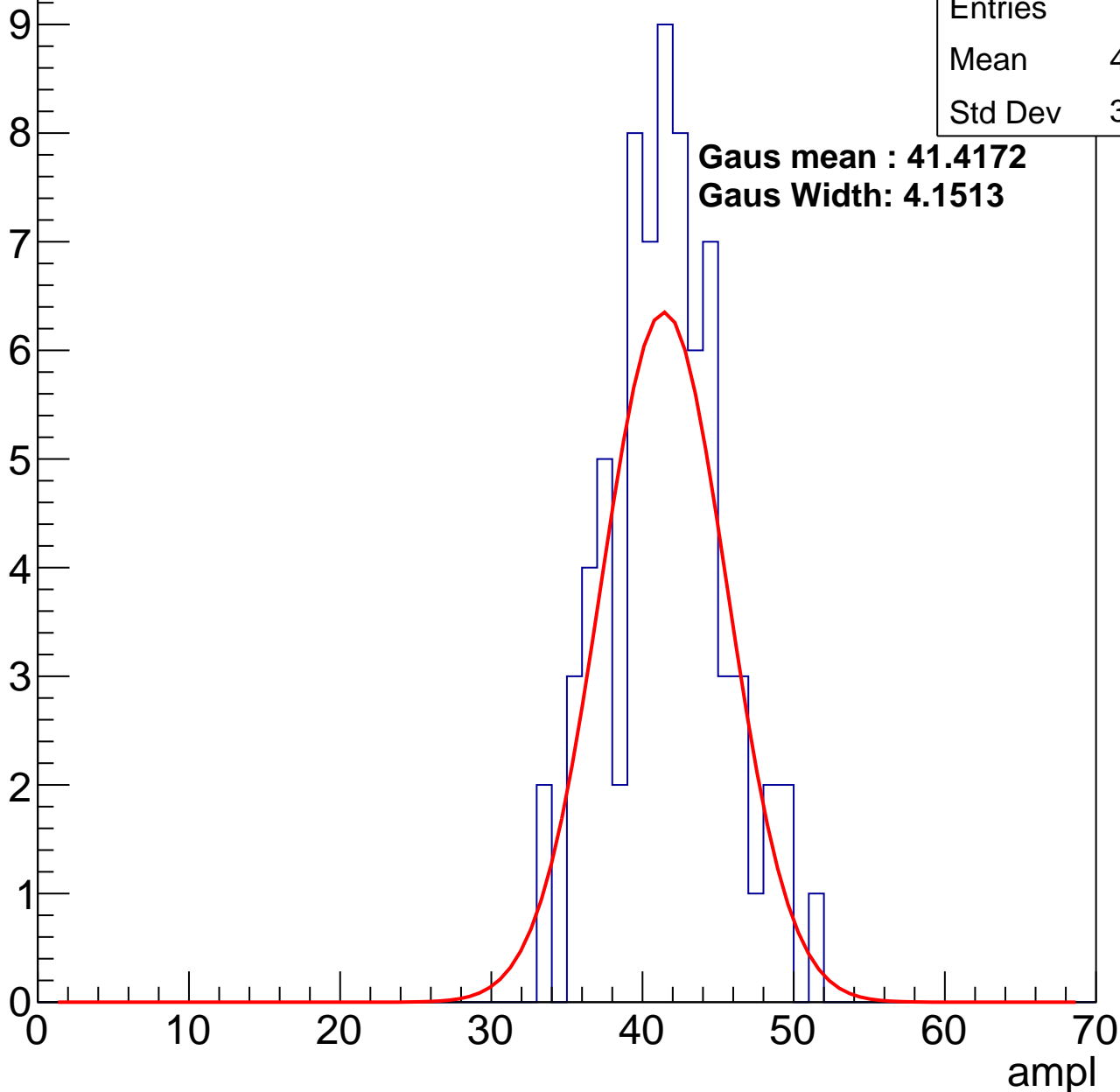
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	41.15
Std Dev	3.795

Gaus mean : 41.4172

Gaus Width: 4.1513

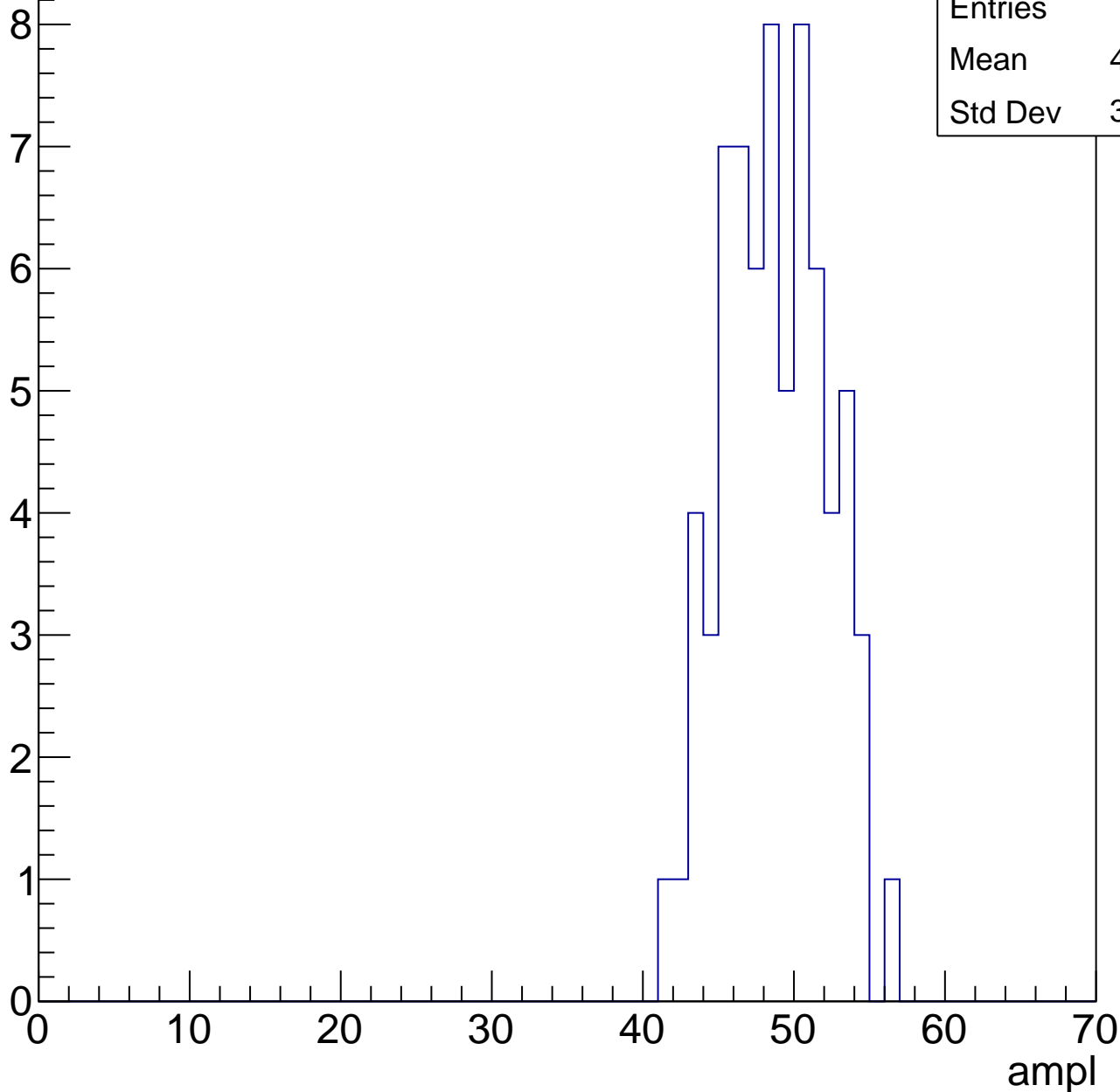


B1L103S, U24-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.29
Std Dev	3.354

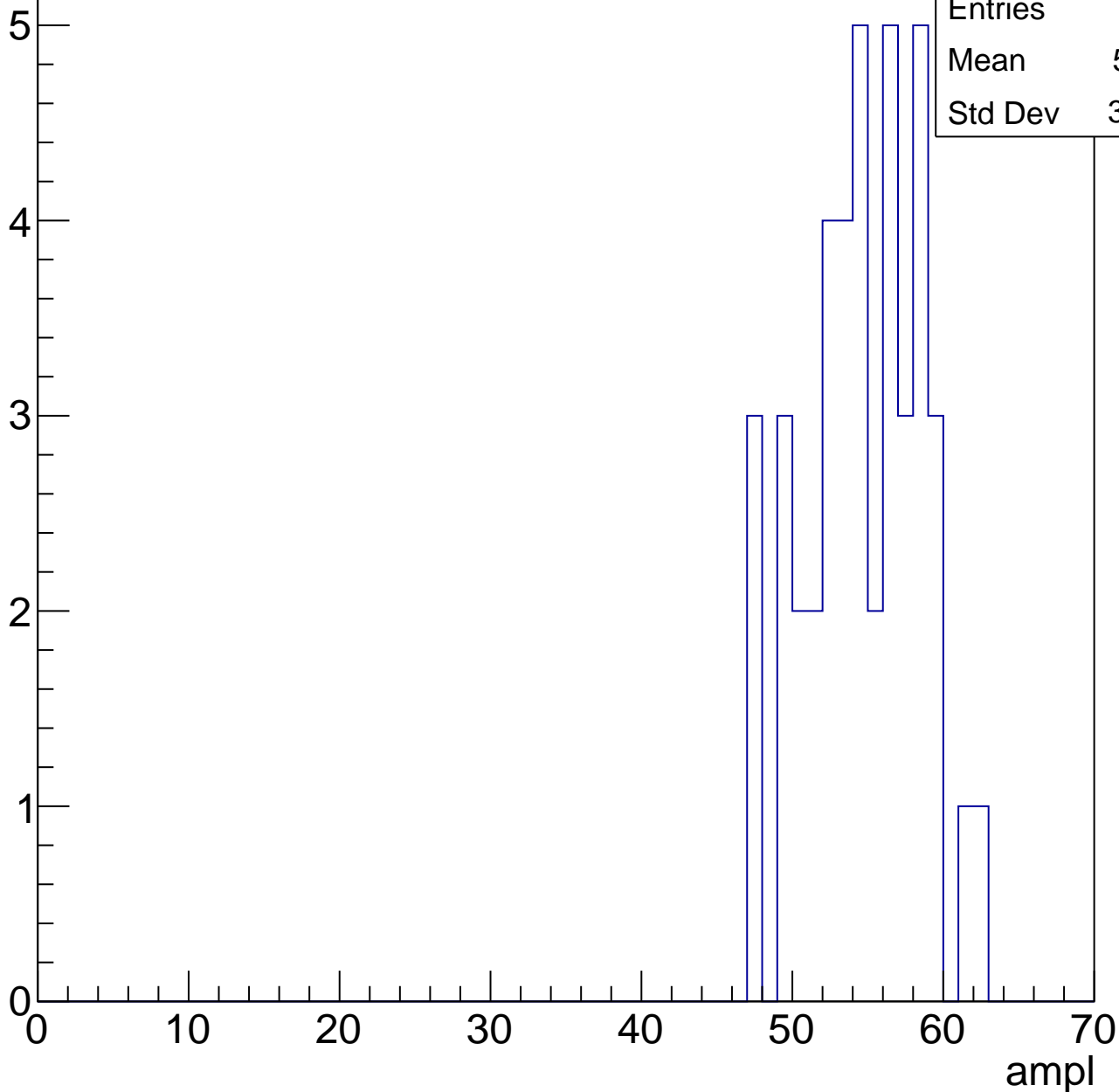


B1L103S, U24-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	54.21
Std Dev	3.758

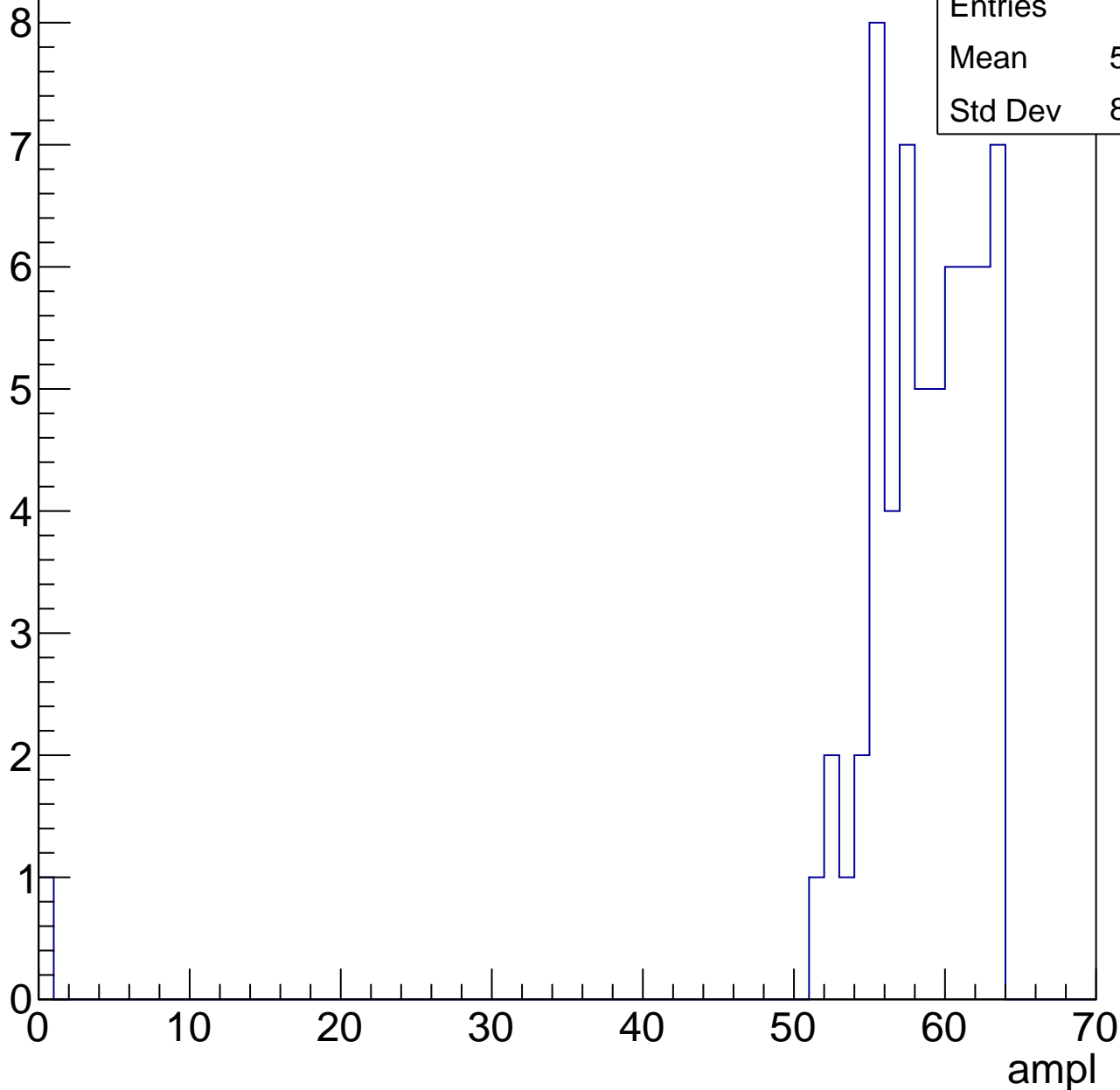


B1L103S, U24-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

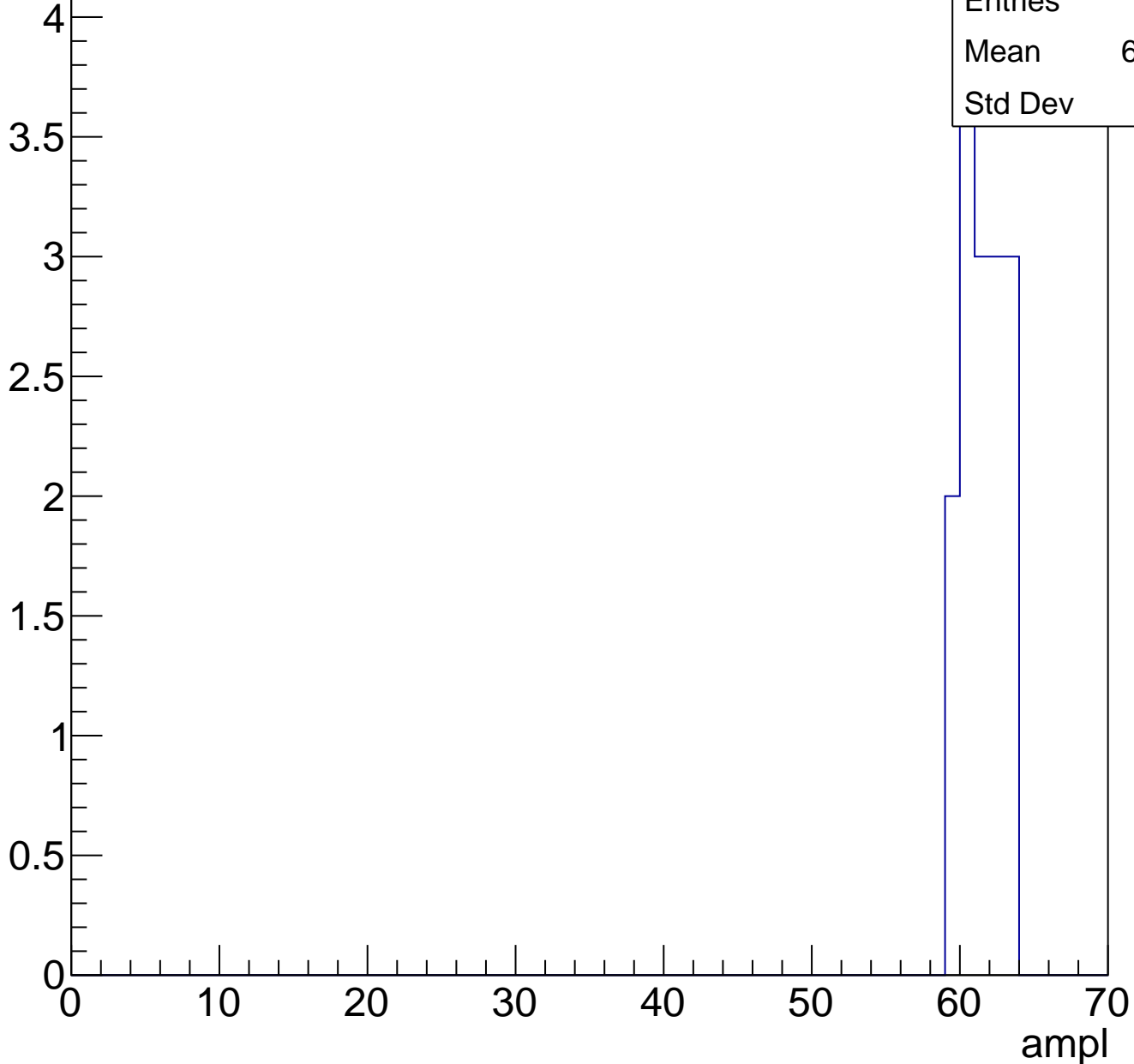
Entries	61
Mean	57.43
Std Dev	8.068



B1L103S, U24-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	15
Mean	61.07
Std Dev	1.34

B1L103S, U24-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch97, adc0

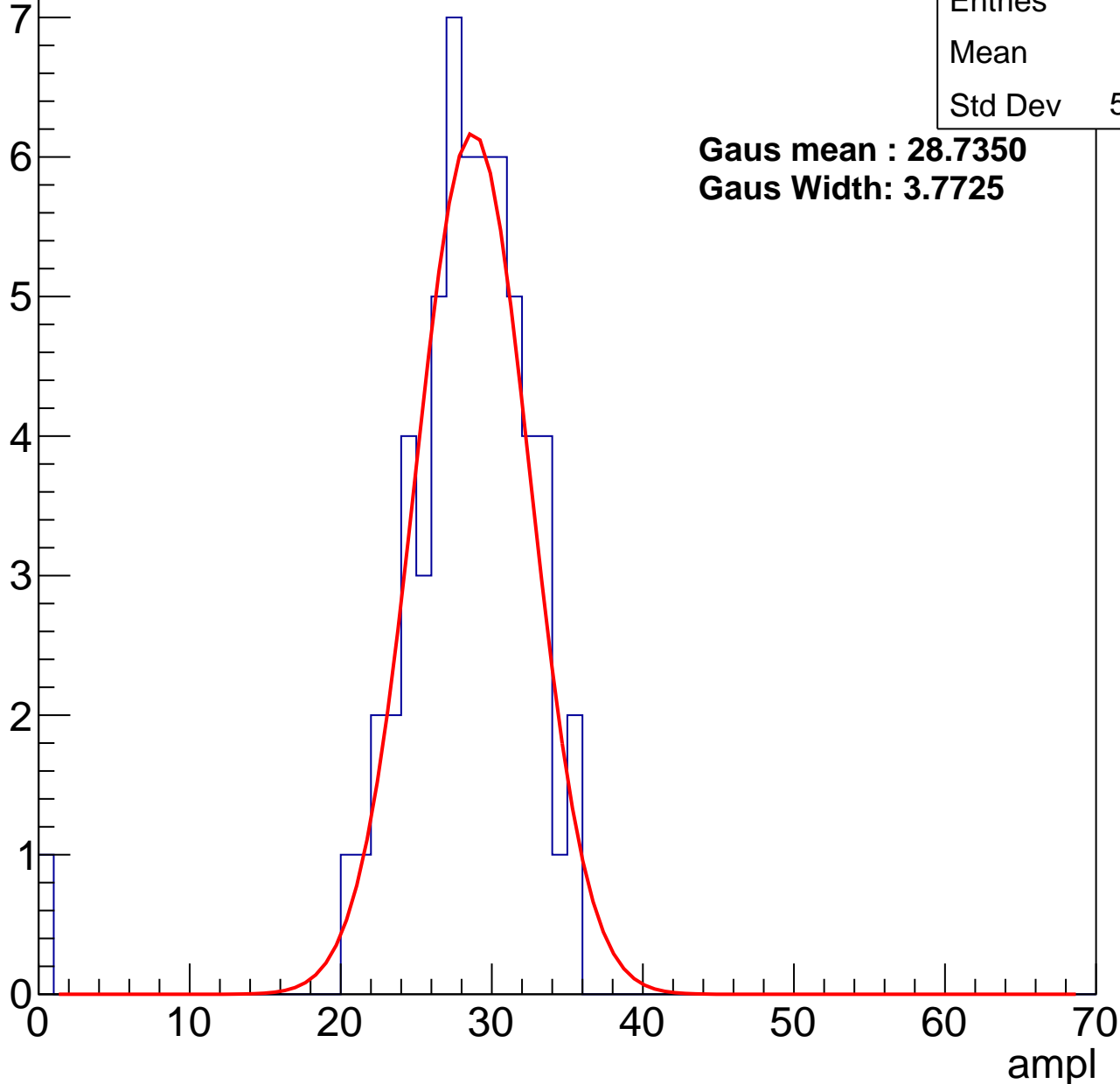
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	27.7
Std Dev	5.008

Gaus mean : 28.7350

Gaus Width: 3.7725



B1L103S, U24-ch97, adc1

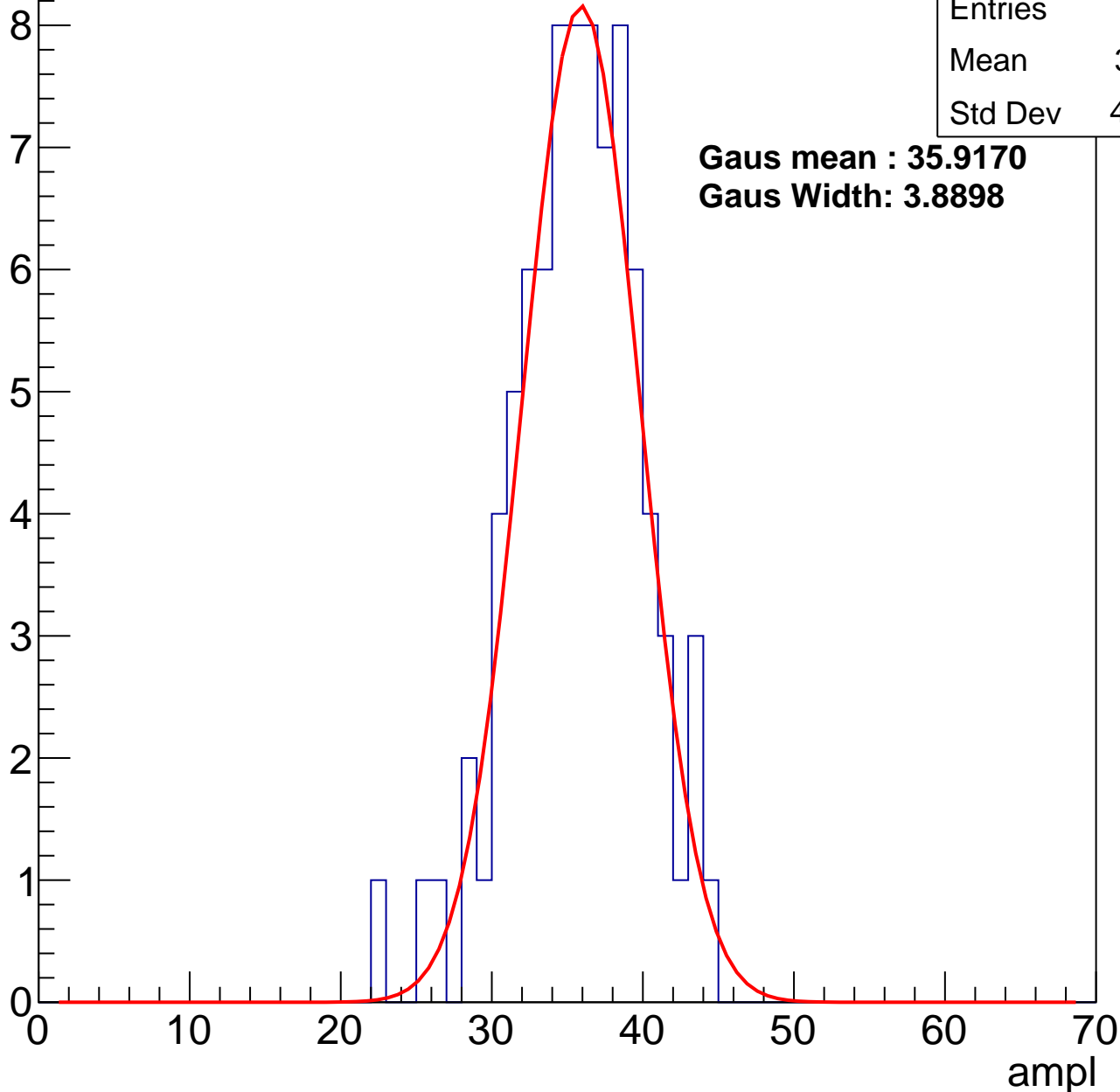
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	35.21
Std Dev	4.198

Gaus mean : 35.9170

Gaus Width: 3.8898



B1L103S, U24-ch97, adc2

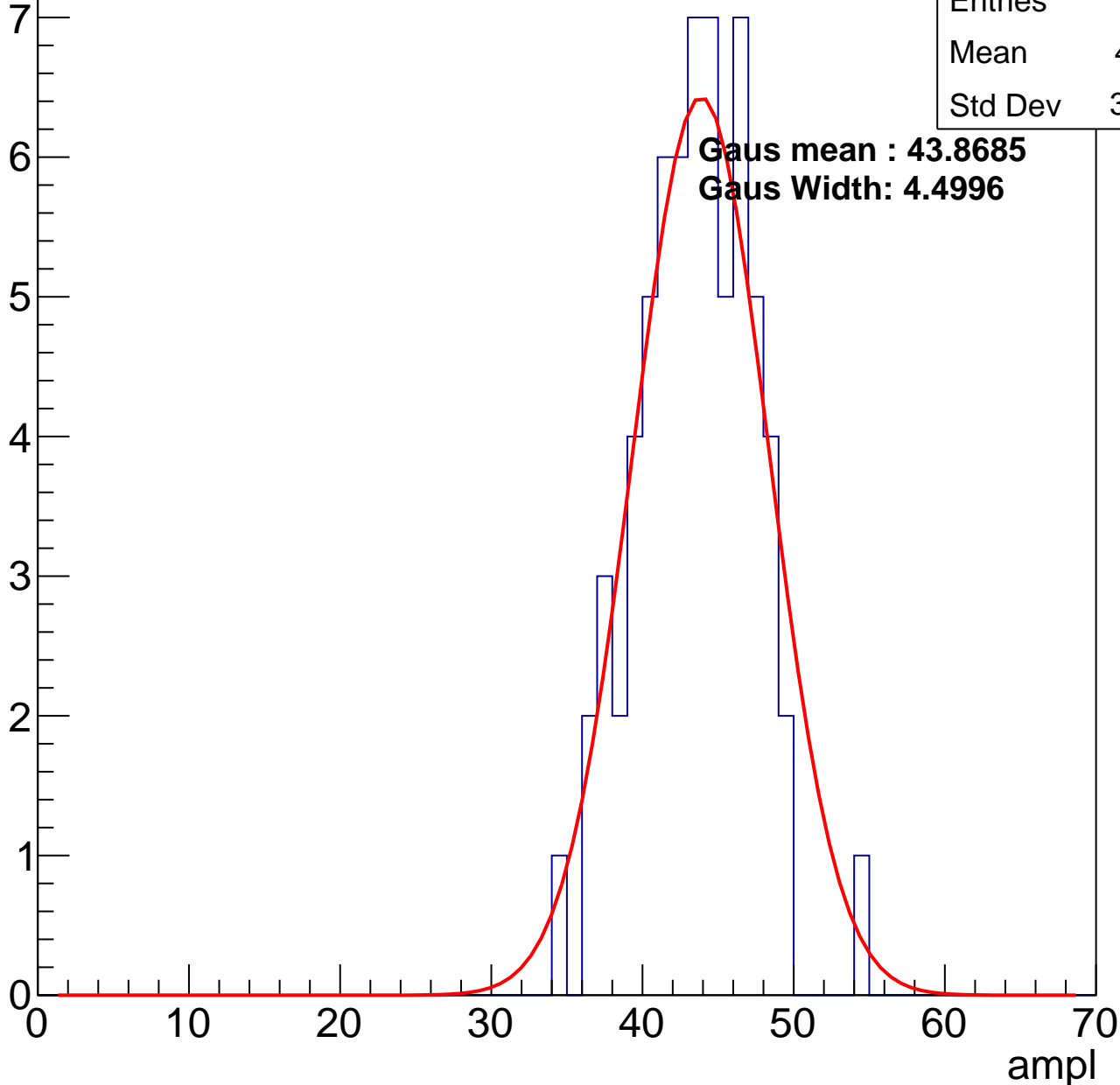
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.01
Std Dev	3.744

Gaus mean : 43.8685

Gaus Width: 4.4996

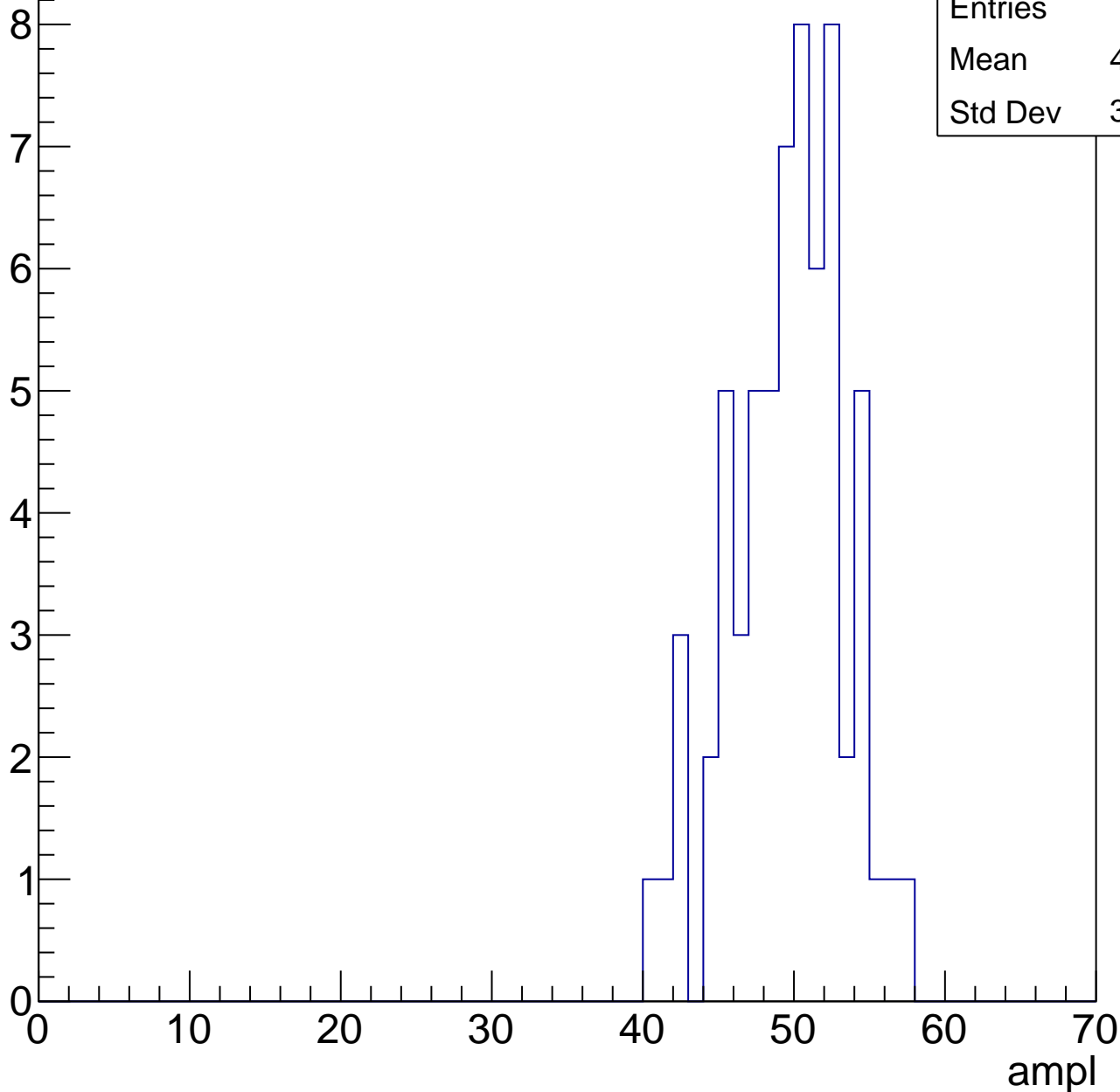


B1L103S, U24-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.09
Std Dev	3.715

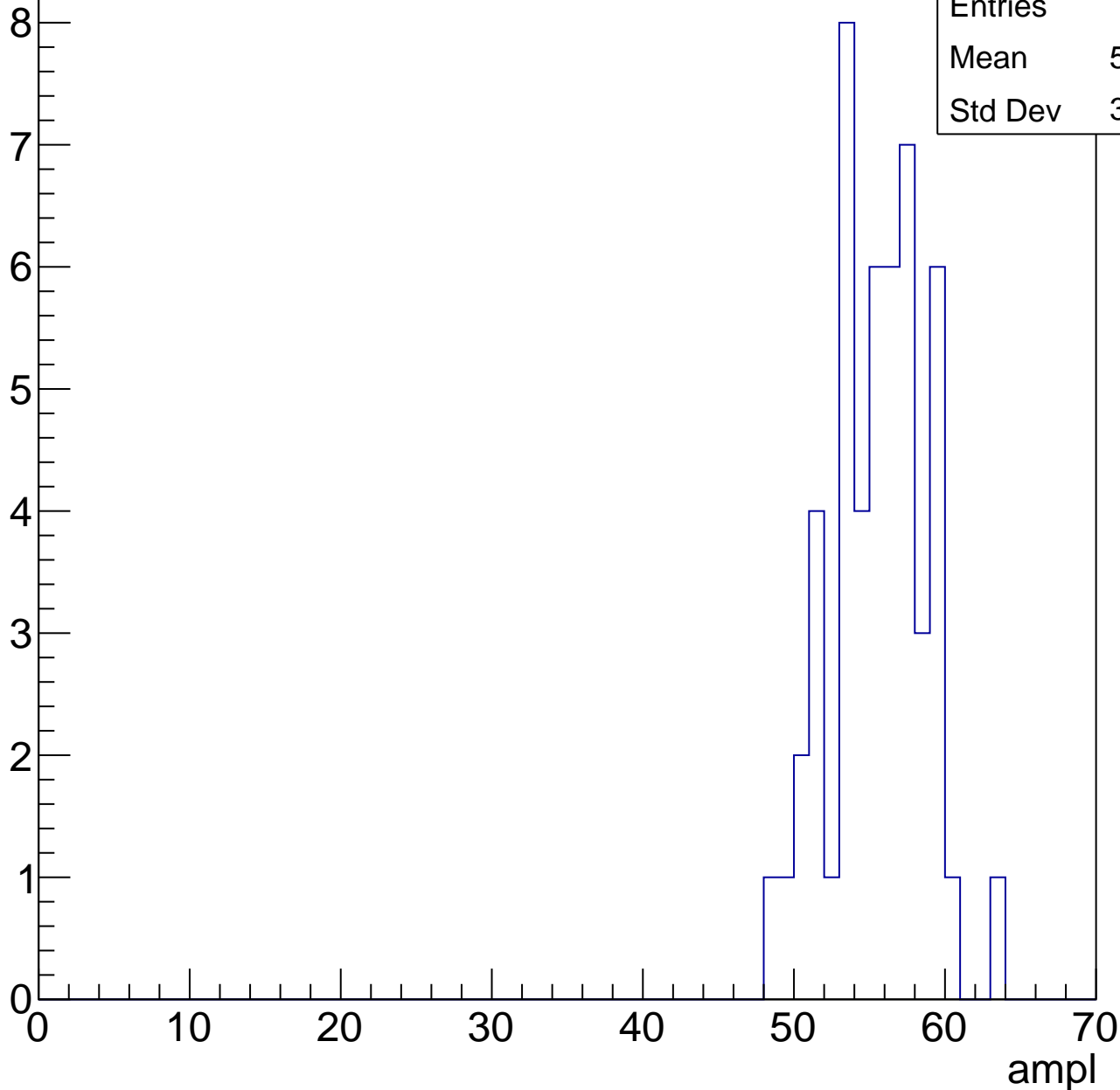


B1L103S, U24-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.08
Std Dev	3.099

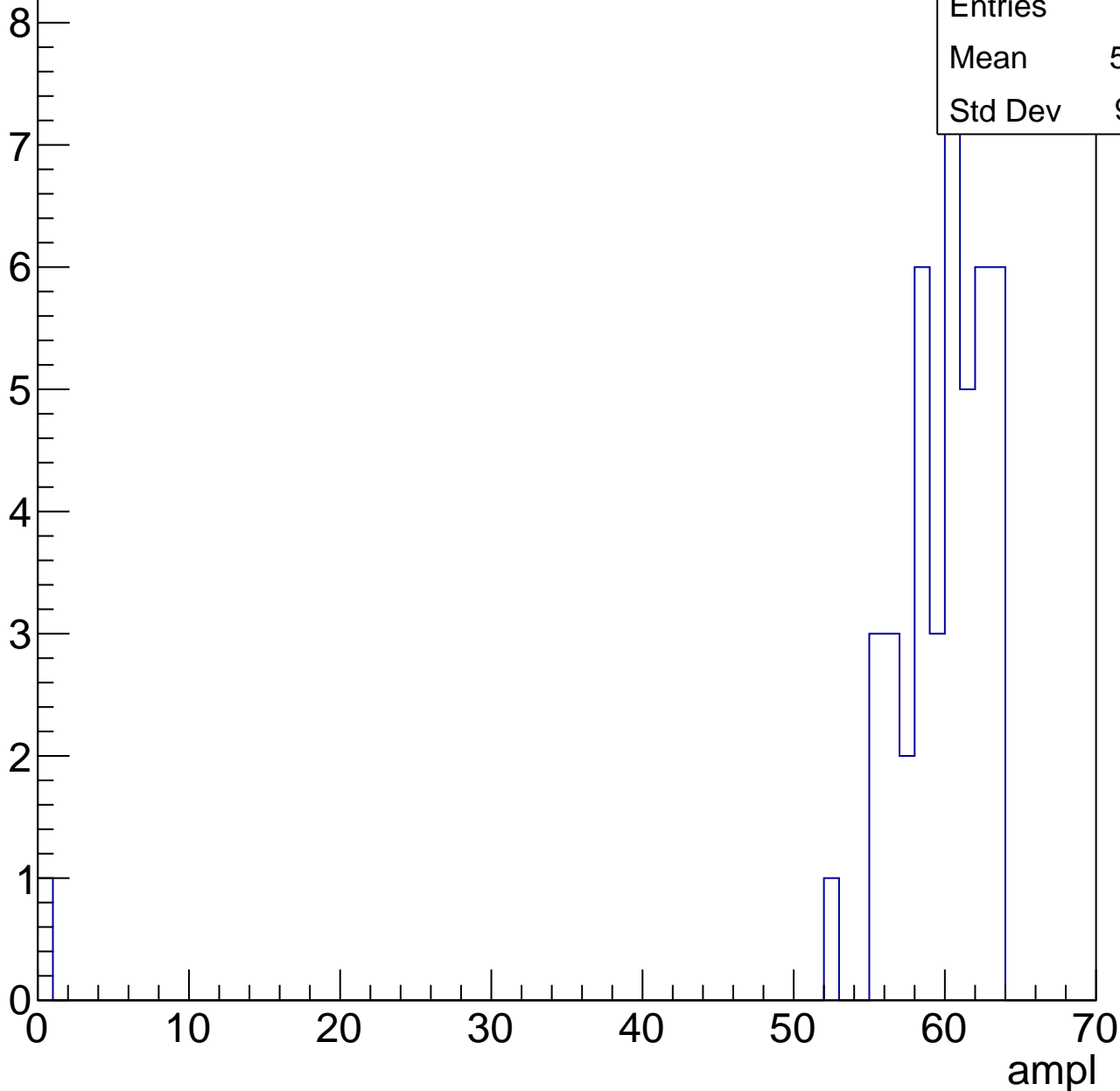


B1L103S, U24-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.16
Std Dev	9.251

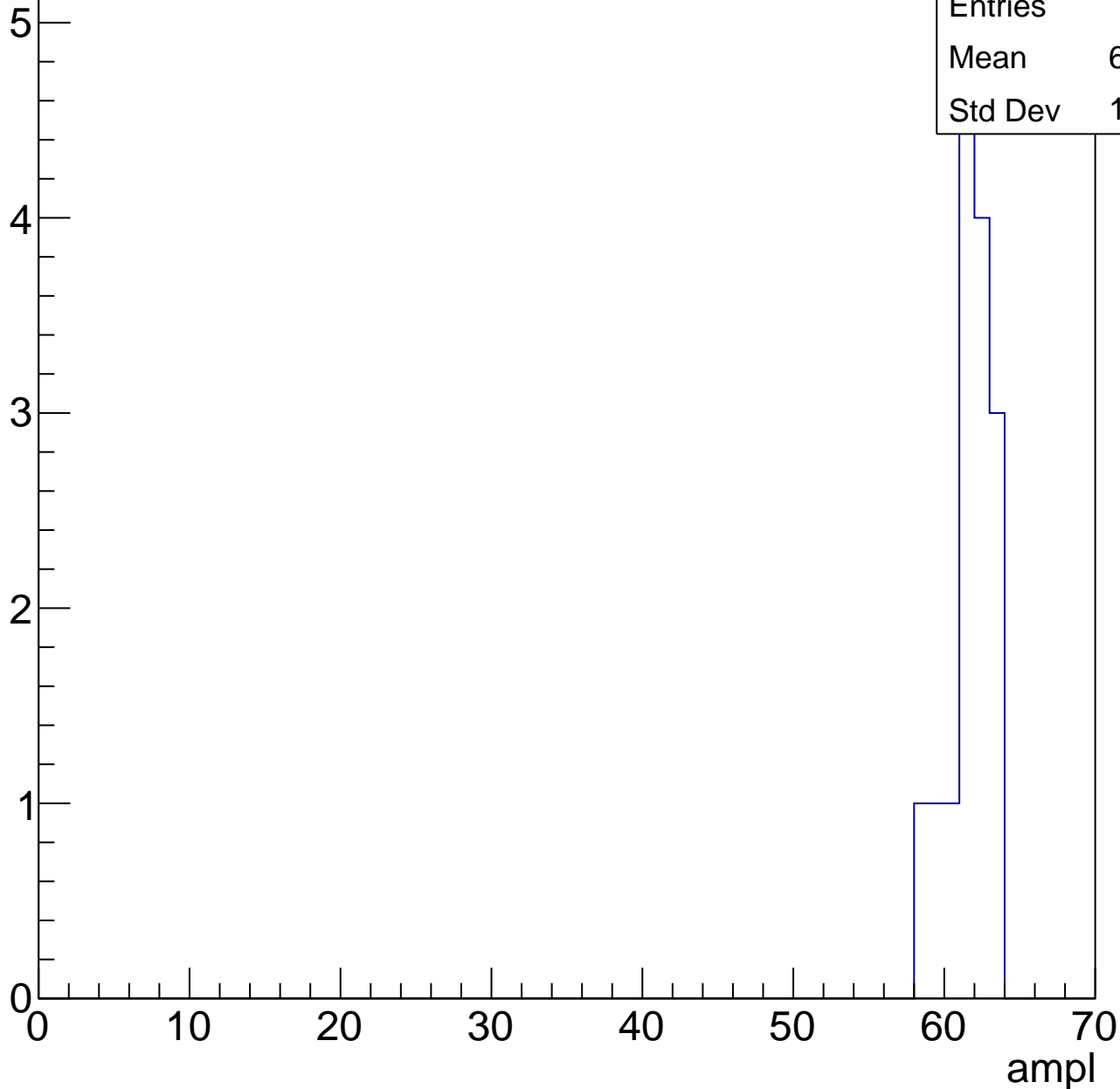


B1L103S, U24-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

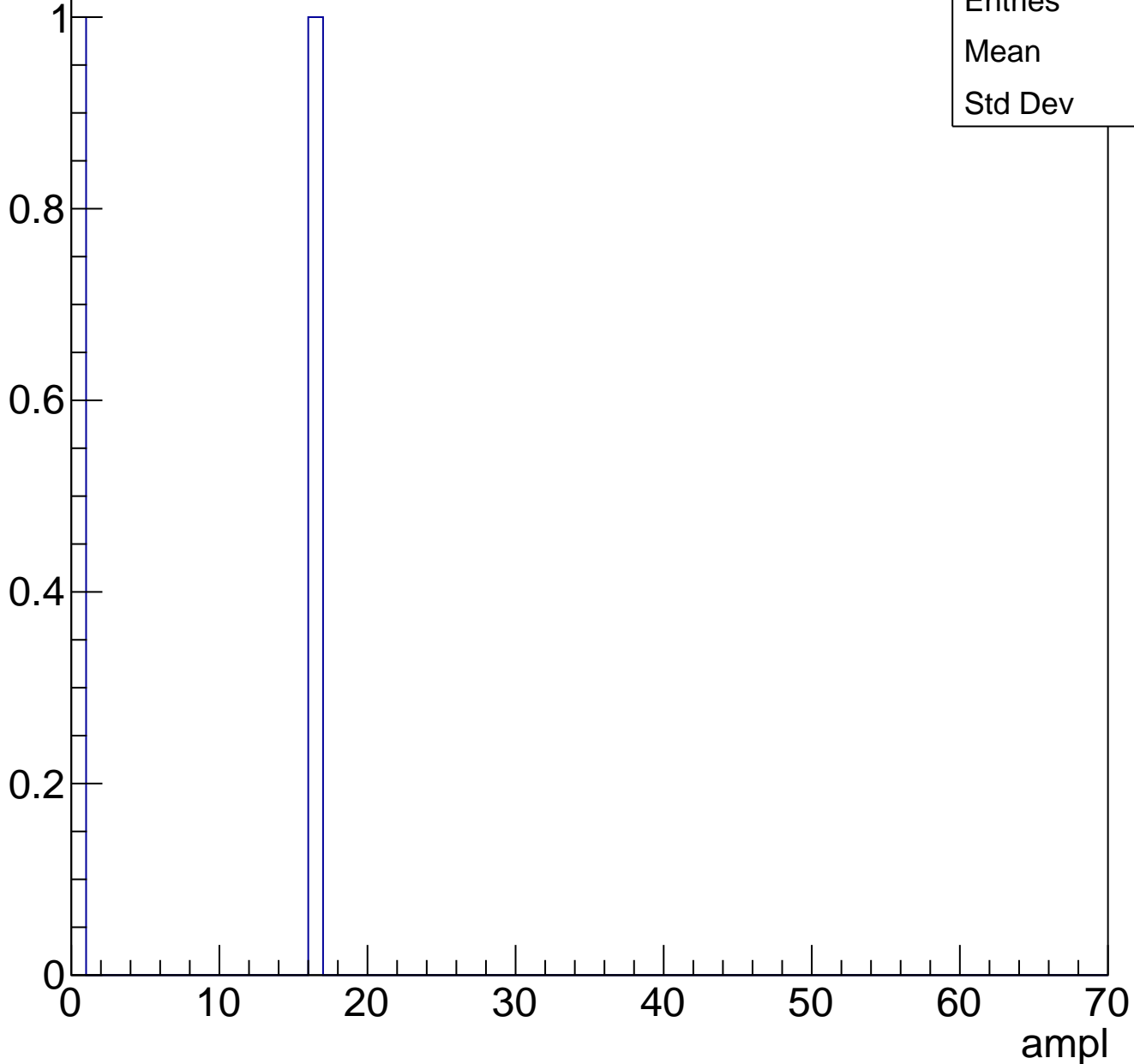
Entries	15
Mean	61.27
Std Dev	1.389



B1L103S, U24-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	8
Std Dev	8

B1L103S, U24-ch98, adc0

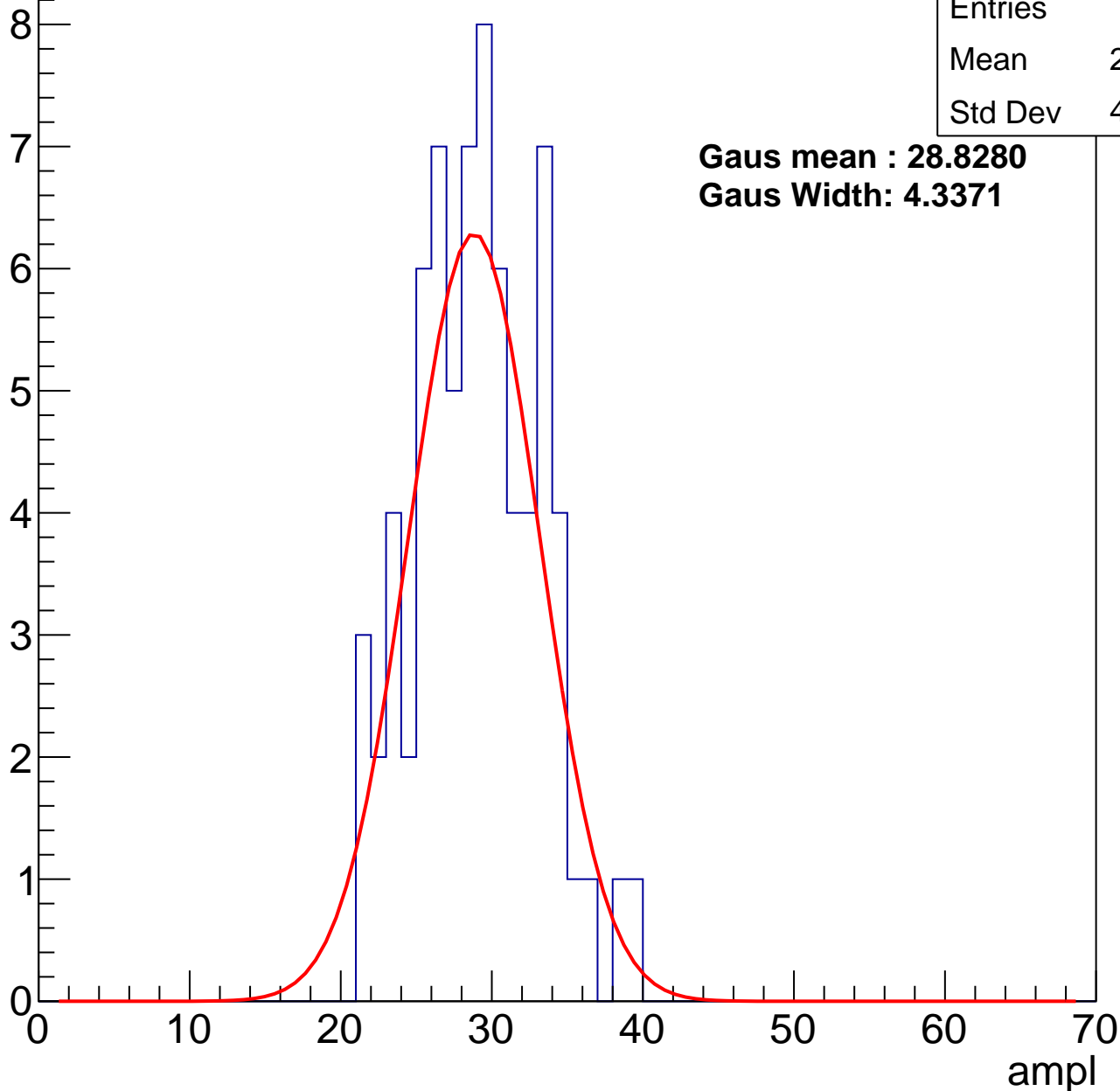
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.62
Std Dev	4.046

Gaus mean : 28.8280

Gaus Width: 4.3371



B1L103S, U24-ch98, adc1

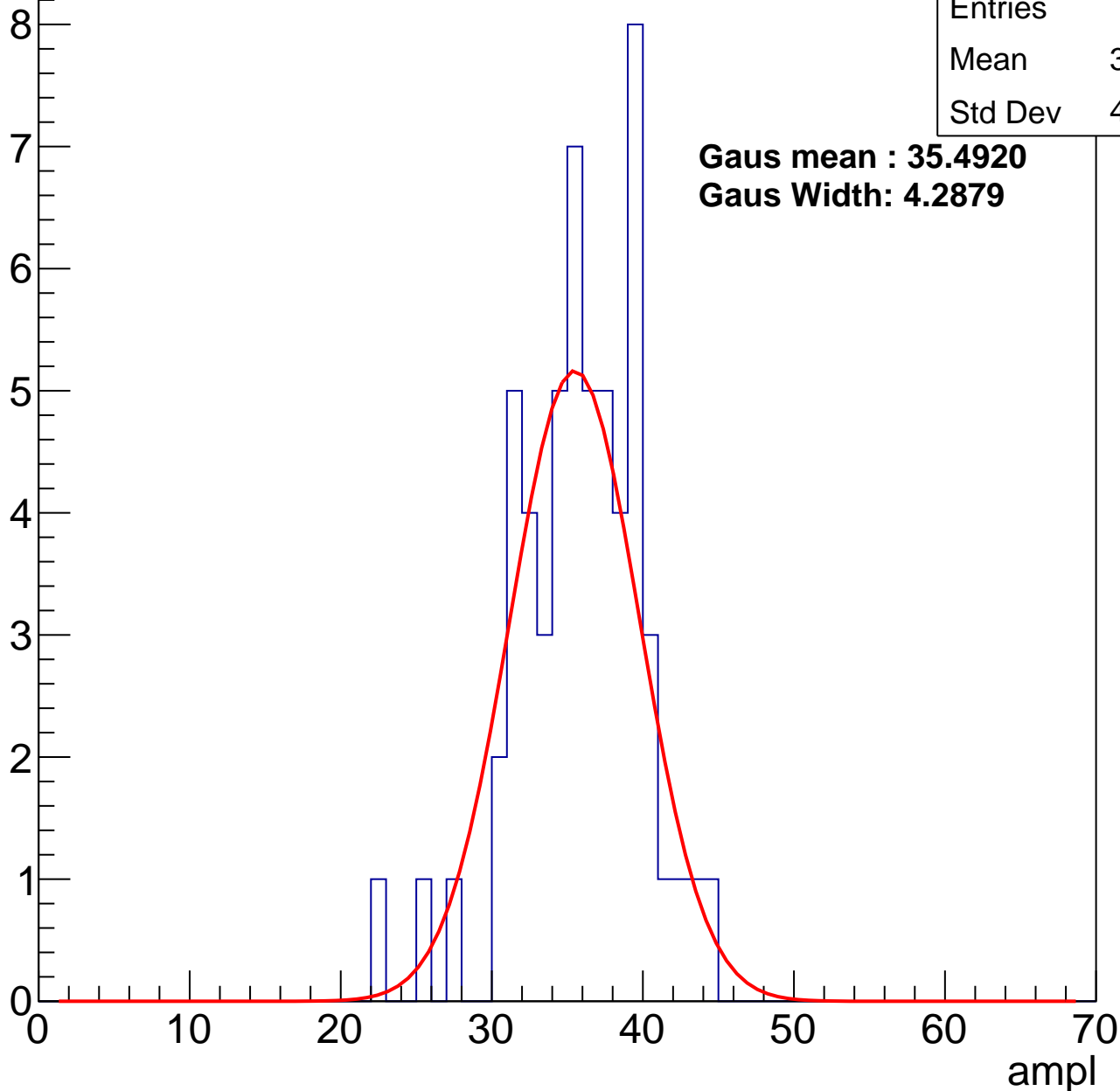
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.34
Std Dev	4.159

Gaus mean : 35.4920

Gaus Width: 4.2879



B1L103S, U24-ch98, adc2

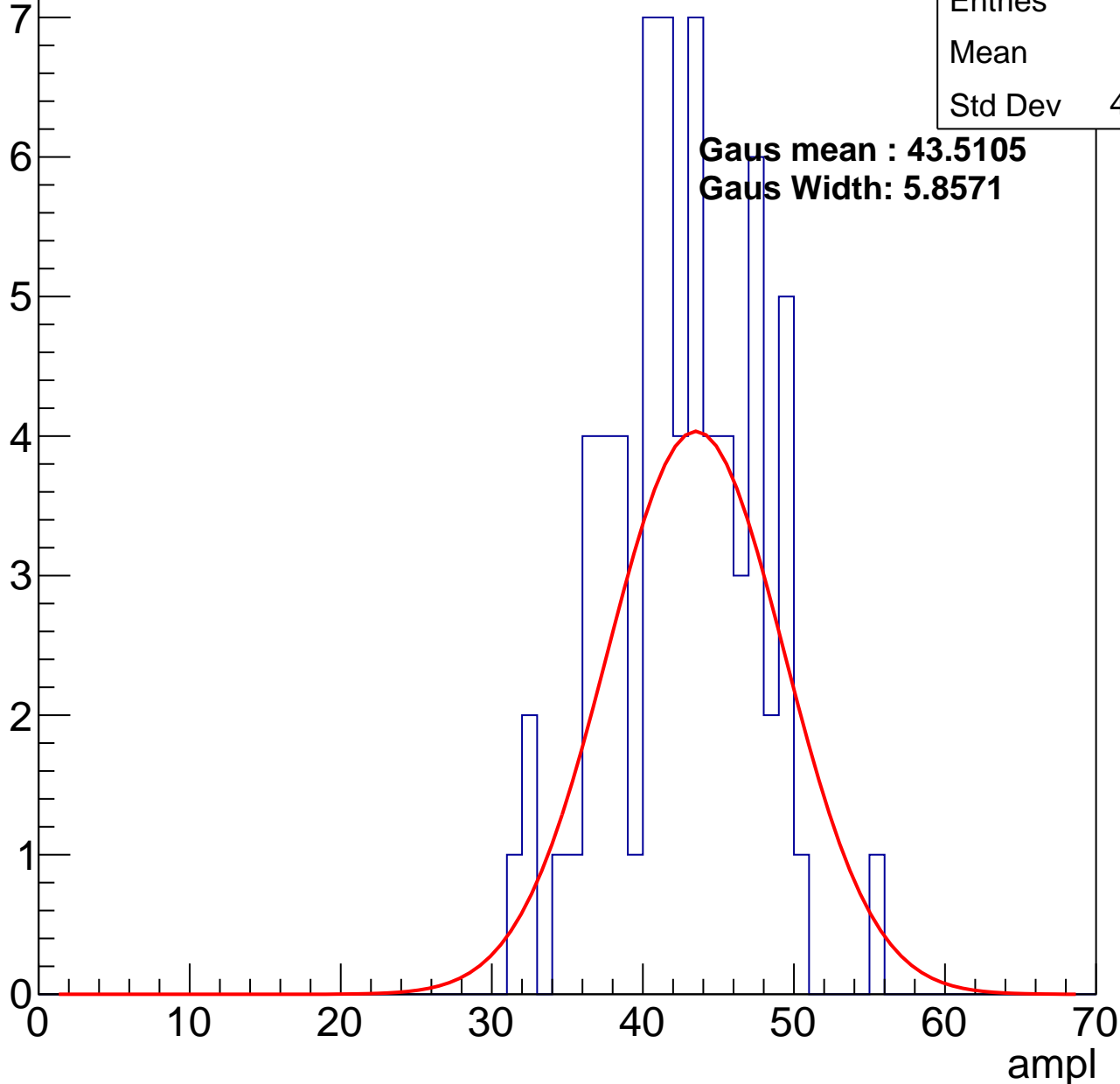
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42.1
Std Dev	4.804

Gaus mean : 43.5105

Gaus Width: 5.8571

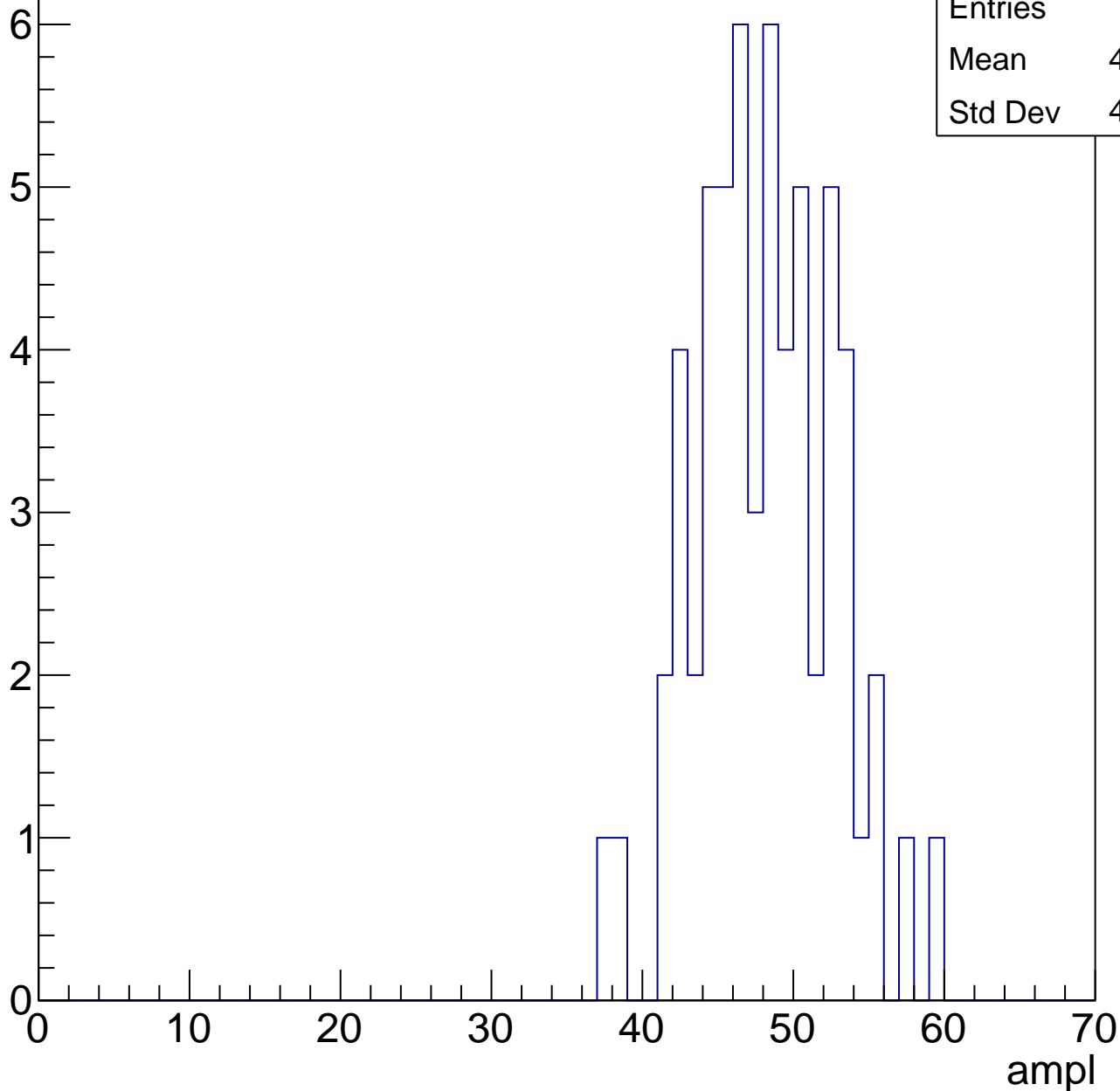


B1L103S, U24-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	47.68
Std Dev	4.504

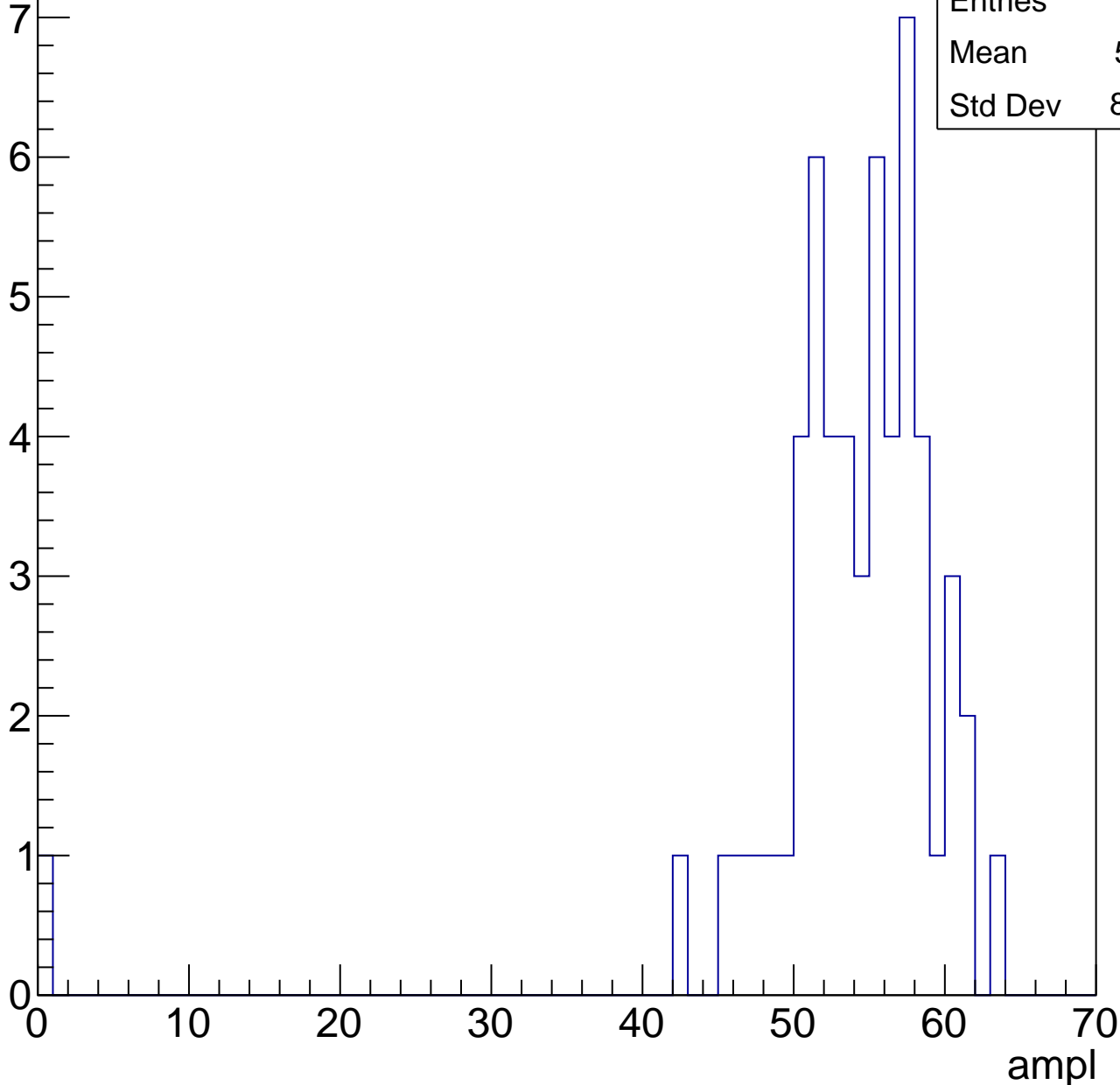


B1L103S, U24-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.11
Std Dev	8.312

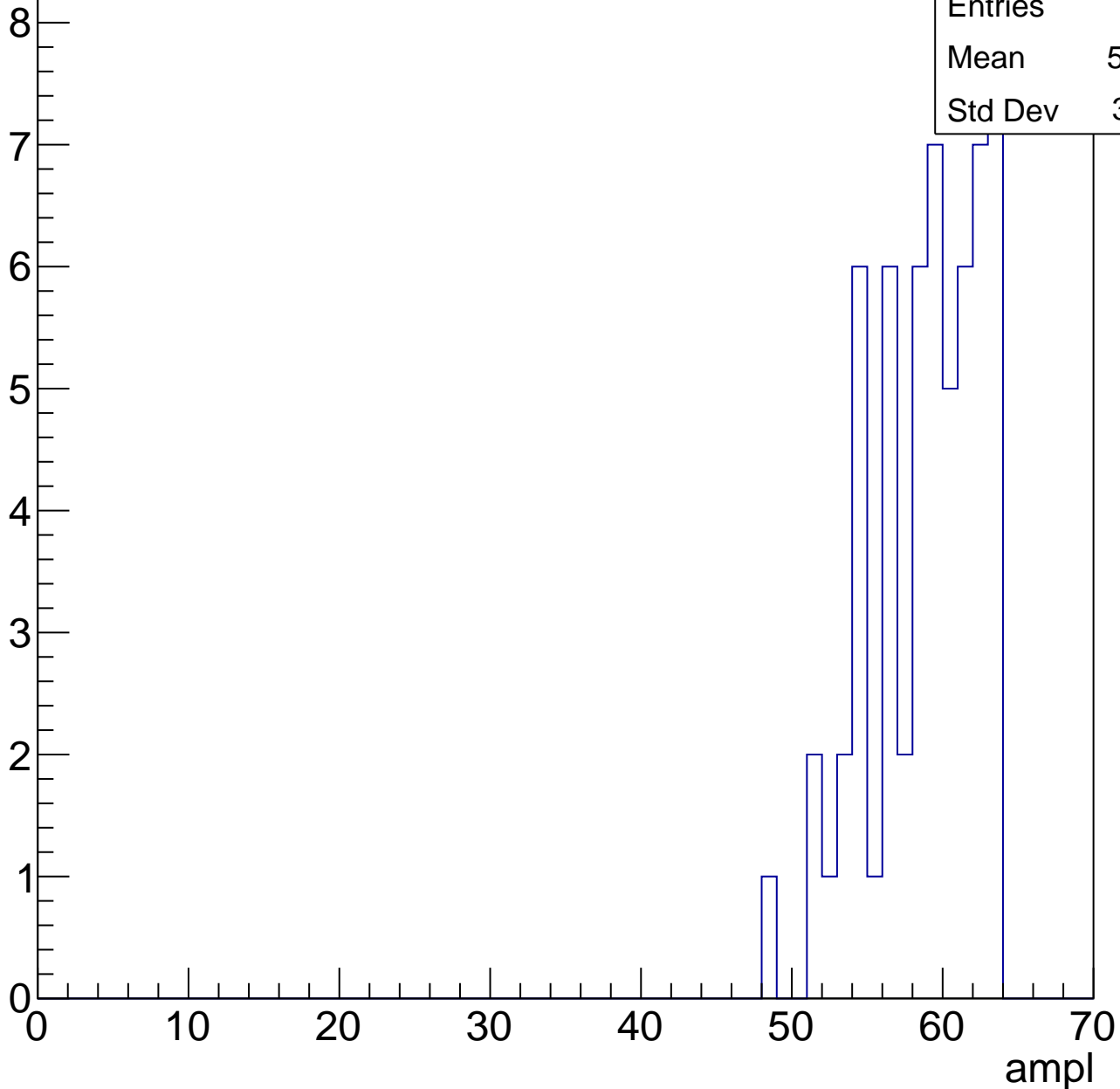


B1L103S, U24-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	58.37
Std Dev	3.651



B1L103S, U24-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

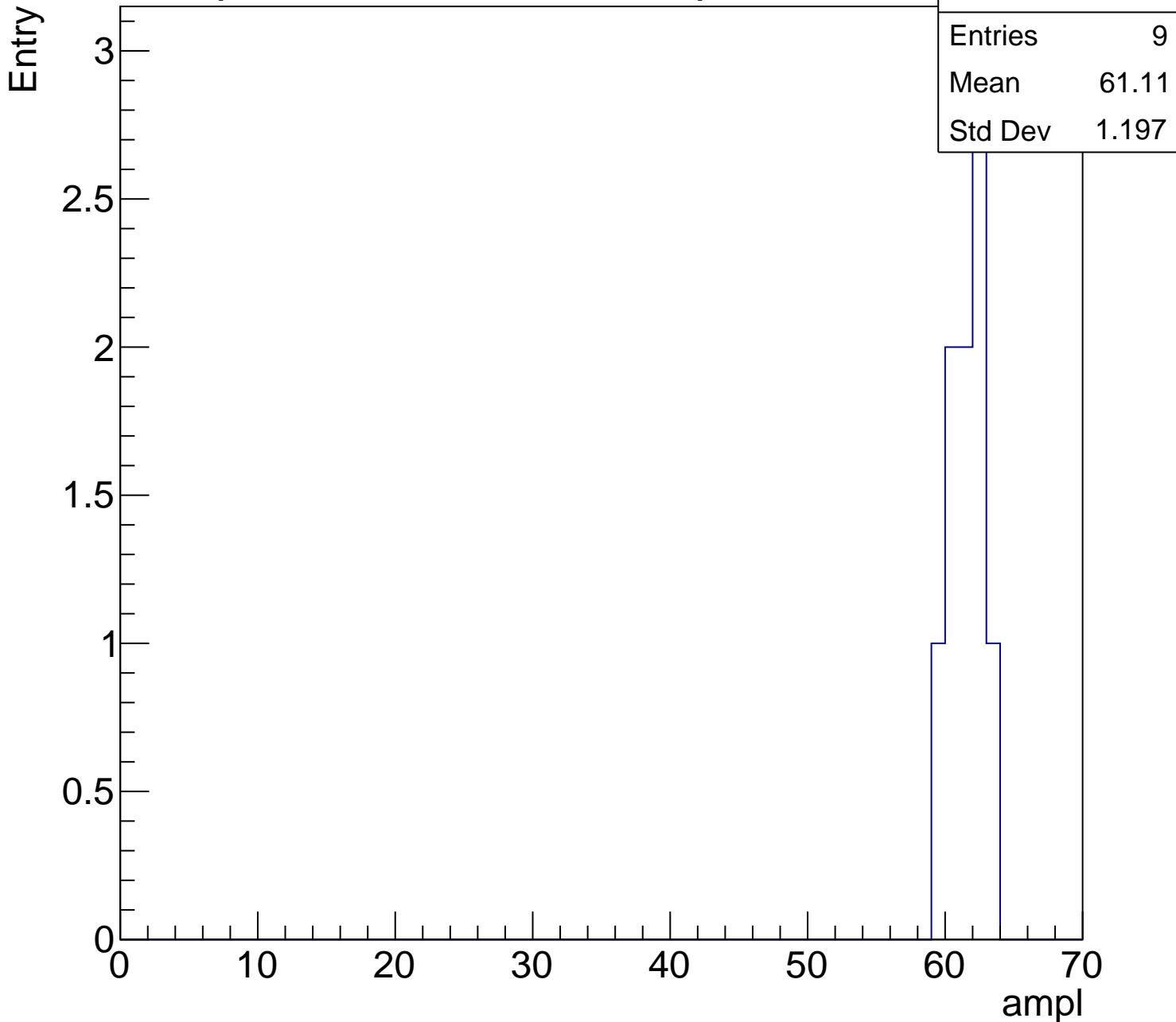
9

Mean

61.11

Std Dev

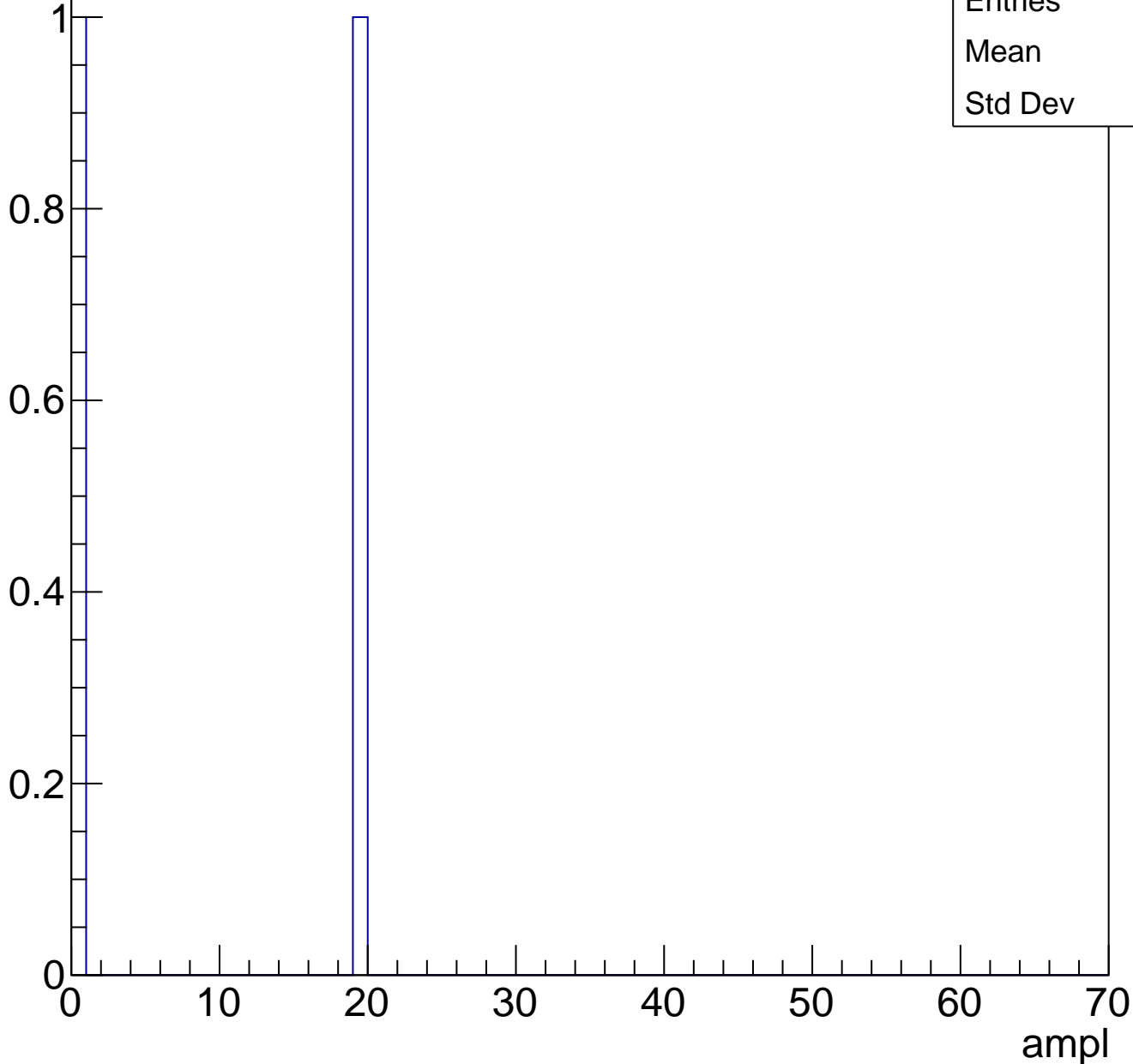
1.197



B1L103S, U24-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch99, adc0

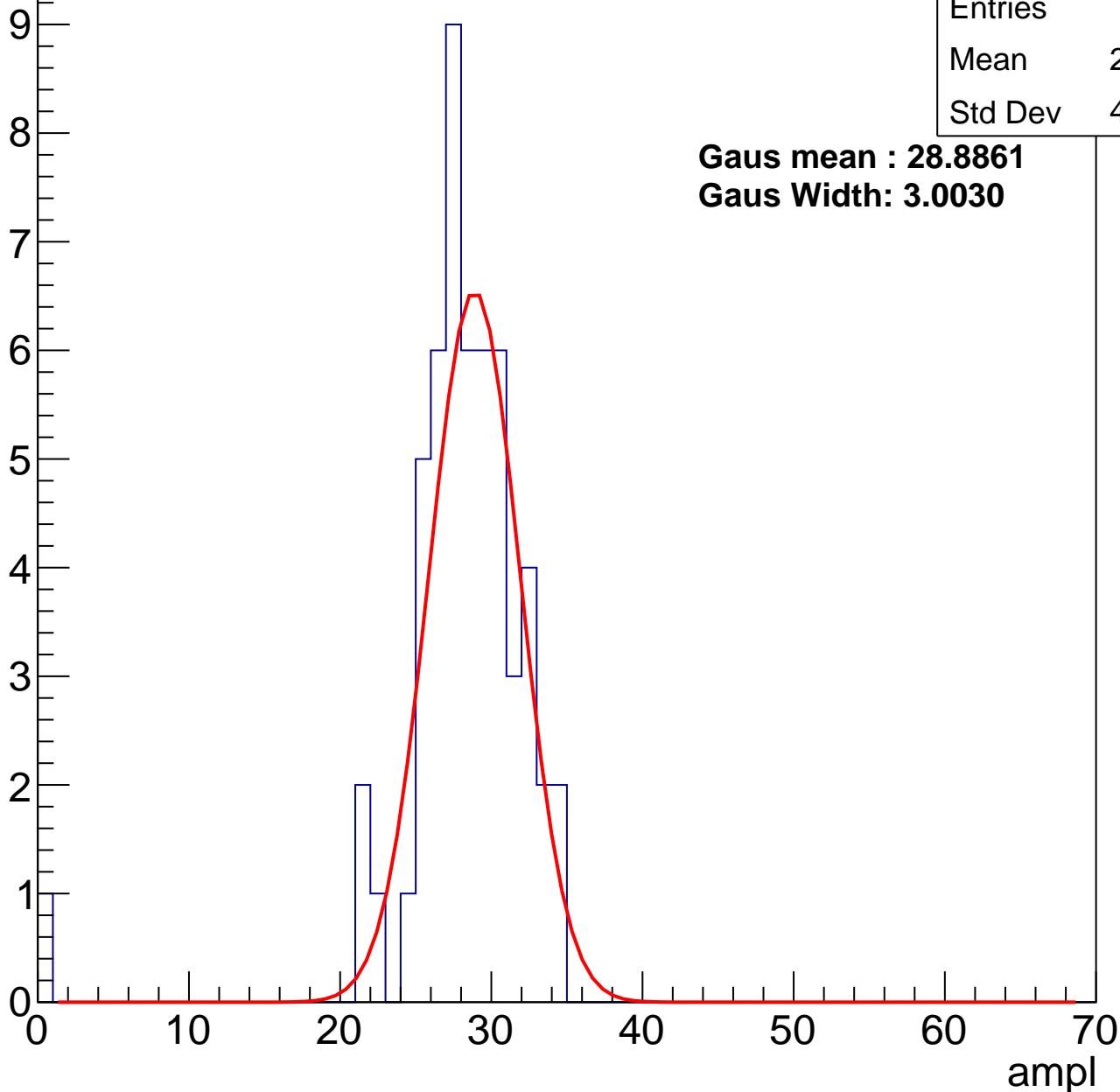
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	27.57
Std Dev	4.802

Gaus mean : 28.8861

Gaus Width: 3.0030



B1L103S, U24-ch99, adc1

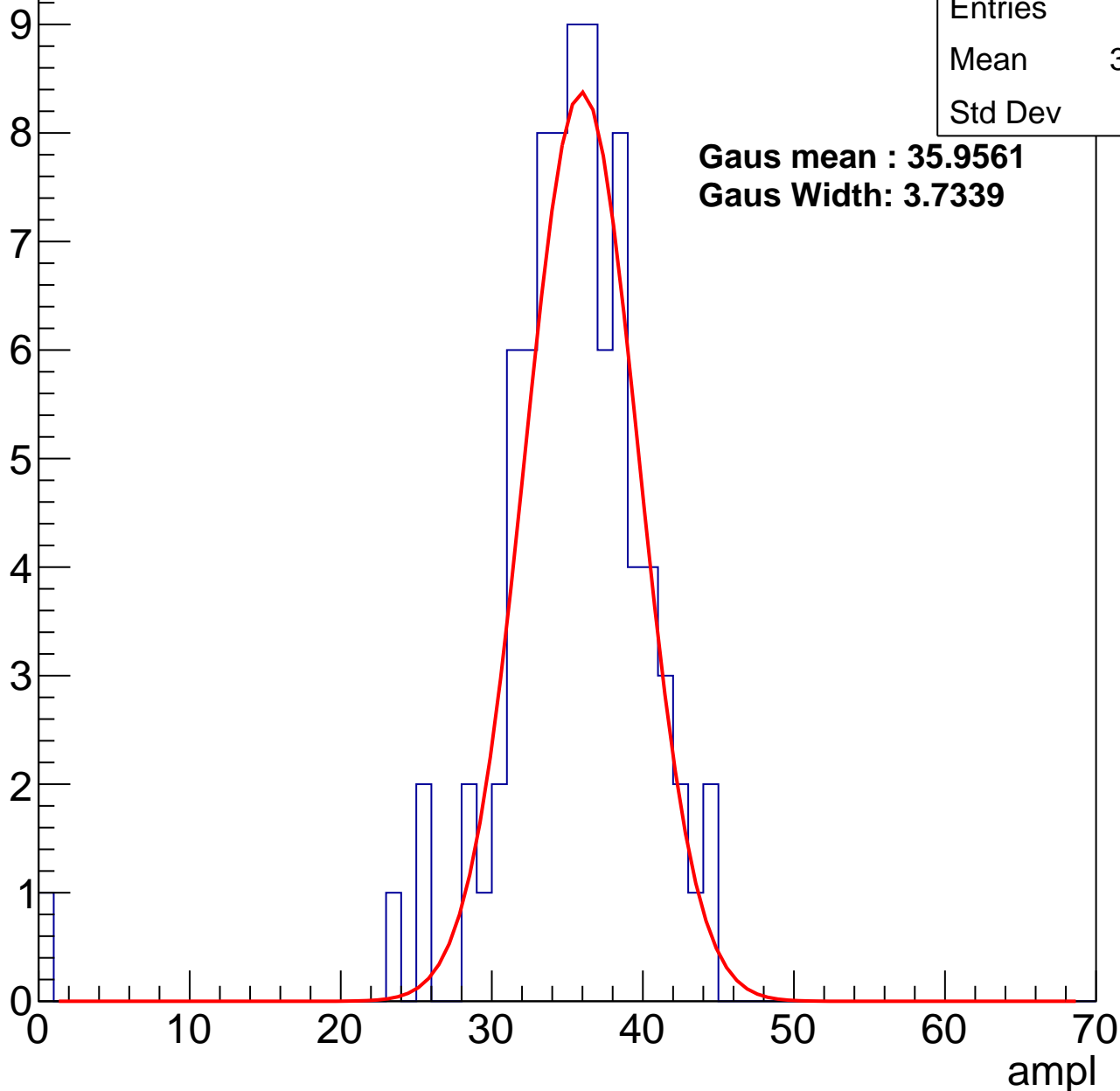
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	34.72
Std Dev	5.57

Gaus mean : 35.9561

Gaus Width: 3.7339



B1L103S, U24-ch99, adc2

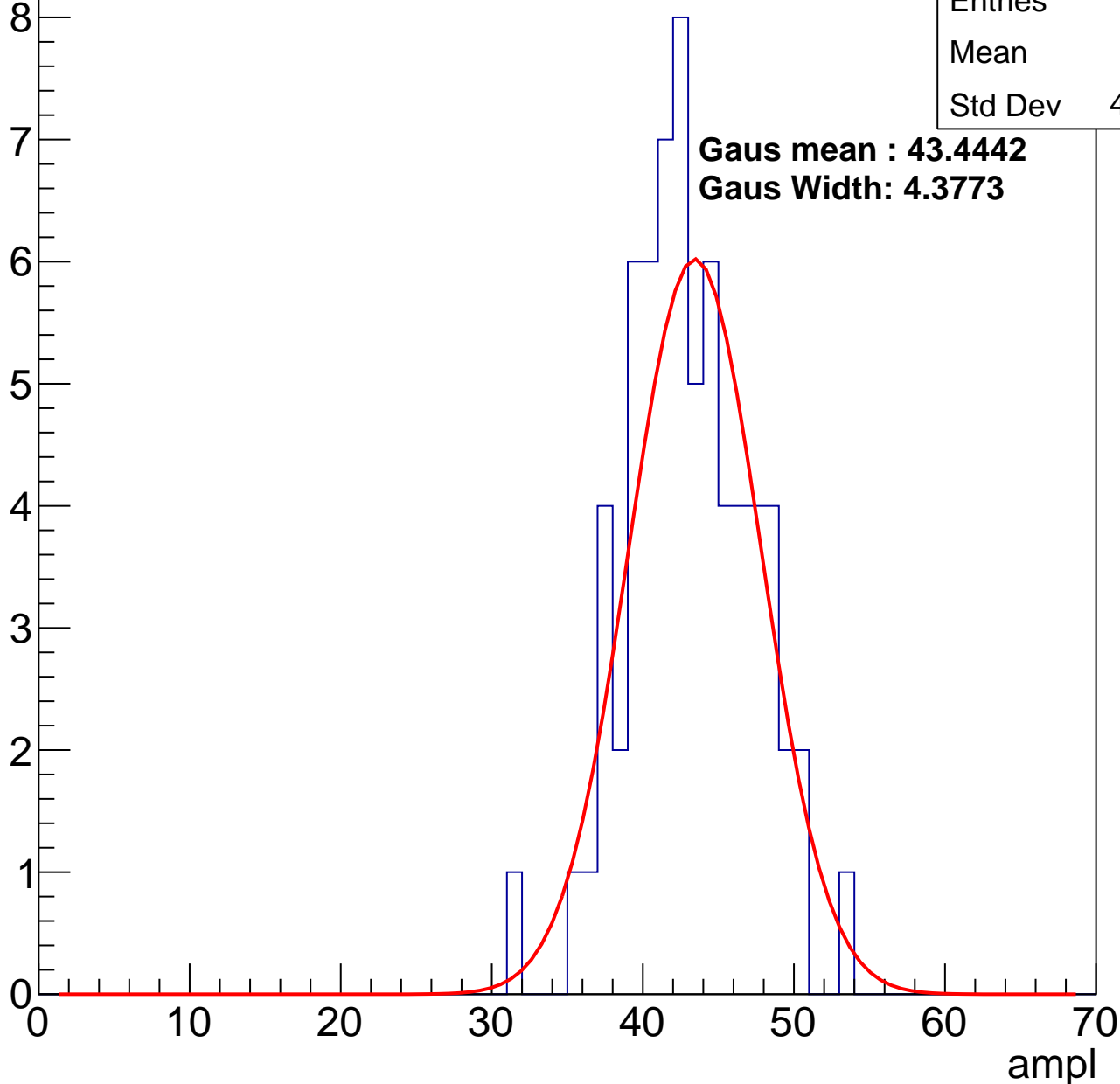
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.6
Std Dev	4.055

Gaus mean : 43.4442

Gaus Width: 4.3773

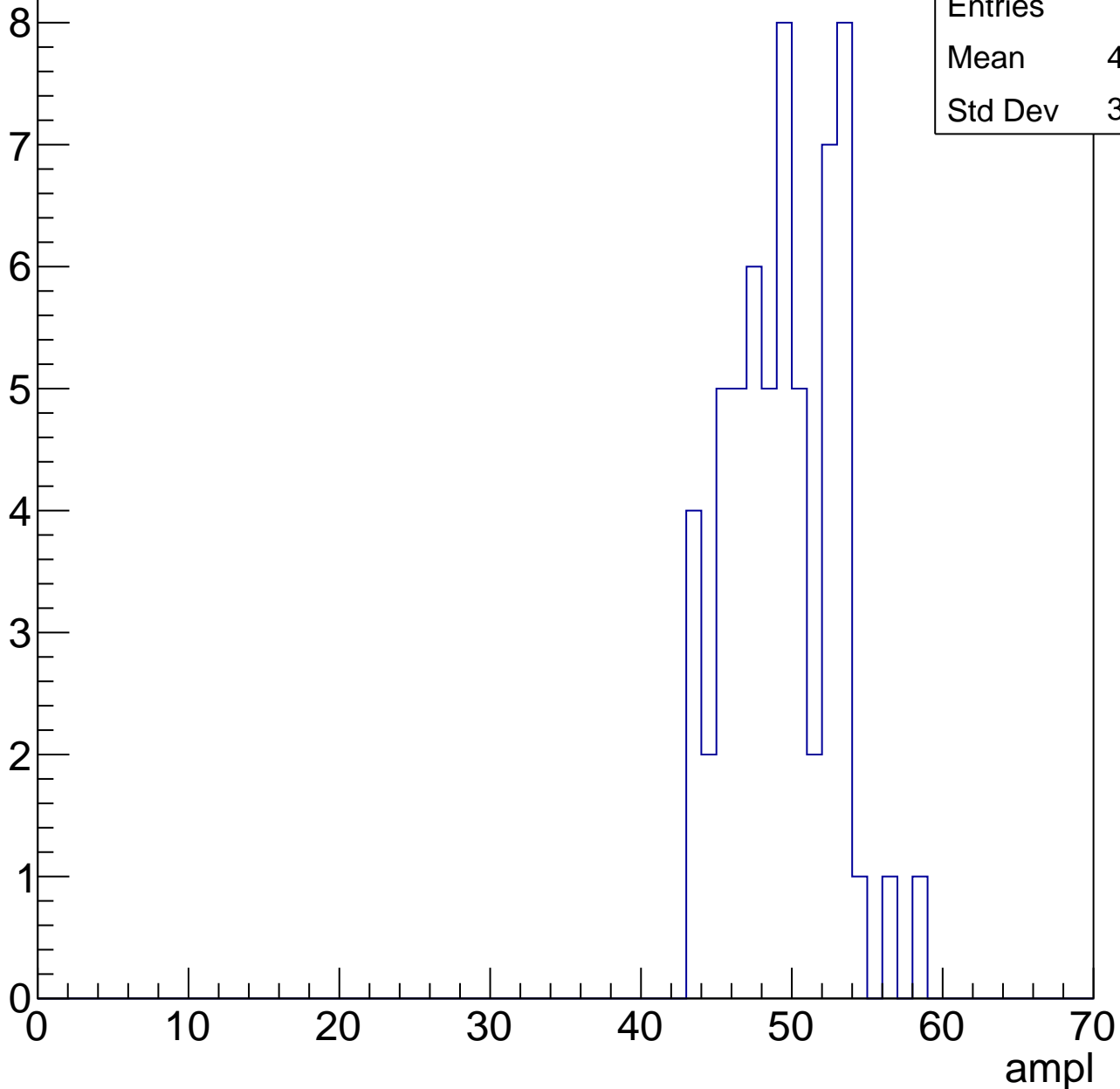


B1L103S, U24-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.95
Std Dev	3.432

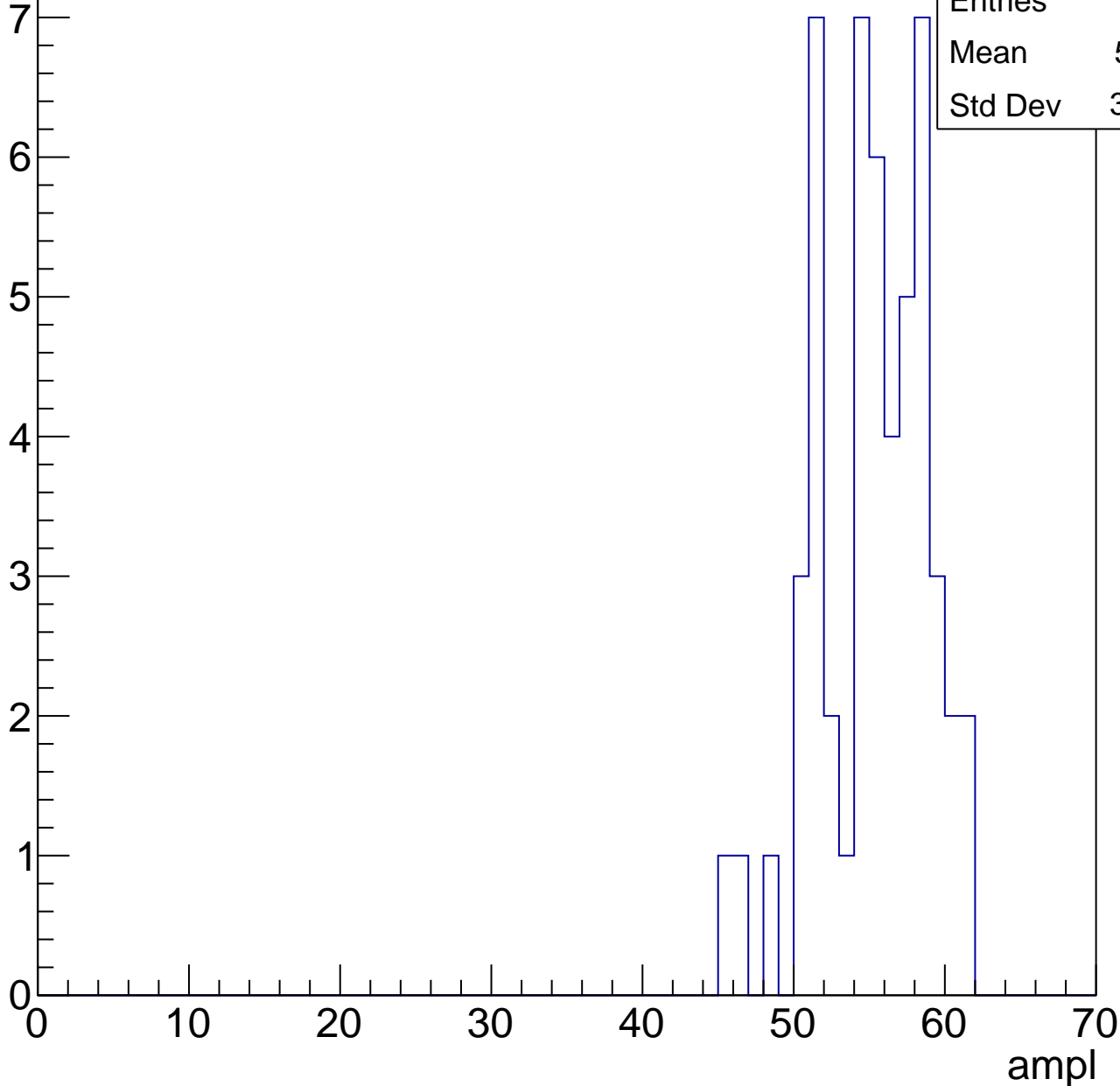


B1L103S, U24-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.71
Std Dev	3.666

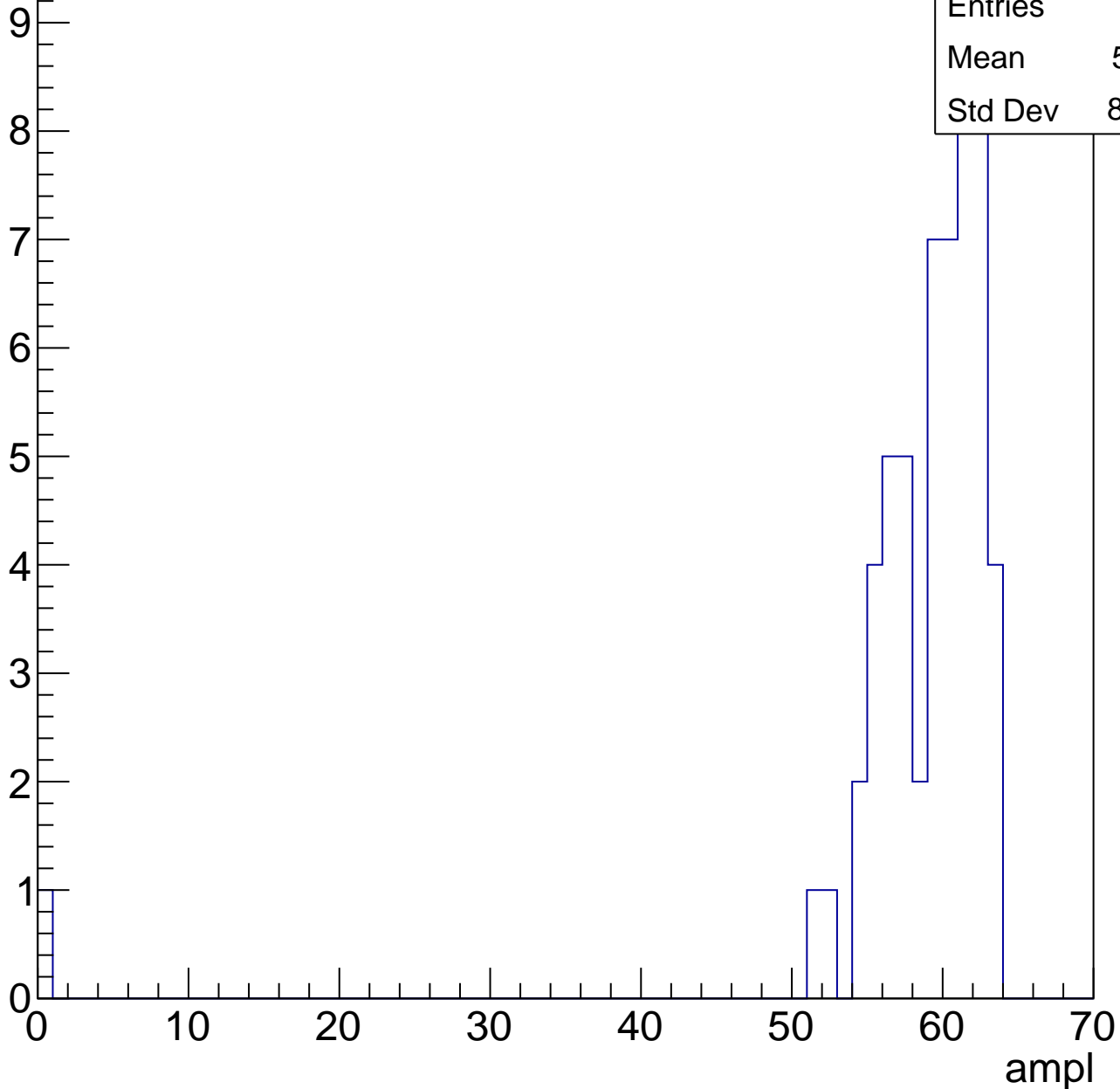


B1L103S, U24-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

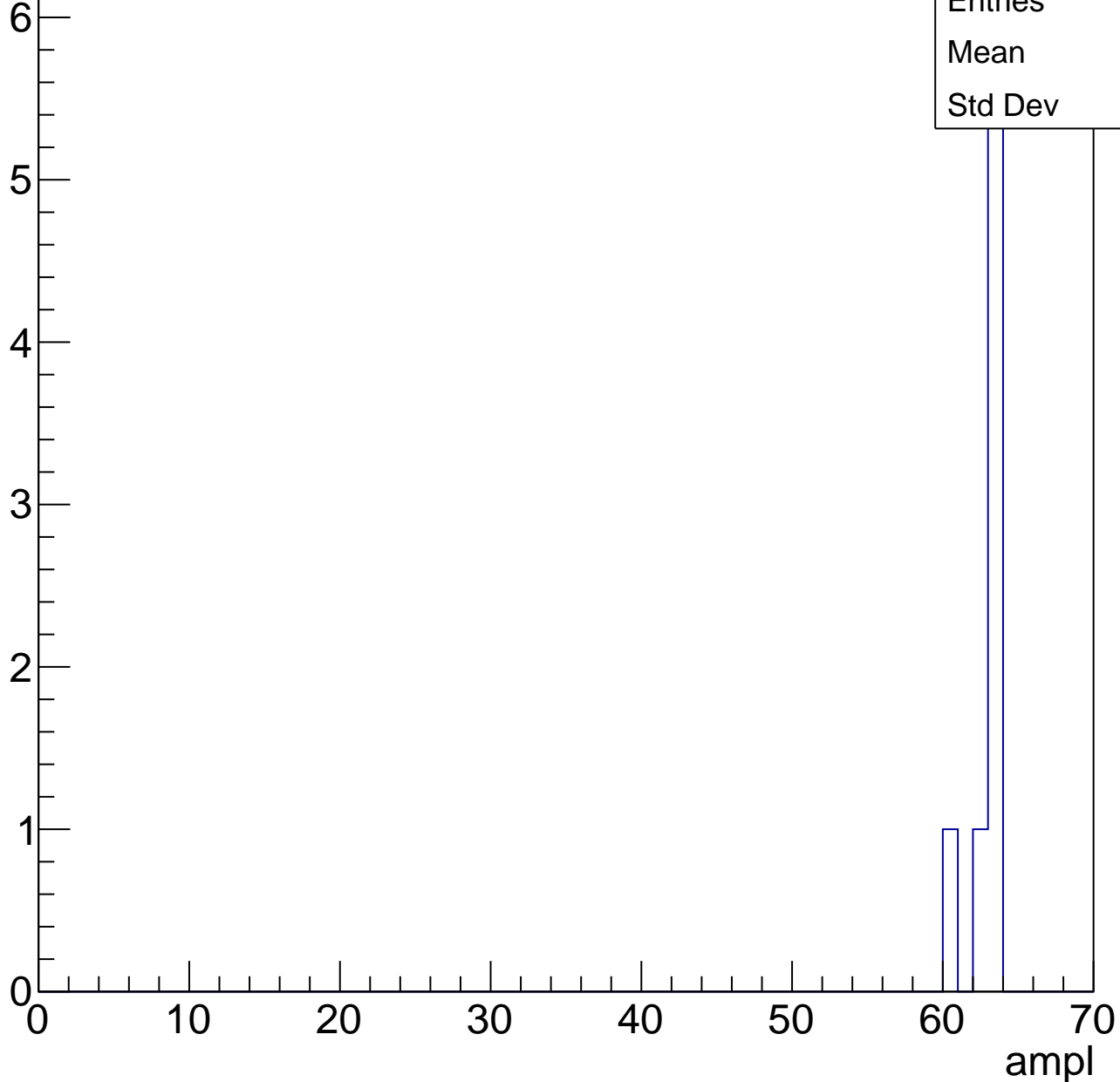
Entries	56
Mean	57.91
Std Dev	8.337



B1L103S, U24-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	8
Mean	62.5
Std Dev	1

B1L103S, U24-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U24-ch100, adc0

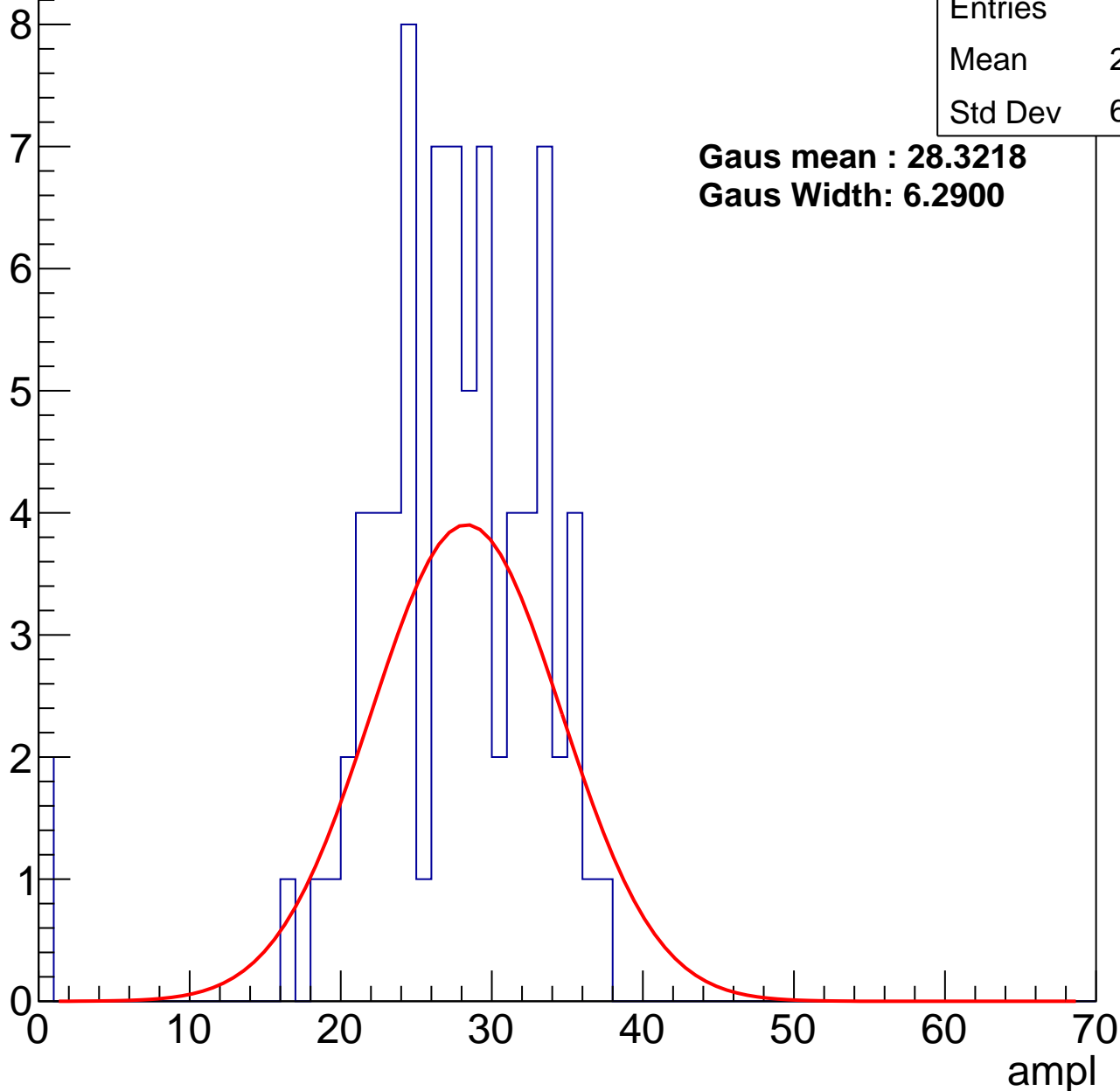
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	26.73
Std Dev	6.374

Gaus mean : 28.3218

Gaus Width: 6.2900



B1L103S, U24-ch100, adc1

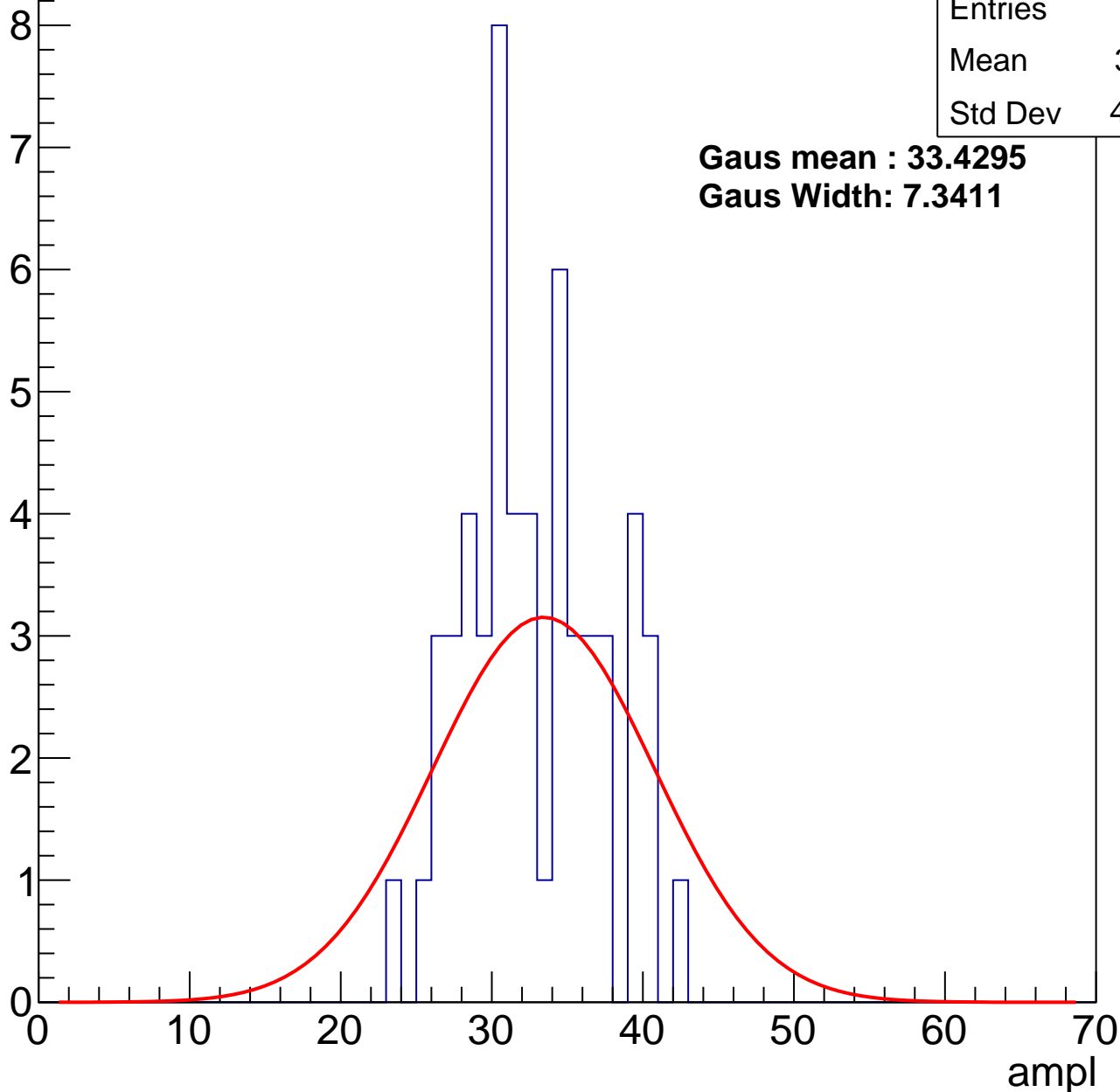
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	32.31
Std Dev	4.468

Gaus mean : 33.4295

Gaus Width: 7.3411



B1L103S, U24-ch100, adc2

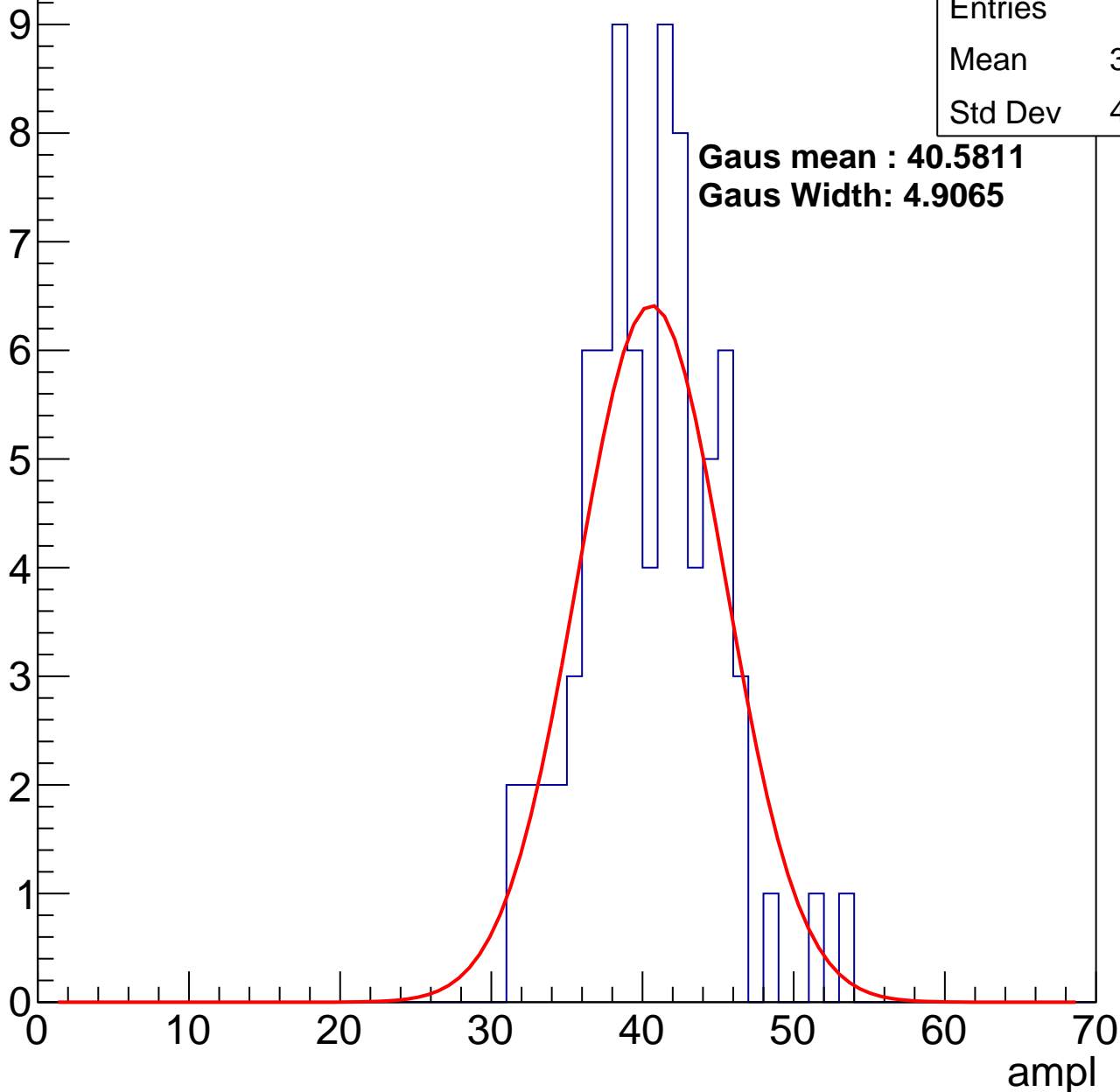
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	39.95
Std Dev	4.324

Gaus mean : 40.5811

Gaus Width: 4.9065

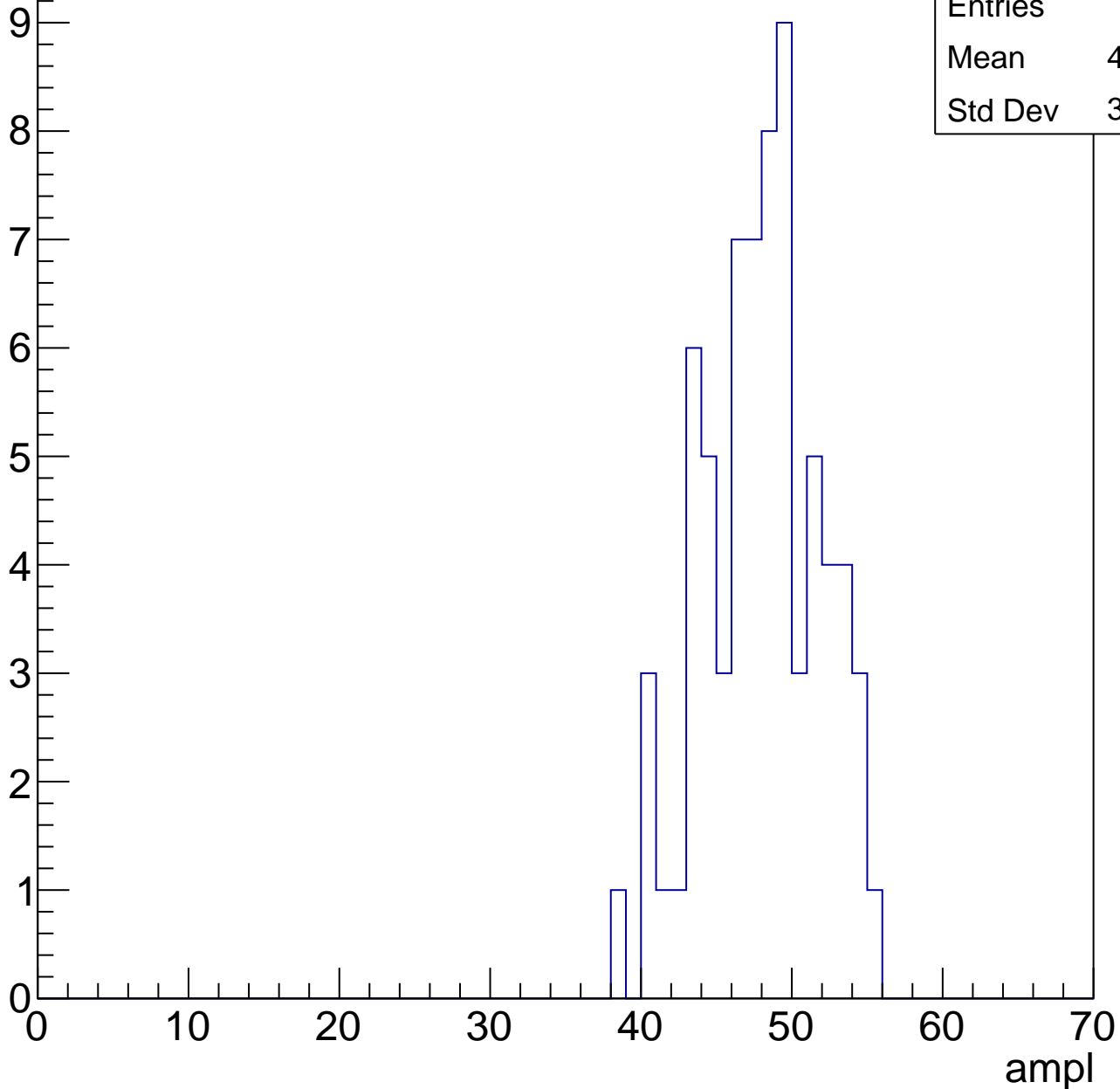


B1L103S, U24-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	47.49
Std Dev	3.823

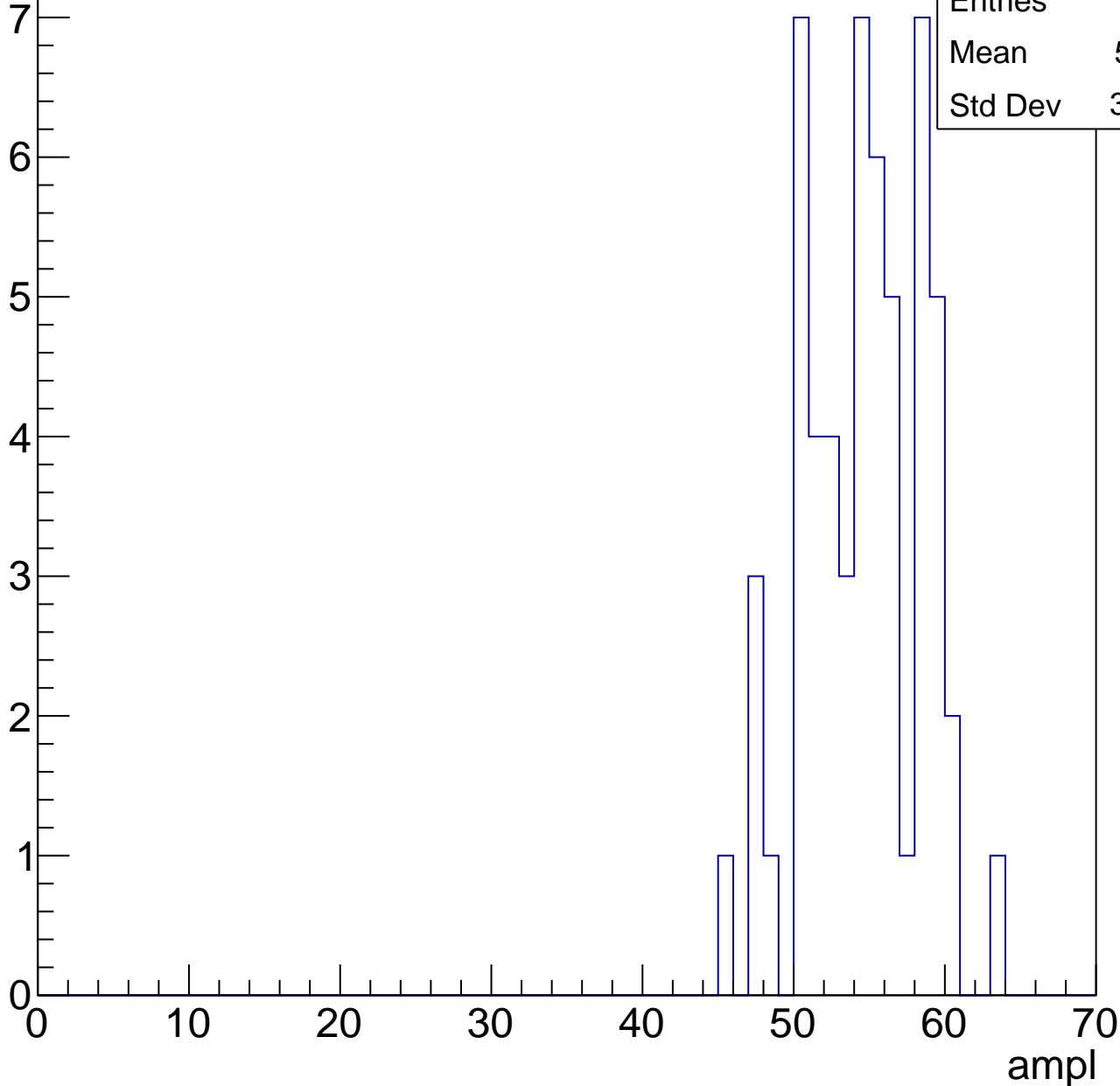


B1L103S, U24-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

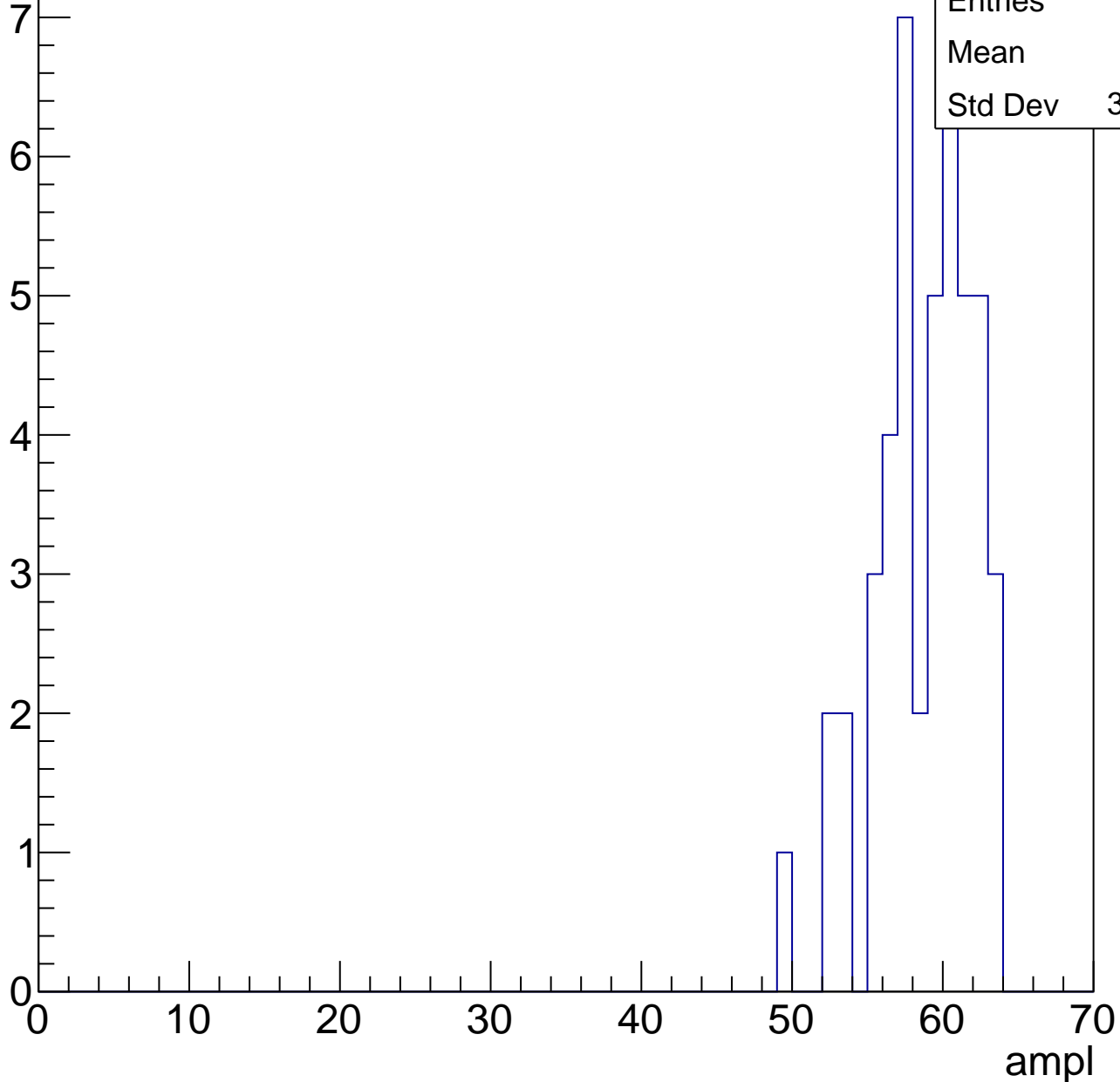
Entries	57
Mean	54.11
Std Dev	3.874



B1L103S, U24-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

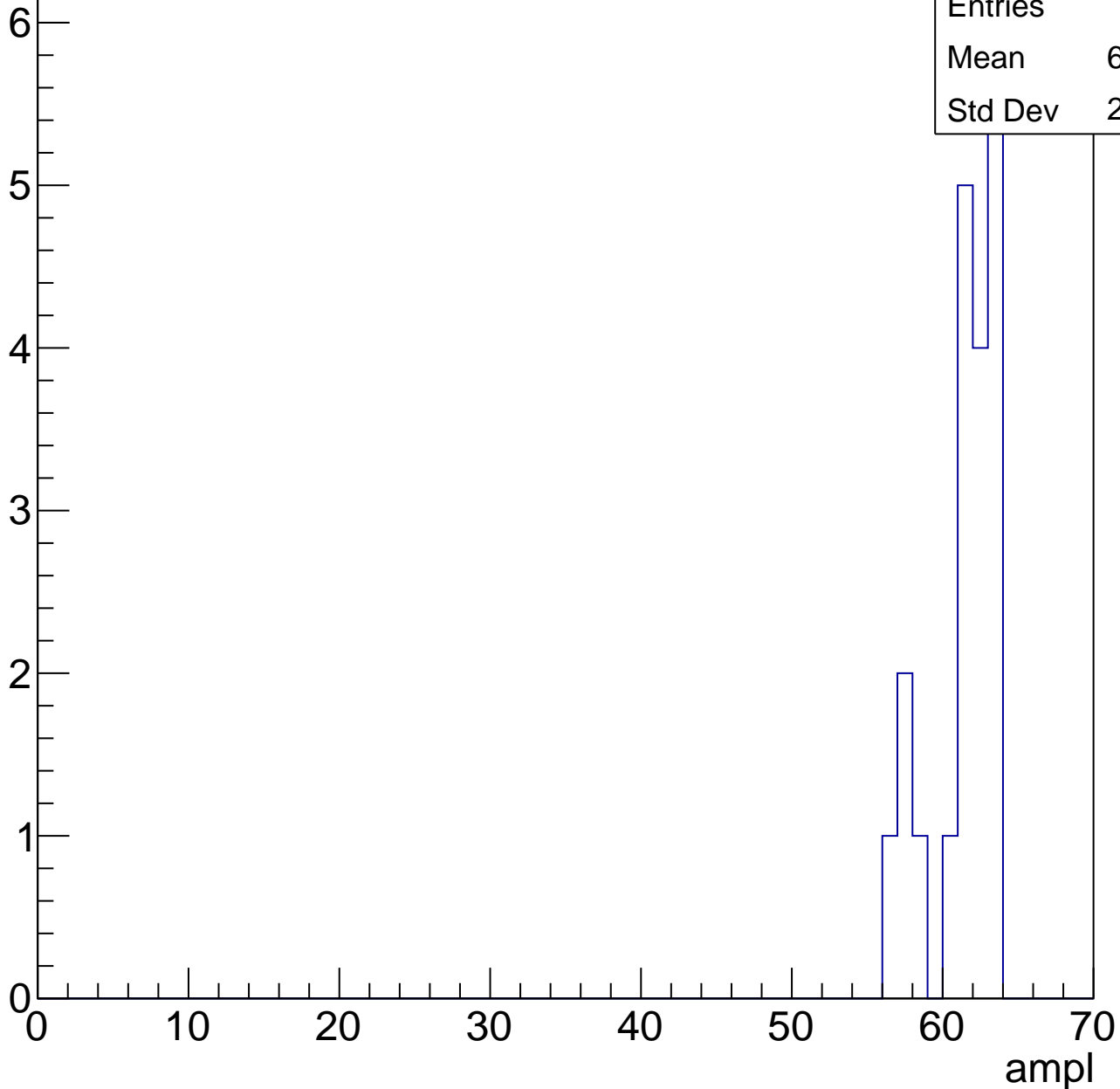


B1L103S, U24-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	60.95
Std Dev	2.179



B1L103S, U24-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch101, adc0

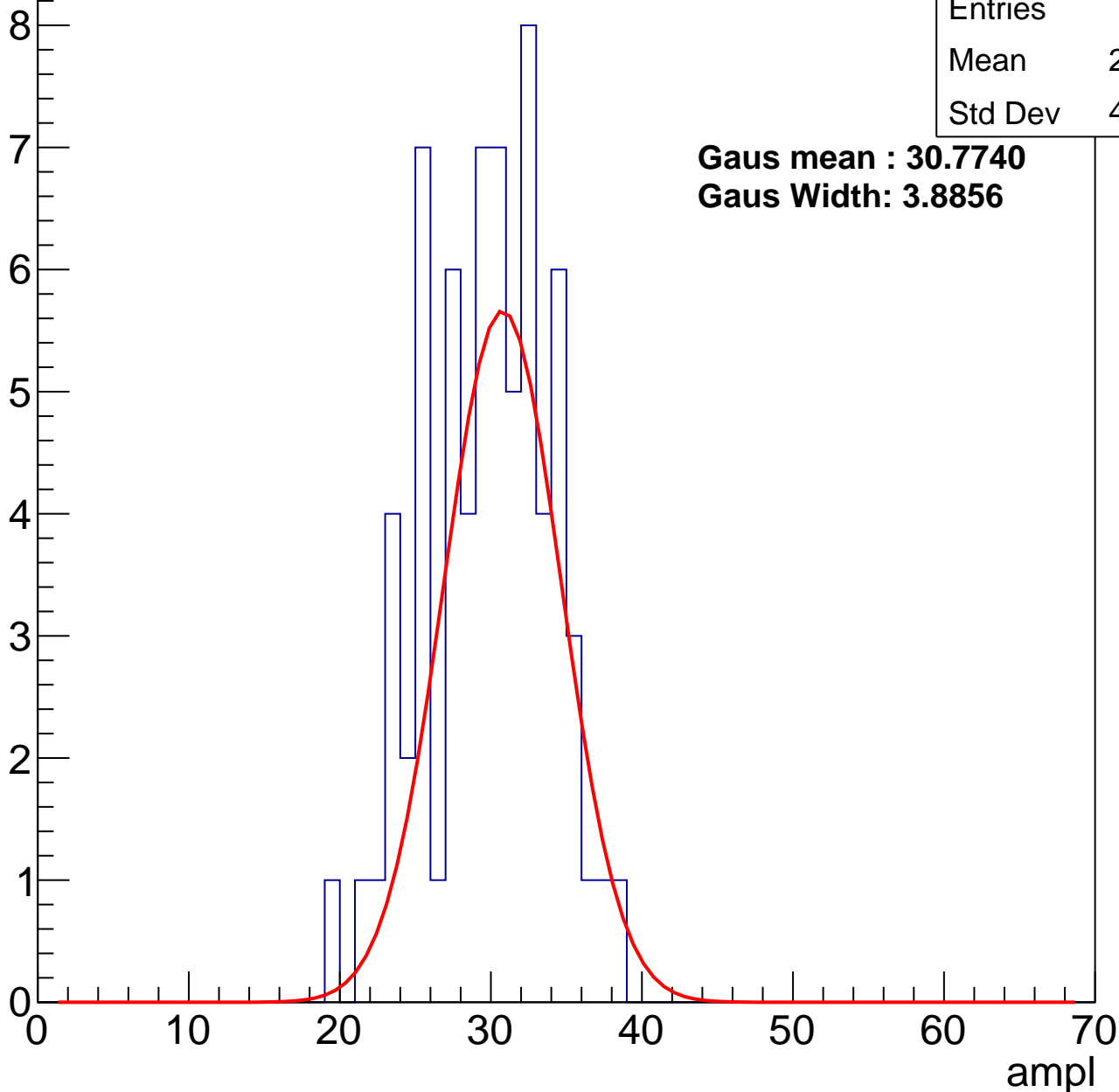
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.33
Std Dev	4.077

Gaus mean : 30.7740

Gaus Width: 3.8856



B1L103S, U24-ch101, adc1

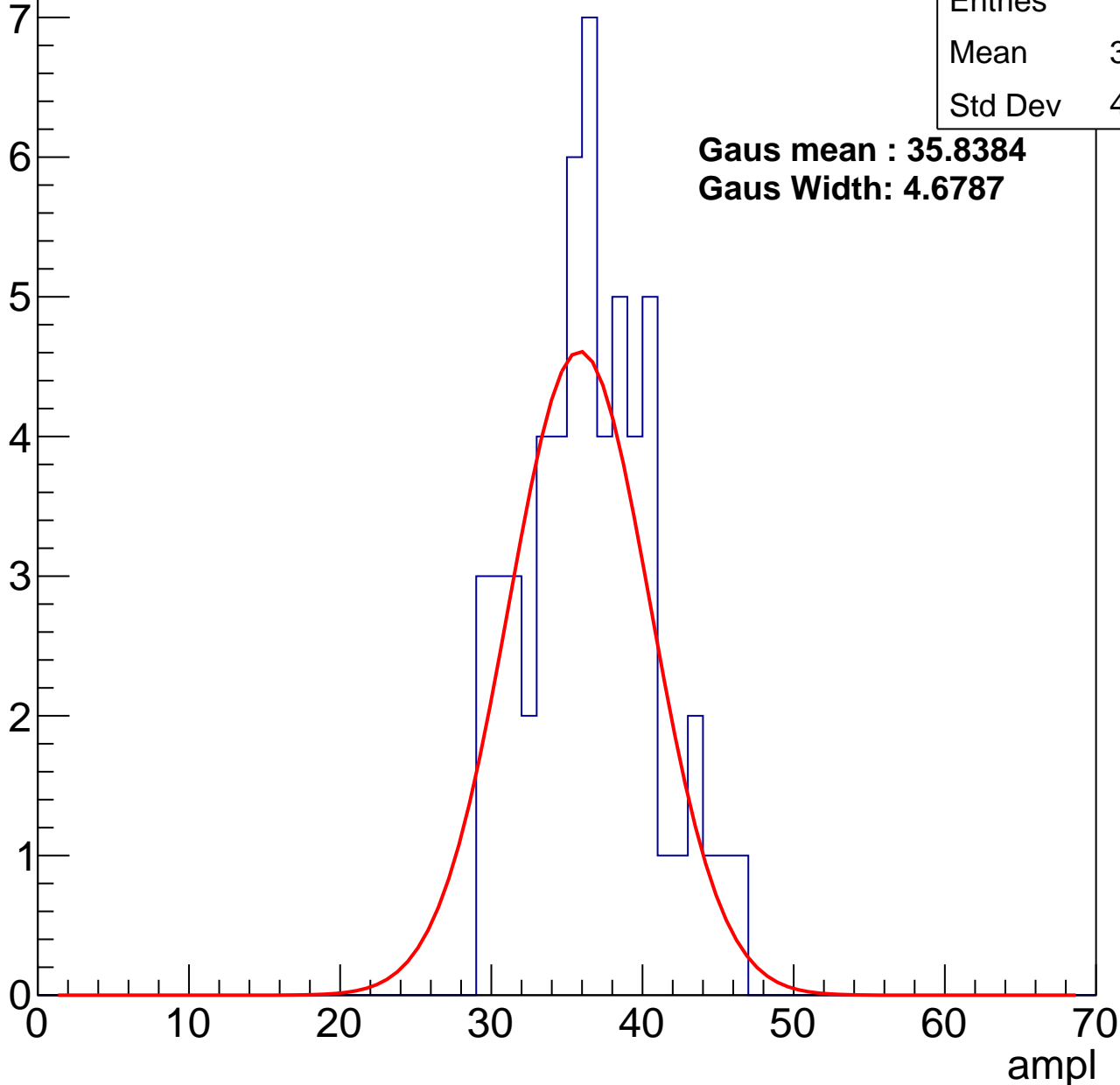
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.18
Std Dev	4.109

Gaus mean : 35.8384

Gaus Width: 4.6787

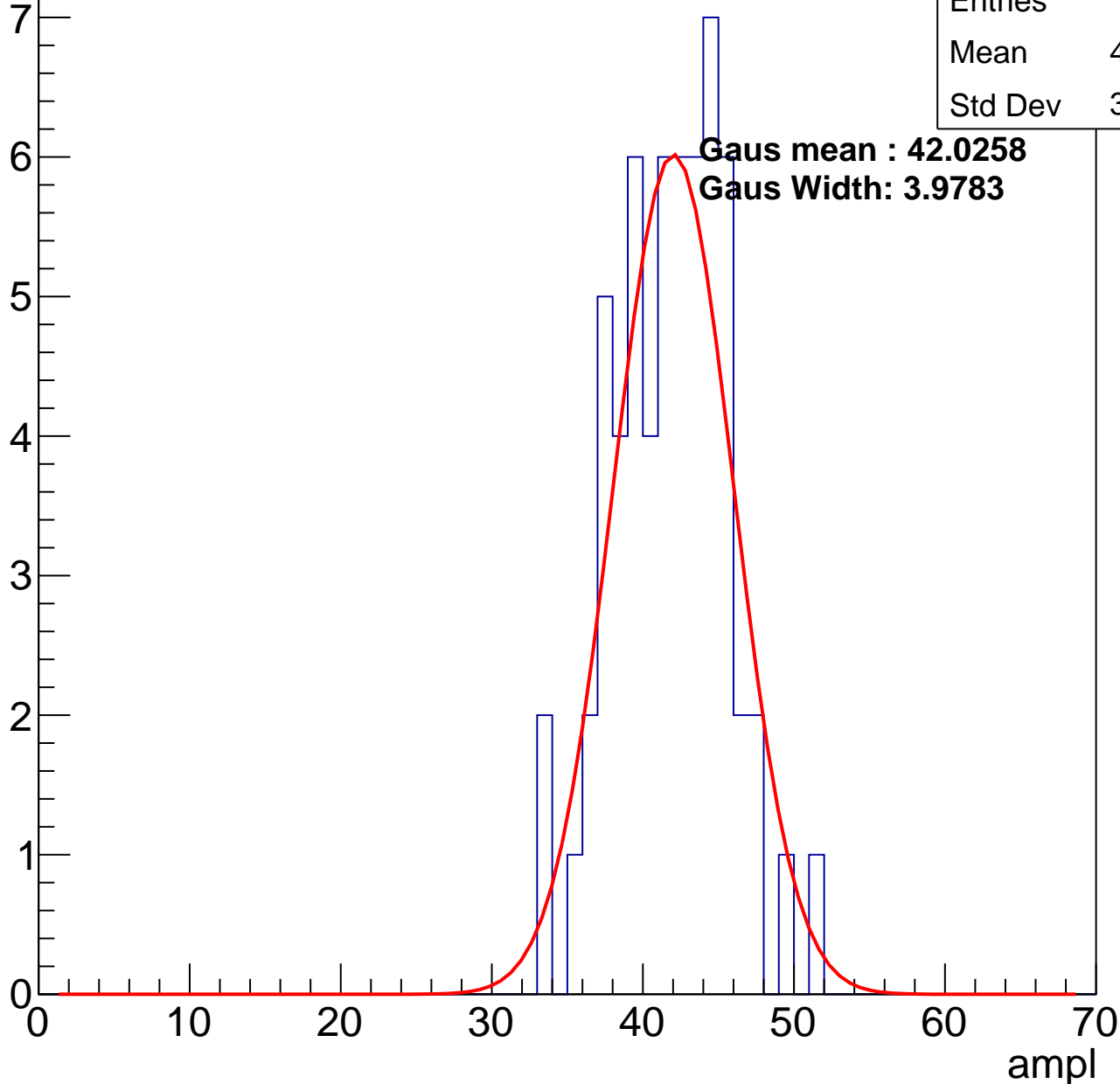


B1L103S, U24-ch101, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.38
Std Dev	3.676

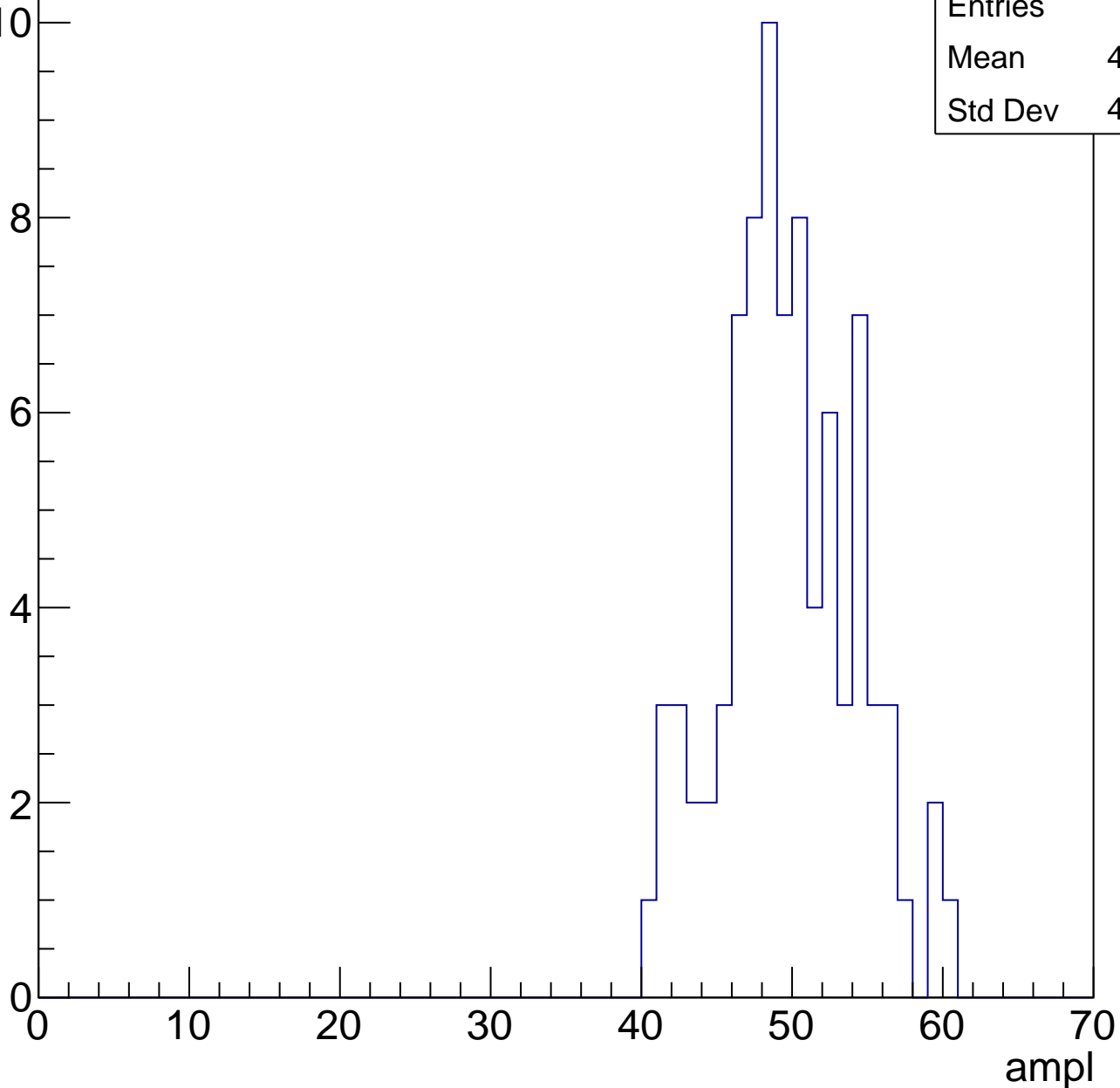


B1L103S, U24-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	49.29
Std Dev	4.404

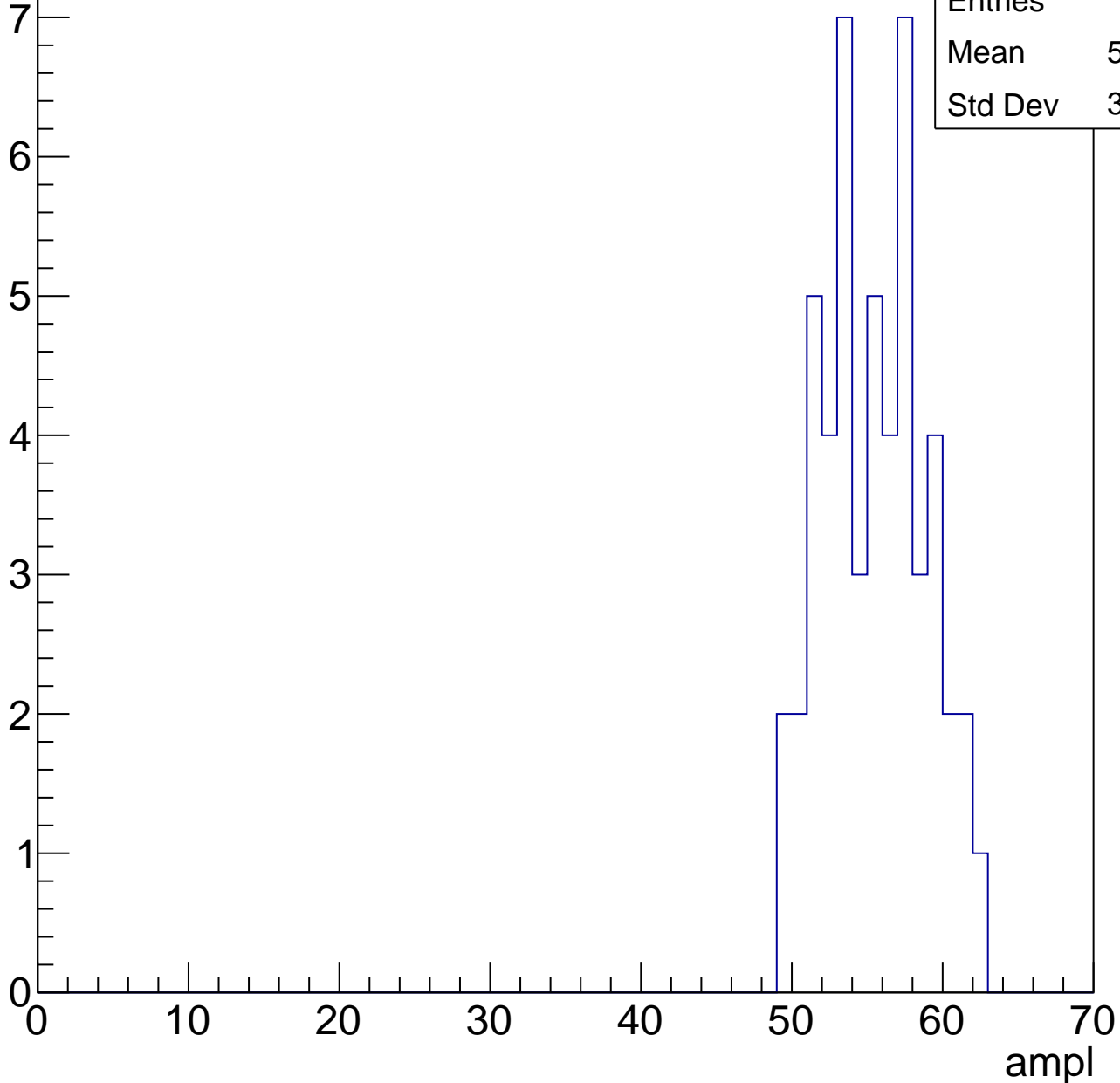


B1L103S, U24-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.02
Std Dev	3.322

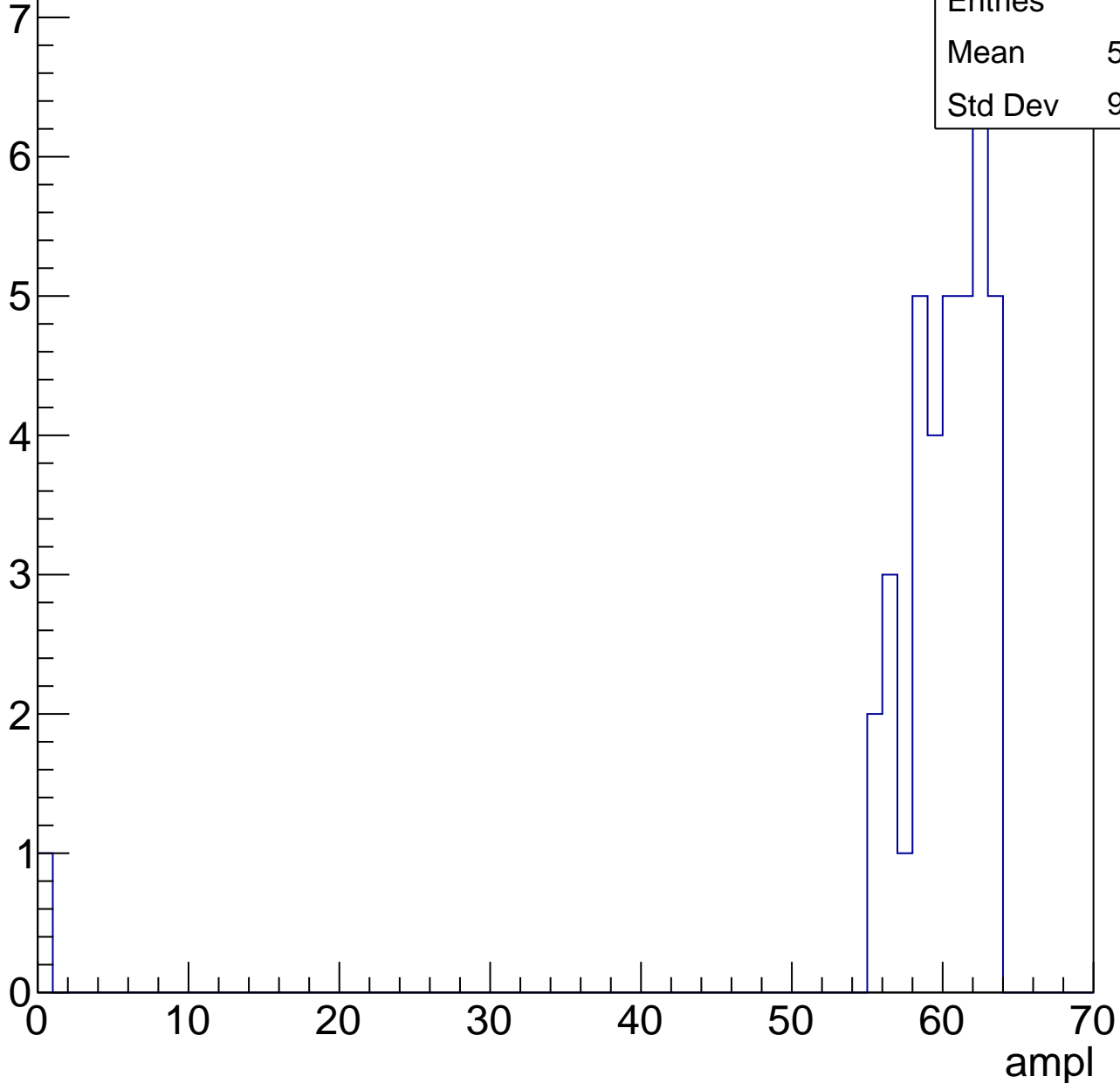


B1L103S, U24-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.29
Std Dev	9.865

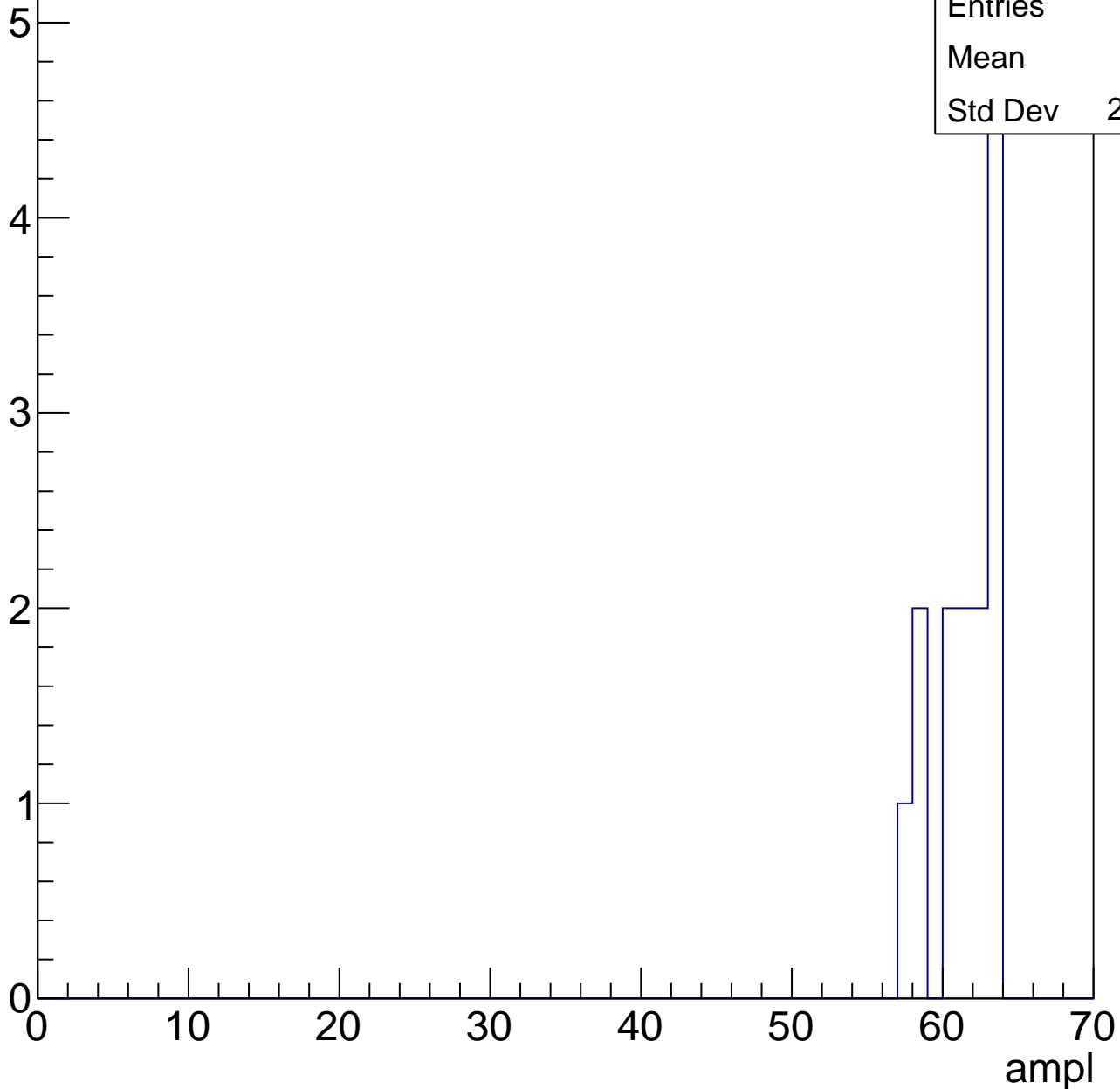


B1L103S, U24-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61
Std Dev	2.035



B1L103S, U24-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch102, adc0

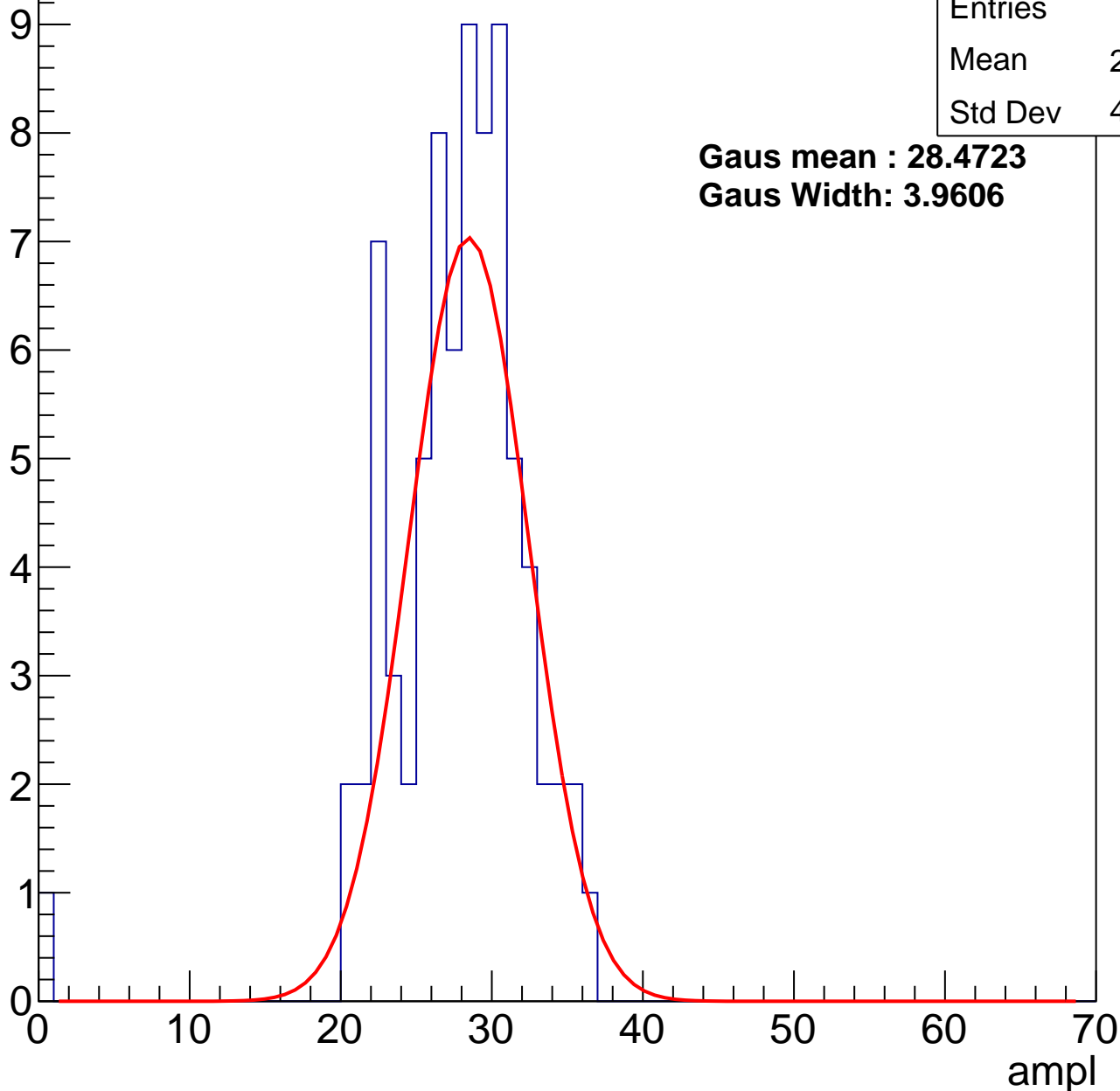
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.24
Std Dev	4.863

Gaus mean : 28.4723

Gaus Width: 3.9606



B1L103S, U24-ch102, adc1

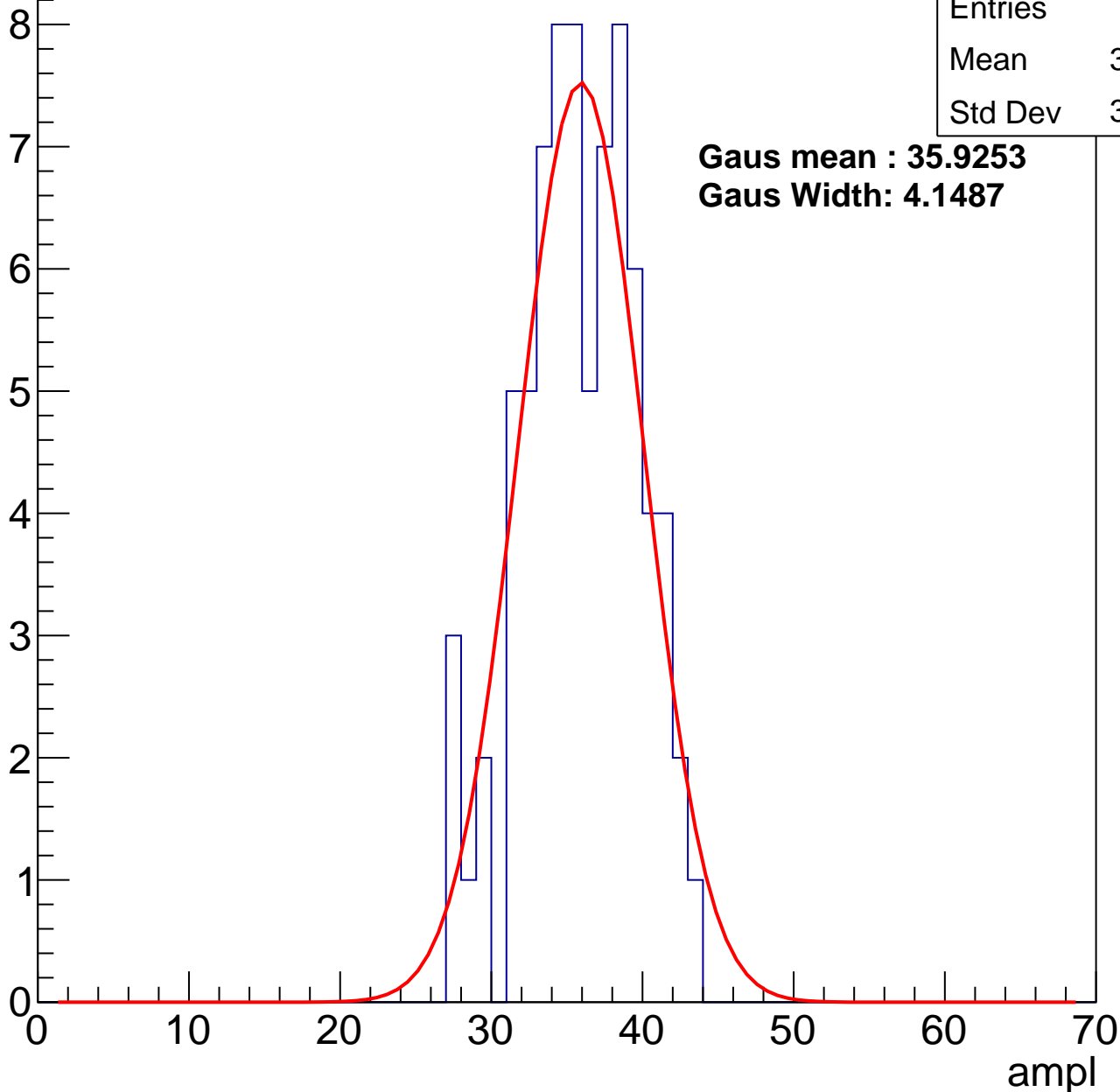
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.43
Std Dev	3.746

Gaus mean : 35.9253

Gaus Width: 4.1487



B1L103S, U24-ch102, adc2

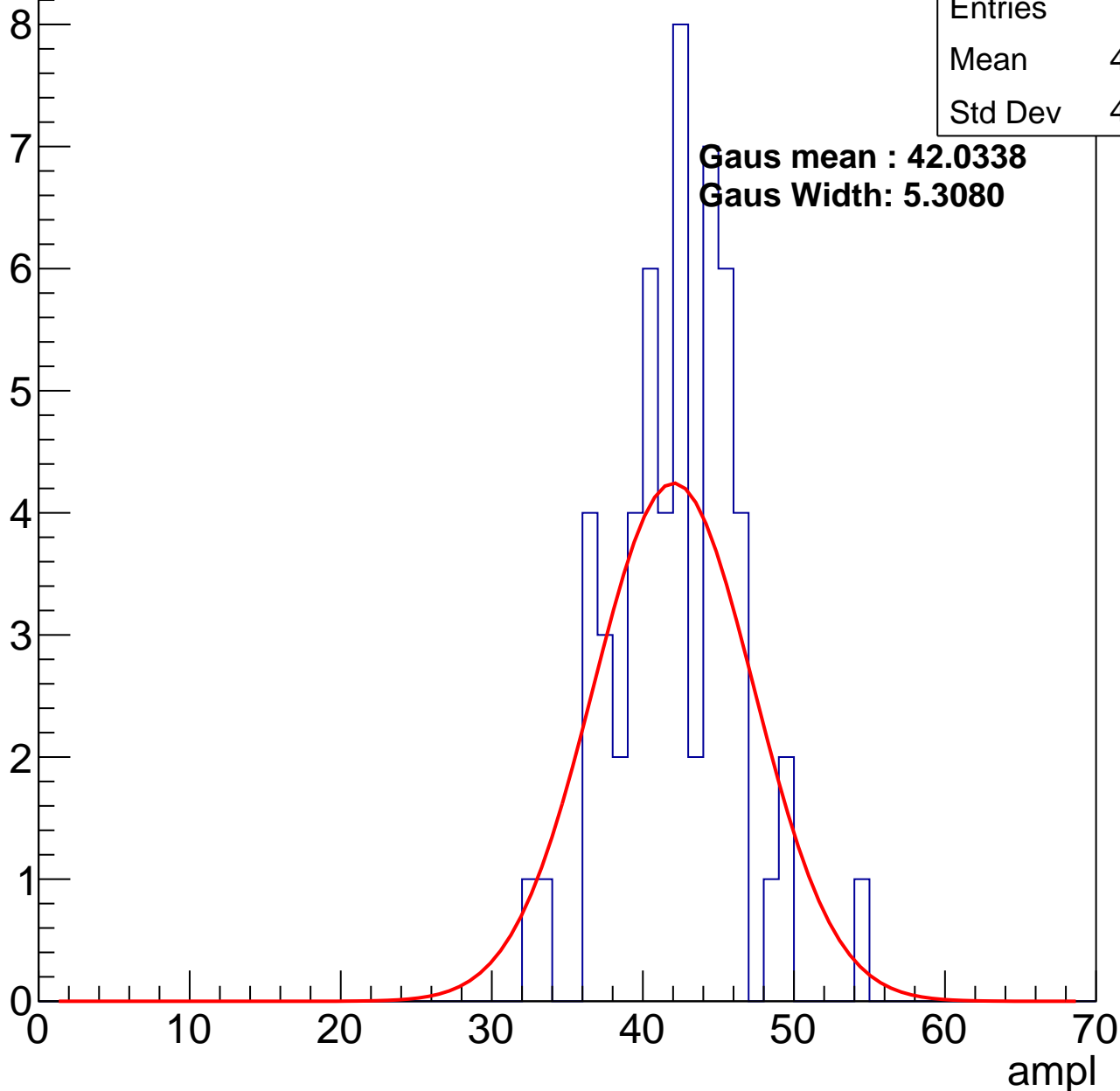
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	41.79
Std Dev	4.052

Gaus mean : 42.0338

Gaus Width: 5.3080

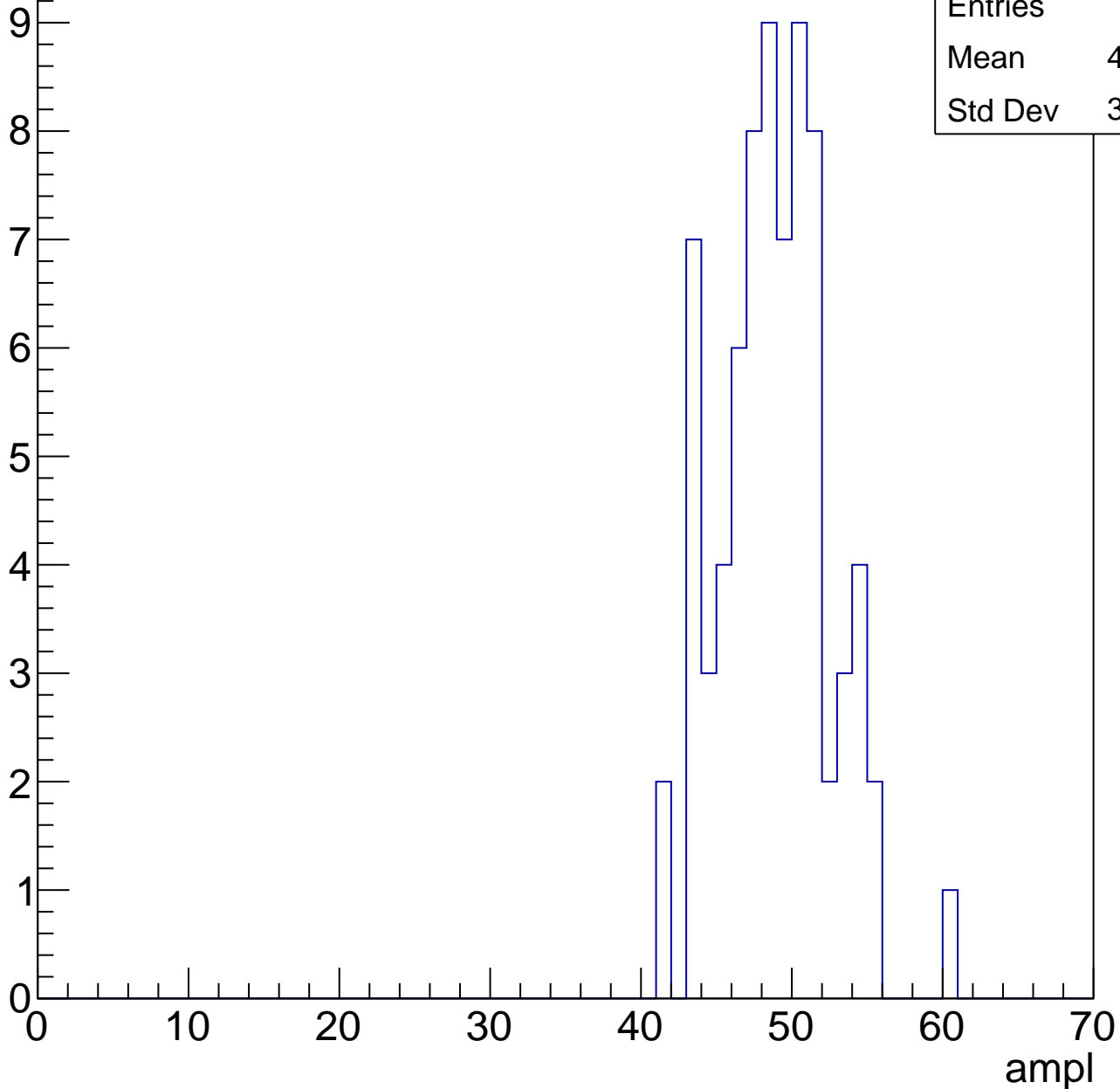


B1L103S, U24-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.39
Std Dev	3.625

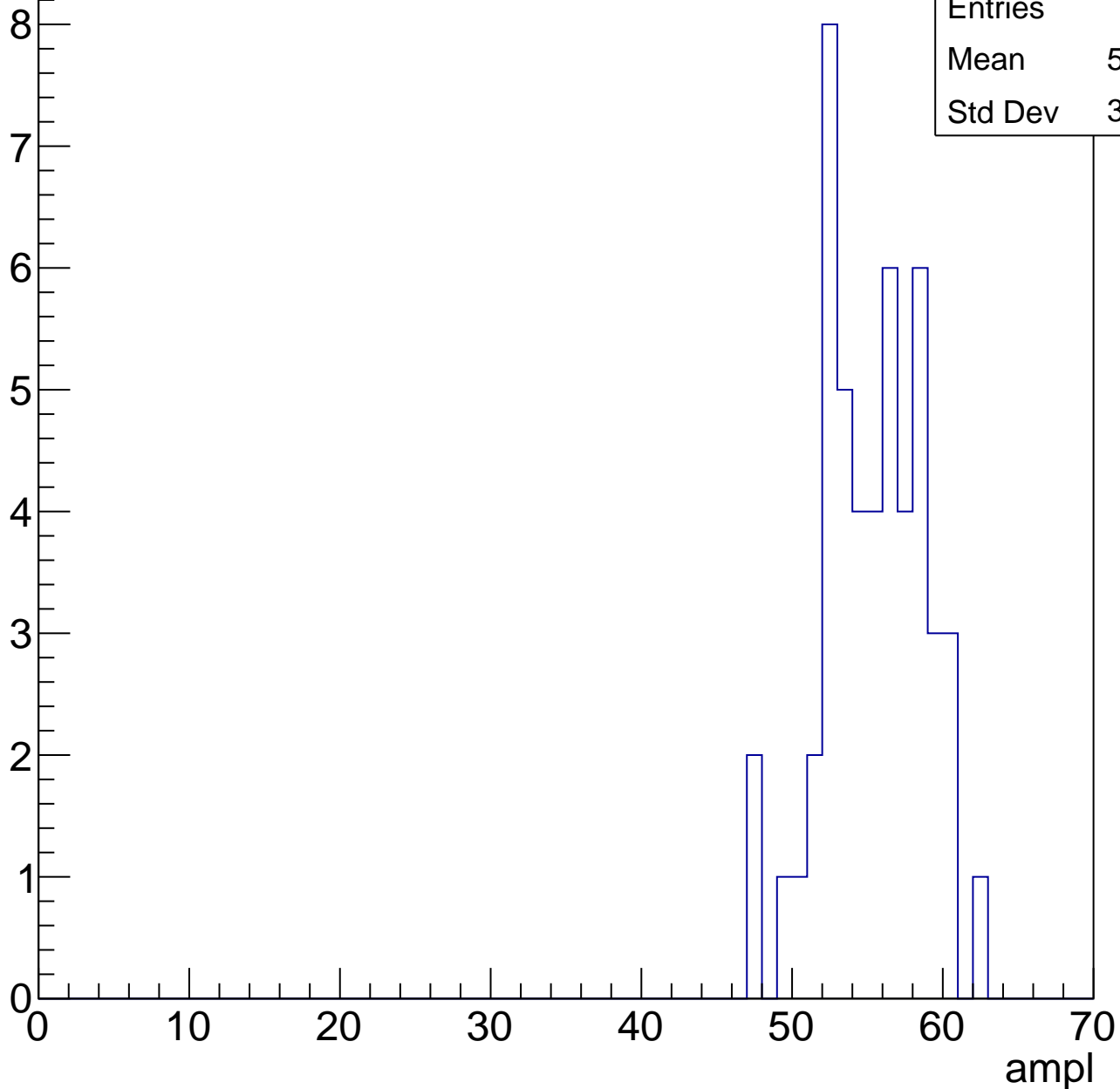


B1L103S, U24-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.86
Std Dev	3.359

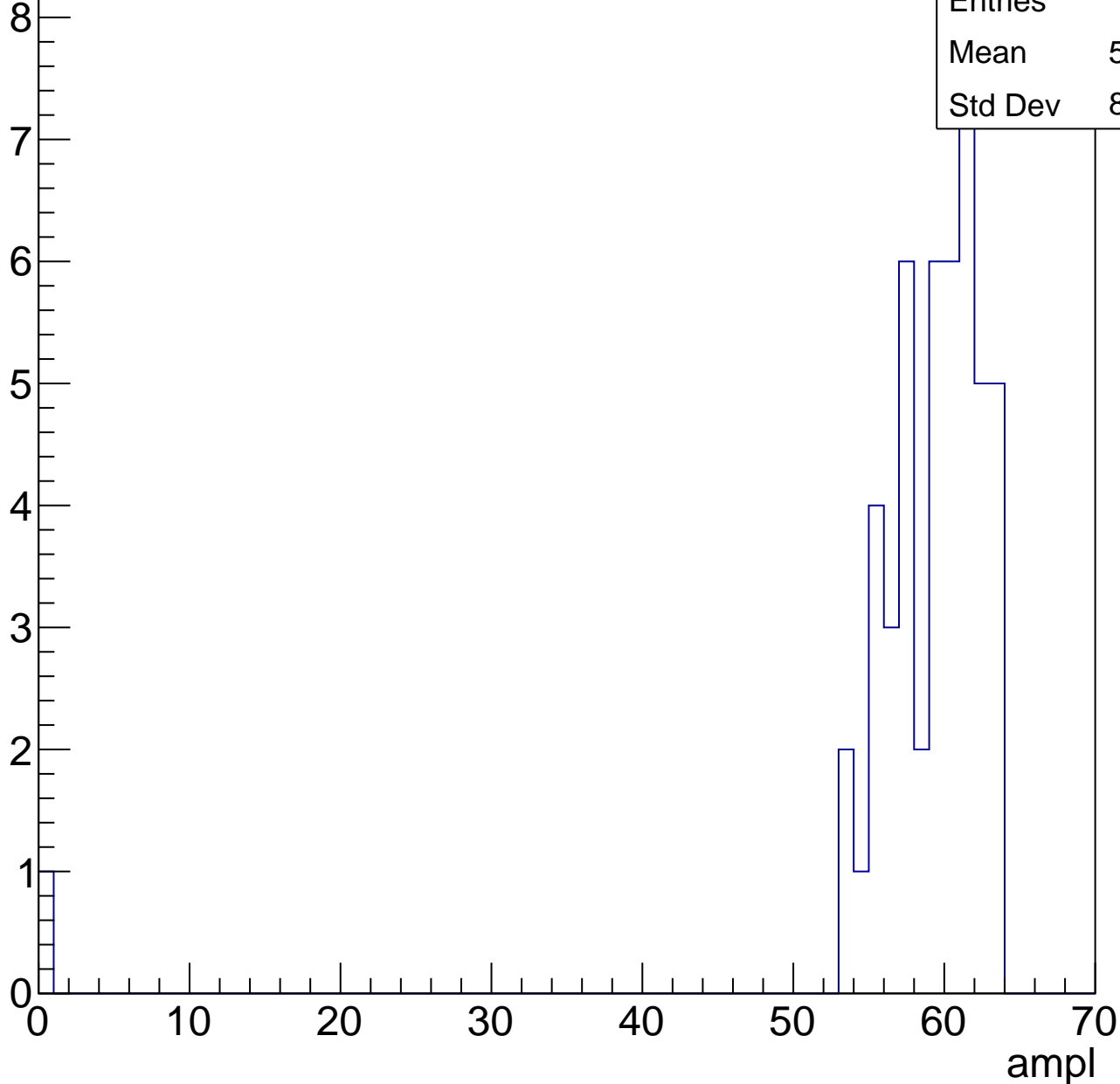


B1L103S, U24-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

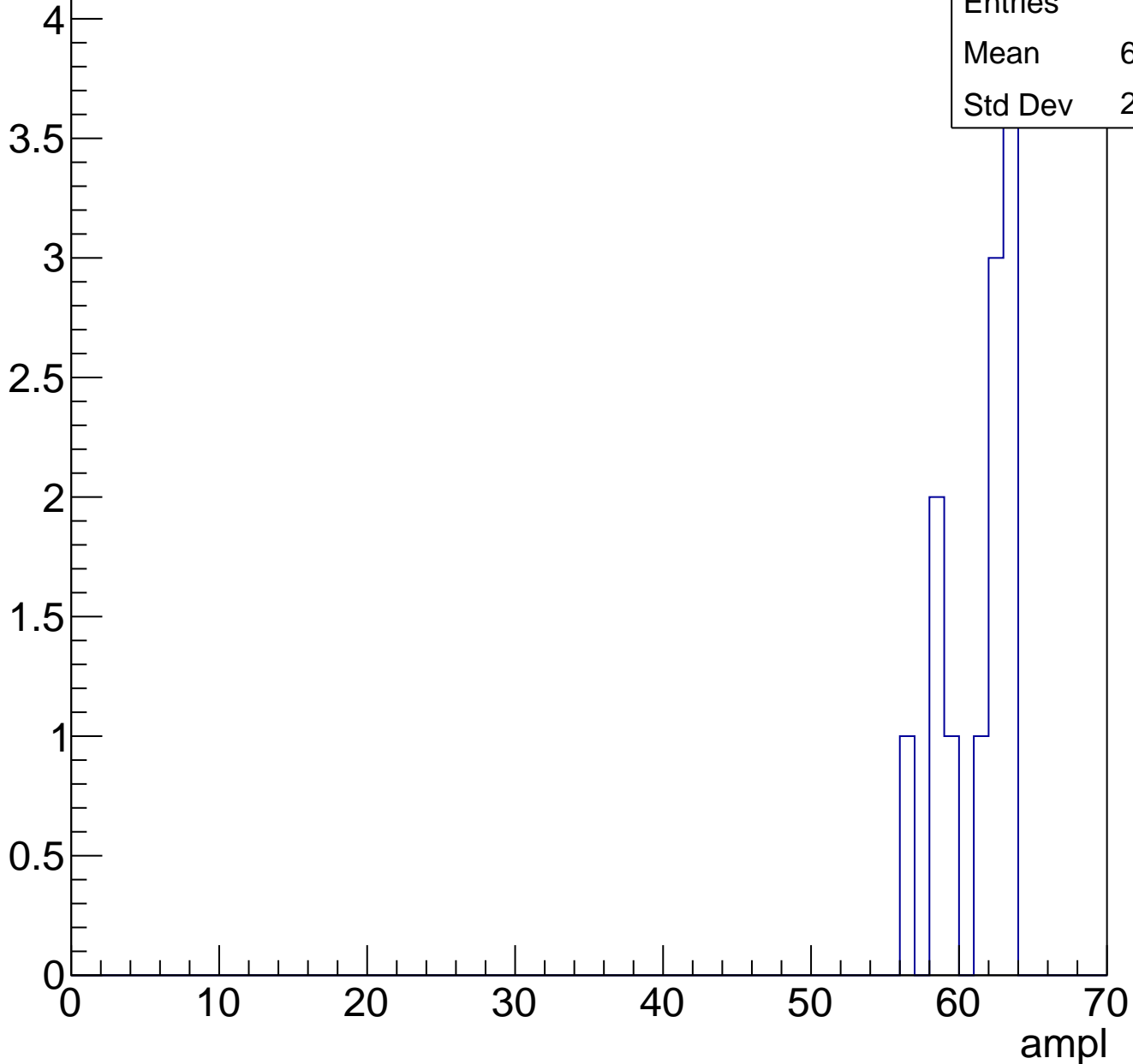
Entries	49
Mean	57.82
Std Dev	8.794



B1L103S, U24-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

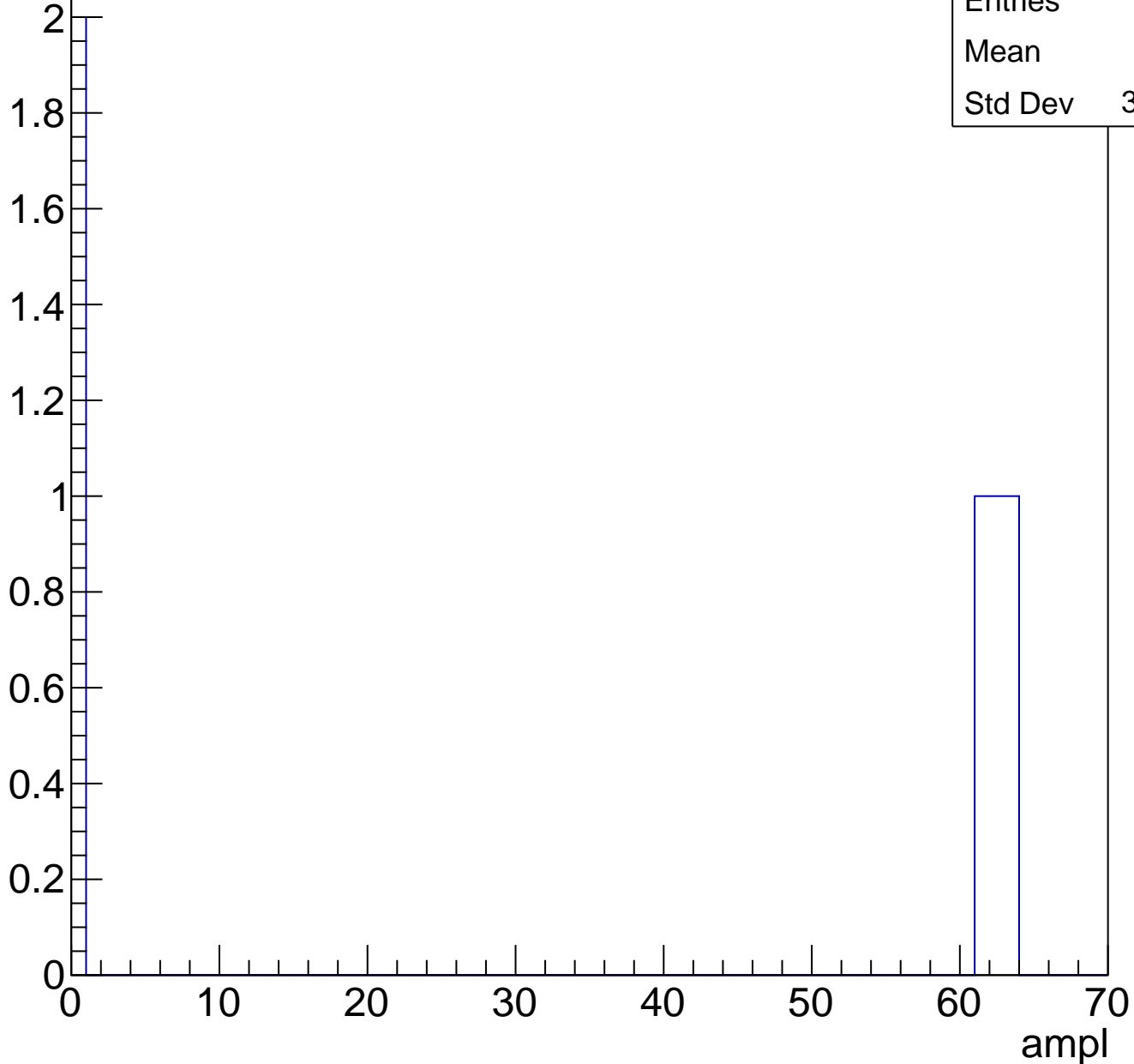
Entry



B1L103S, U24-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch103, adc0

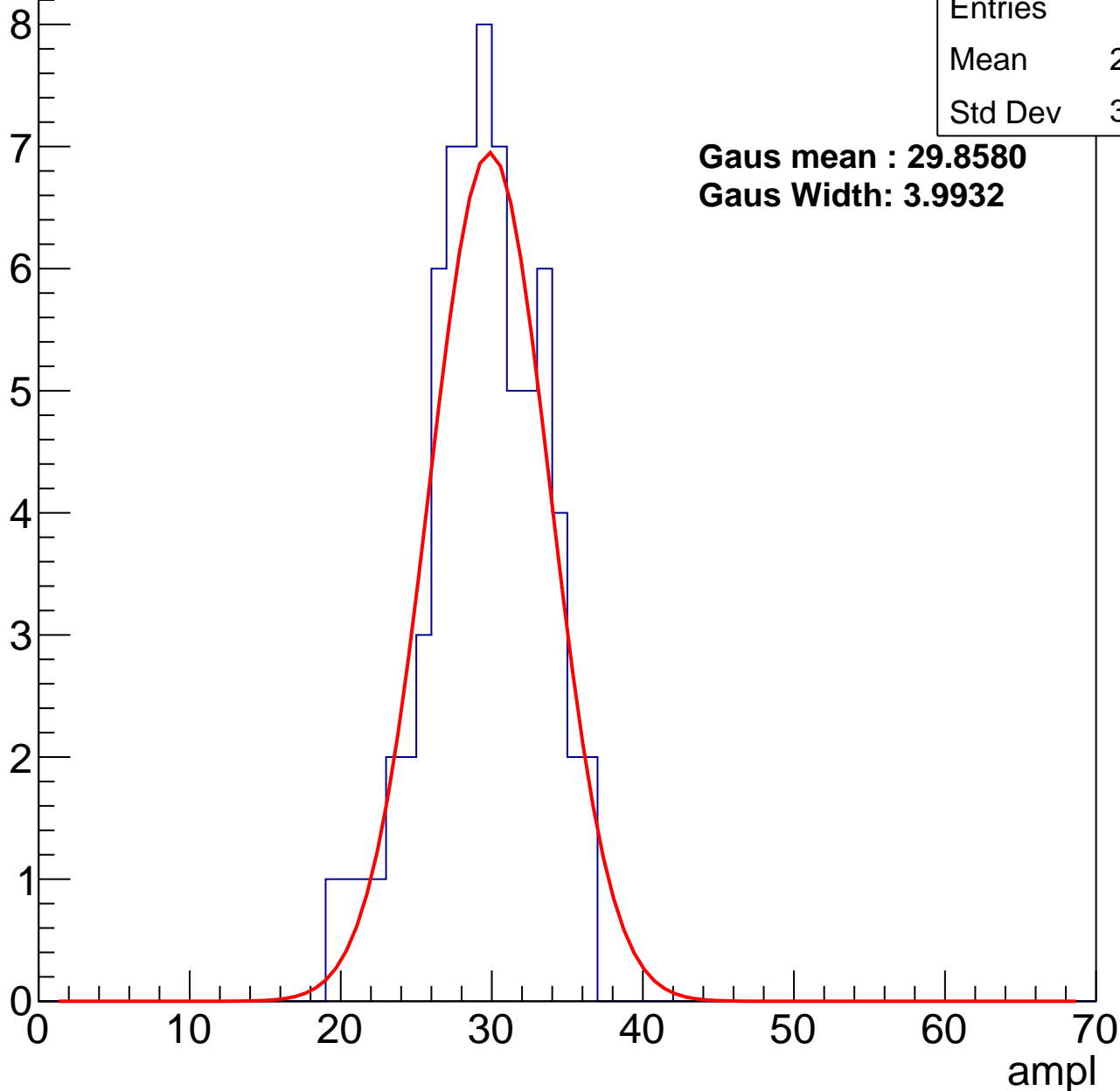
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.93
Std Dev	3.762

Gaus mean : 29.8580

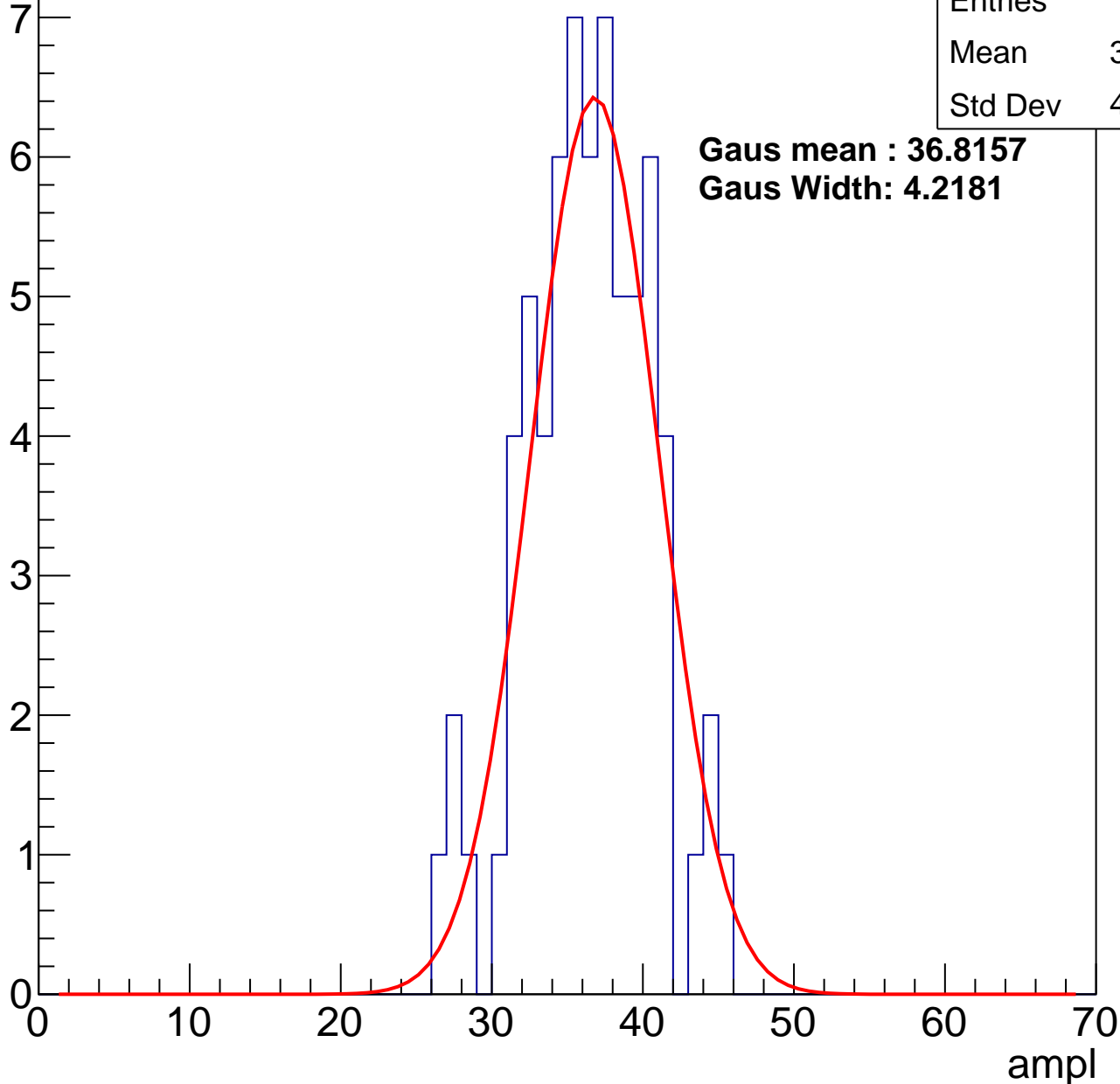
Gaus Width: 3.9932



B1L103S, U24-ch103, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch103, adc2

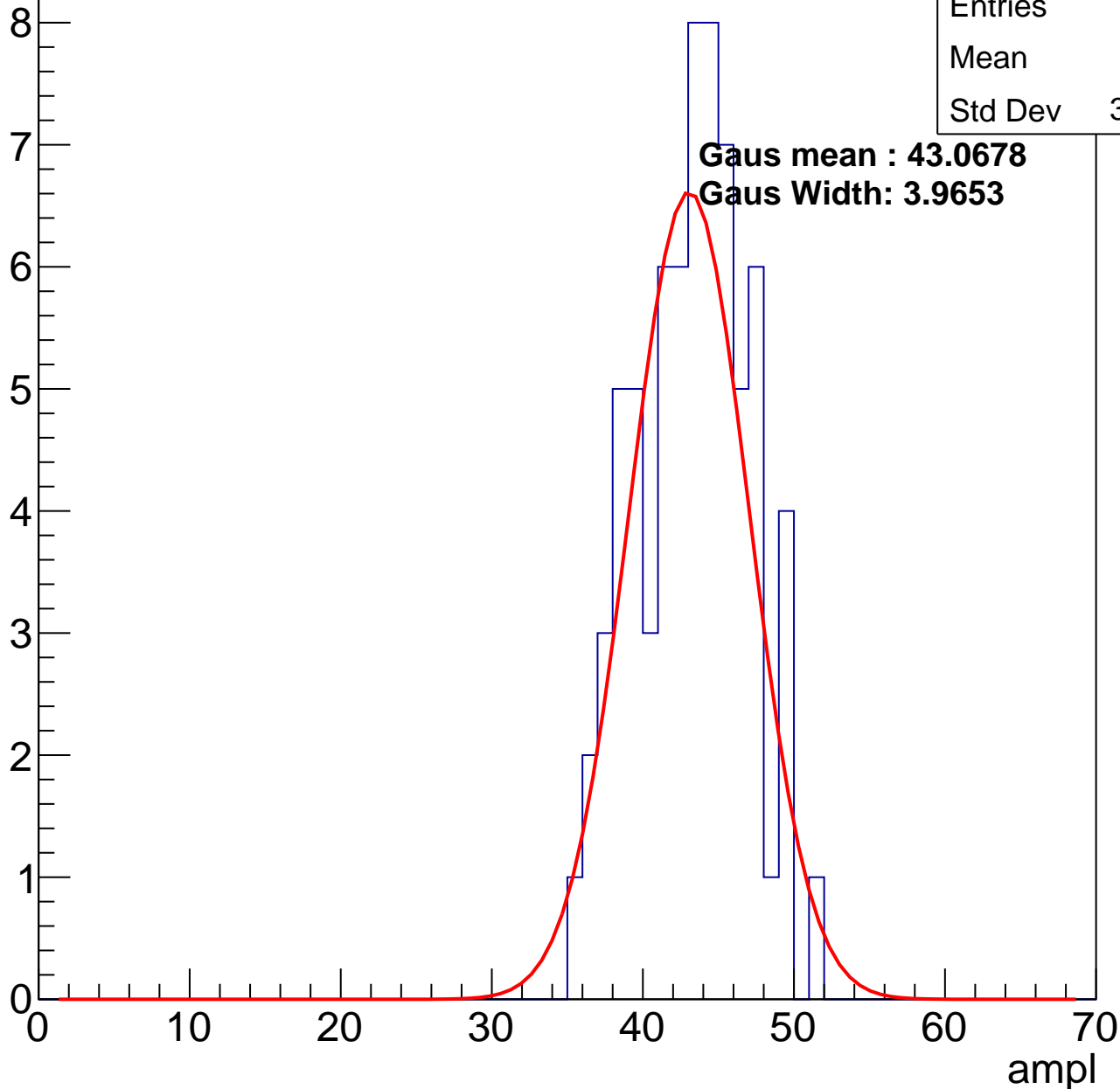
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.8
Std Dev	3.637

Gaus mean : 43.0678

Gaus Width: 3.9653

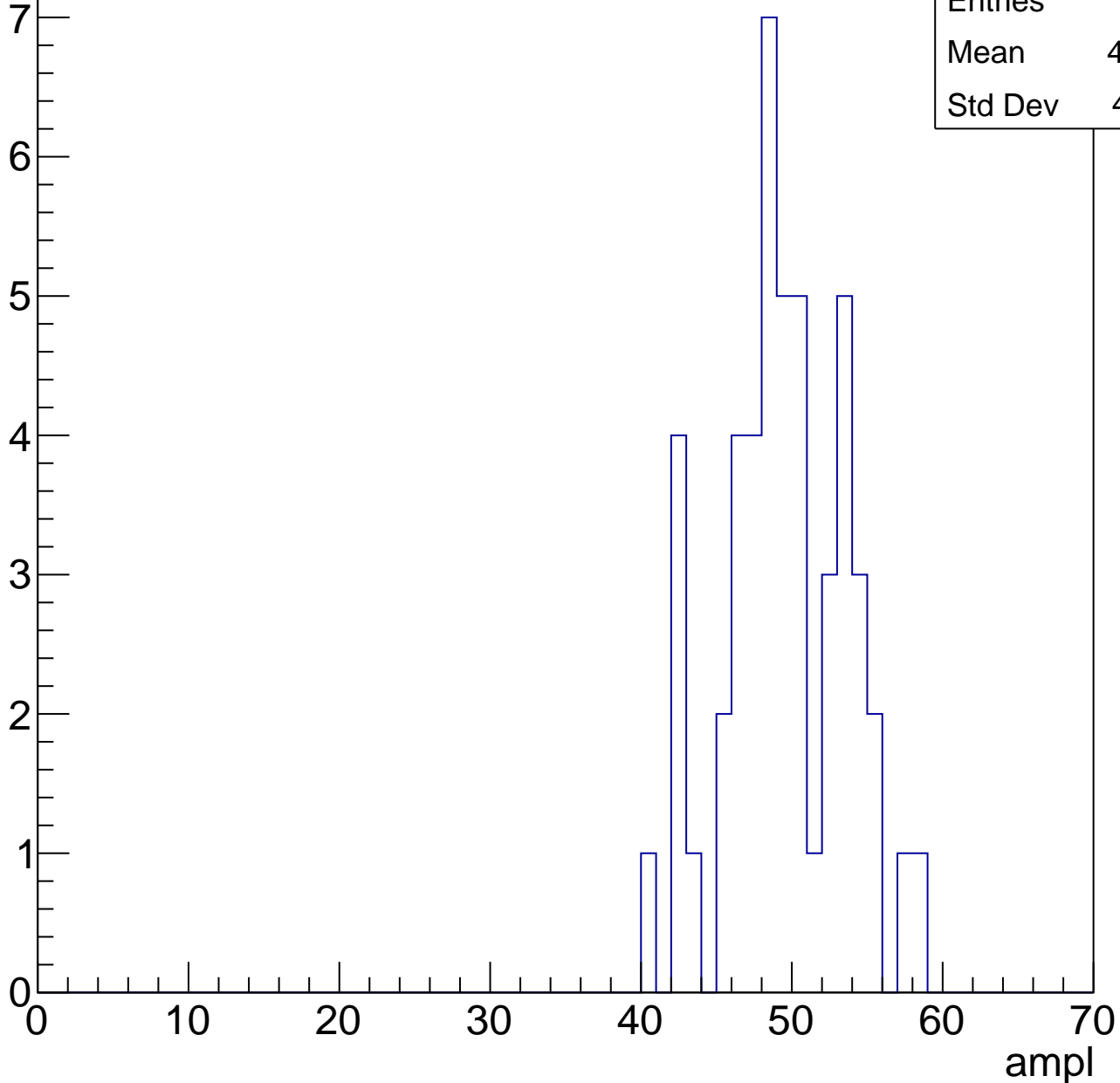


B1L103S, U24-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	49.04
Std Dev	4.071

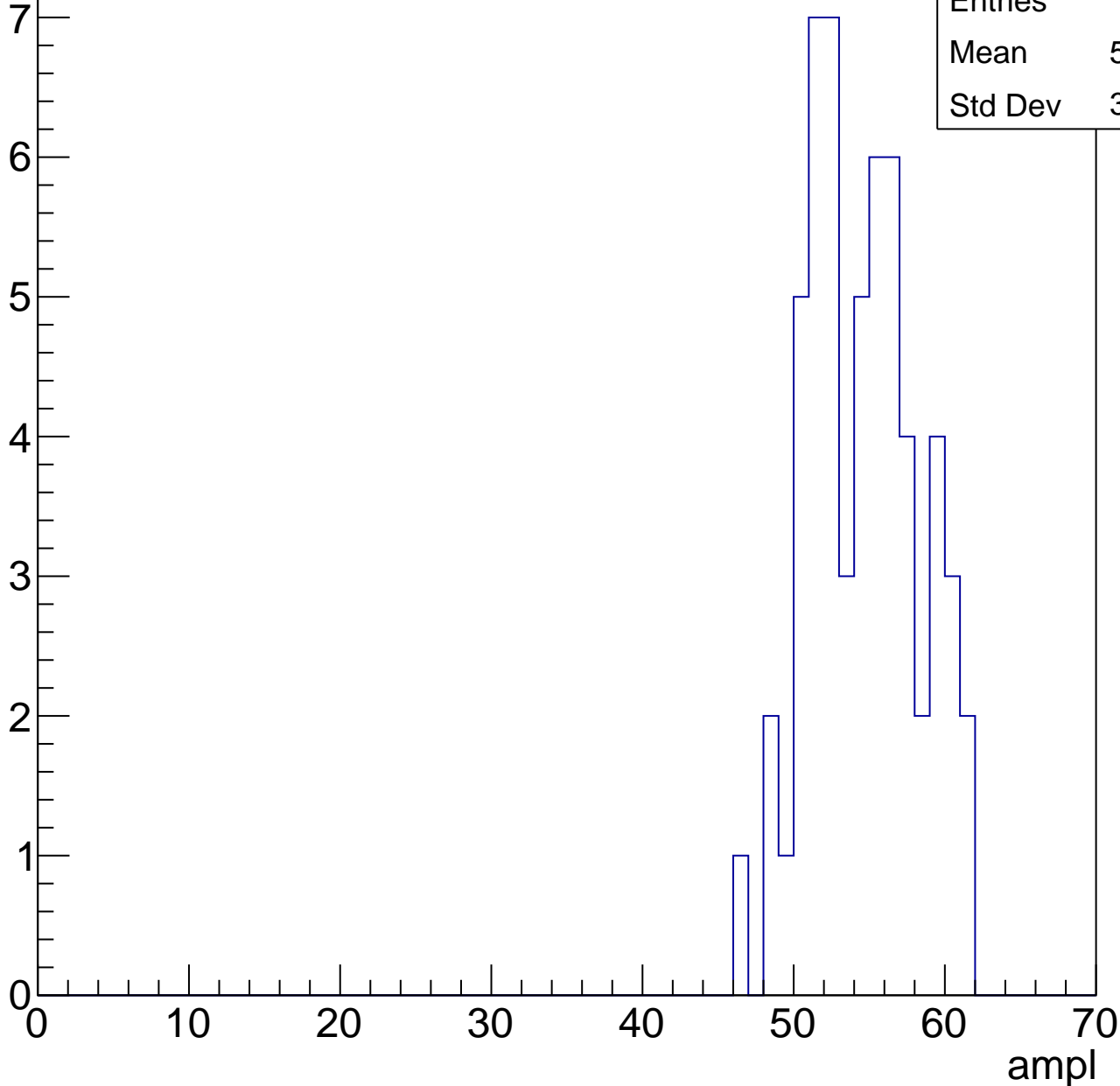


B1L103S, U24-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.12
Std Dev	3.567

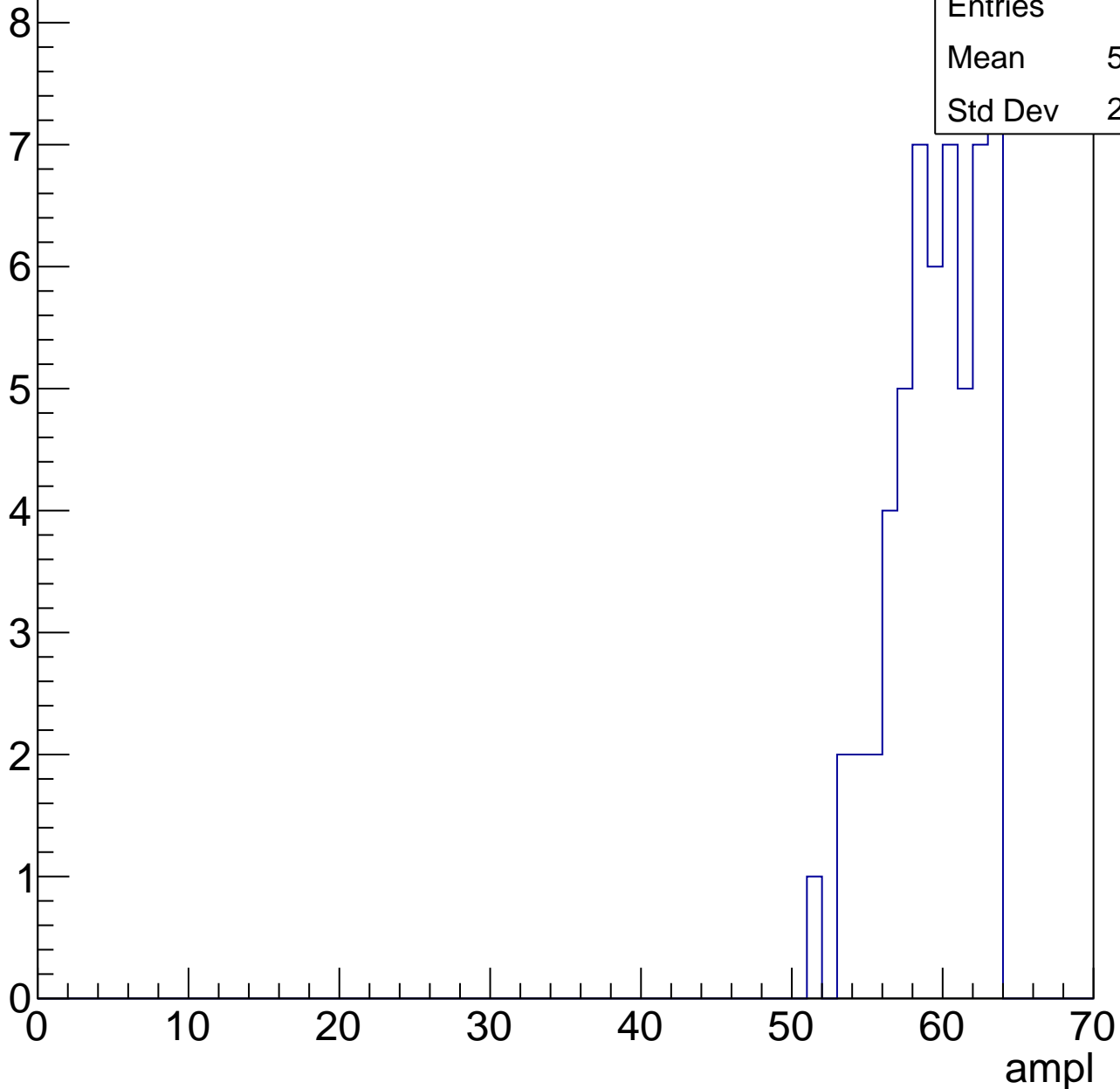


B1L103S, U24-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	59.05
Std Dev	2.985



B1L103S, U24-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

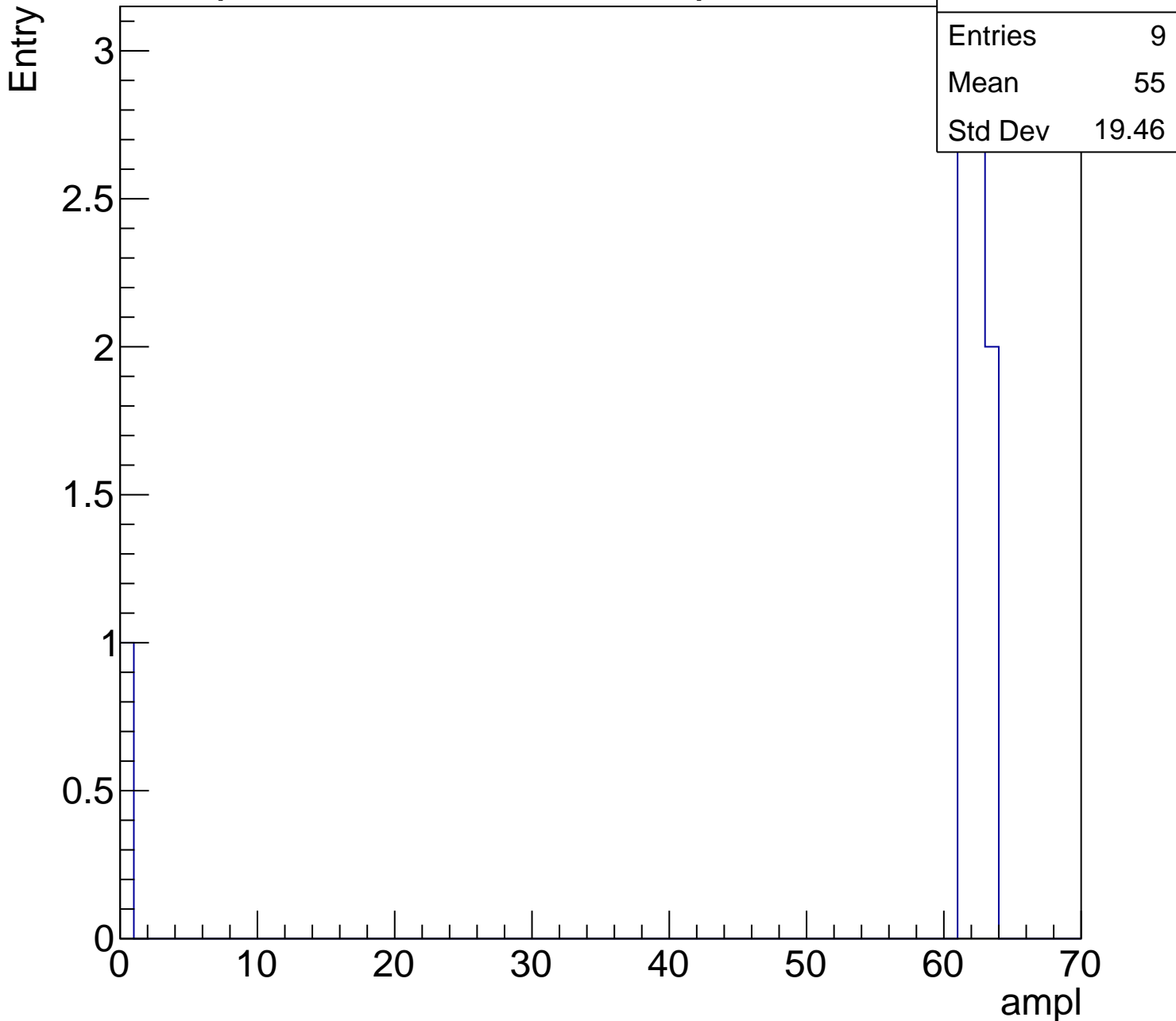
Mean

55

Std Dev

19.46

ampl



B1L103S, U24-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch104, adc0

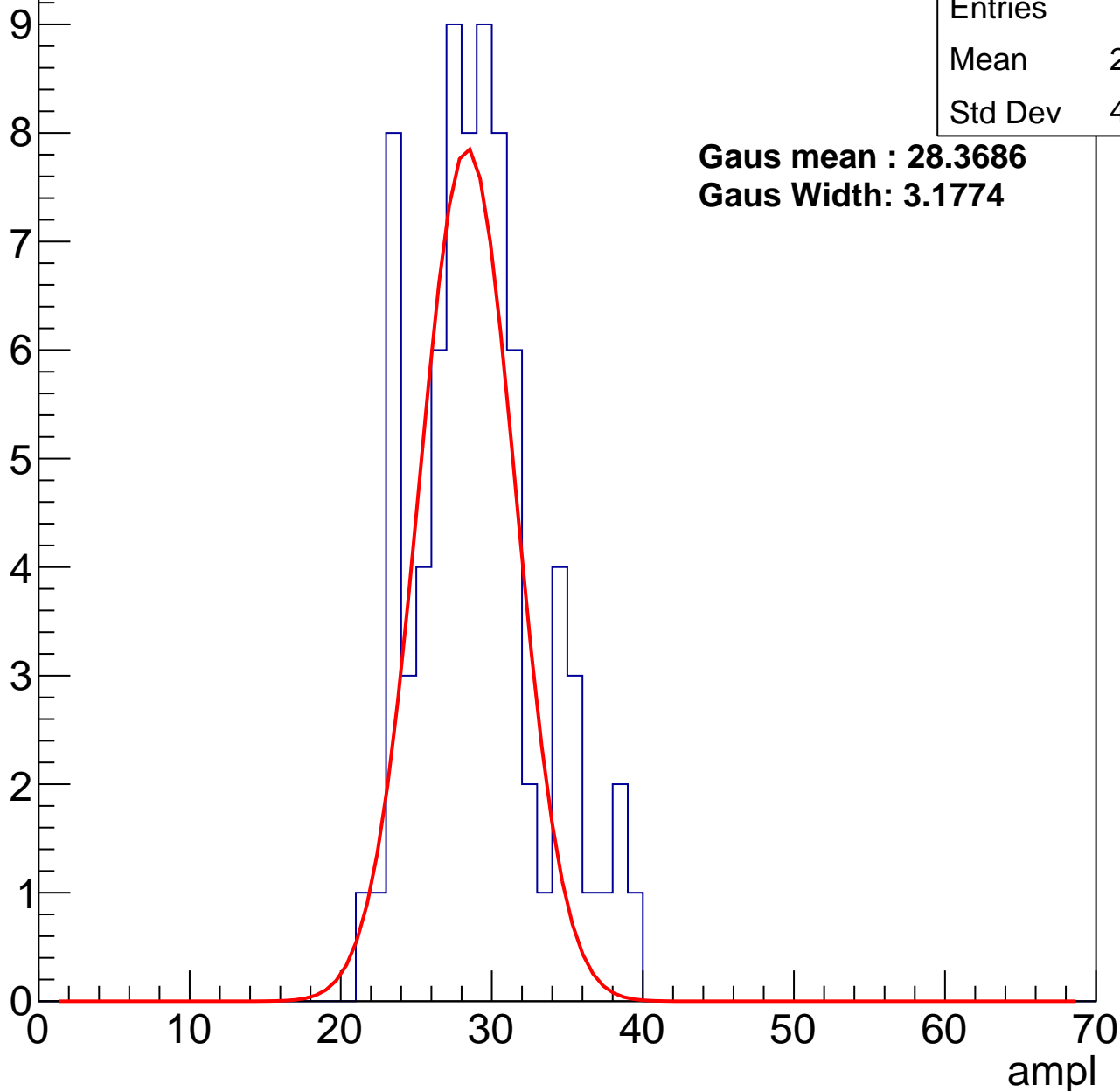
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	28.65
Std Dev	4.047

Gaus mean : 28.3686

Gaus Width: 3.1774



B1L103S, U24-ch104, adc1

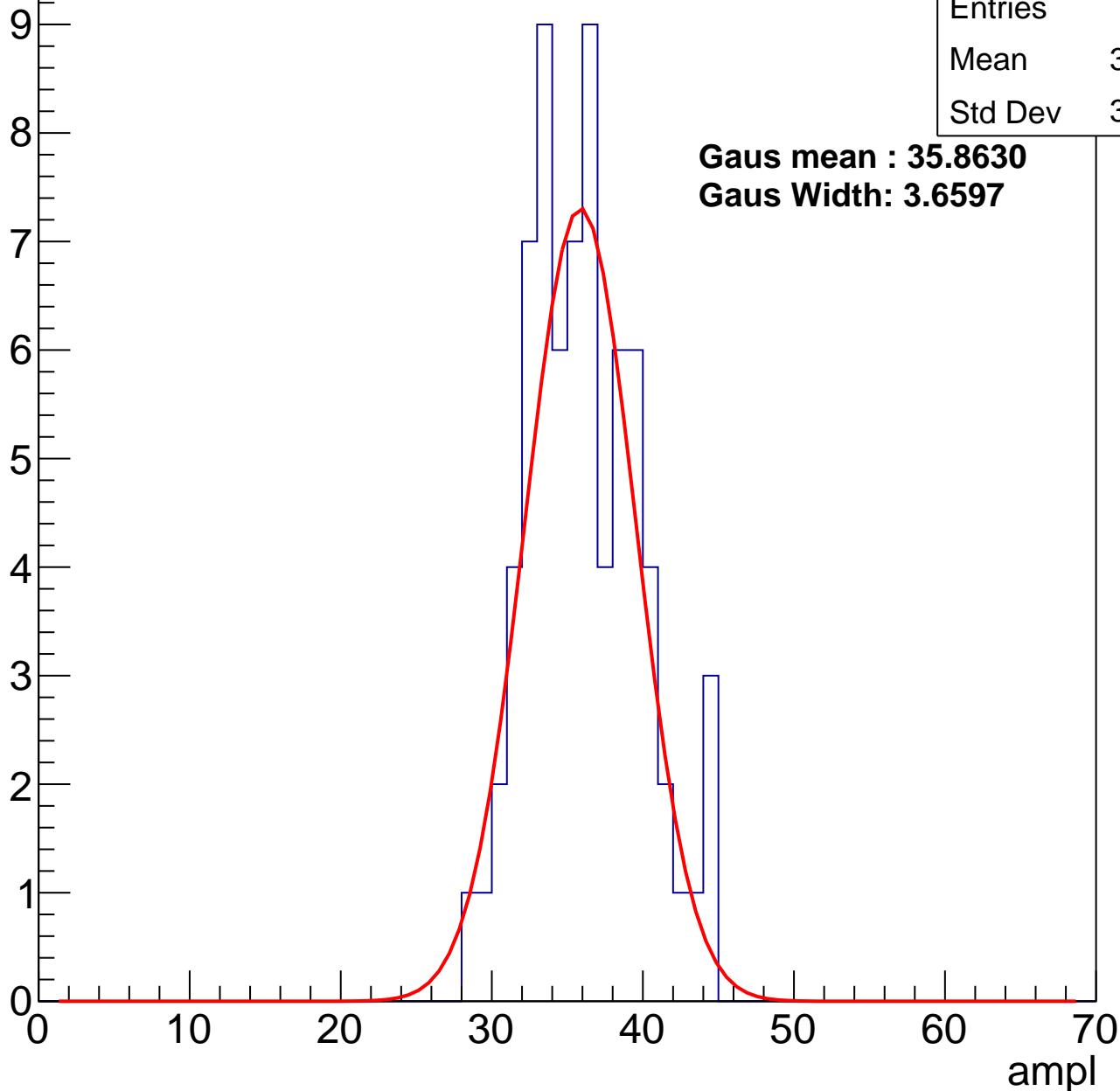
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.67
Std Dev	3.653

Gaus mean : 35.8630

Gaus Width: 3.6597



B1L103S, U24-ch104, adc2

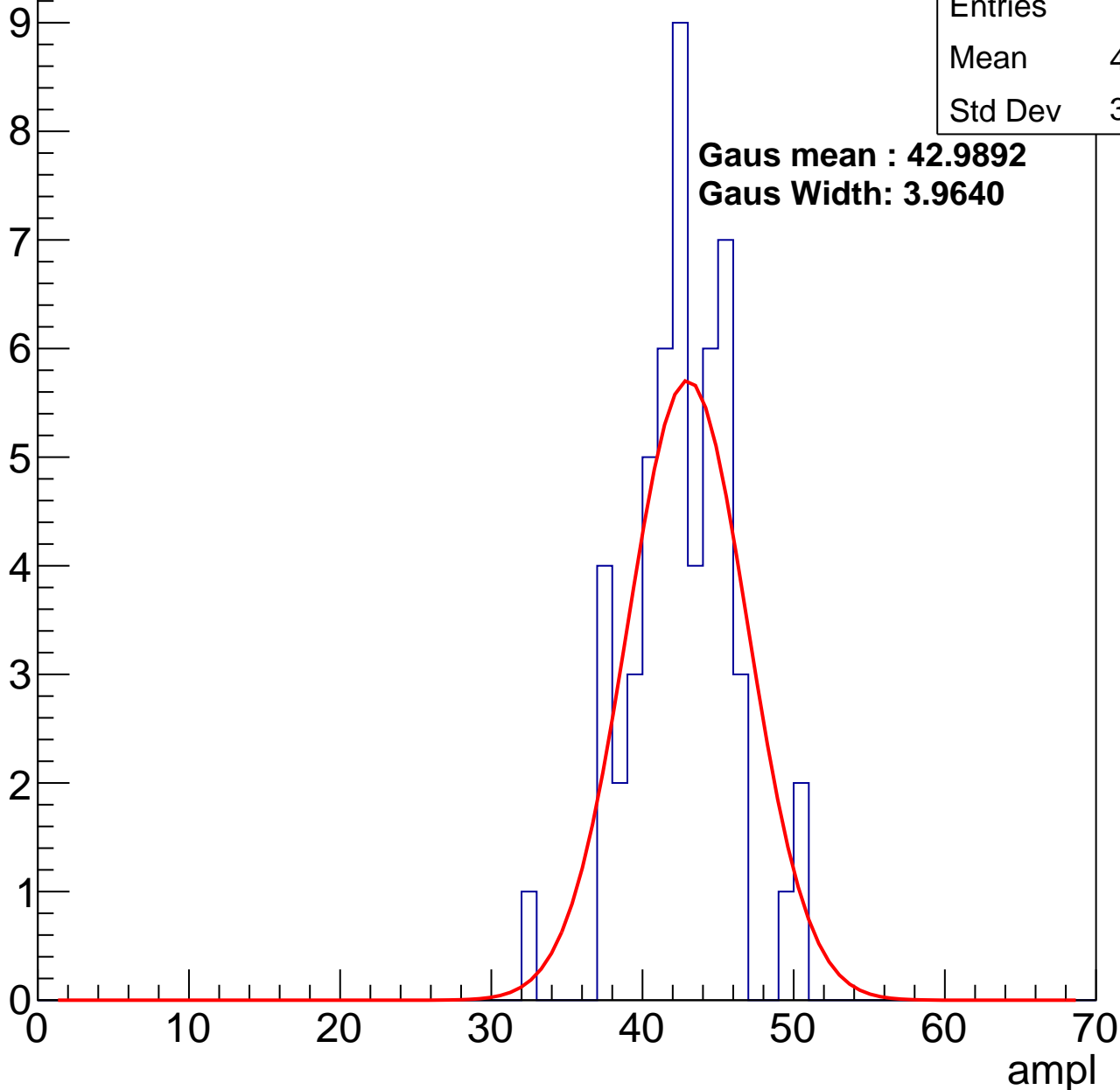
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.17
Std Dev	3.363

Gaus mean : 42.9892

Gaus Width: 3.9640

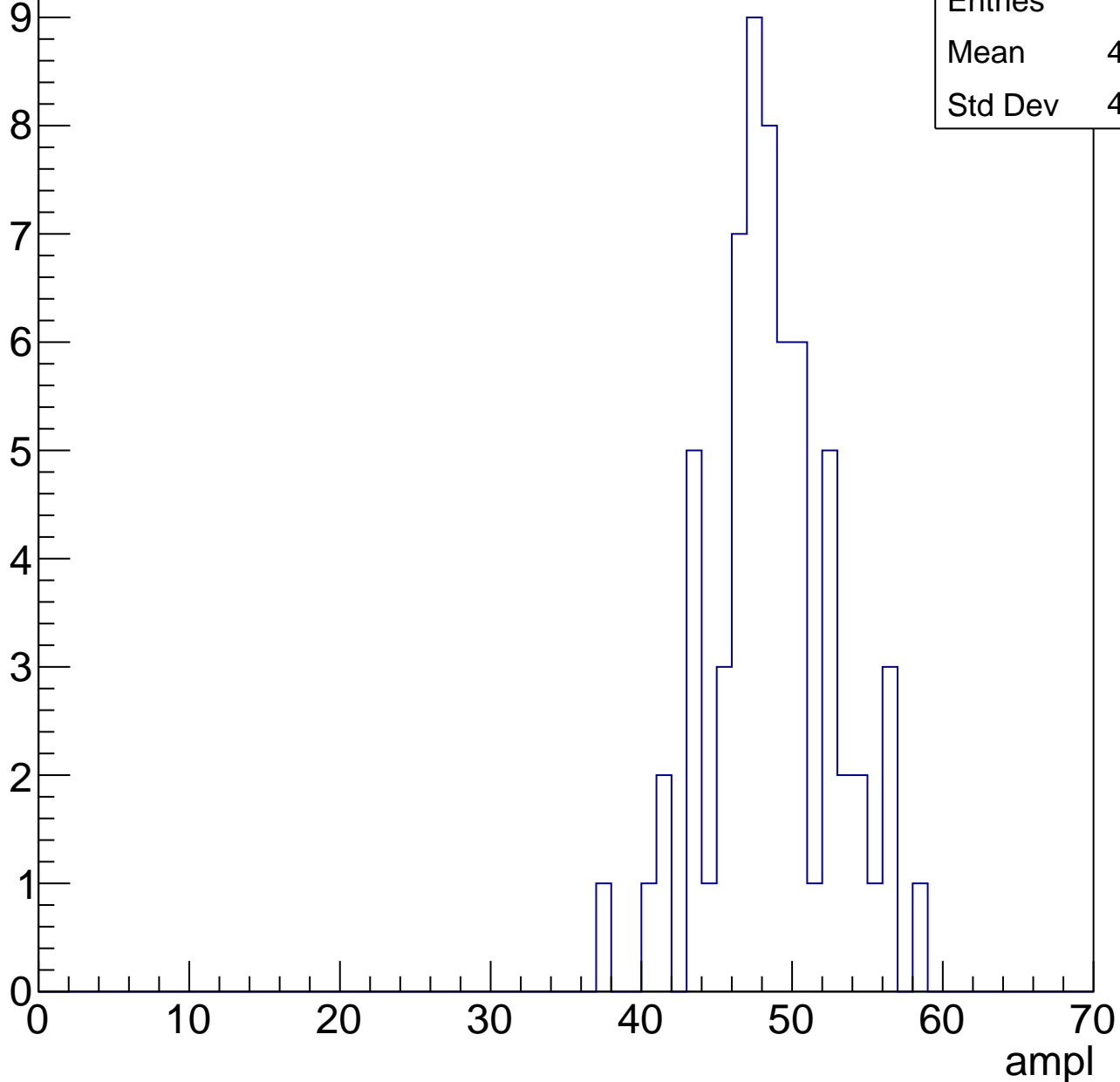


B1L103S, U24-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	48.16
Std Dev	4.086

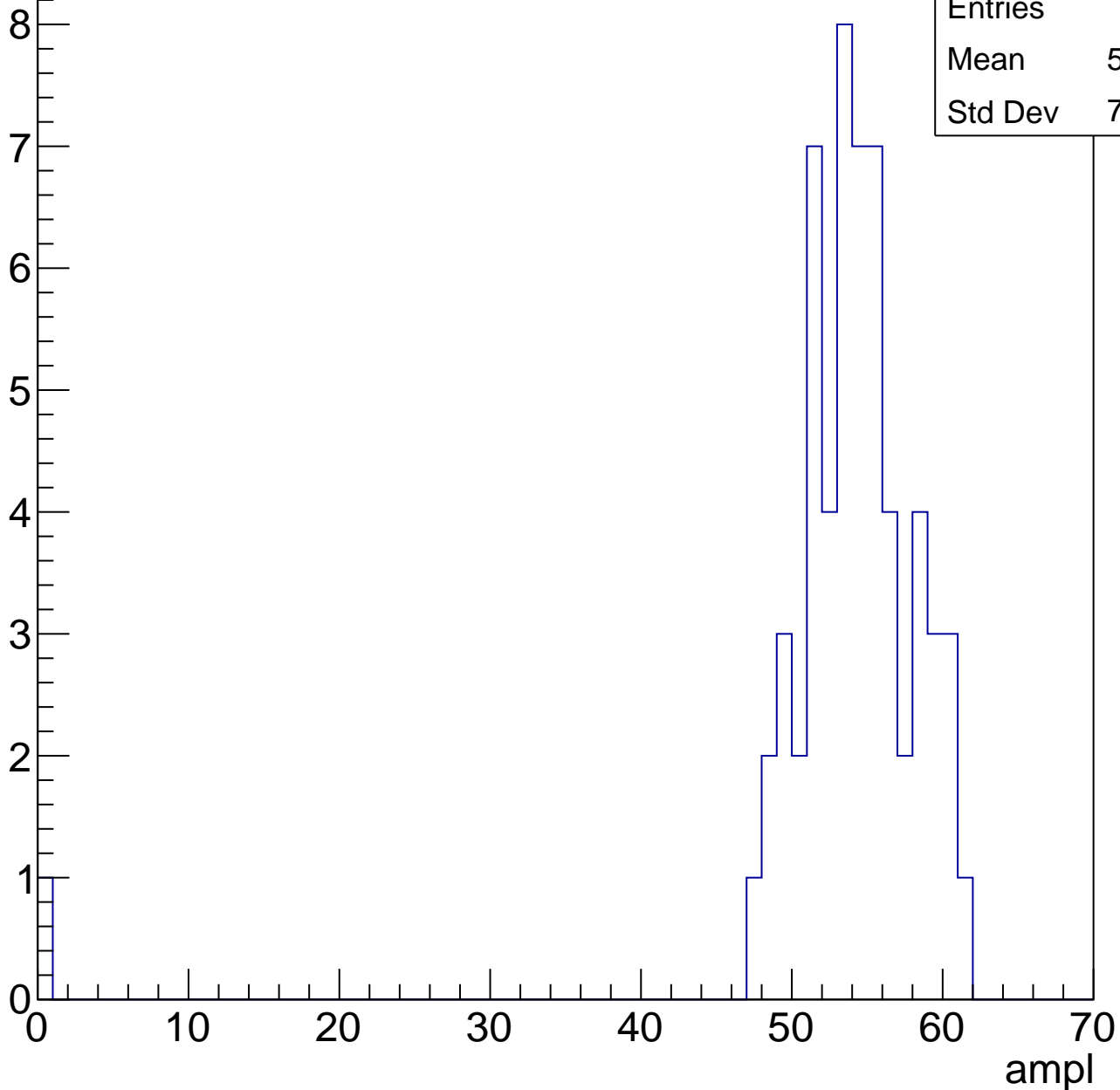


B1L103S, U24-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	53.05
Std Dev	7.718

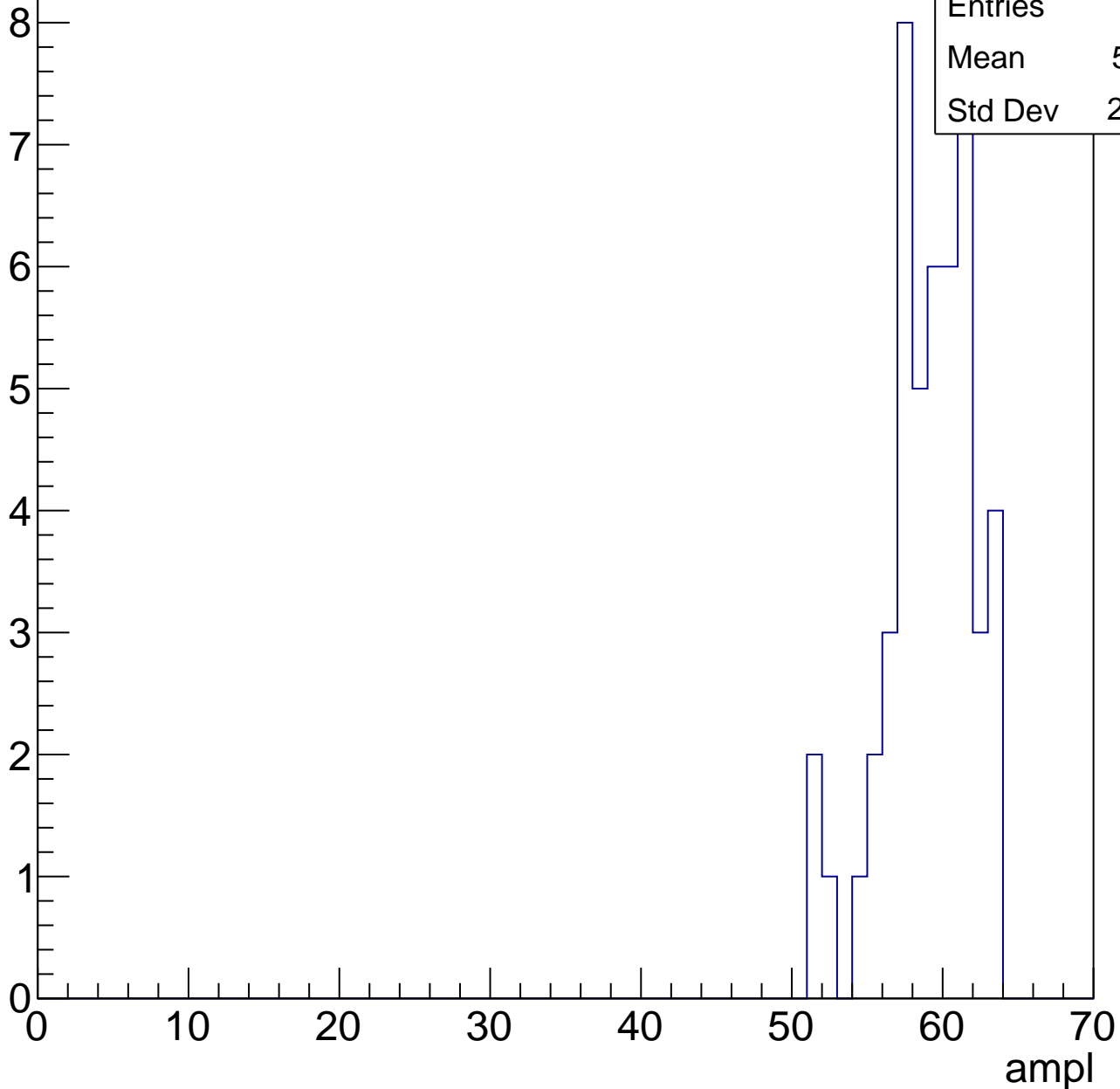


B1L103S, U24-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.61
Std Dev	2.926

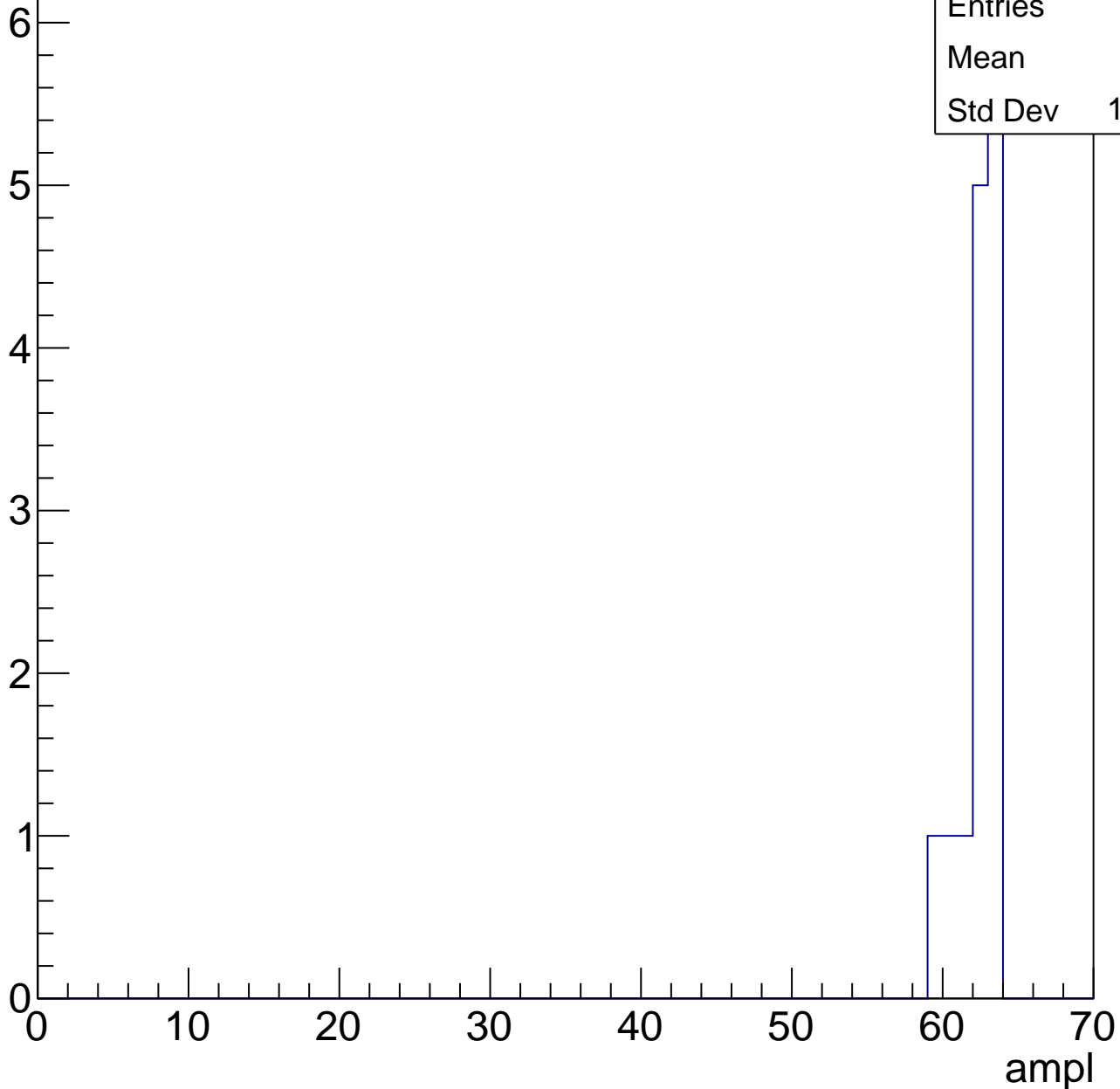


B1L103S, U24-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	62
Std Dev	1.195



B1L103S, U24-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	41.33
Std Dev	29.23

ampl

B1L103S, U24-ch105, adc0

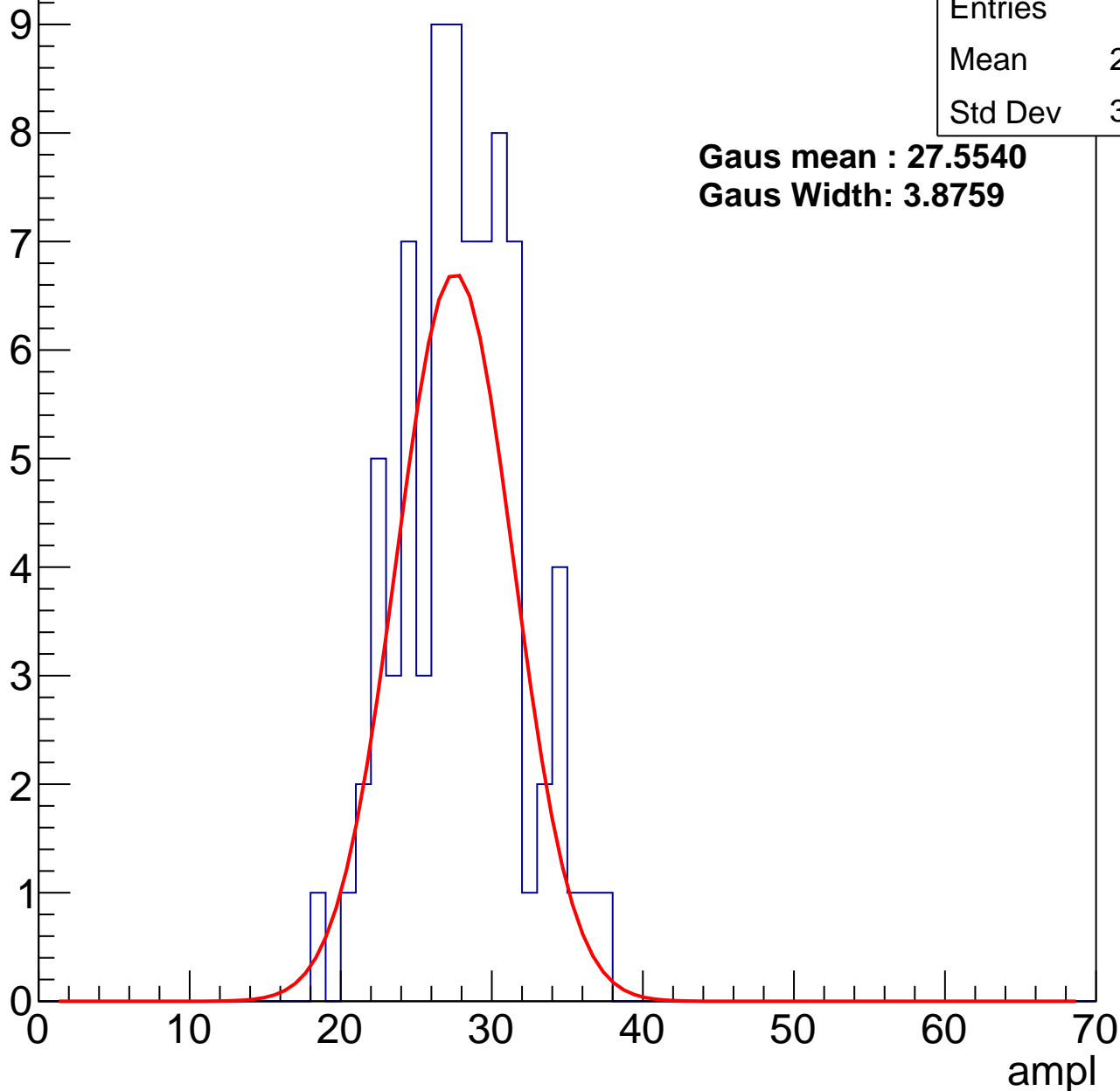
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	27.56
Std Dev	3.884

Gaus mean : 27.5540

Gaus Width: 3.8759



B1L103S, U24-ch105, adc1

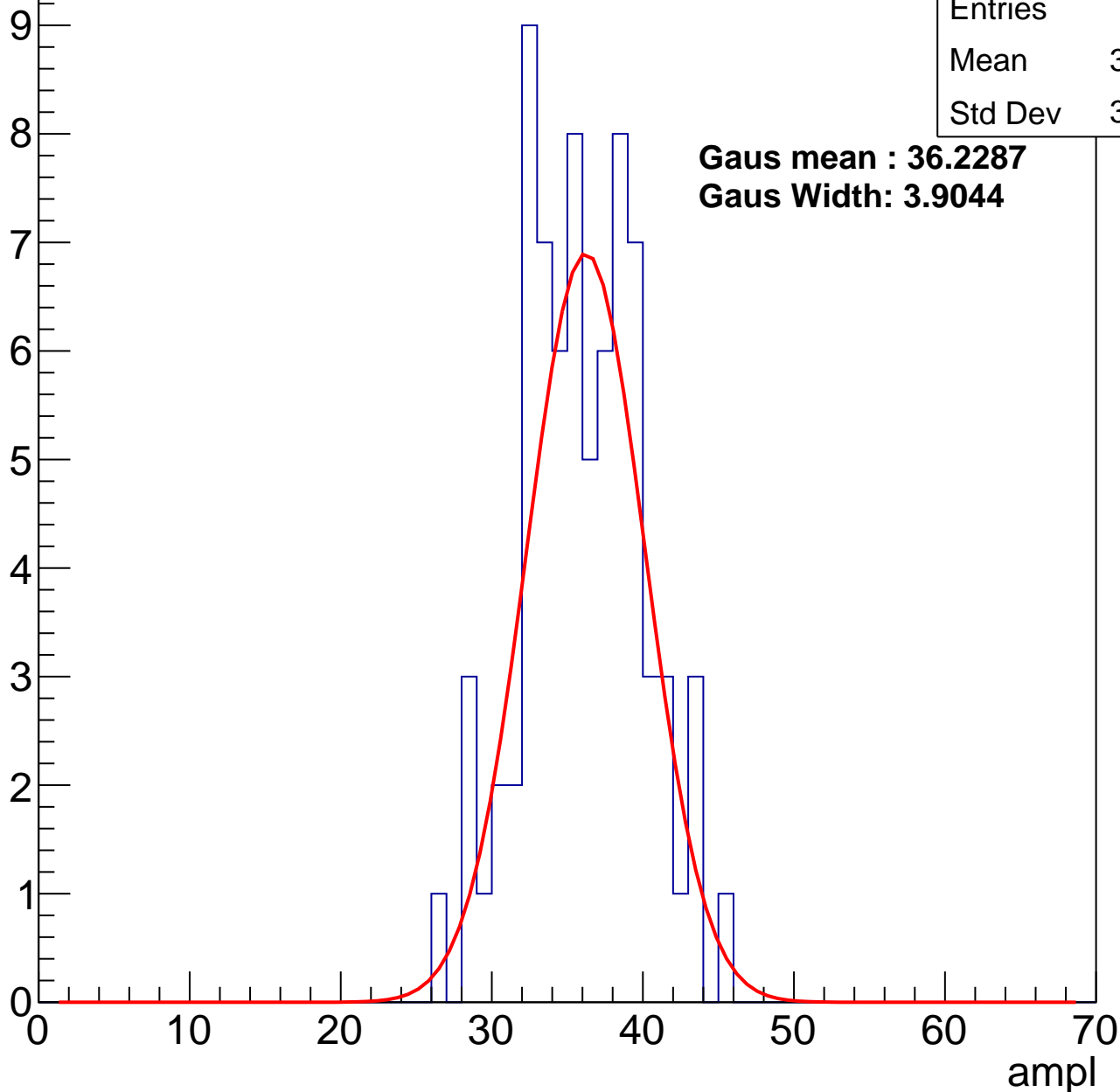
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.55
Std Dev	3.932

Gaus mean : 36.2287

Gaus Width: 3.9044



B1L103S, U24-ch105, adc2

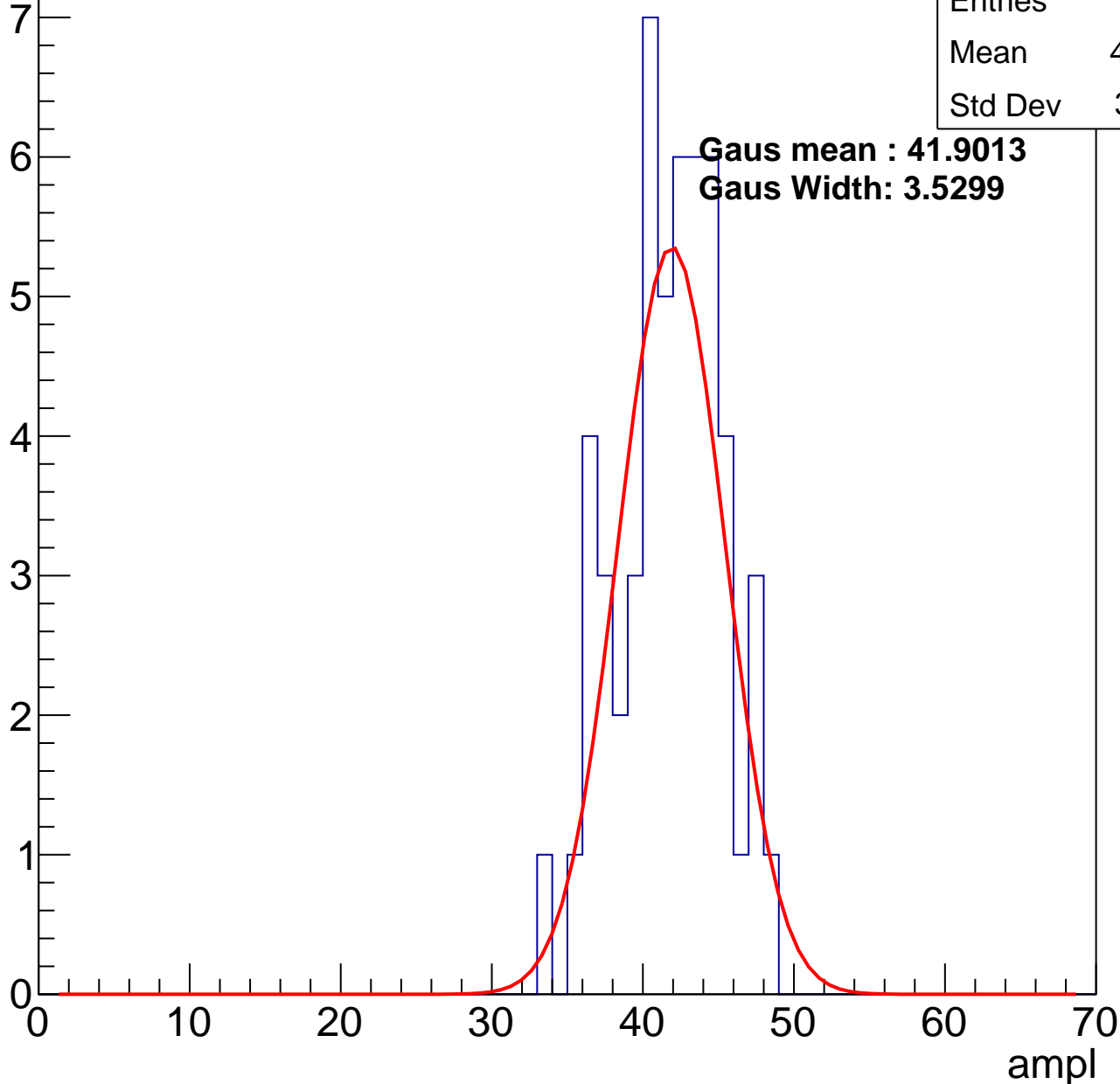
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	41.32
Std Dev	3.391

Gaus mean : 41.9013

Gaus Width: 3.5299

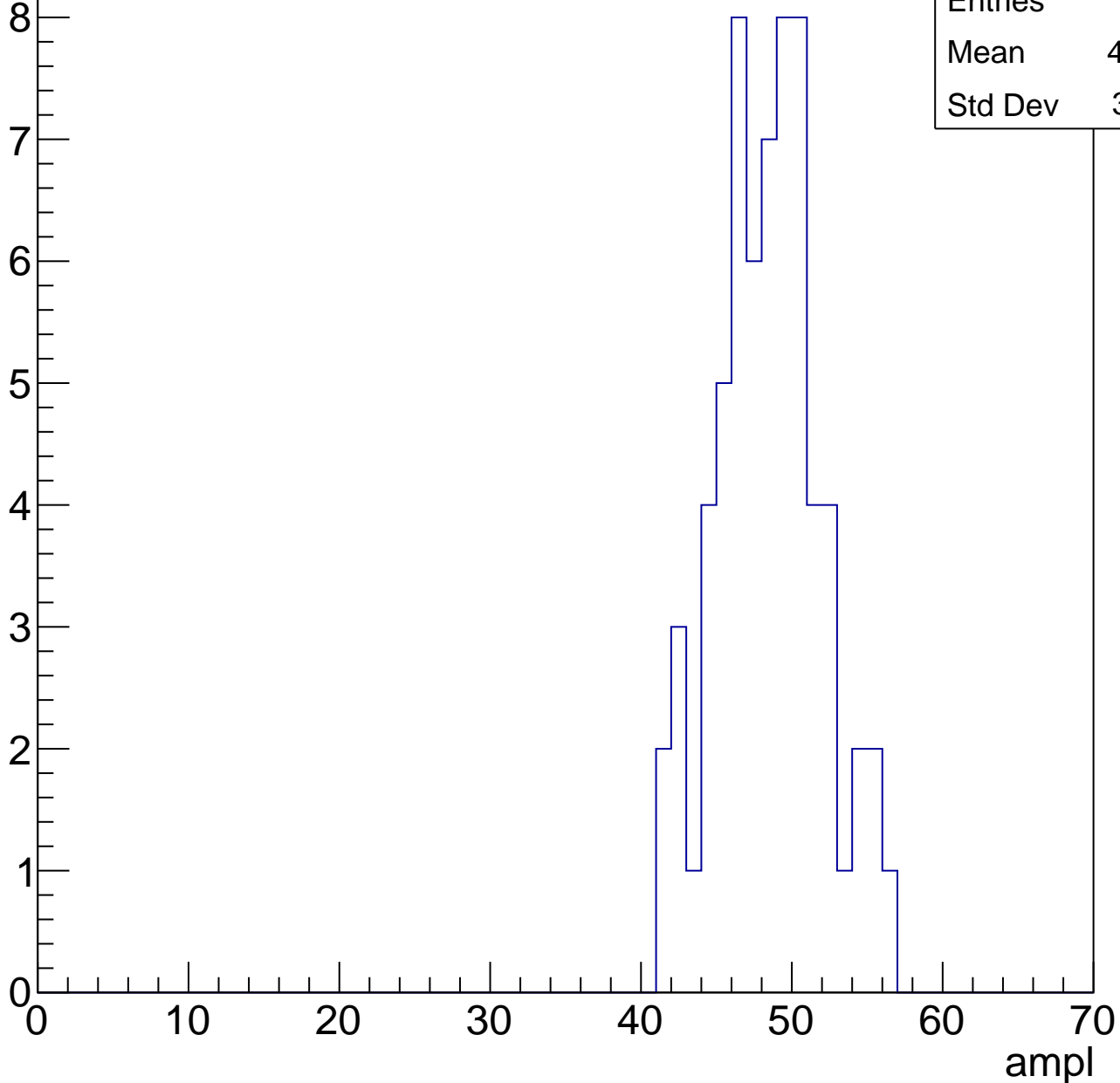


B1L103S, U24-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	48.02
Std Dev	3.431

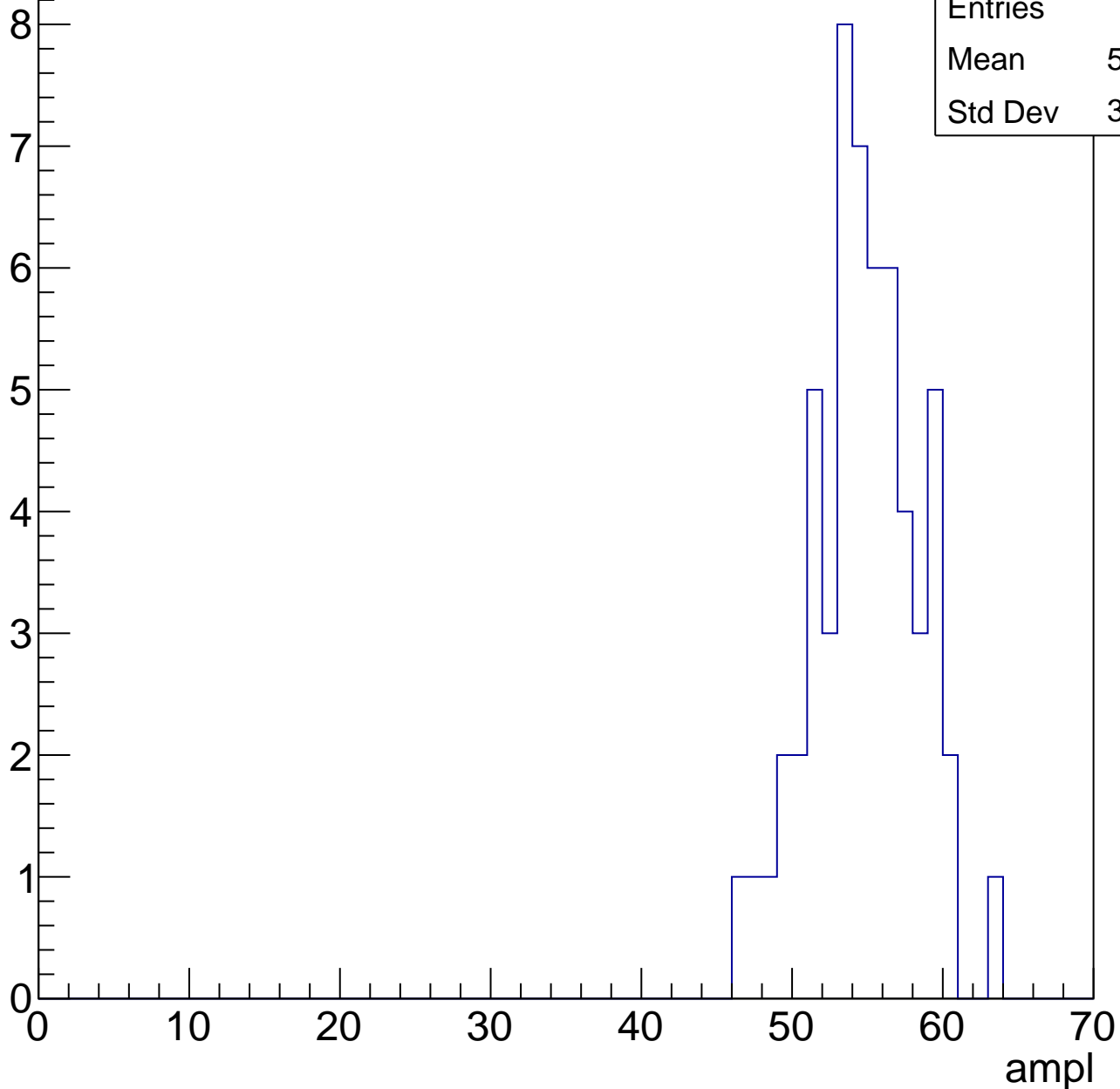


B1L103S, U24-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.35
Std Dev	3.456

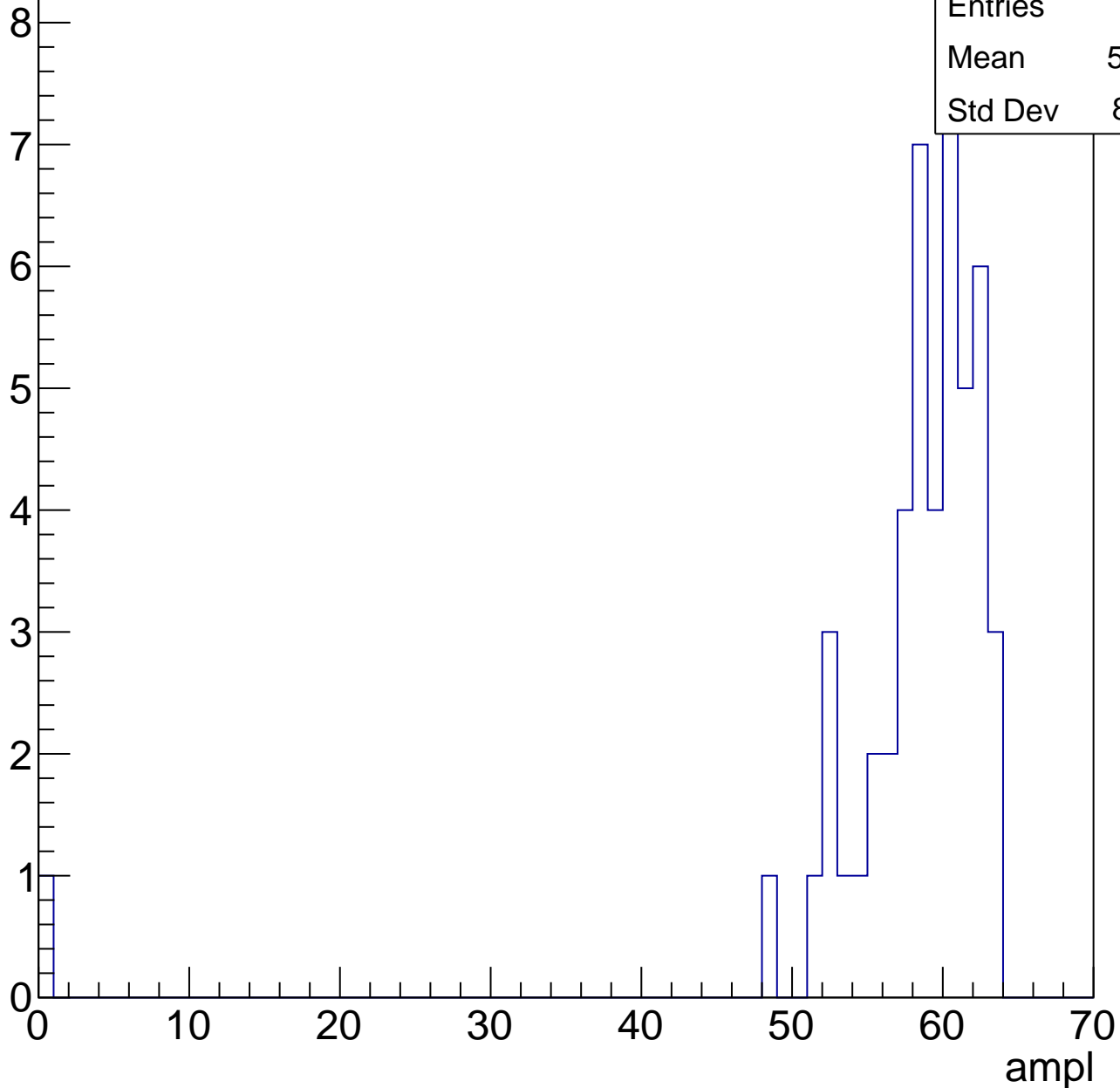


B1L103S, U24-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.14
Std Dev	8.931

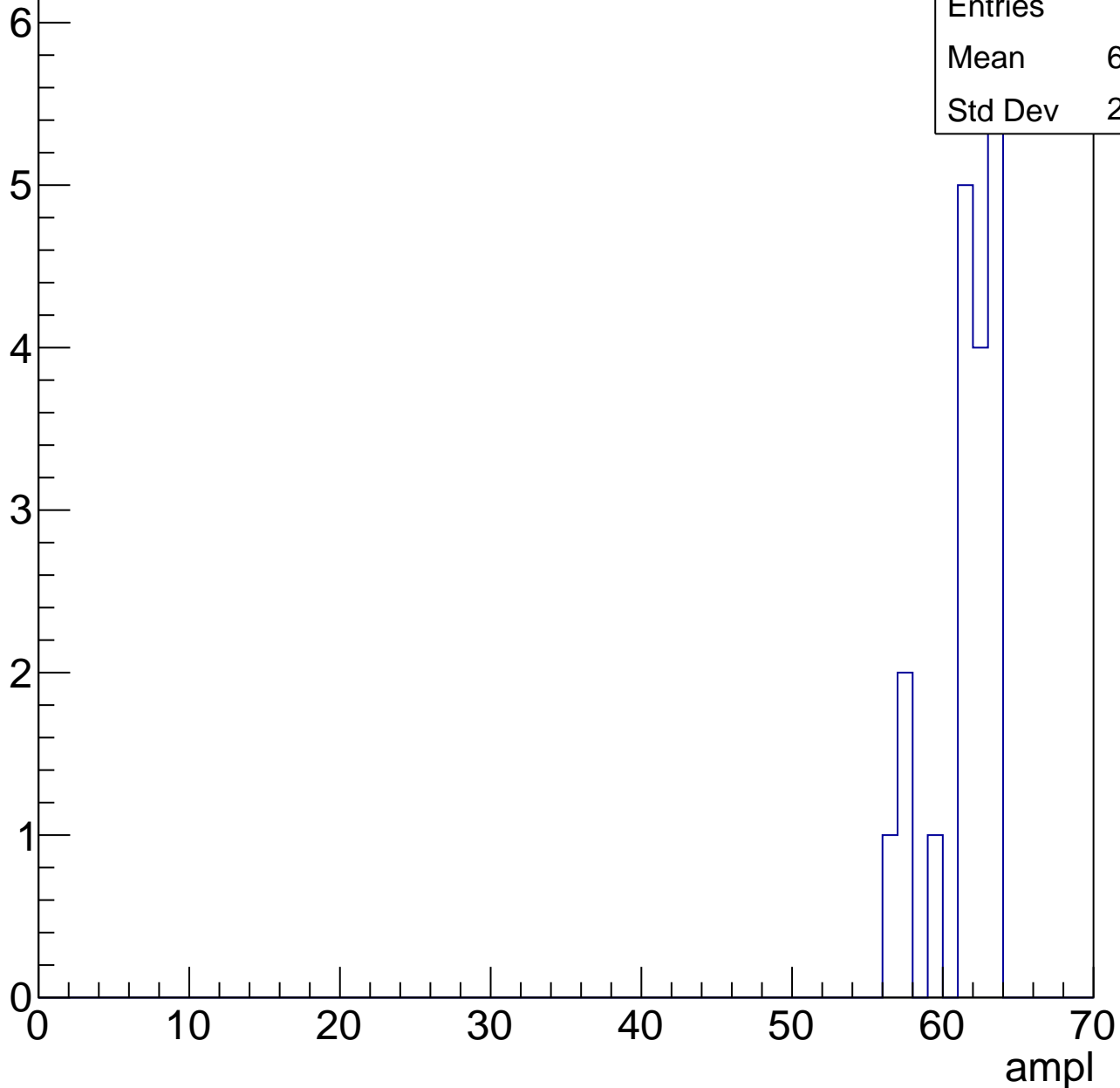


B1L103S, U24-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	61.05
Std Dev	2.164



B1L103S, U24-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch106, adc0

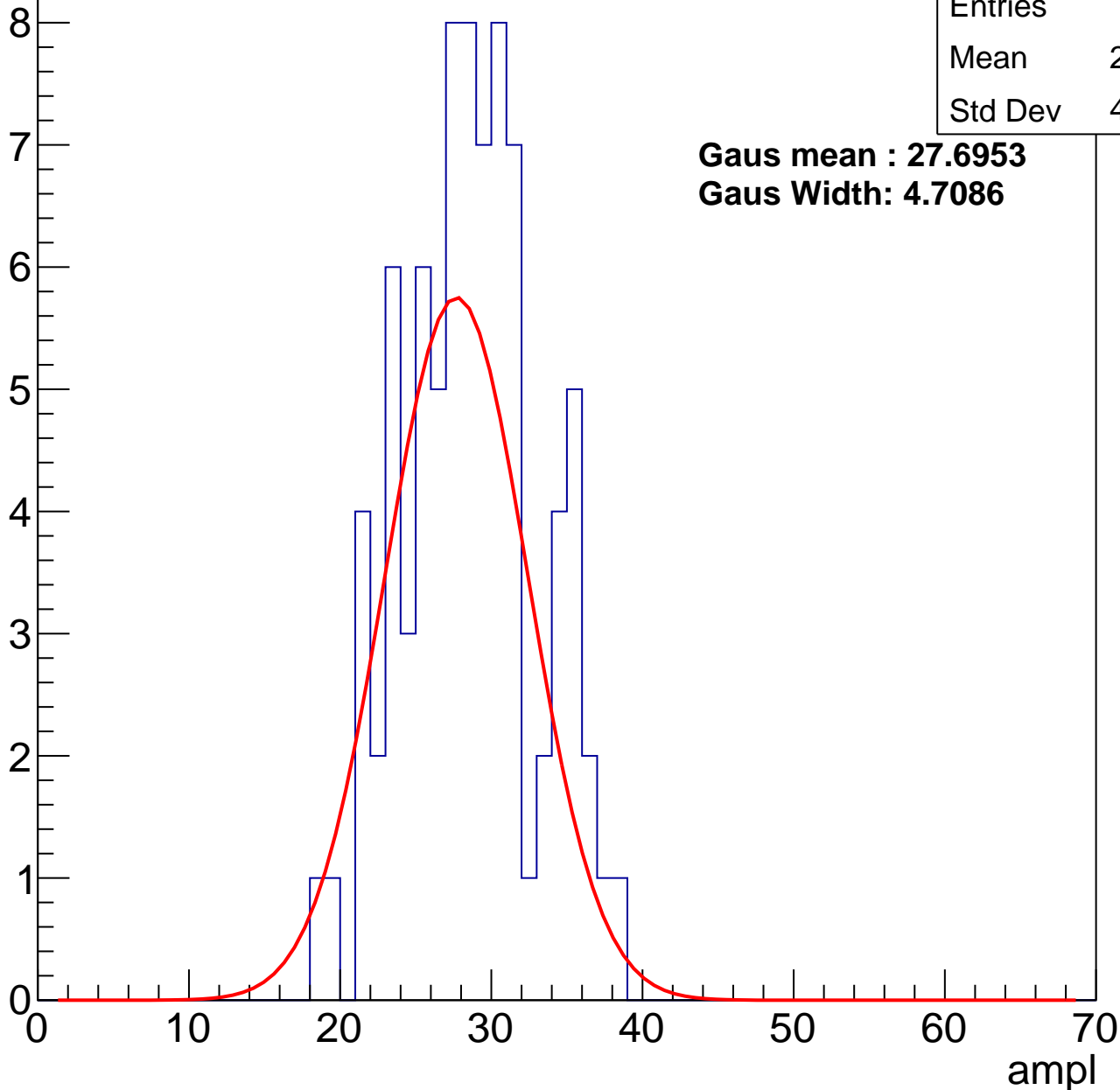
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.18
Std Dev	4.407

Gaus mean : 27.6953

Gaus Width: 4.7086



B1L103S, U24-ch106, adc1

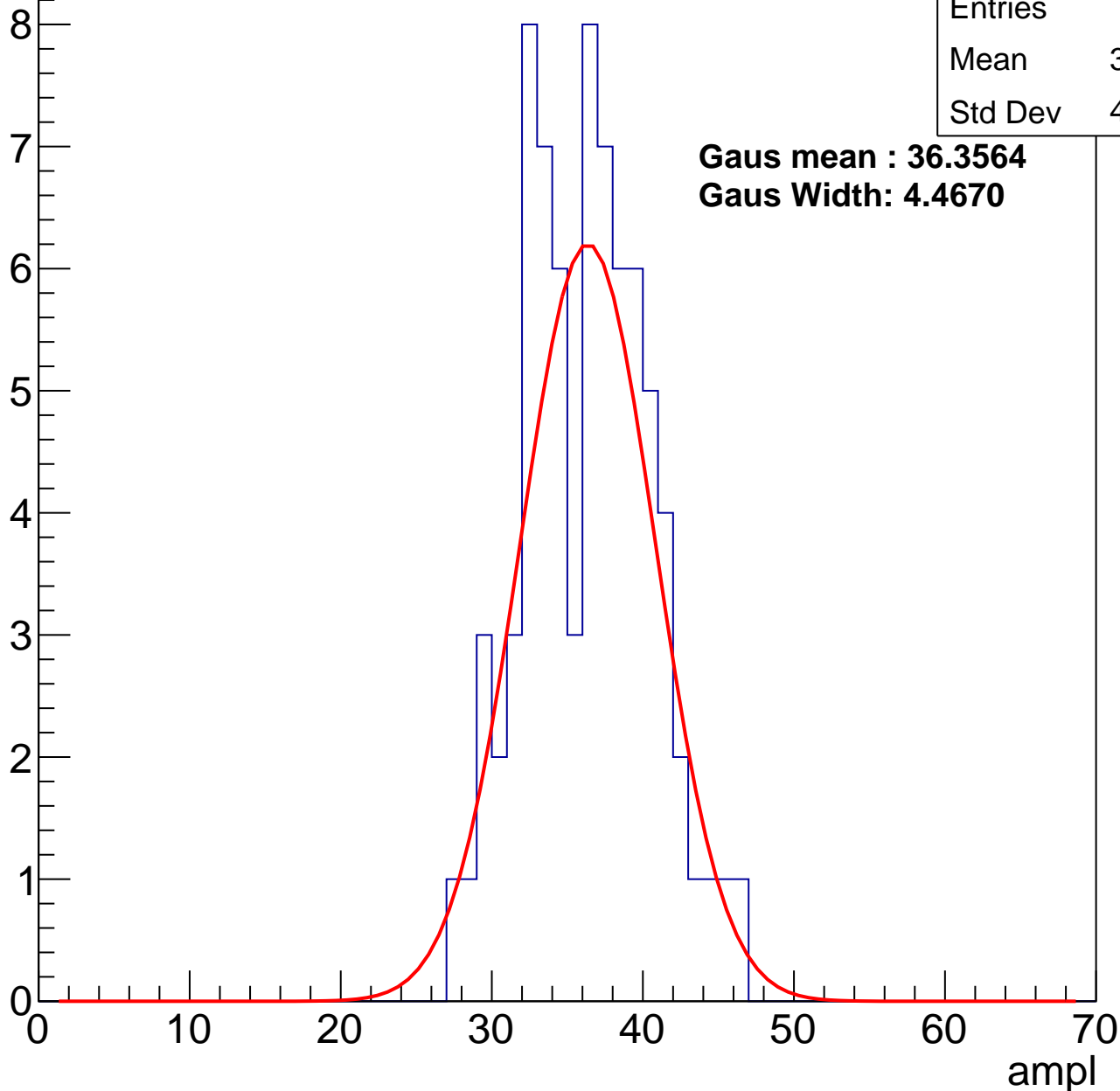
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.87
Std Dev	4.115

Gaus mean : 36.3564

Gaus Width: 4.4670



B1L103S, U24-ch106, adc2

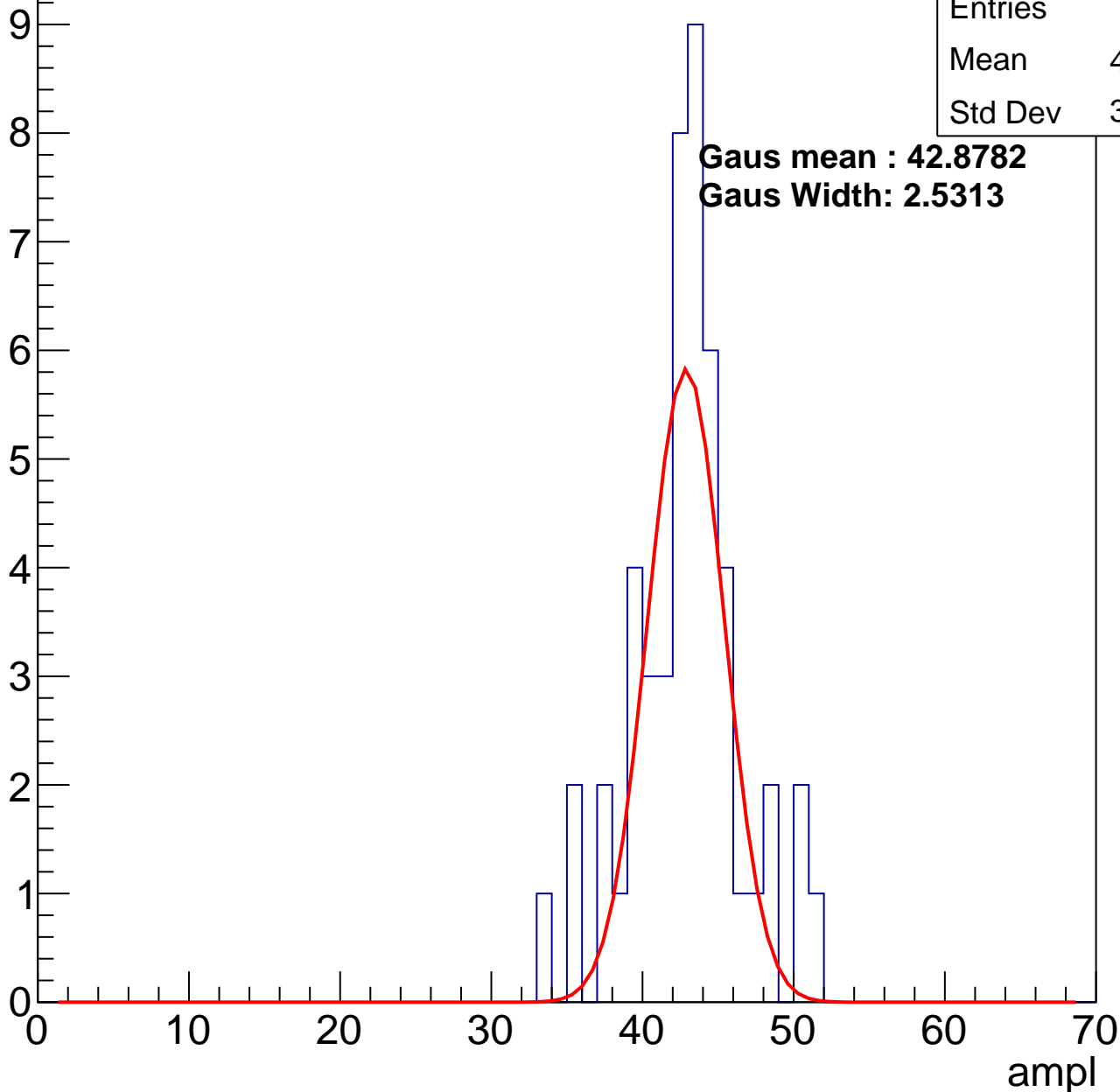
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.42
Std Dev	3.683

Gaus mean : 42.8782

Gaus Width: 2.5313

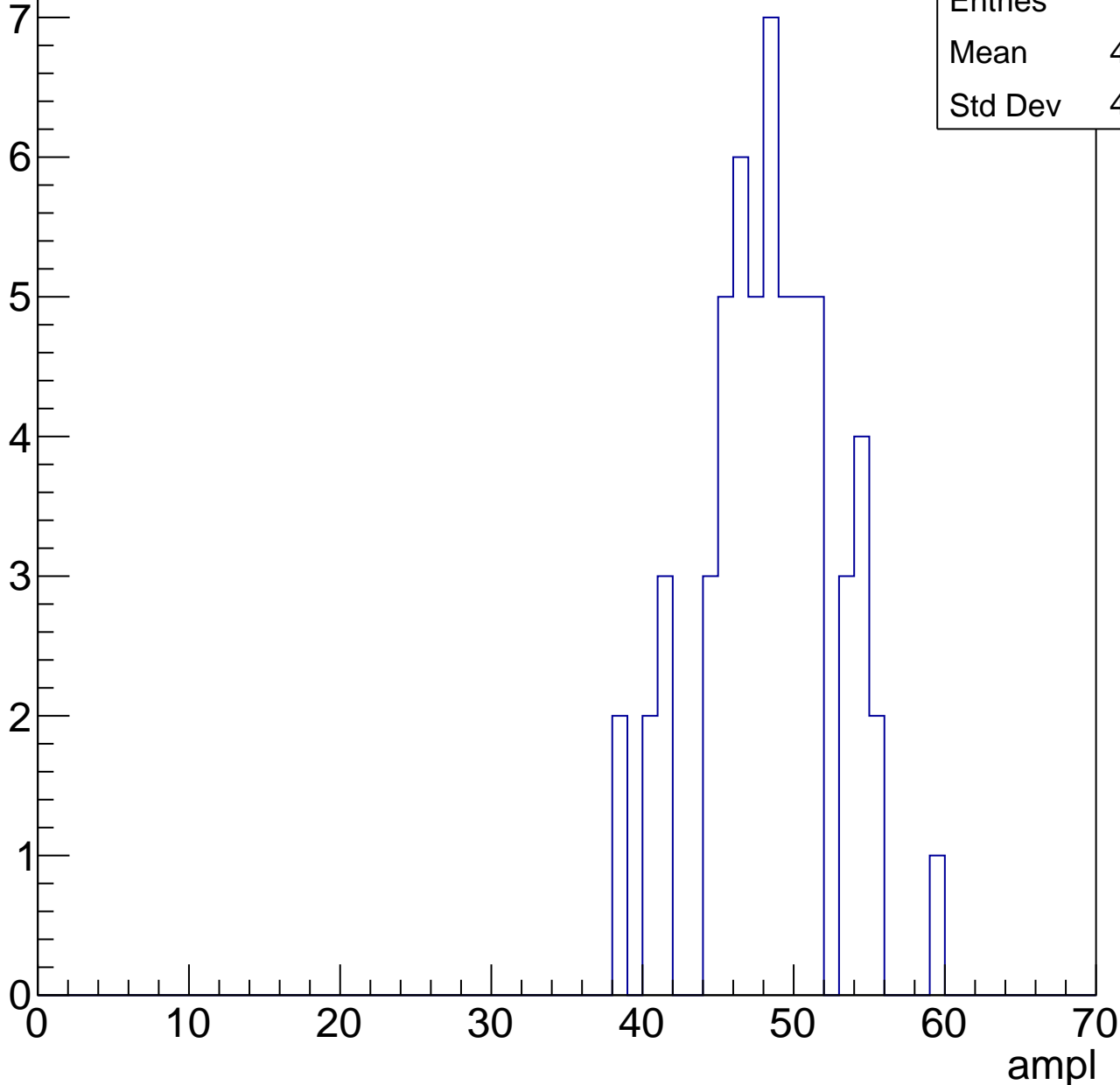


B1L103S, U24-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	47.88
Std Dev	4.363

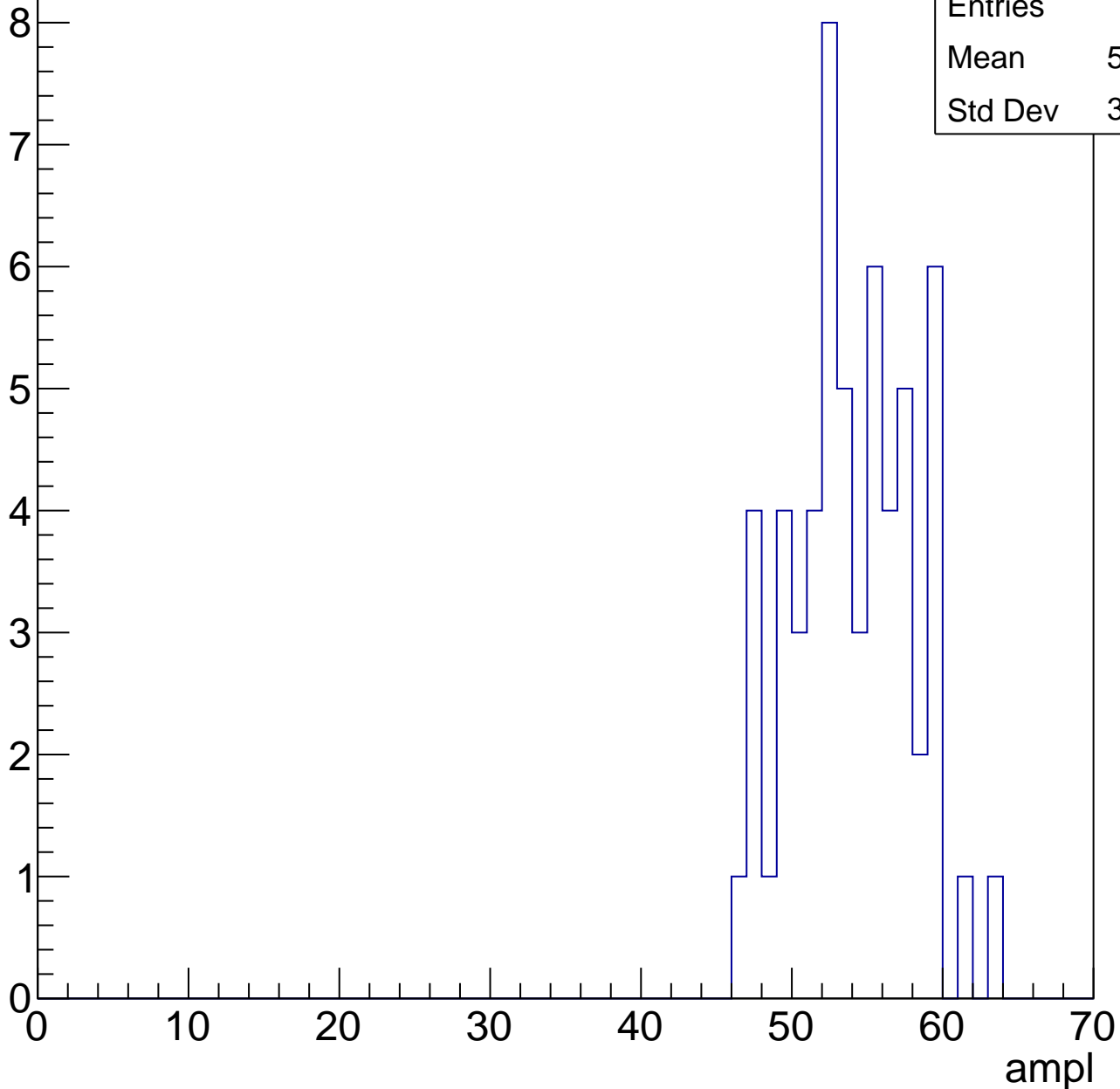


B1L103S, U24-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	53.59
Std Dev	3.909

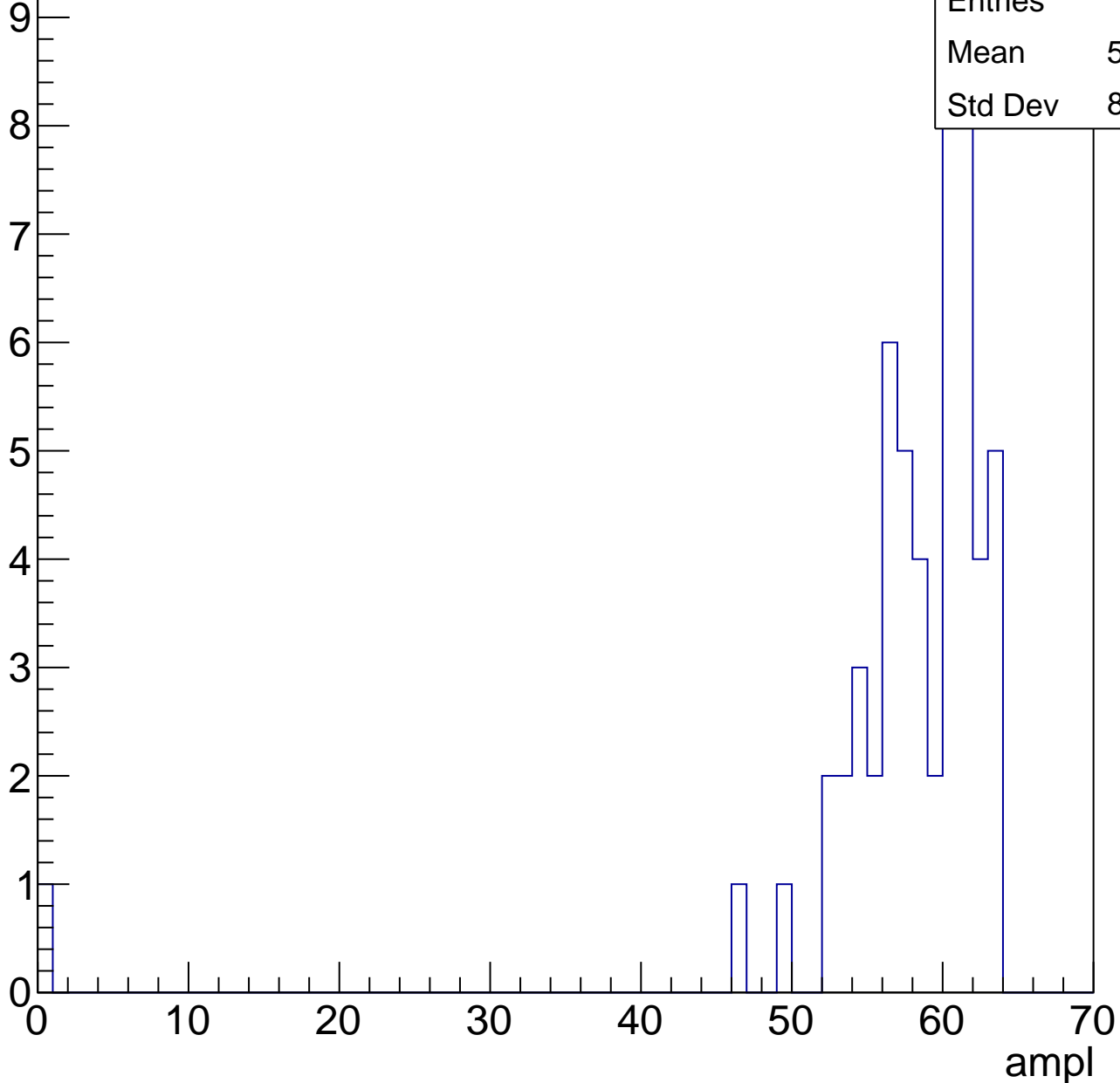


B1L103S, U24-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.09
Std Dev	8.597

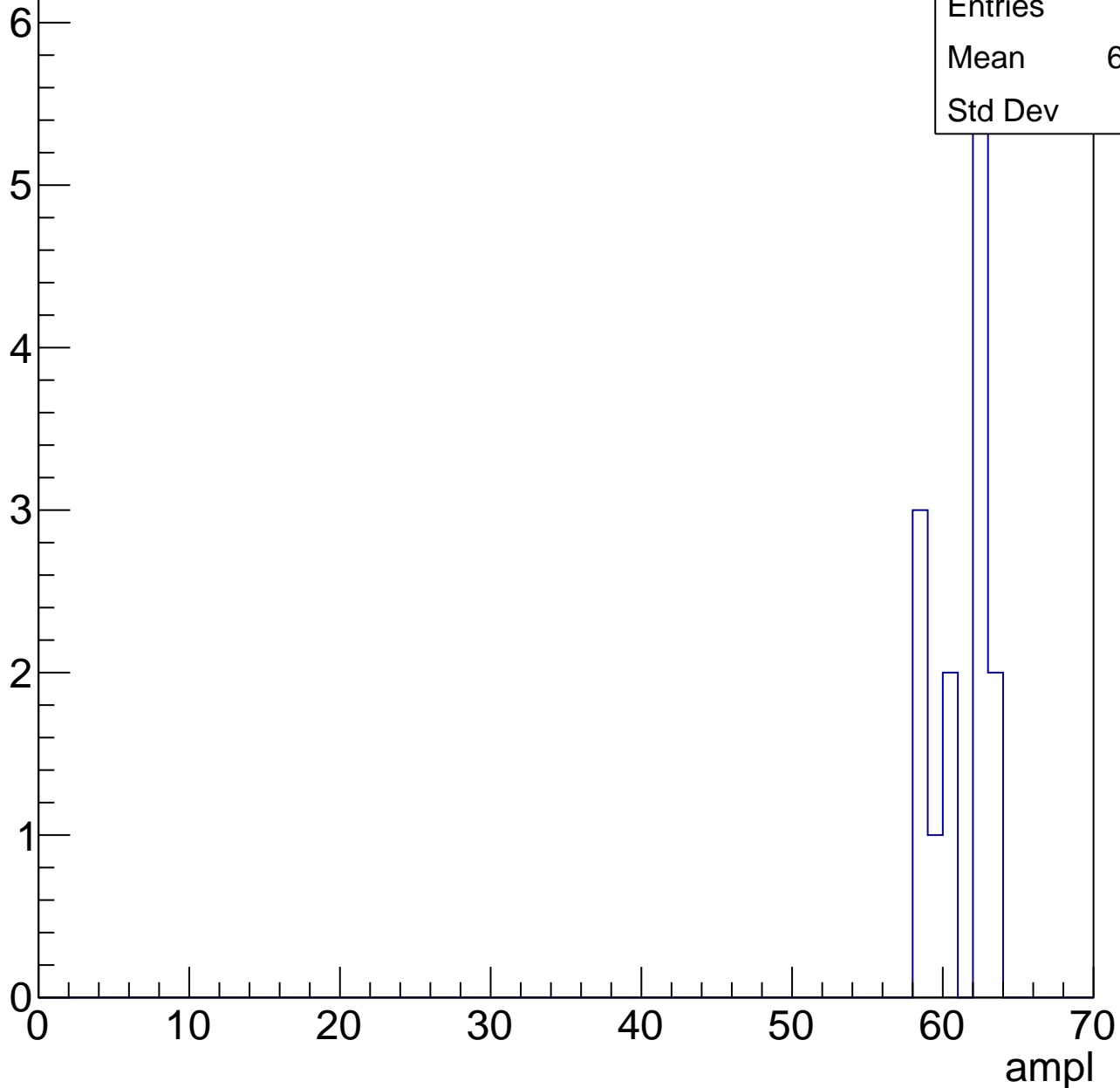


B1L103S, U24-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	60.79
Std Dev	1.82



B1L103S, U24-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

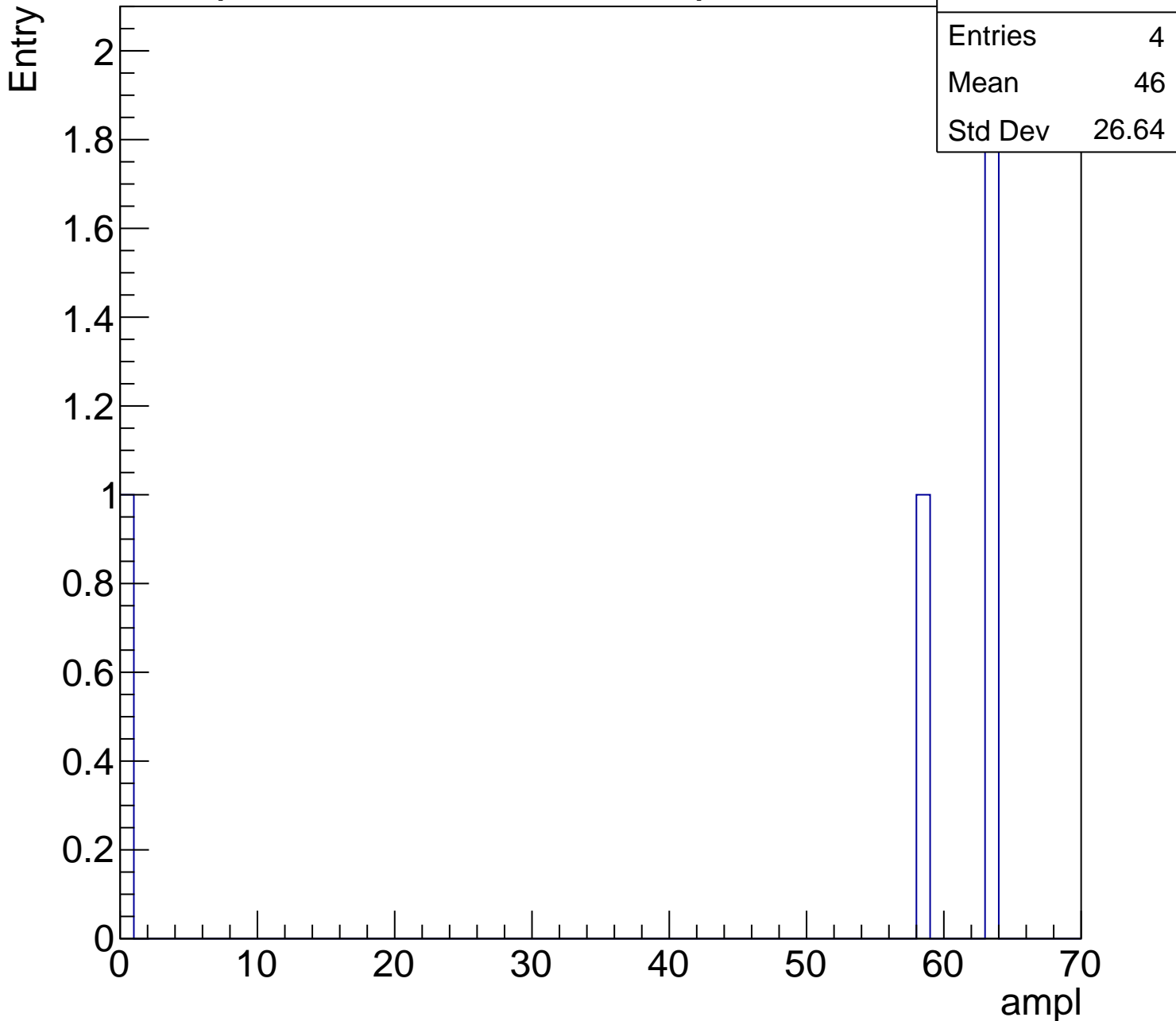
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46
Std Dev	26.64

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch107, adc0

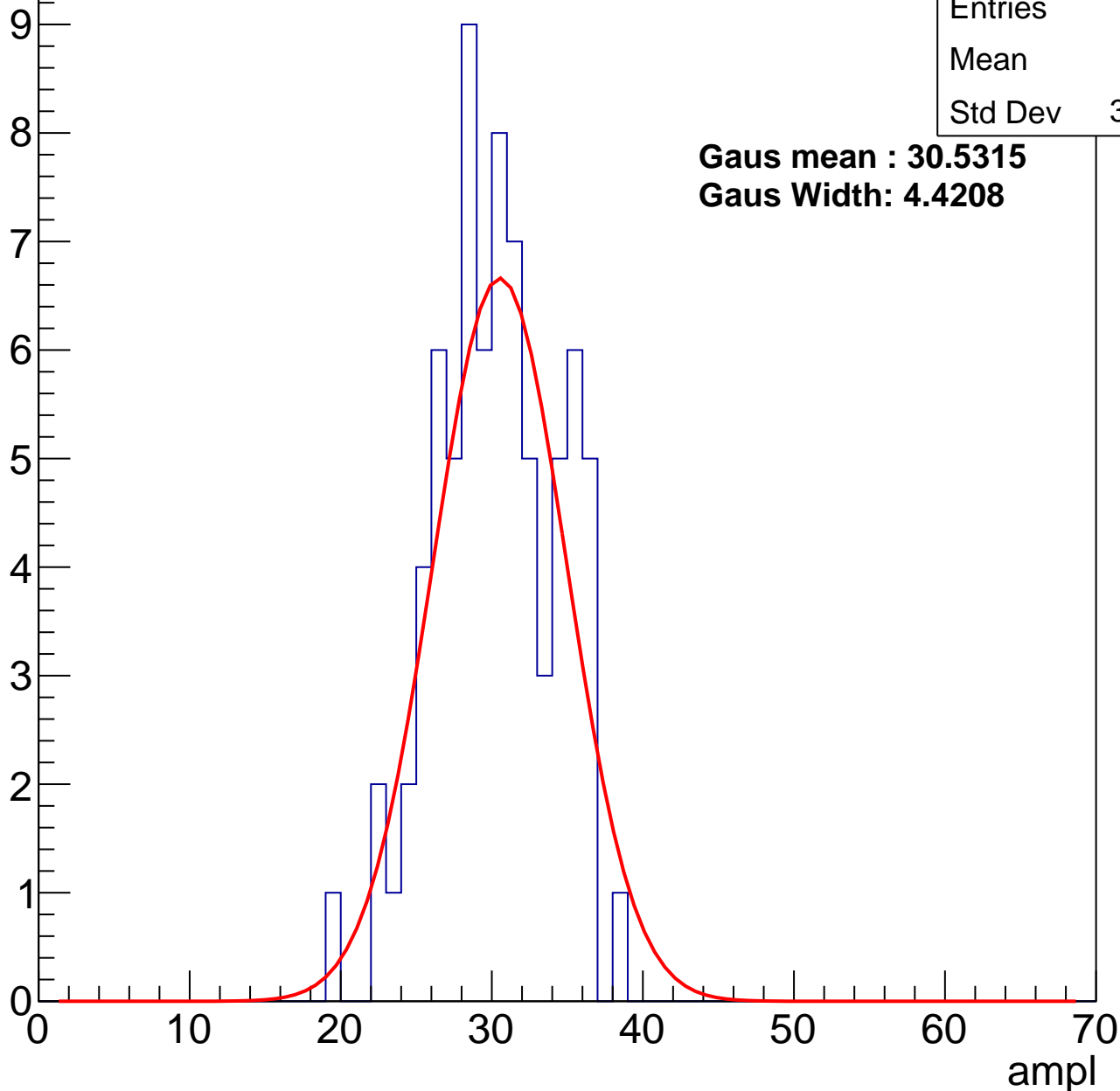
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.8
Std Dev	3.937

Gaus mean : 30.5315

Gaus Width: 4.4208



B1L103S, U24-ch107, adc1

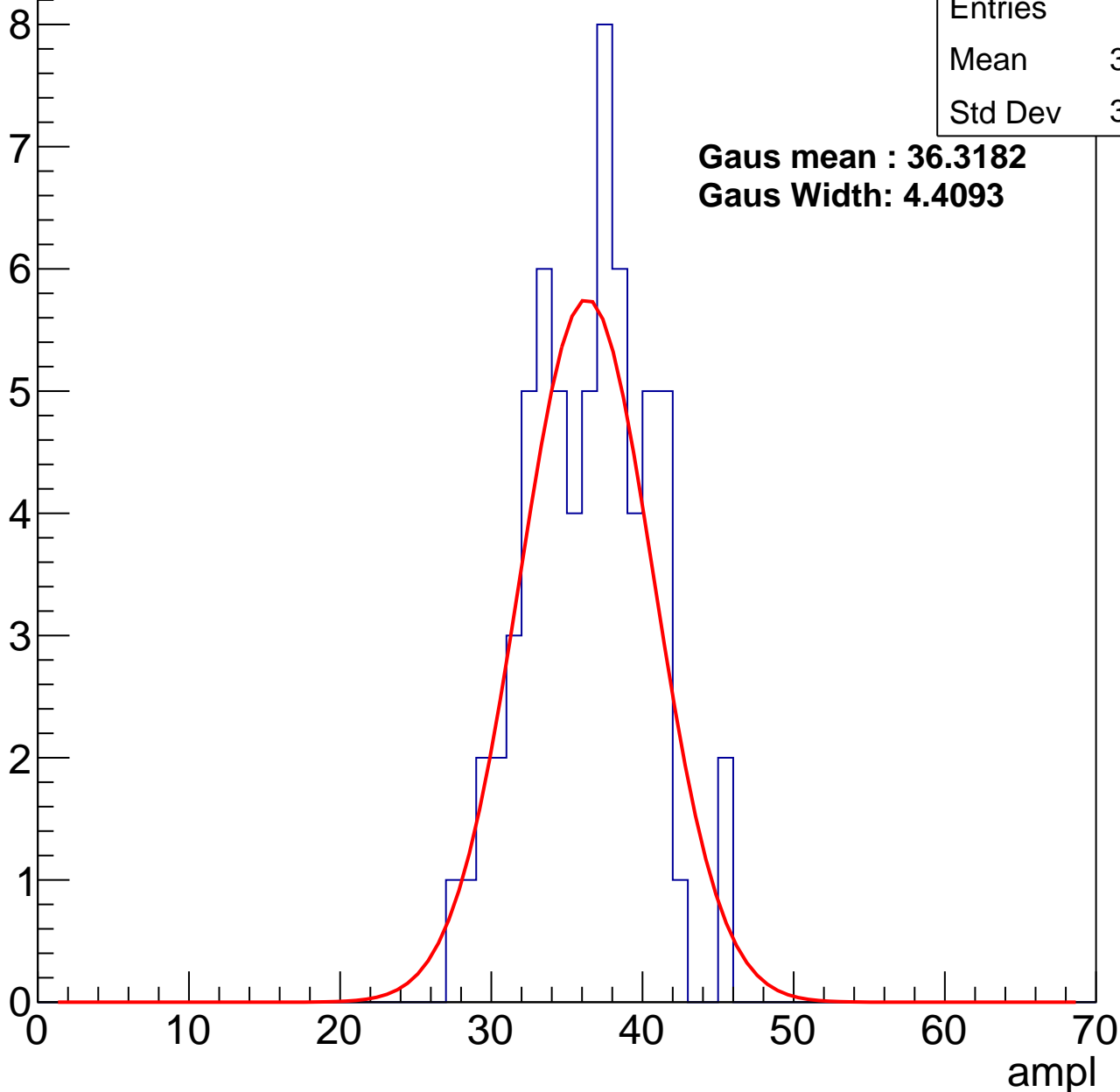
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.86
Std Dev	3.957

Gaus mean : 36.3182

Gaus Width: 4.4093



B1L103S, U24-ch107, adc2

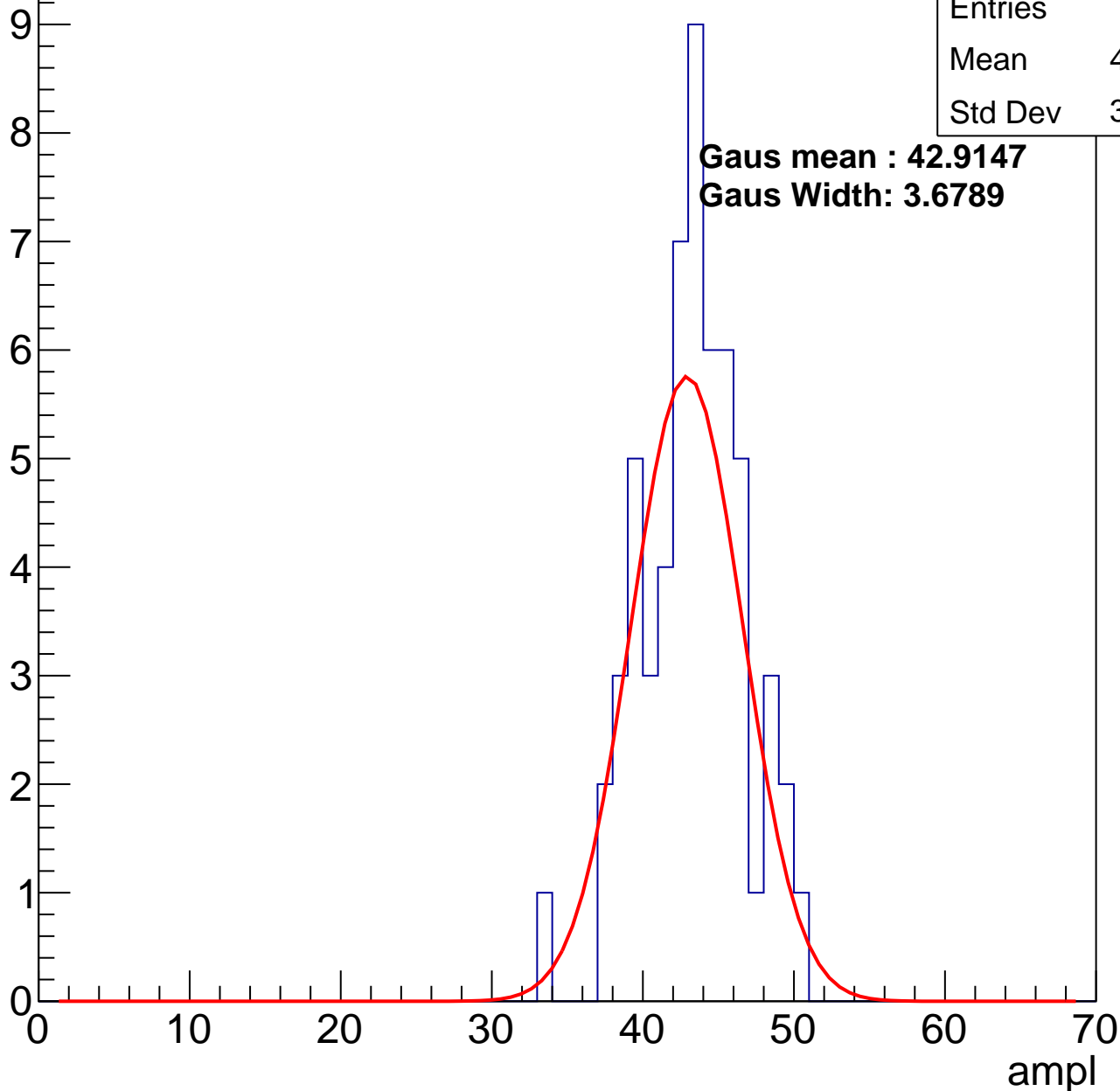
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.83
Std Dev	3.379

Gaus mean : 42.9147

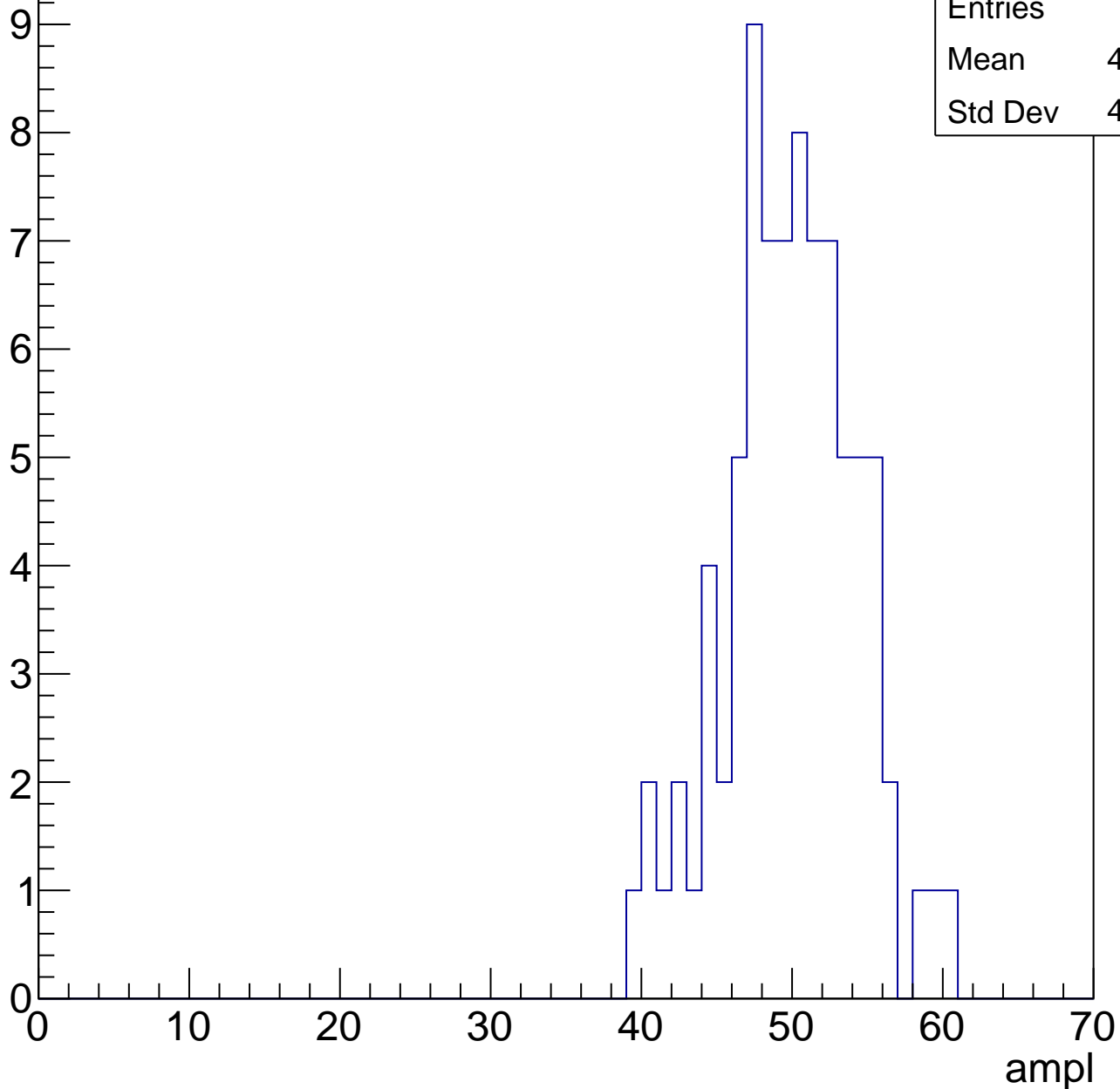
Gaus Width: 3.6789



B1L103S, U24-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

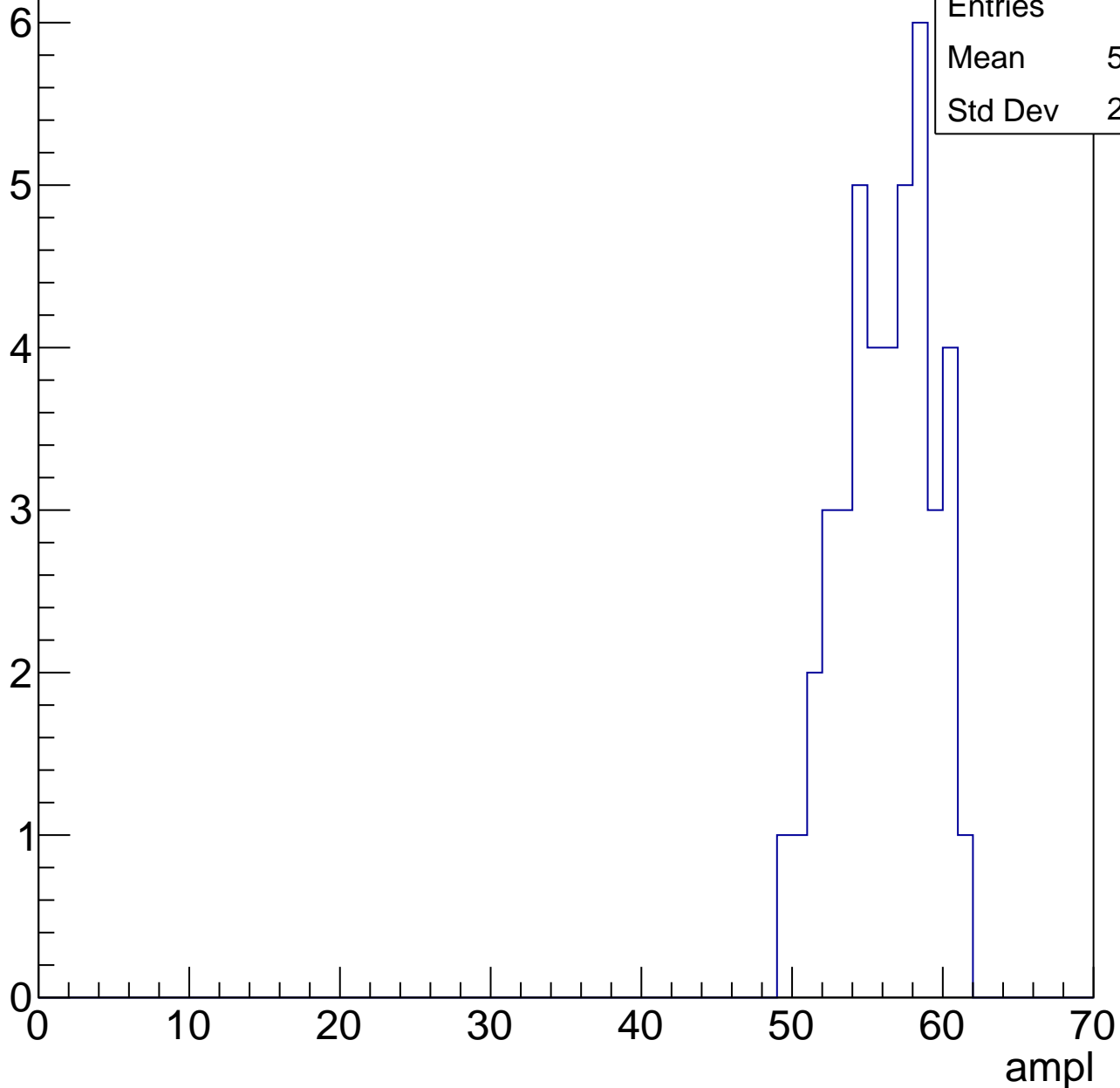


Entries	83
Mean	49.46
Std Dev	4.317

B1L103S, U24-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

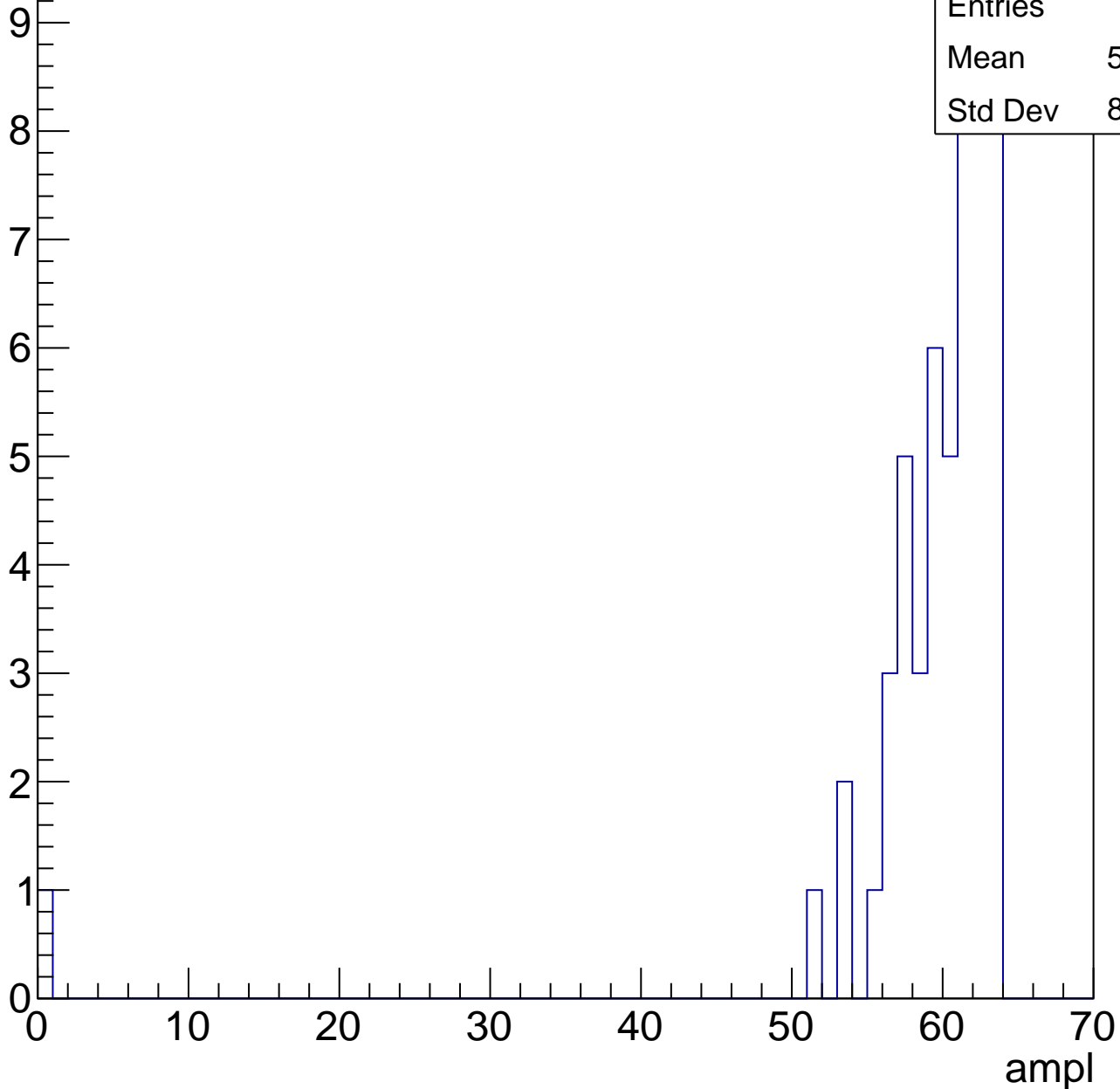


B1L103S, U24-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

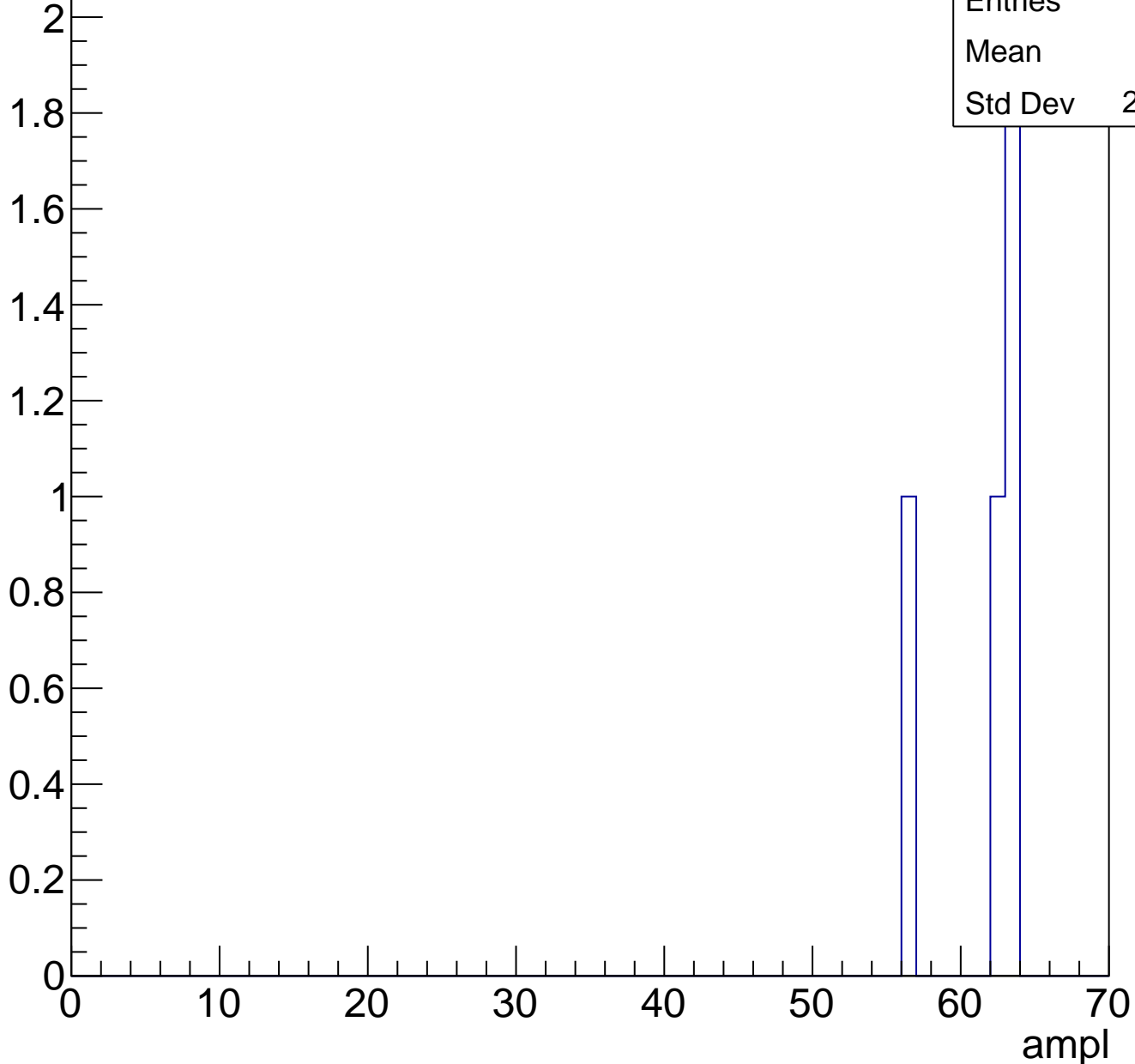
Entries	53
Mean	58.57
Std Dev	8.603



B1L103S, U24-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch108, adc0

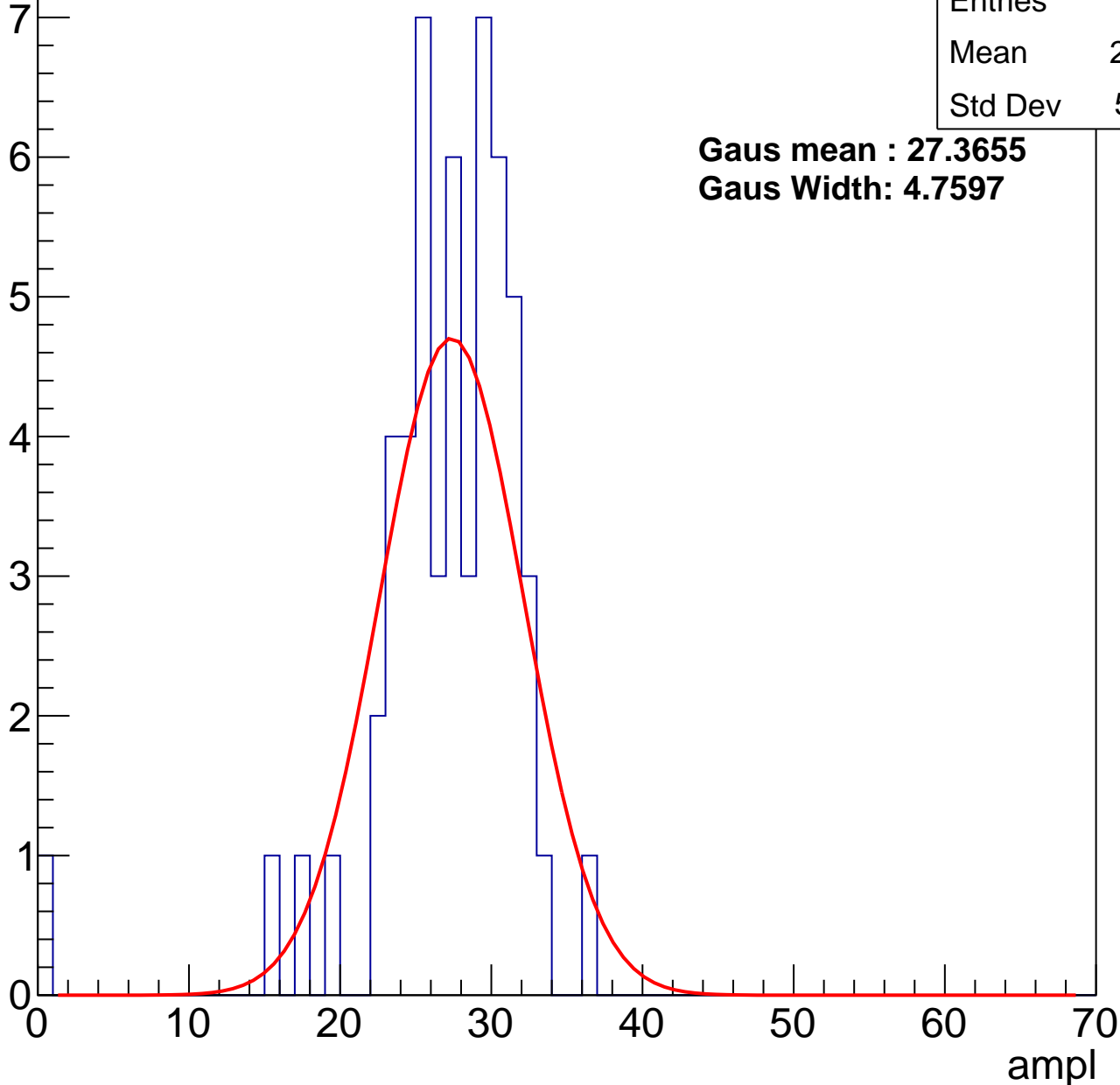
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	26.52
Std Dev	5.281

Gaus mean : 27.3655

Gaus Width: 4.7597



B1L103S, U24-ch108, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	34.45
Std Dev	4.378

Gaus mean : 35.6115

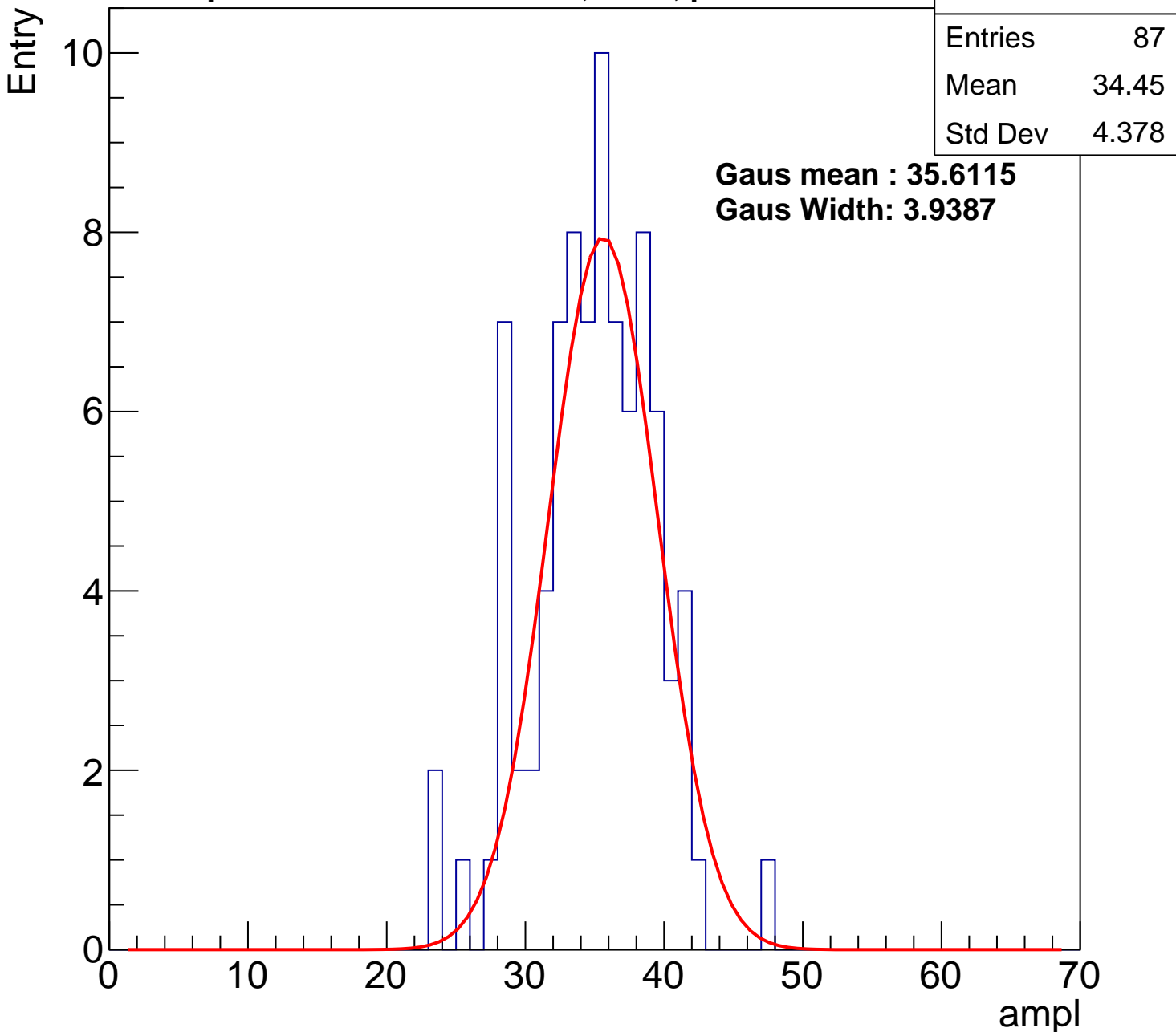
Gaus Width: 3.9387

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch108, adc2

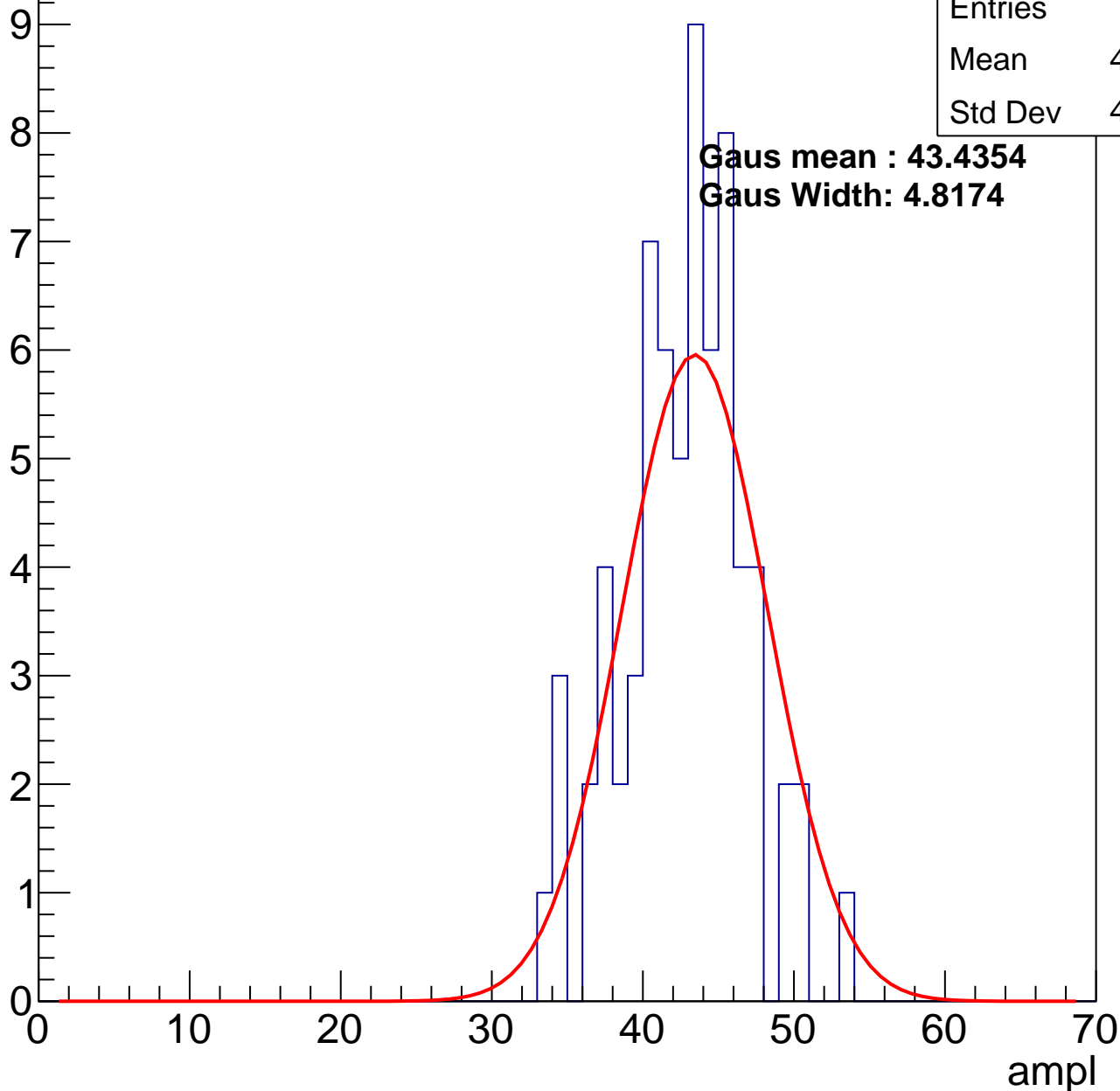
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42.29
Std Dev	4.079

Gaus mean : 43.4354

Gaus Width: 4.8174

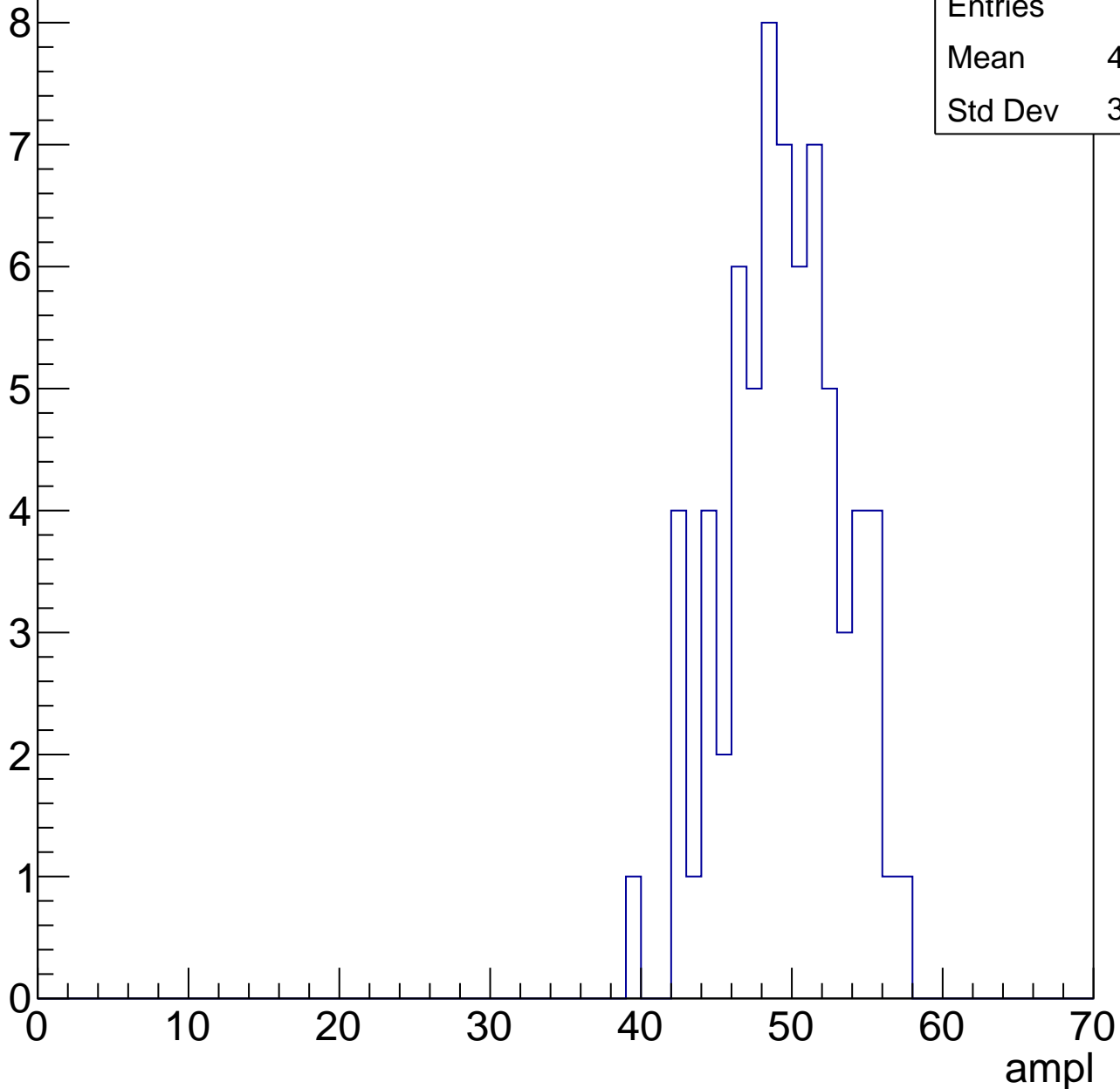


B1L103S, U24-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.97
Std Dev	3.864

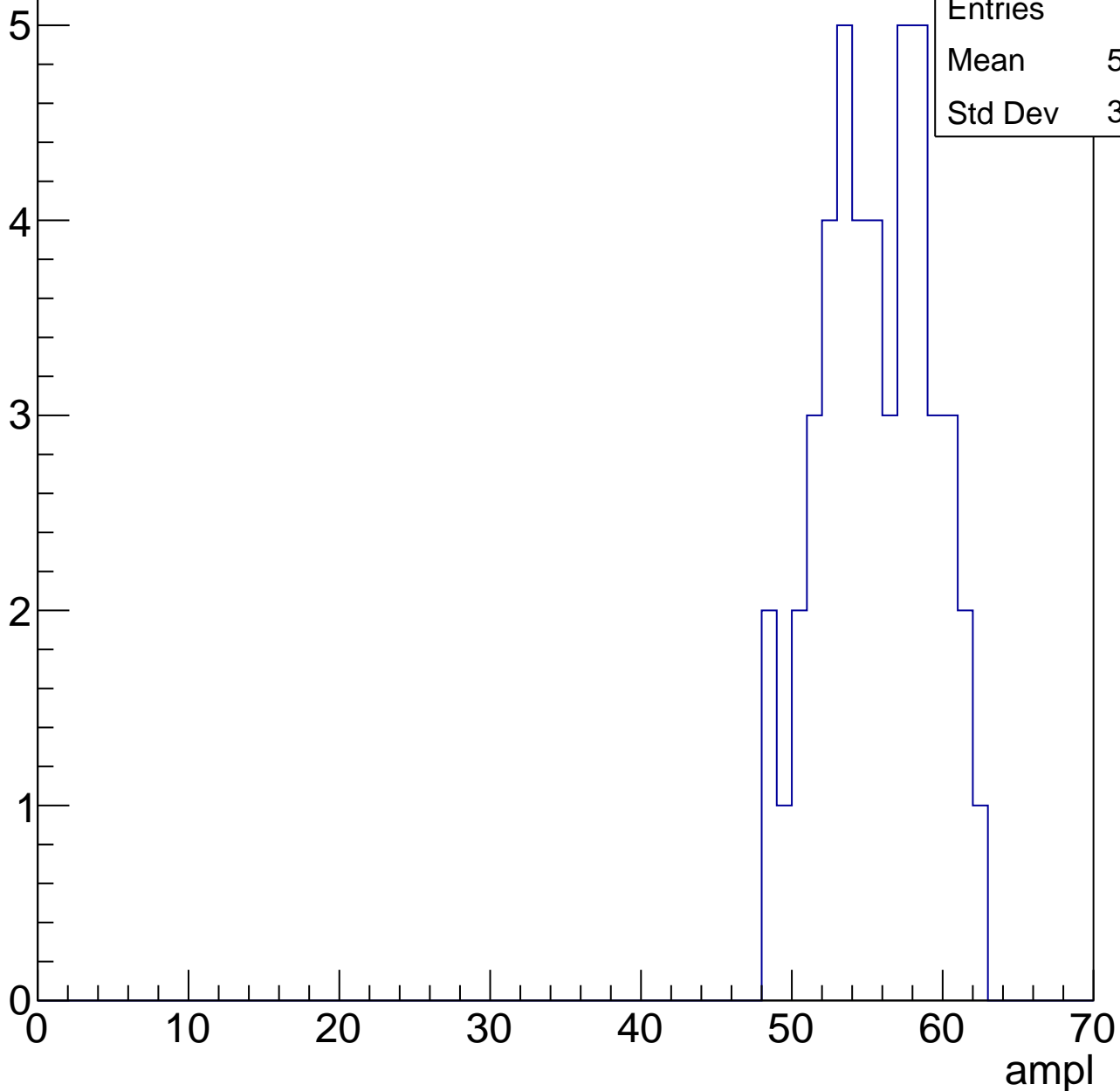


B1L103S, U24-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	55.13
Std Dev	3.583

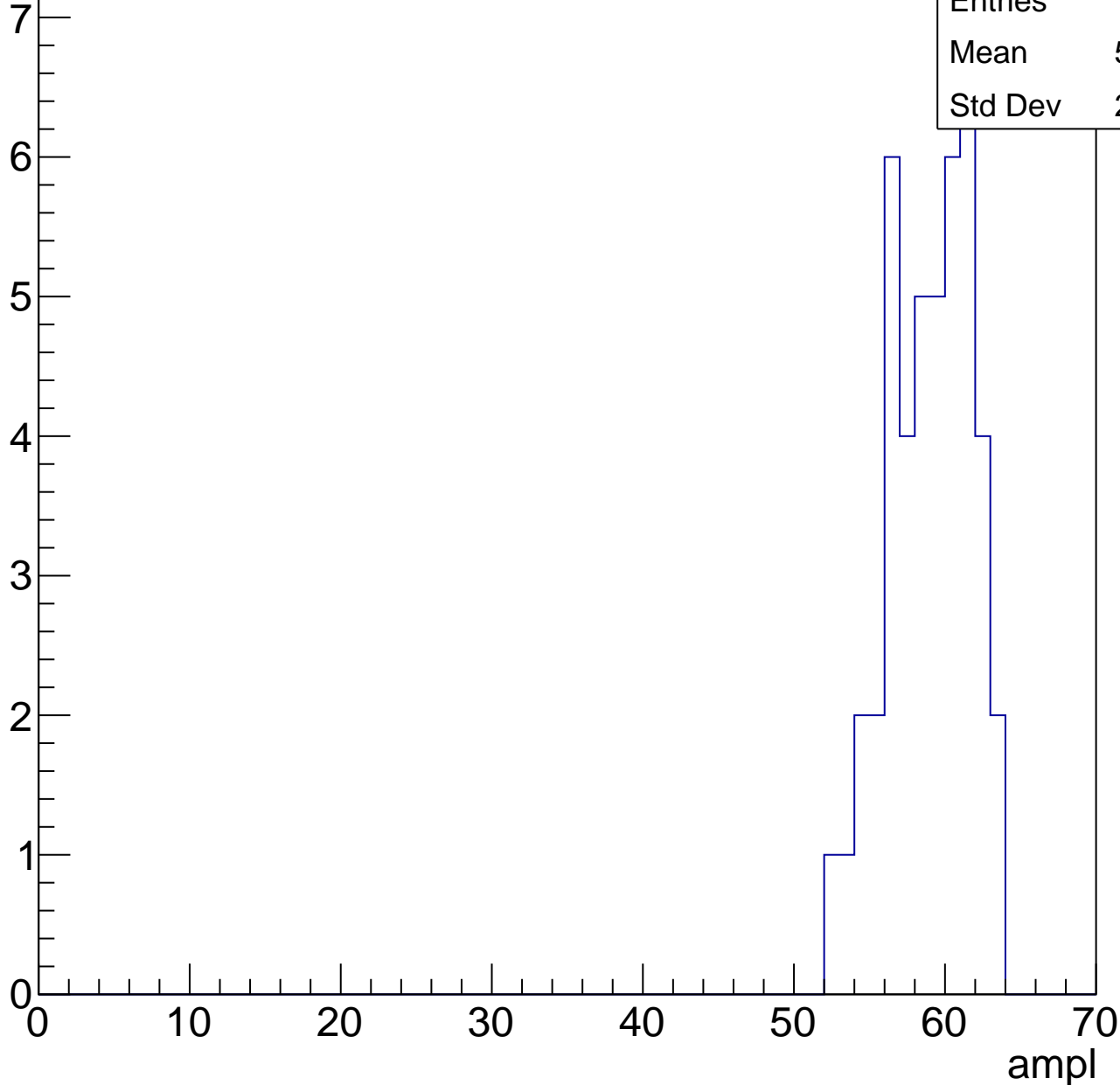


B1L103S, U24-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.51
Std Dev	2.721

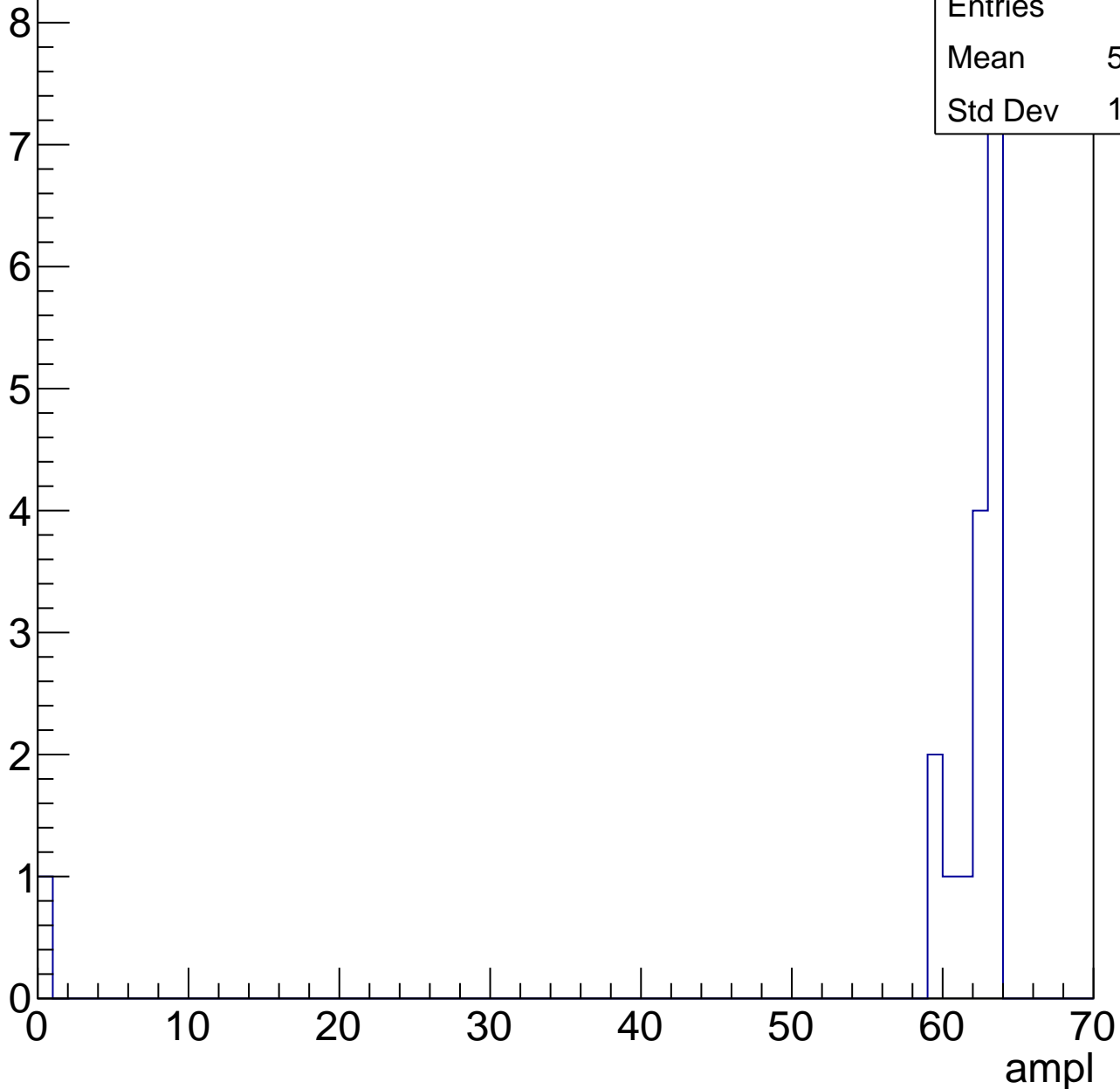


B1L103S, U24-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	58.29
Std Dev	14.64



B1L103S, U24-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch109, adc0

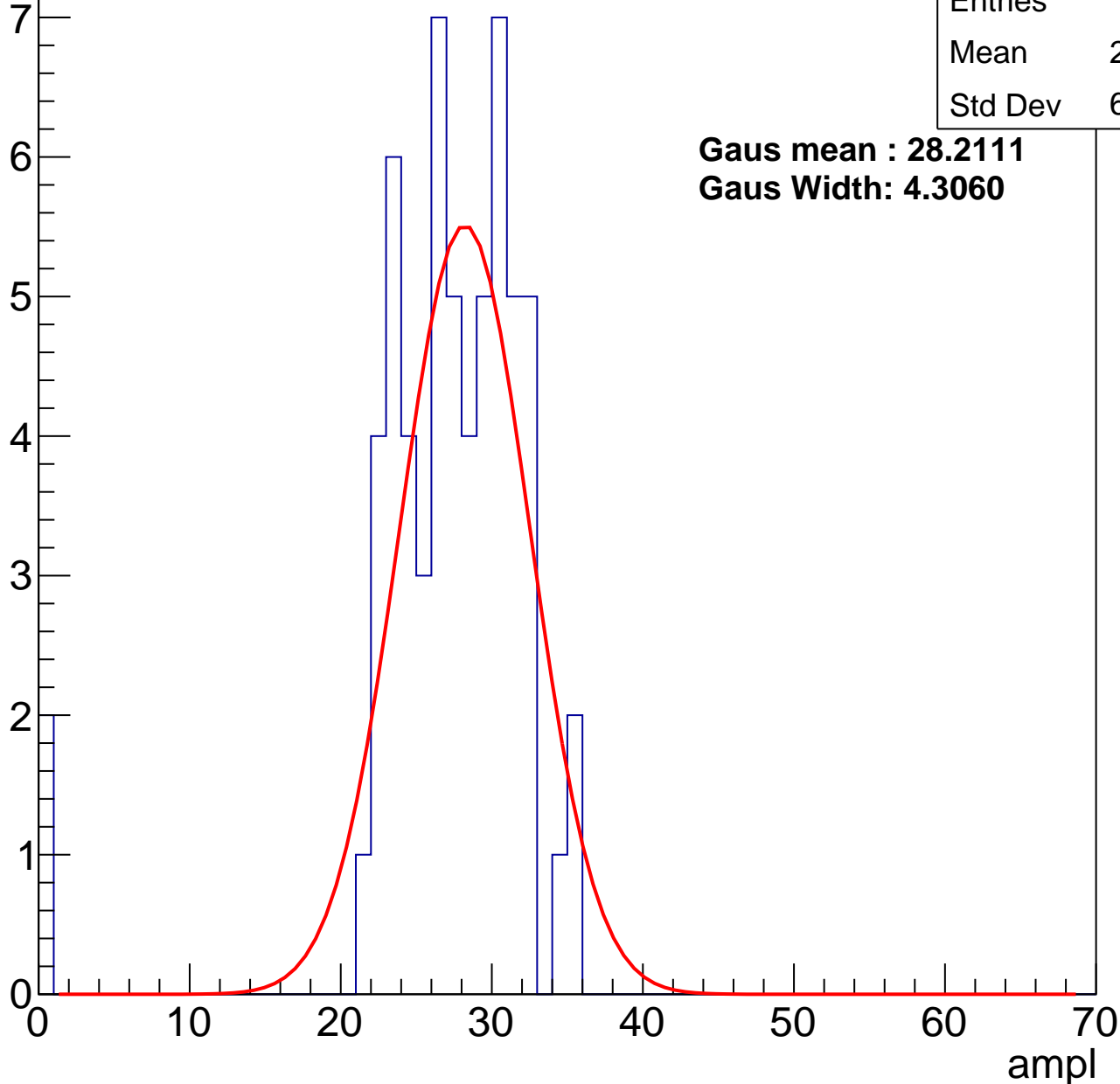
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	26.57
Std Dev	6.009

Gaus mean : 28.2111

Gaus Width: 4.3060



B1L103S, U24-ch109, adc1

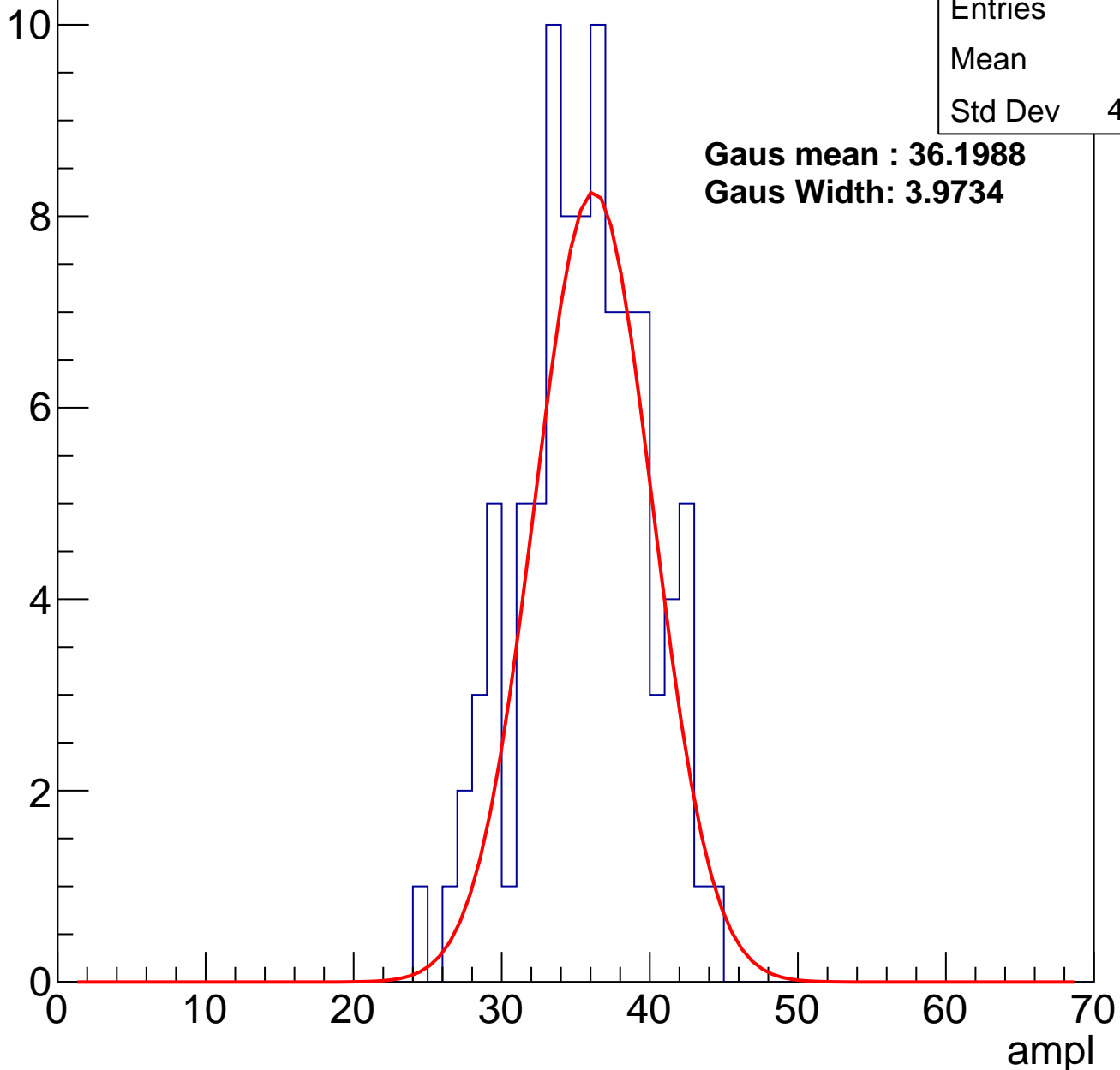
calib_packv5_042523_0143.root, FC#7, port C2

Entries	94
Mean	35.1
Std Dev	4.223

Gaus mean : 36.1988

Gaus Width: 3.9734

Entry



B1L103S, U24-ch109, adc2

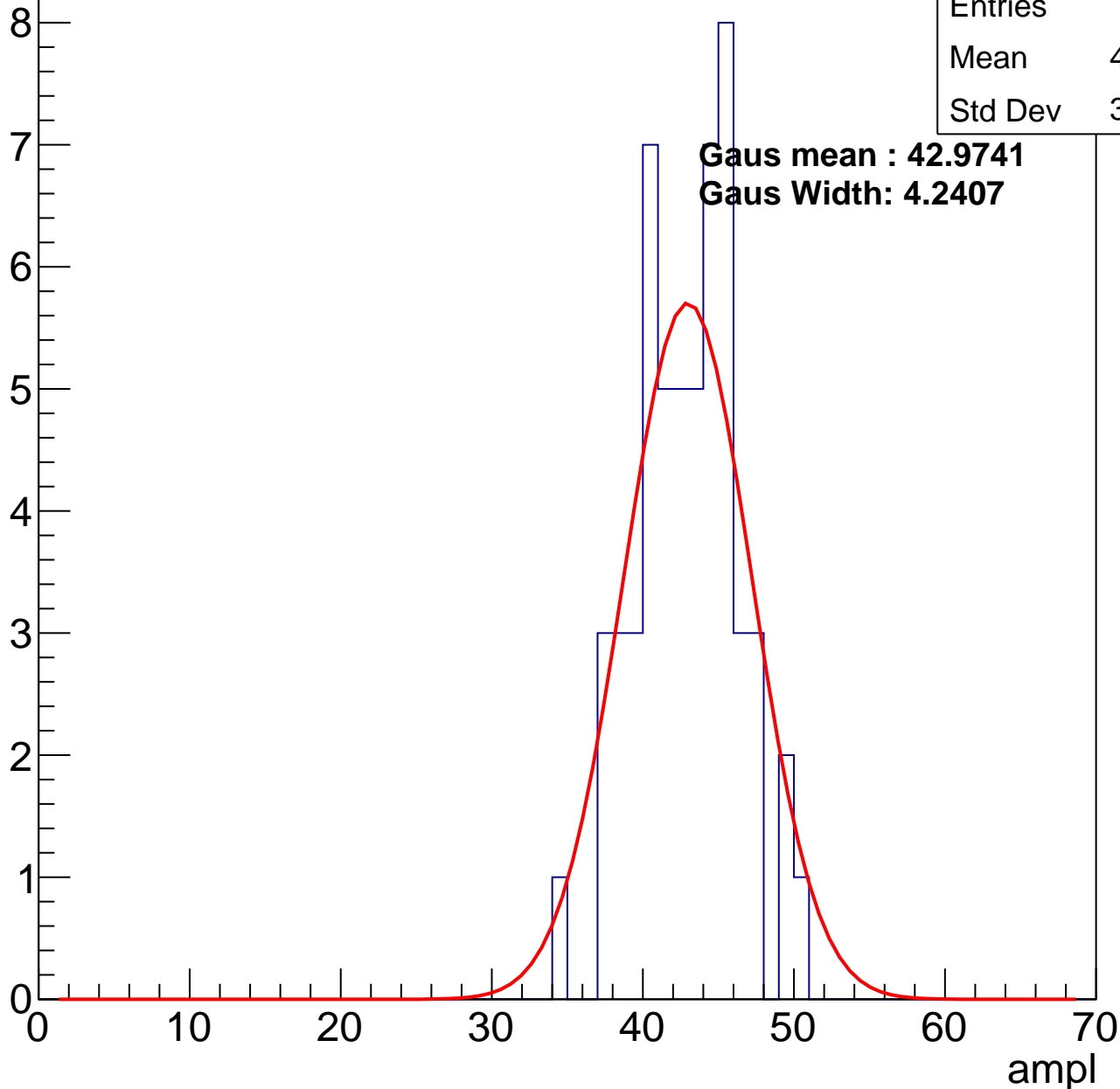
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.52
Std Dev	3.333

Gaus mean : 42.9741

Gaus Width: 4.2407

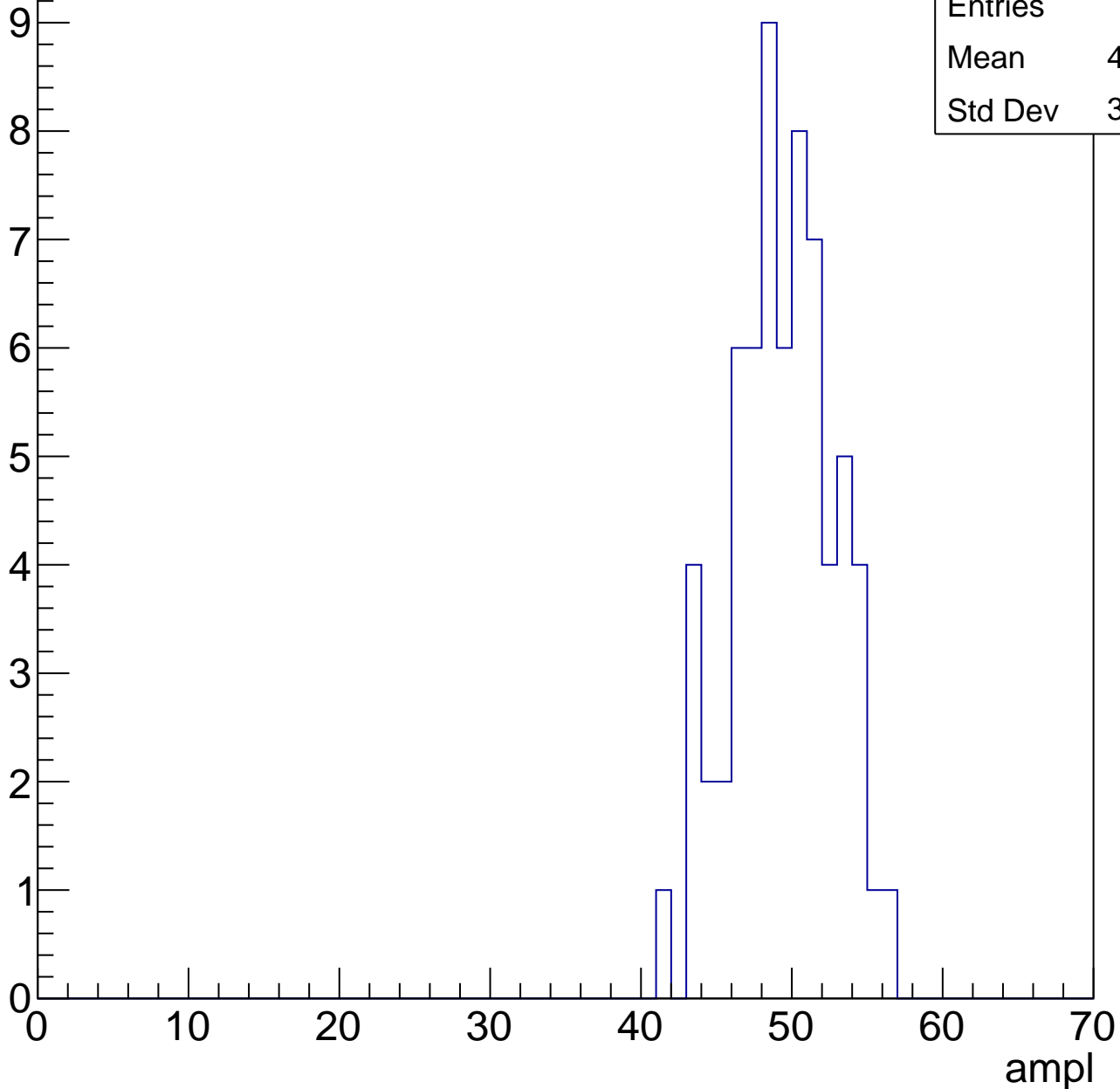


B1L103S, U24-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

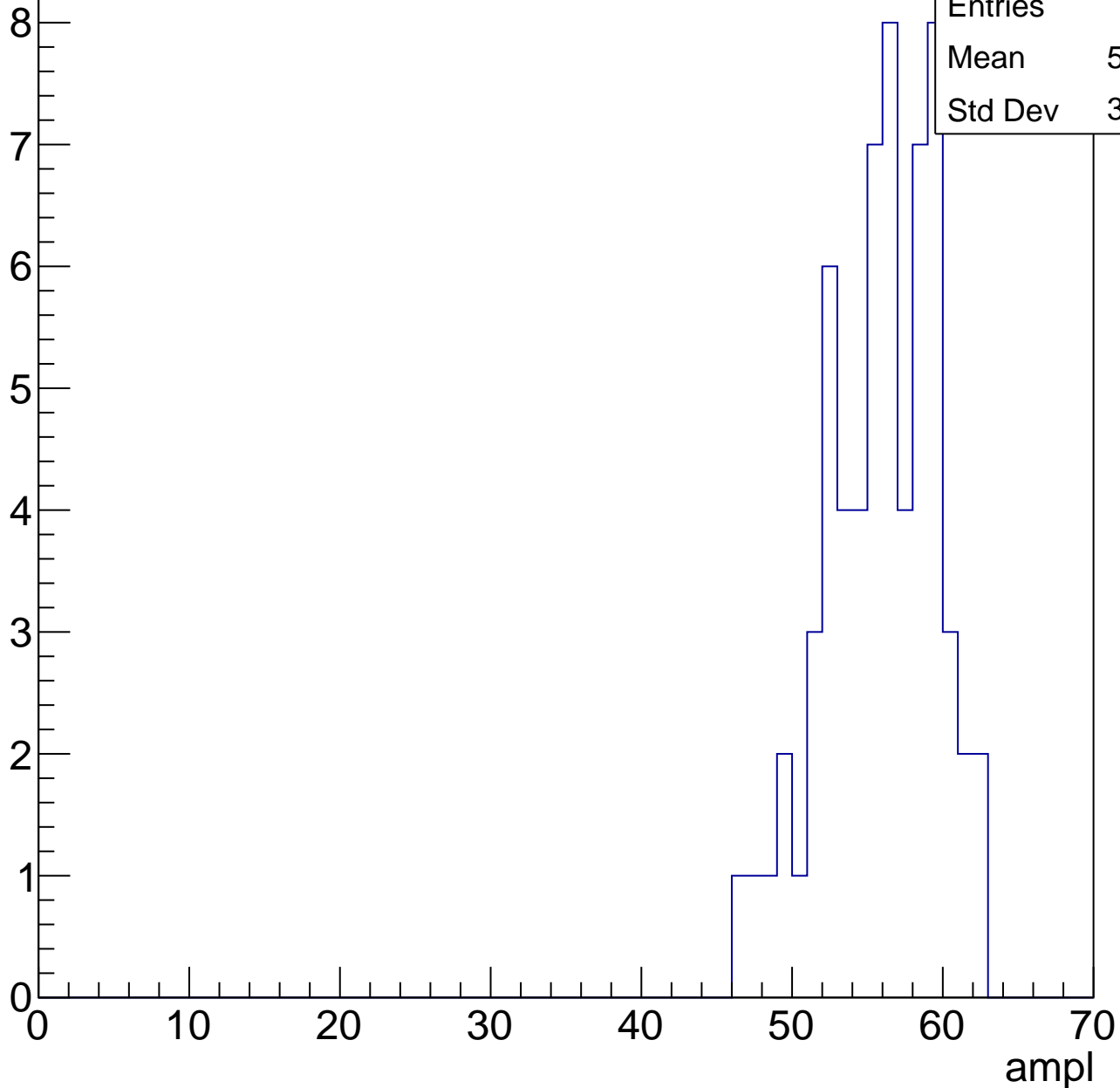
Entries	66
Mean	48.97
Std Dev	3.289



B1L103S, U24-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



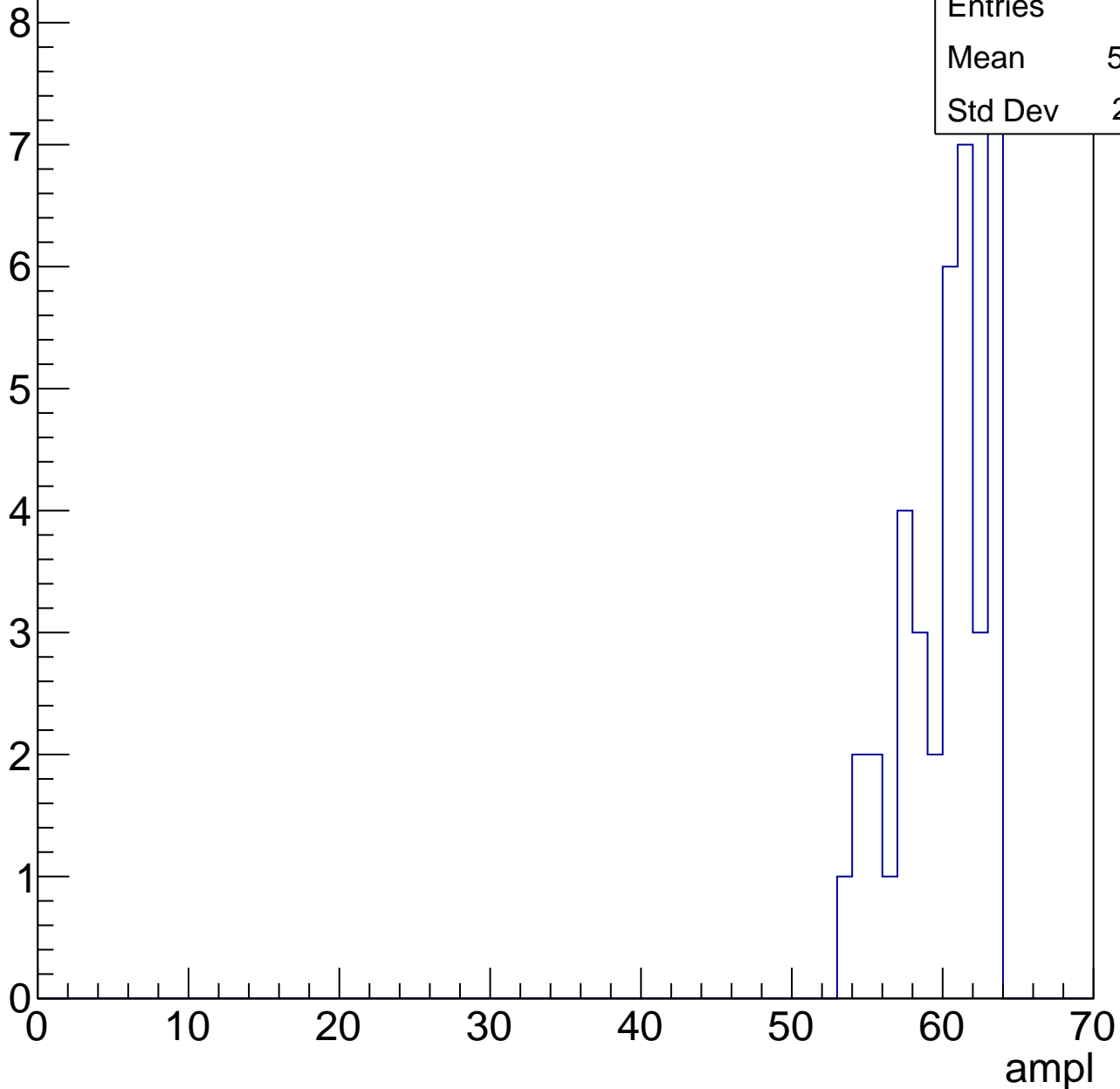
Entries	64
Mean	55.42
Std Dev	3.669

B1L103S, U24-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

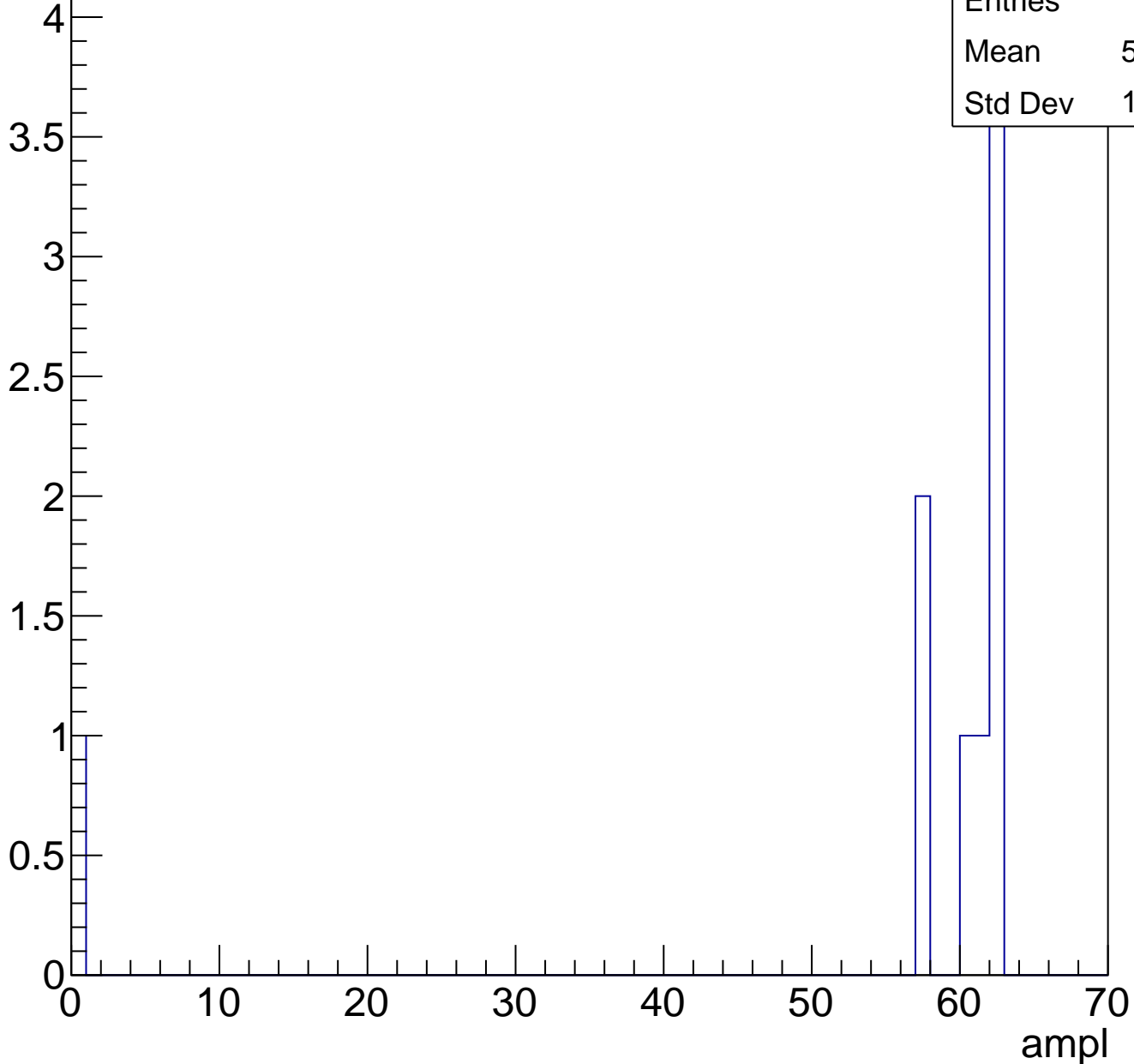
Entries	39
Mean	59.59
Std Dev	2.871



B1L103S, U24-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch110, adc0

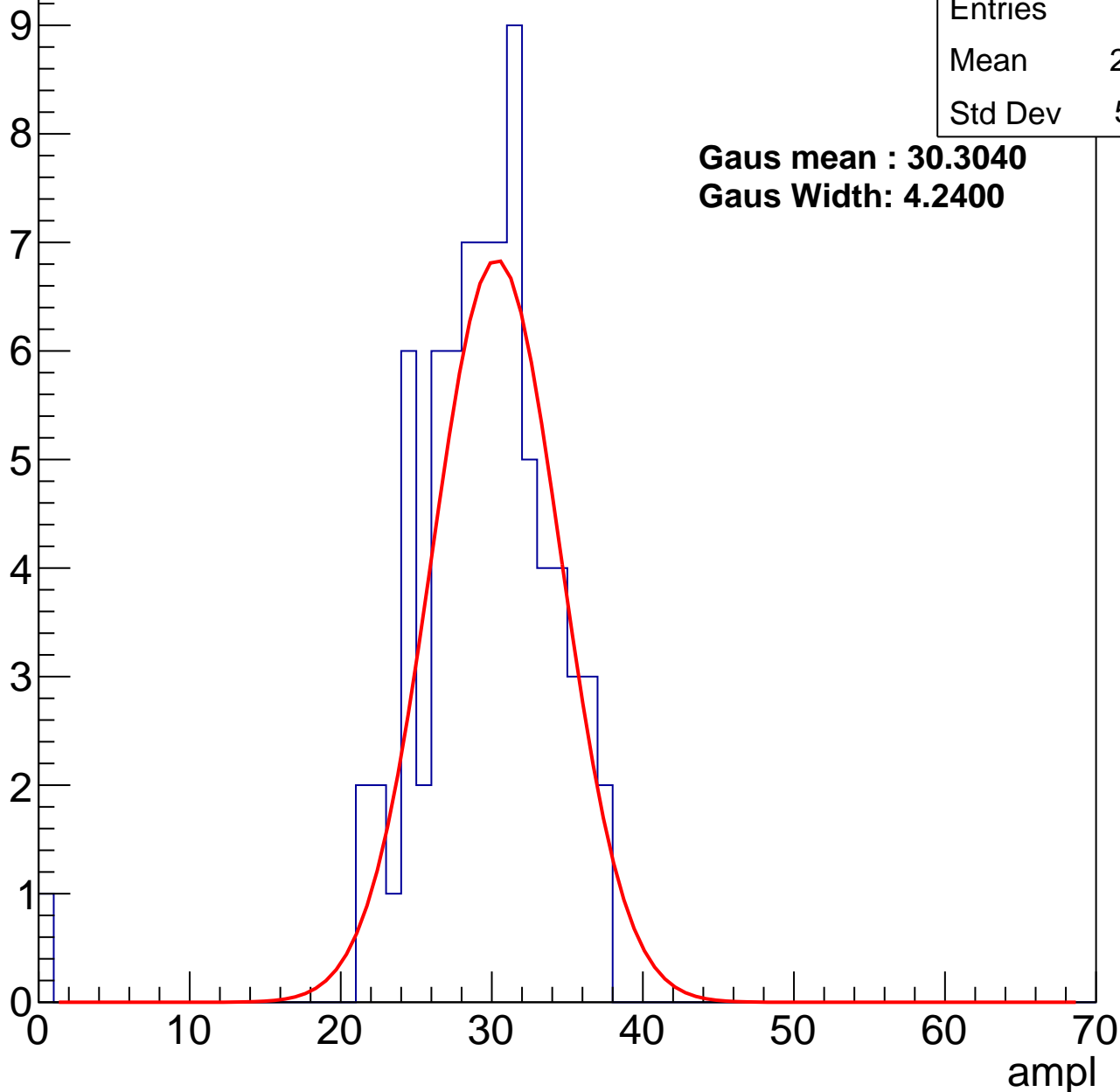
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.88
Std Dev	5.101

Gaus mean : 30.3040

Gaus Width: 4.2400



B1L103S, U24-ch110, adc1

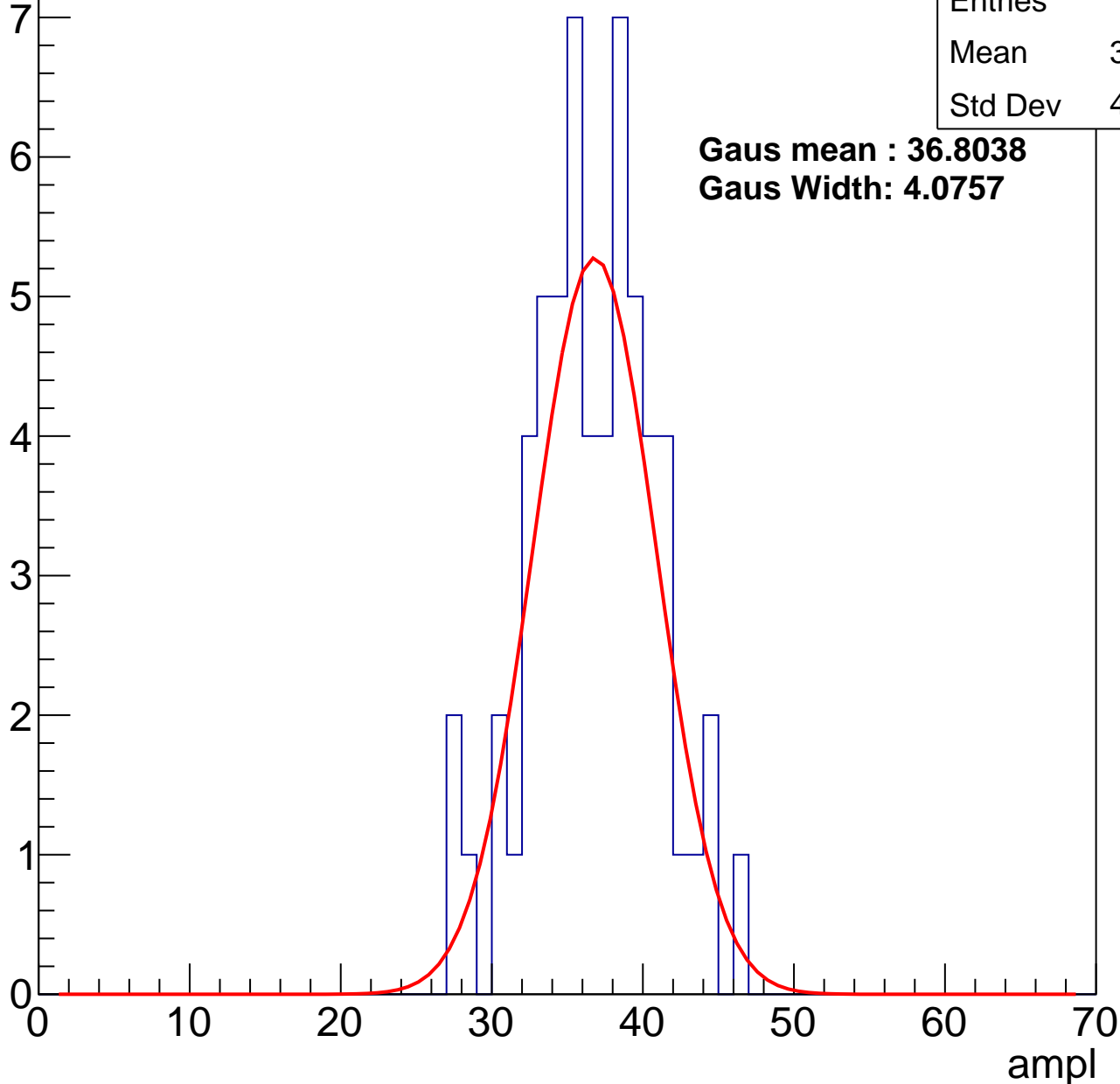
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.28
Std Dev	4.099

Gaus mean : 36.8038

Gaus Width: 4.0757



B1L103S, U24-ch110, adc2

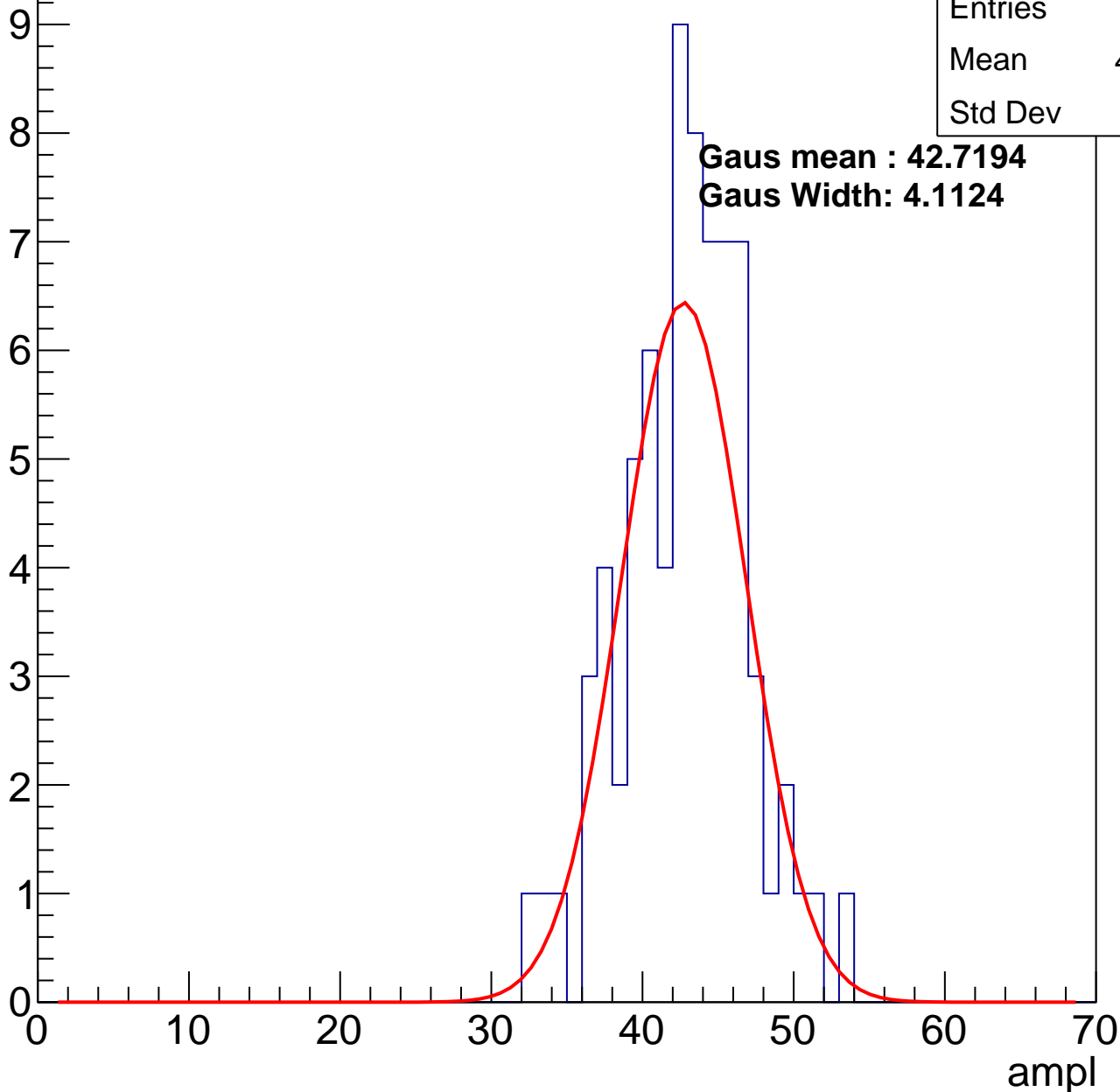
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.41
Std Dev	4.08

Gaus mean : 42.7194

Gaus Width: 4.1124

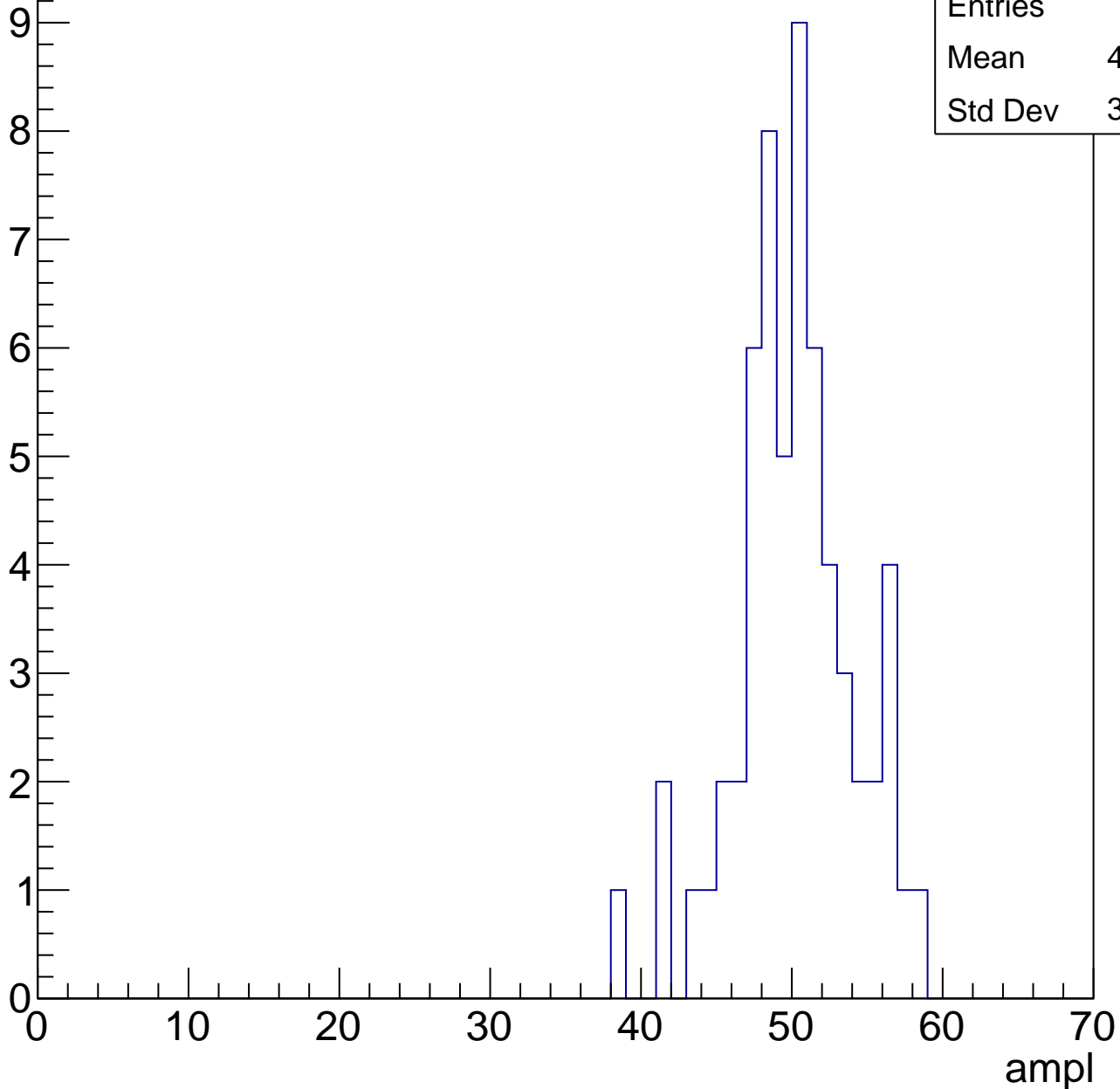


B1L103S, U24-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.67
Std Dev	3.969

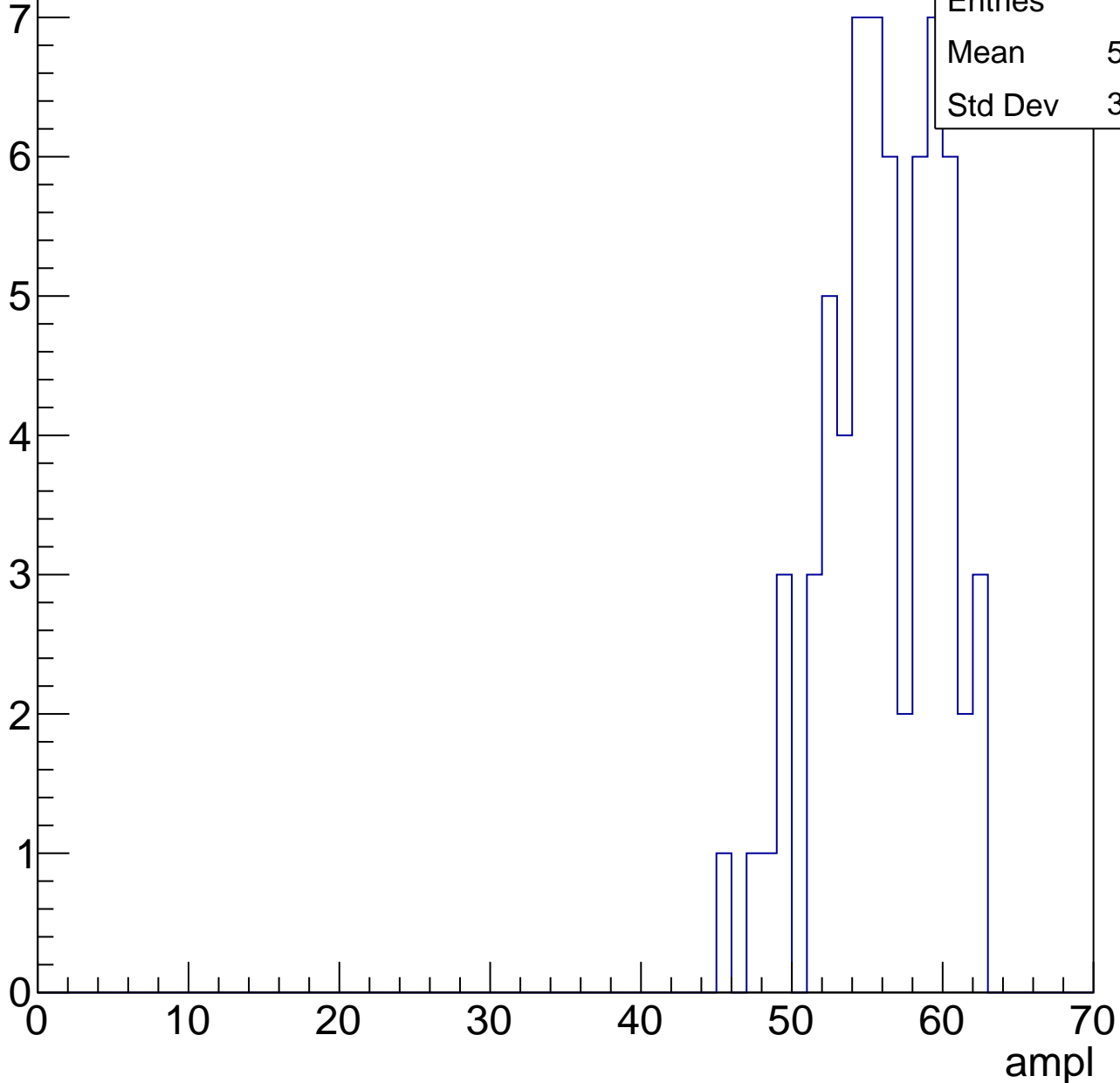


B1L103S, U24-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.53
Std Dev	3.889

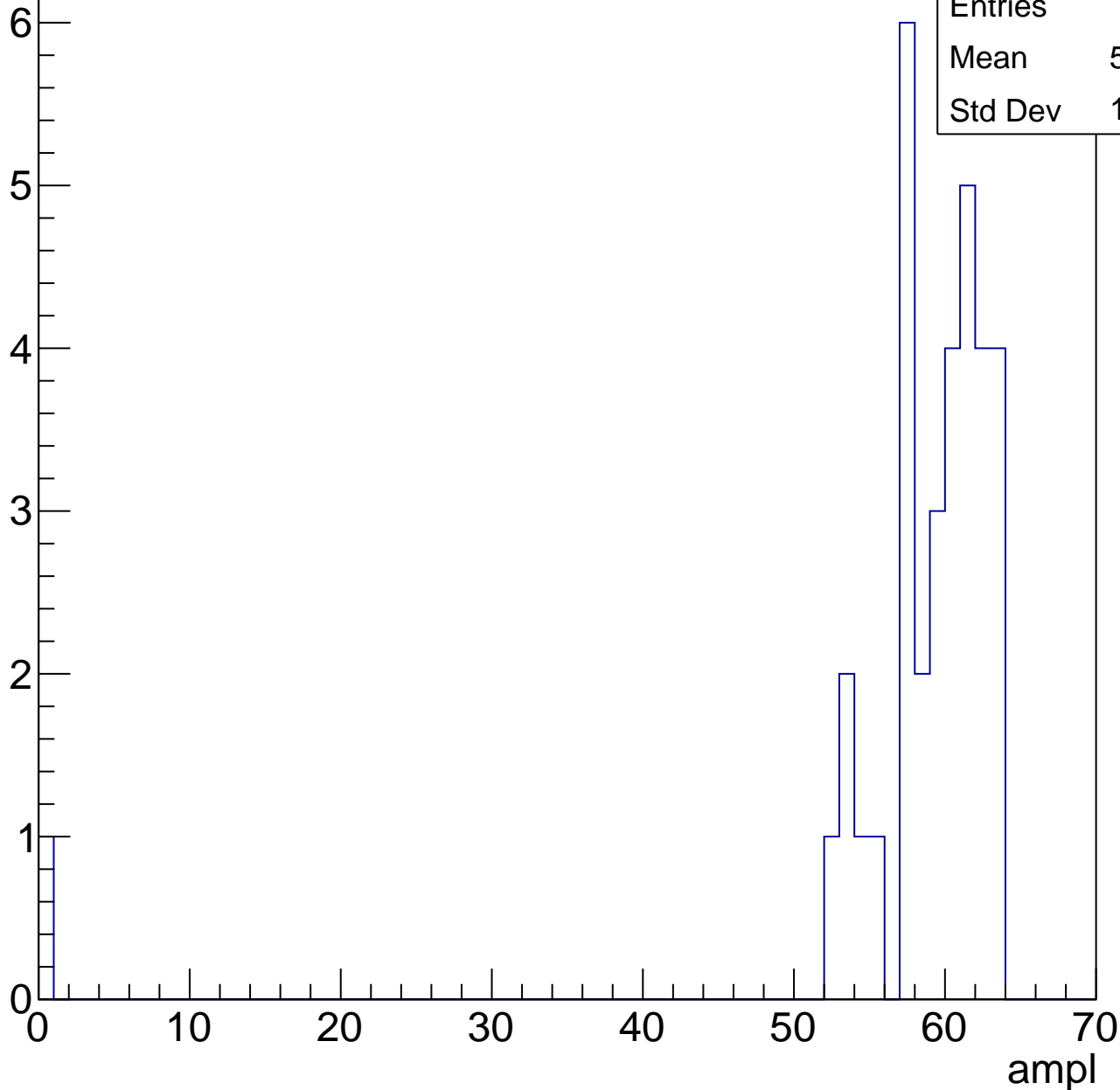


B1L103S, U24-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	57.26
Std Dev	10.42

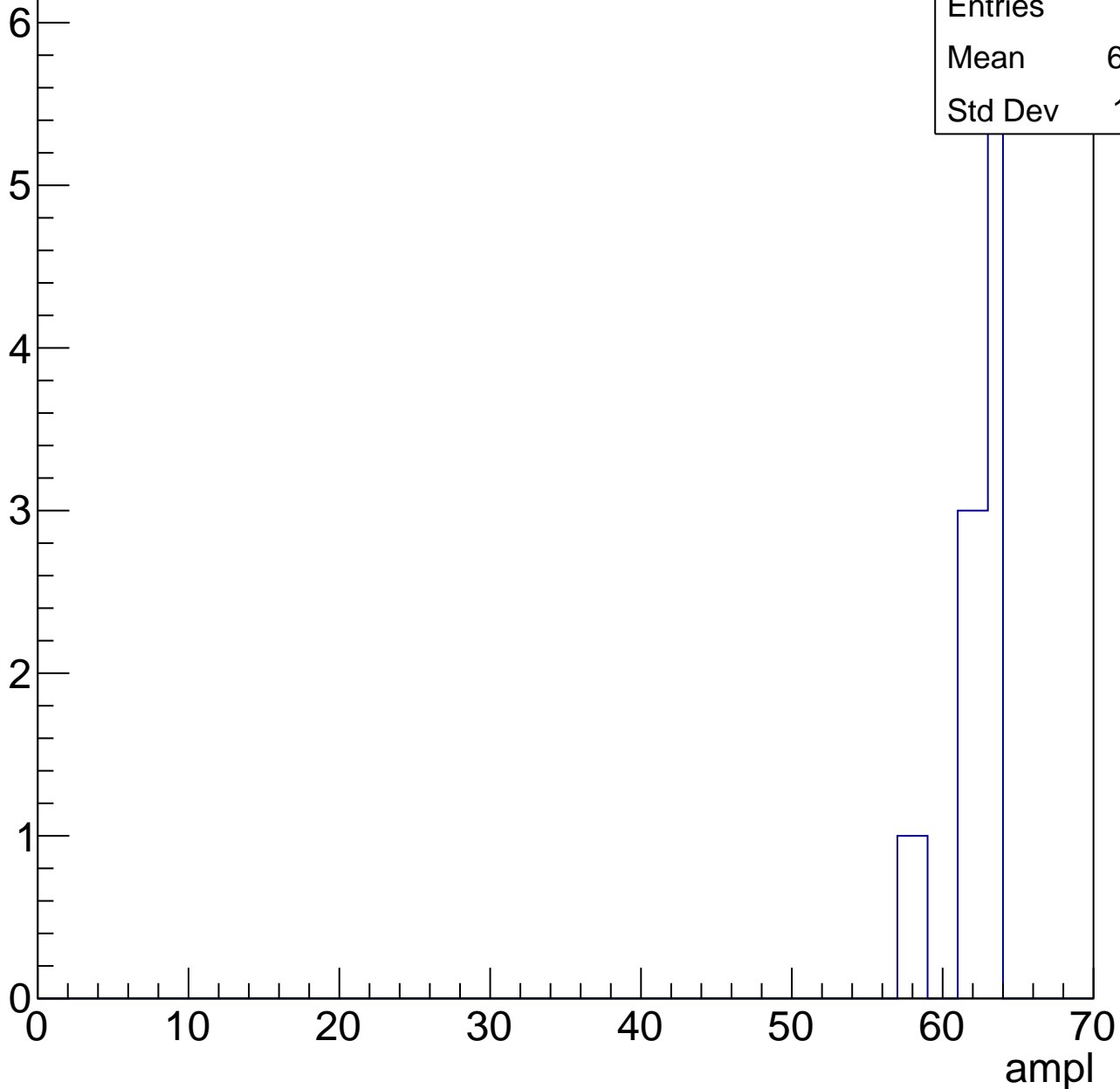


B1L103S, U24-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.57
Std Dev	1.841



B1L103S, U24-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U24-ch111, adc0

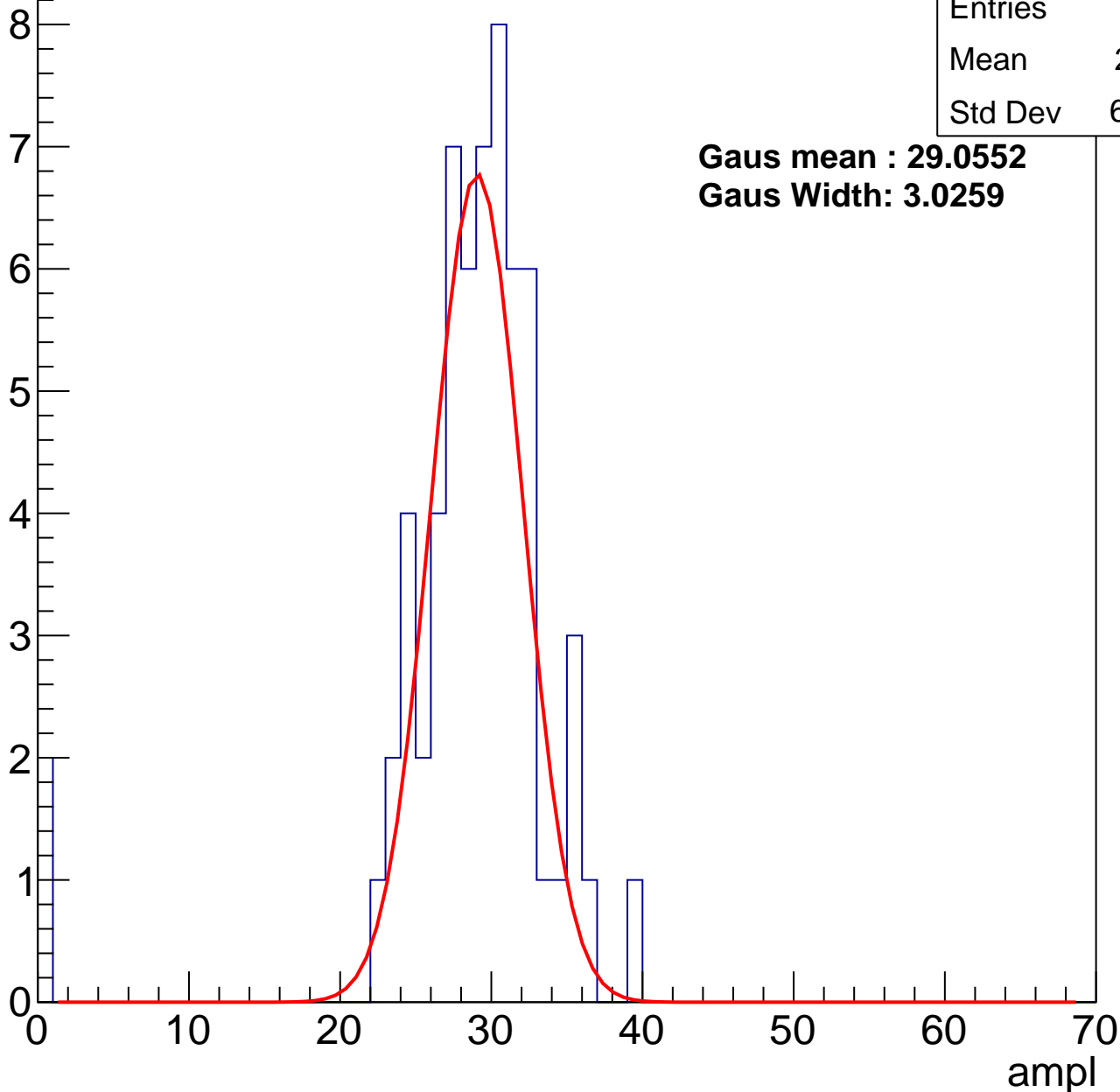
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.11
Std Dev	6.144

Gaus mean : 29.0552

Gaus Width: 3.0259



B1L103S, U24-ch111, adc1

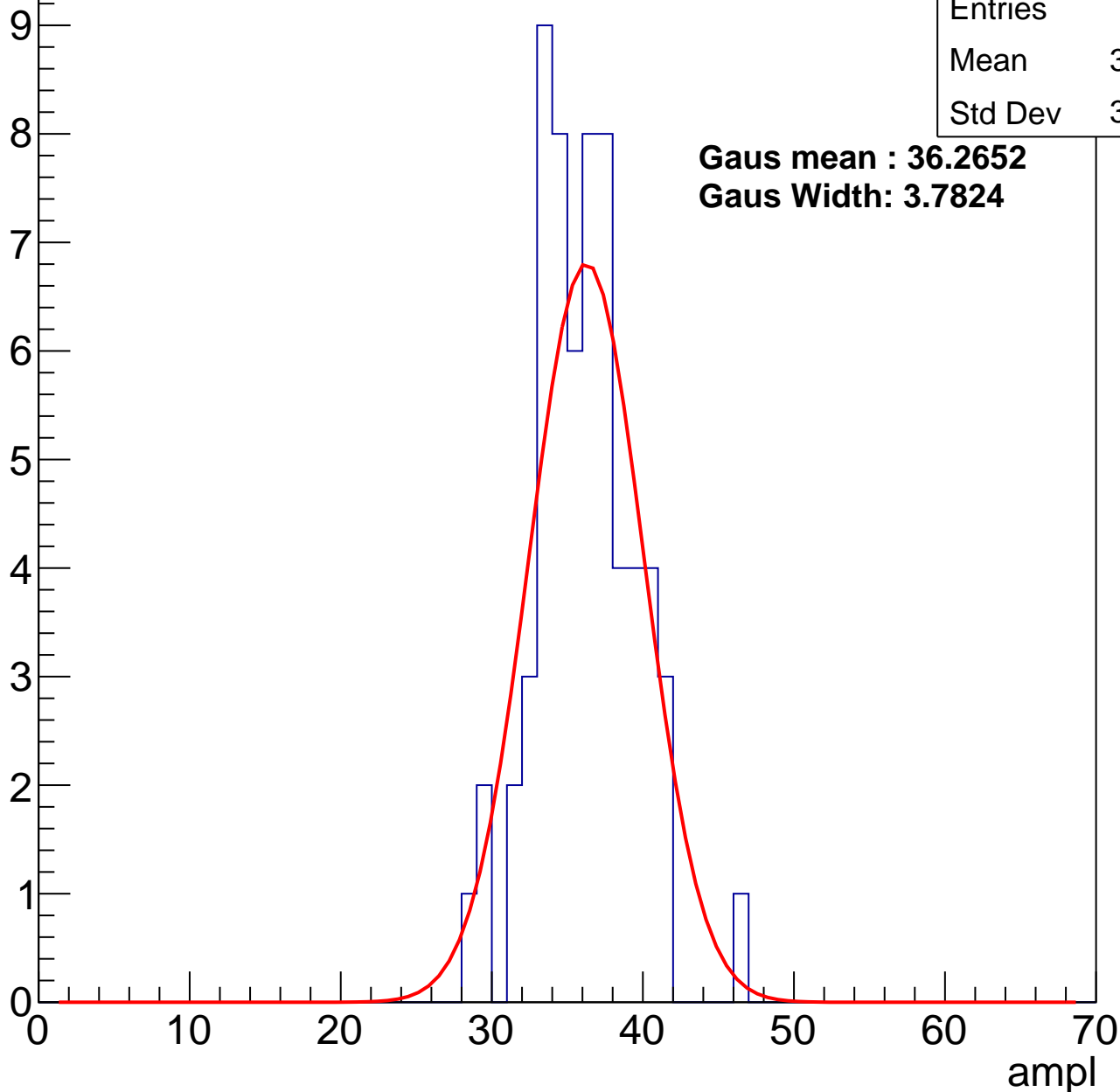
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.62
Std Dev	3.258

Gaus mean : 36.2652

Gaus Width: 3.7824



B1L103S, U24-ch111, adc2

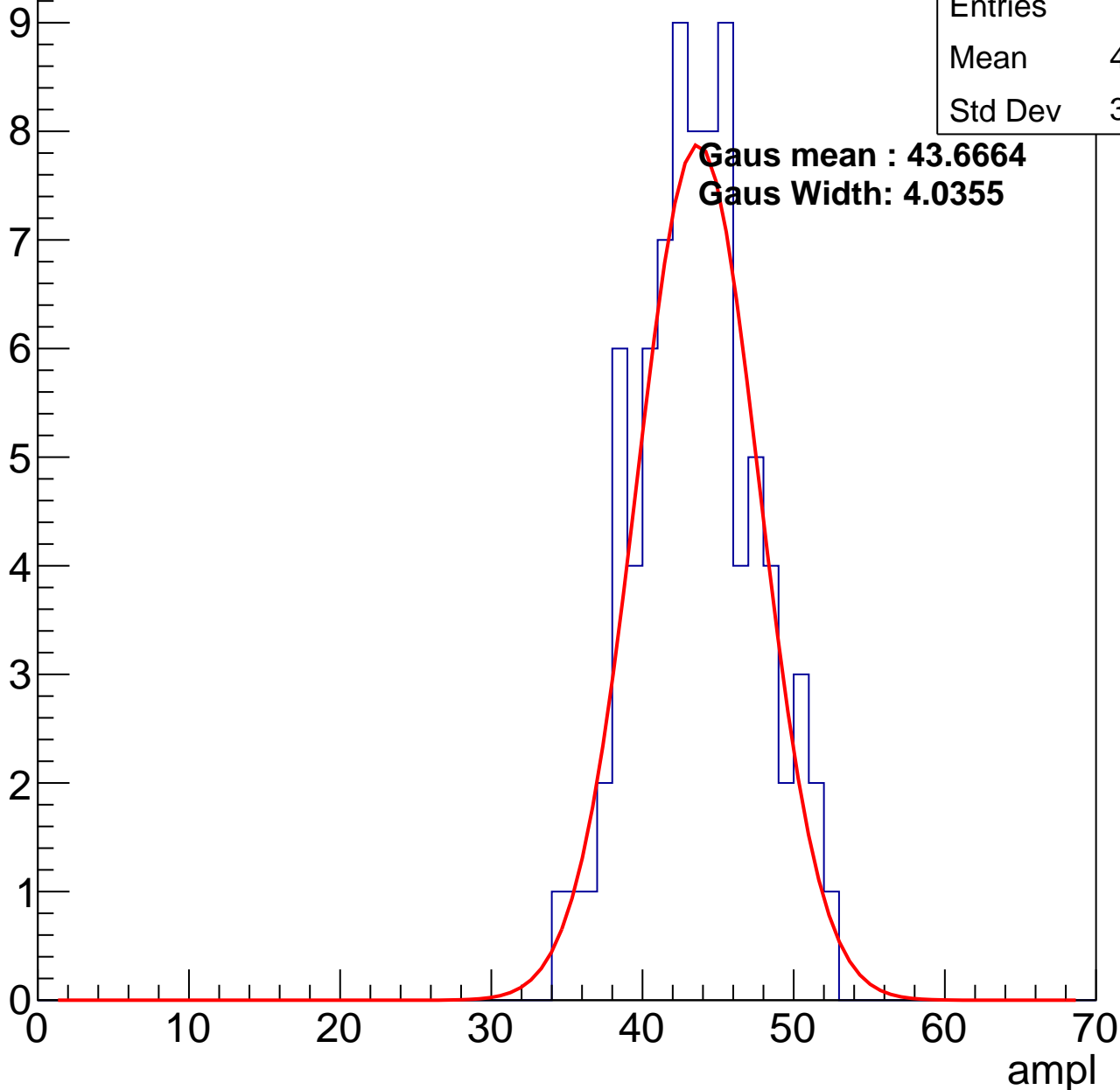
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	43.16
Std Dev	3.892

Gaus mean : 43.6664

Gaus Width: 4.0355

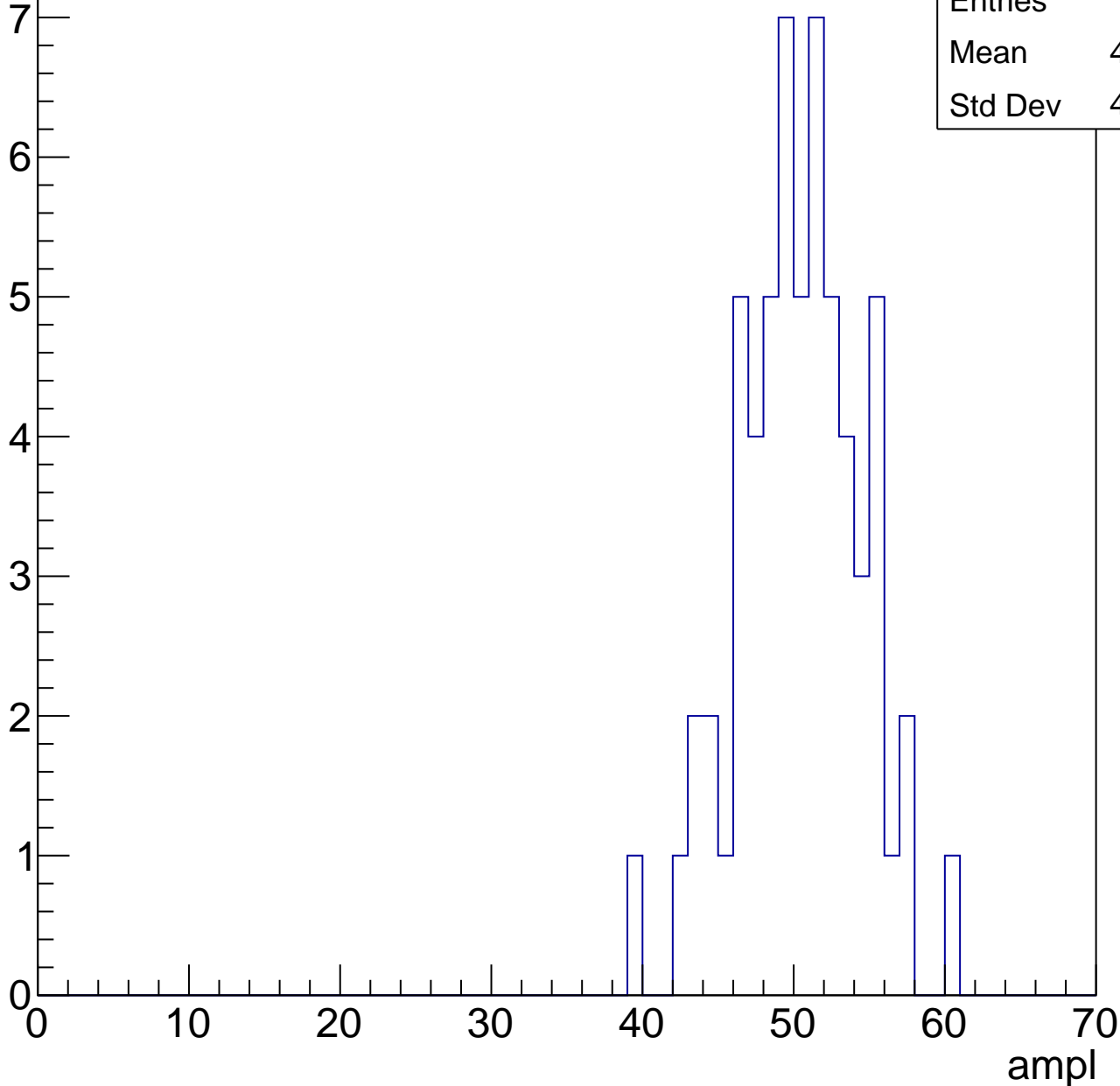


B1L103S, U24-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.95
Std Dev	4.034

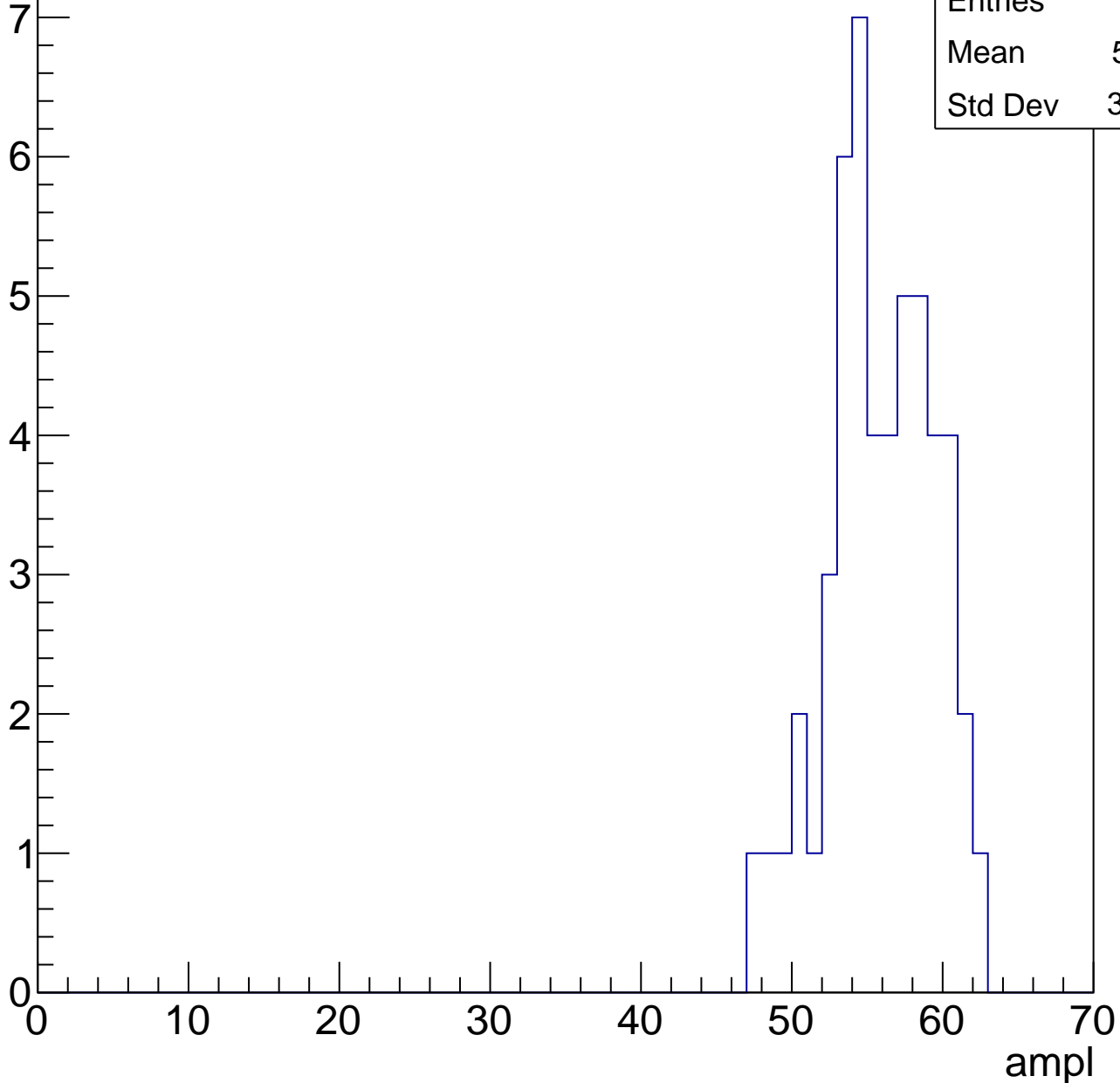


B1L103S, U24-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.41
Std Dev	3.482

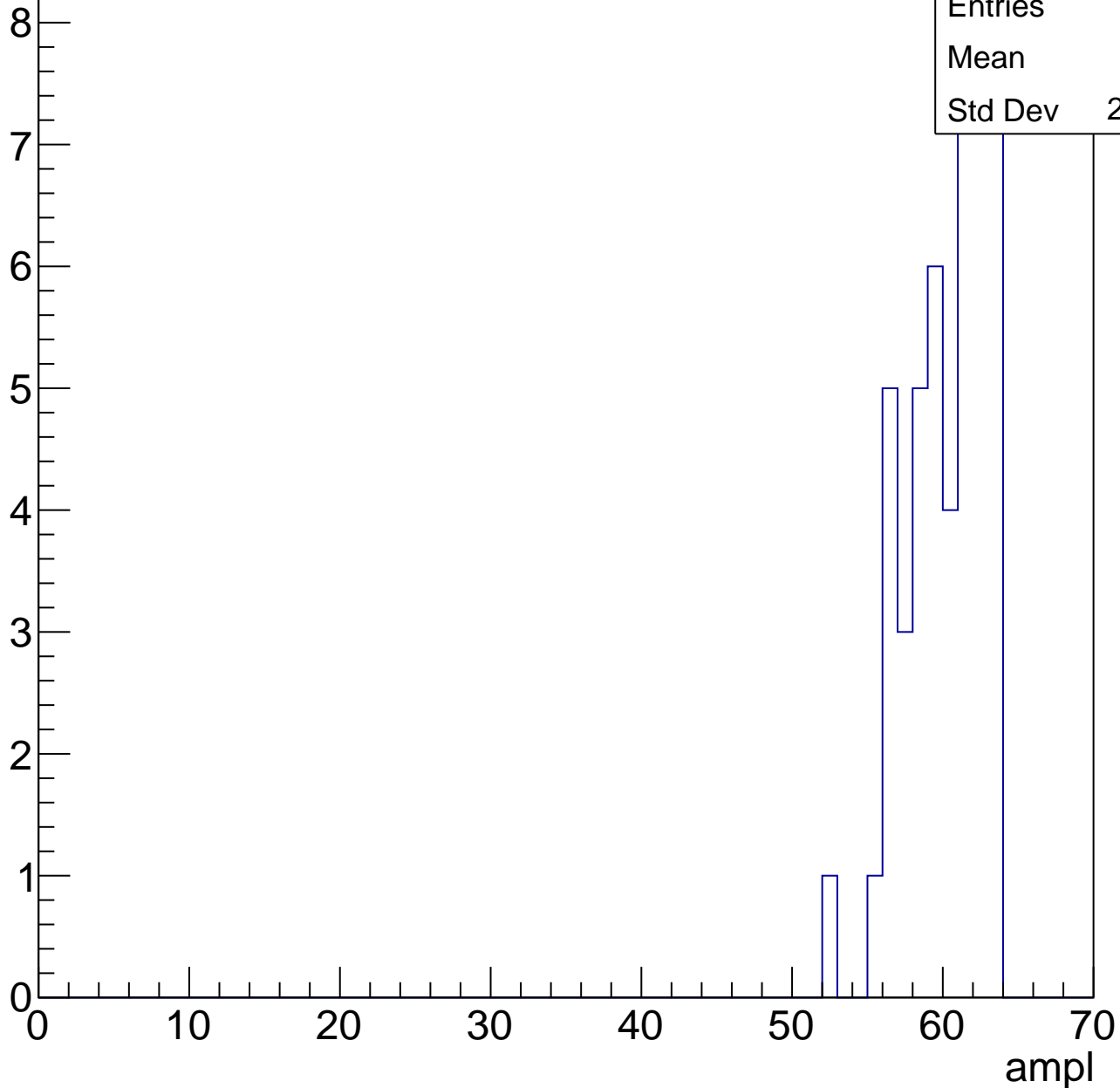


B1L103S, U24-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.8
Std Dev	2.603



B1L103S, U24-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

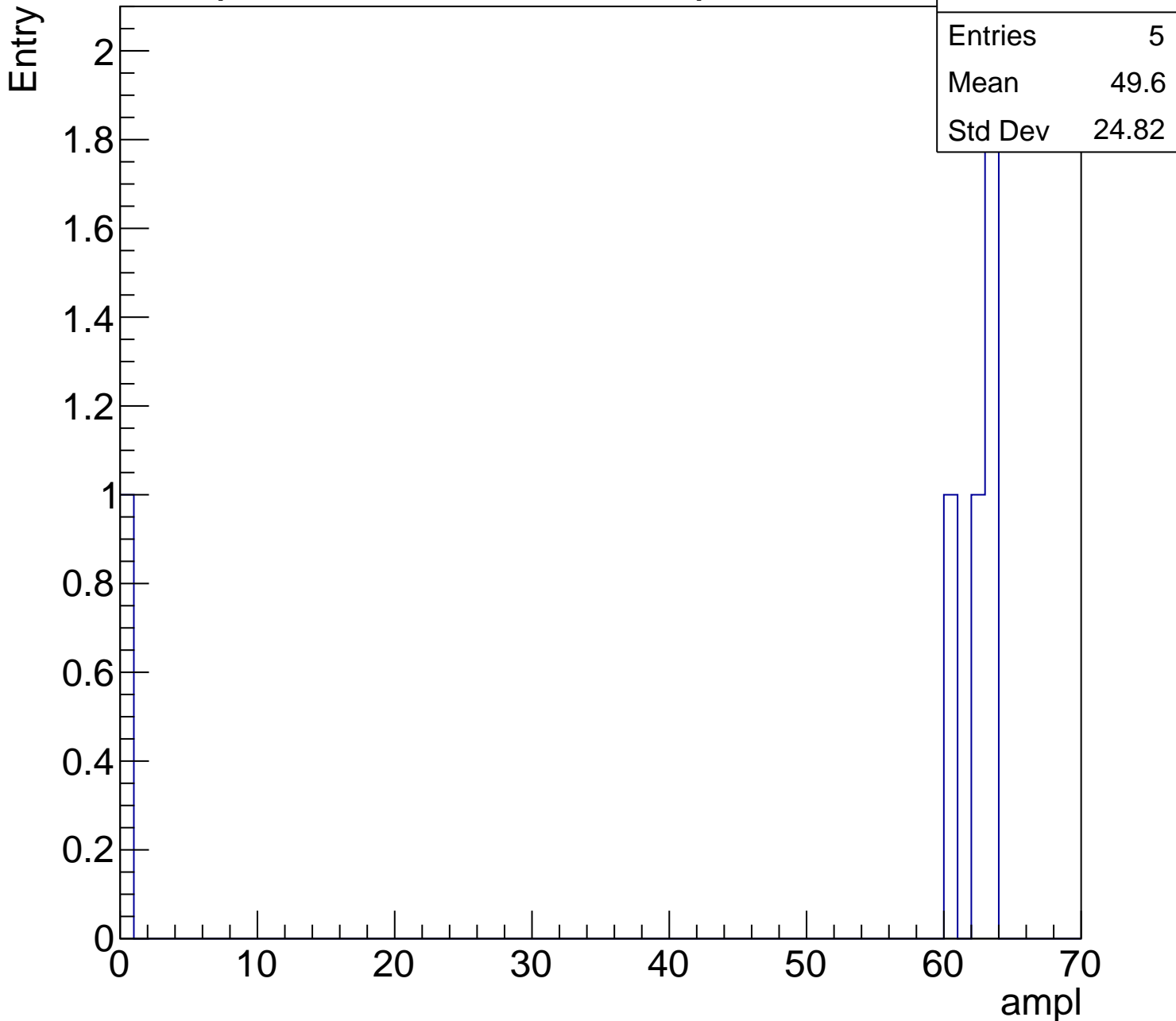
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

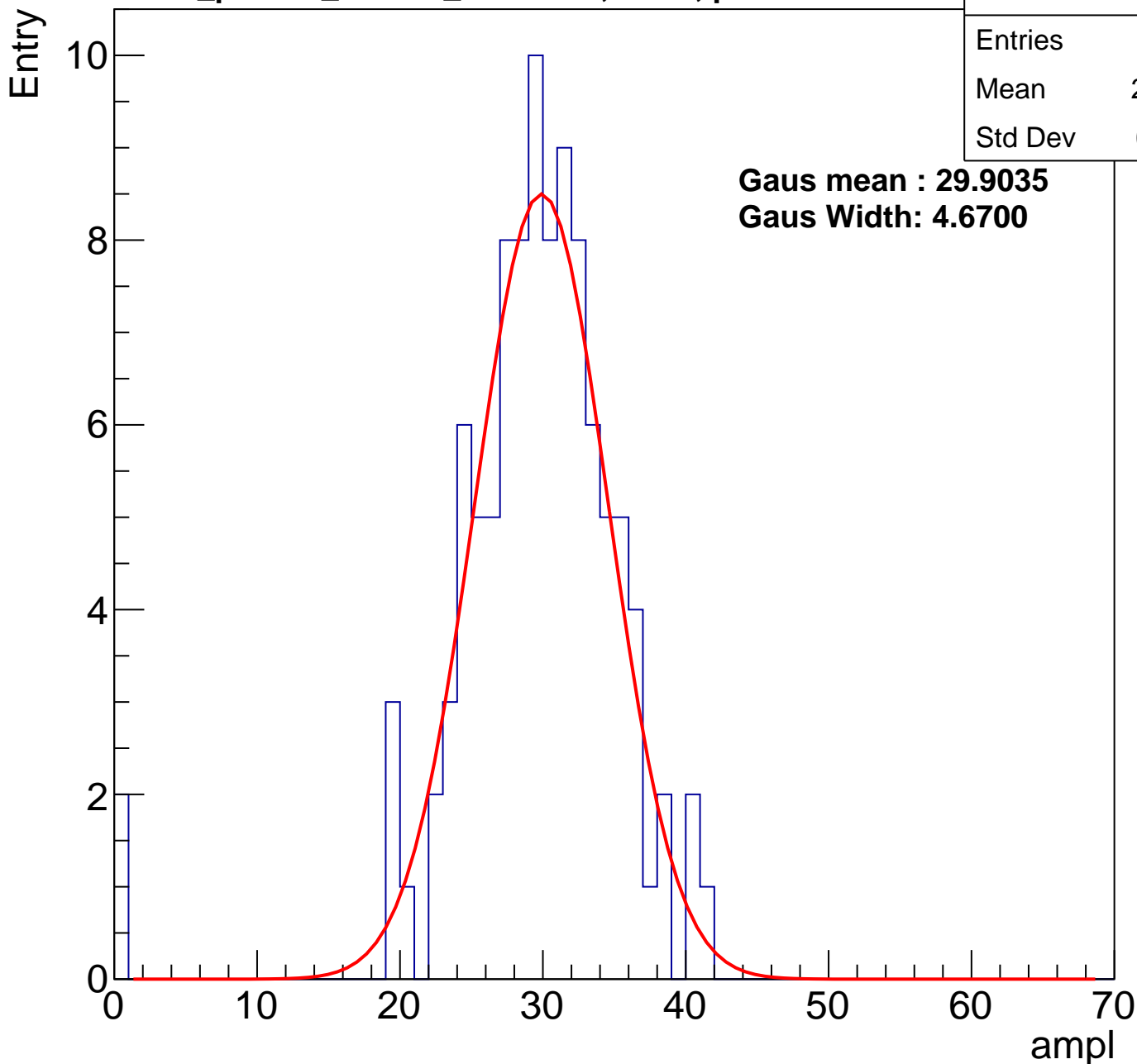
0

B1L103S, U24-ch112, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	104
Mean	28.99
Std Dev	6.131

Gaus mean : 29.9035
Gaus Width: 4.6700



B1L103S, U24-ch112, adc1

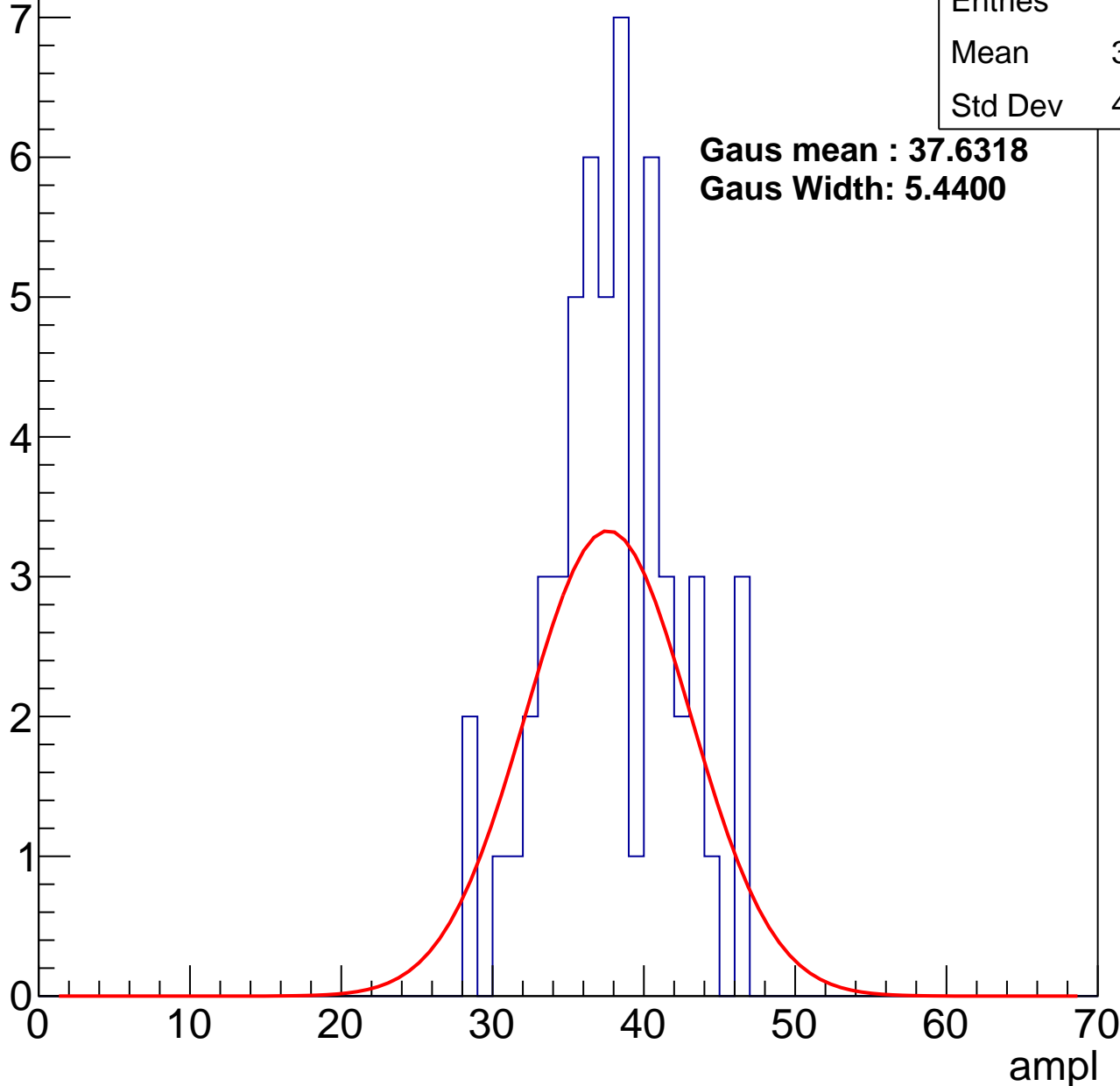
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	37.43
Std Dev	4.197

Gaus mean : 37.6318

Gaus Width: 5.4400



B1L103S, U24-ch112, adc2

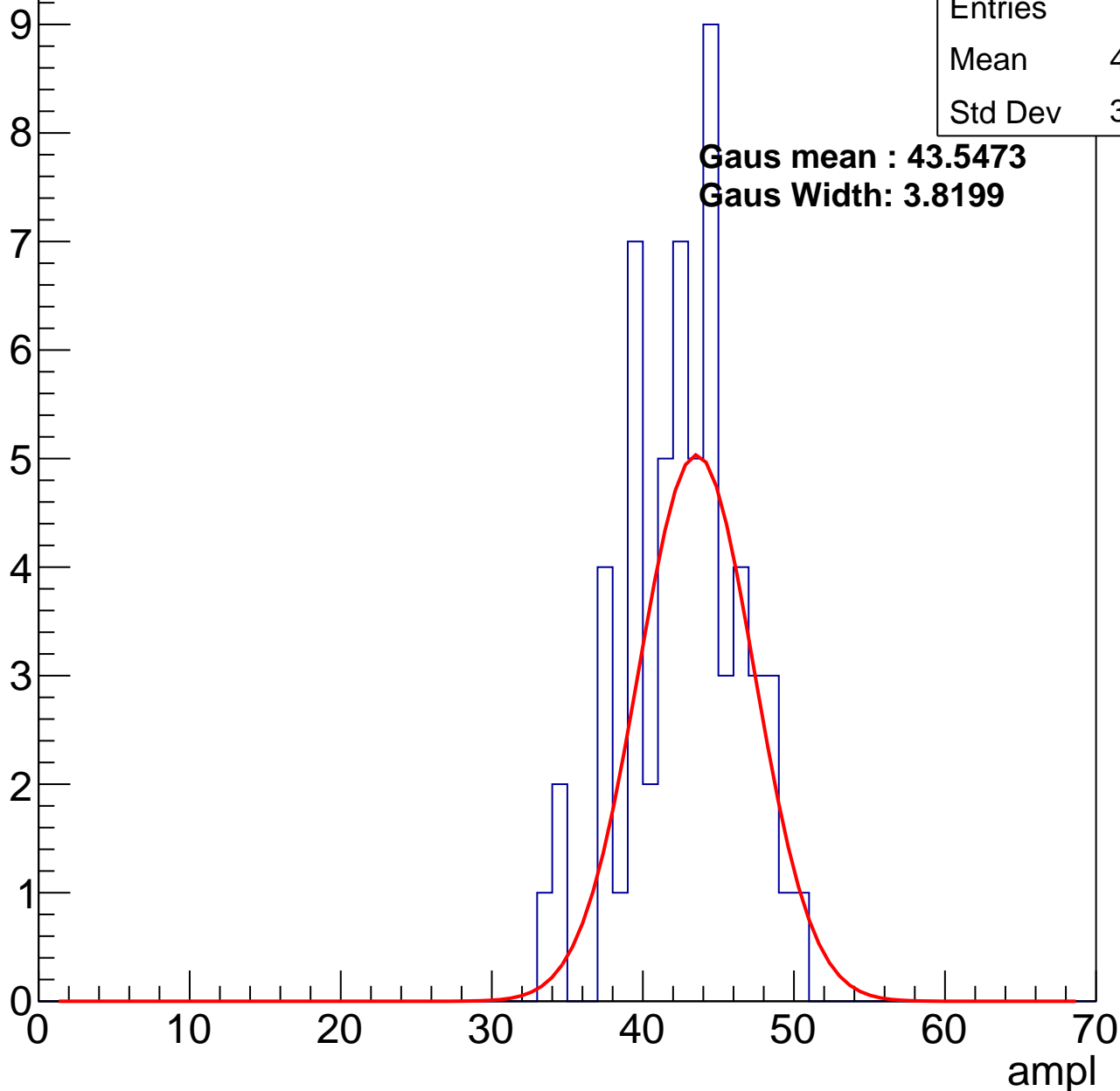
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.29
Std Dev	3.769

Gaus mean : 43.5473

Gaus Width: 3.8199

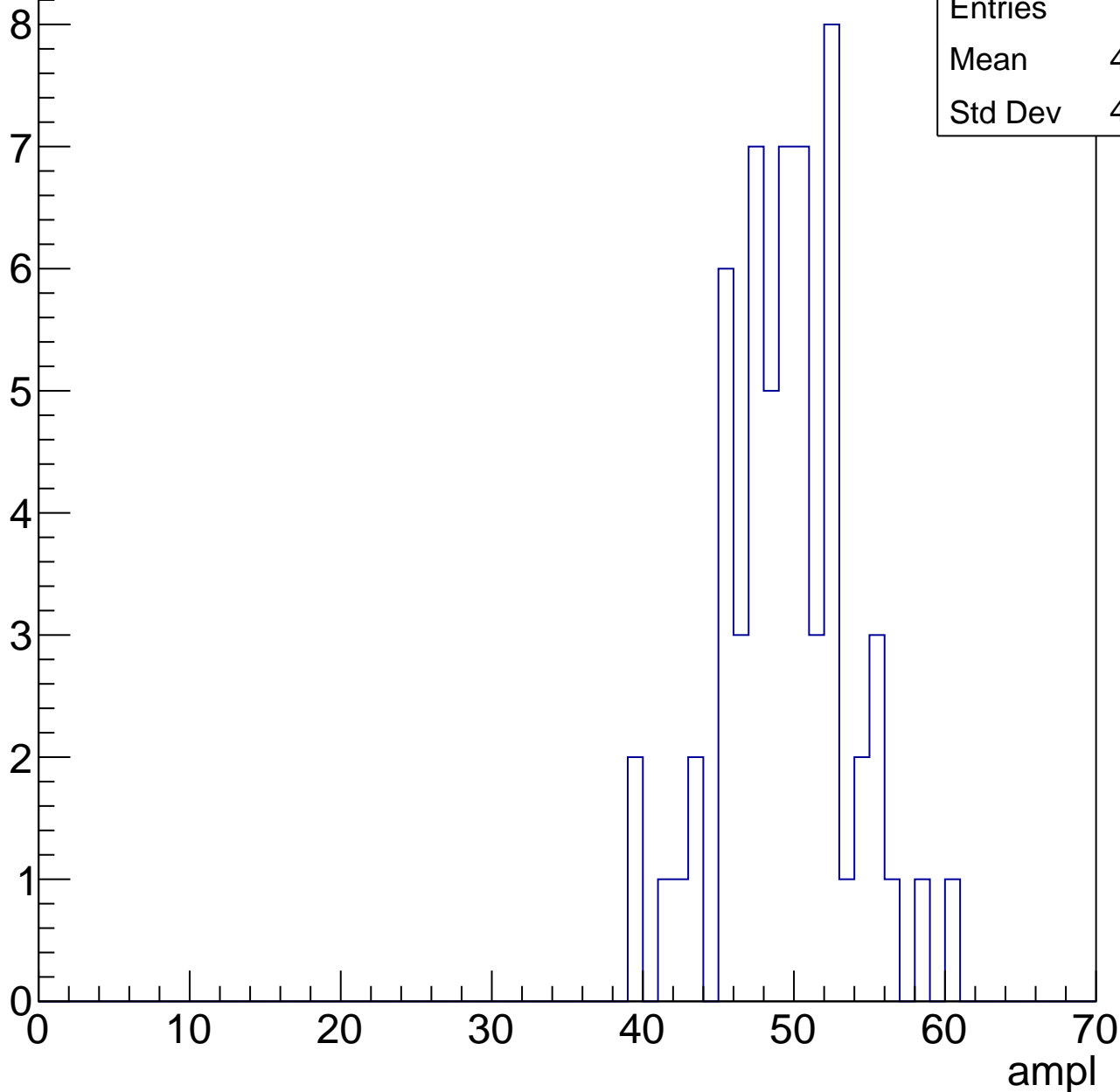


B1L103S, U24-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.95
Std Dev	4.162

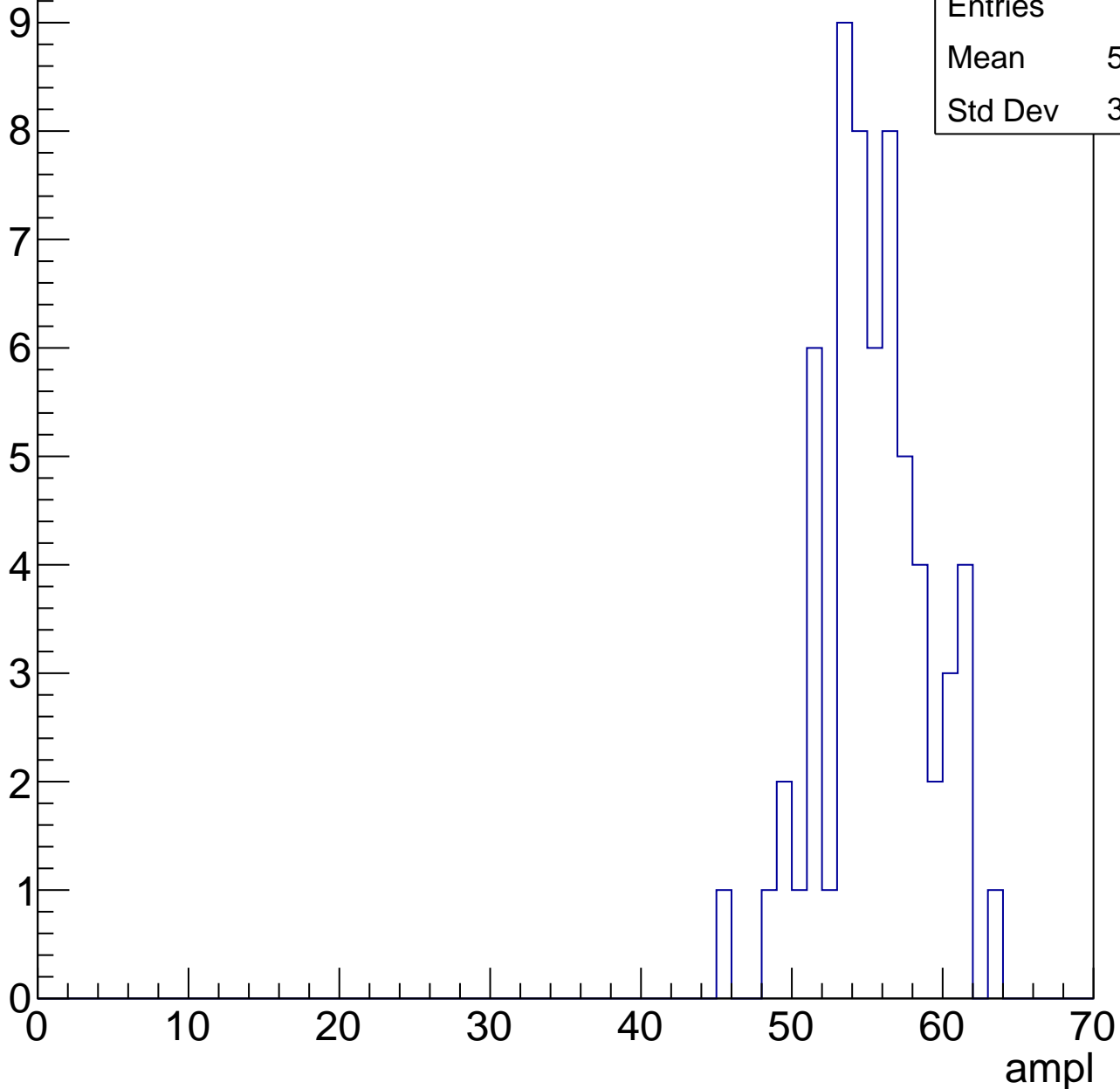


B1L103S, U24-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.97
Std Dev	3.533

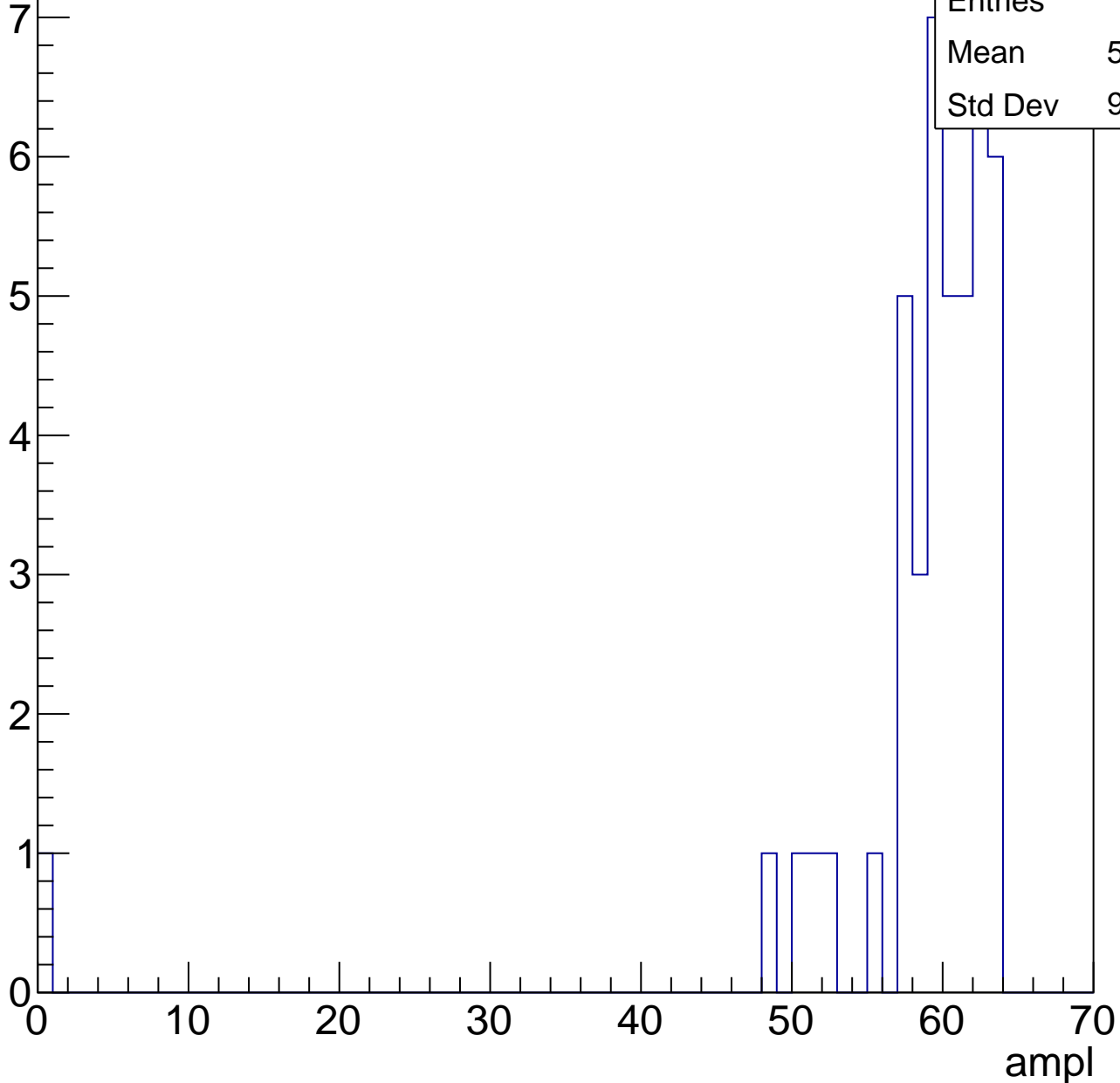


B1L103S, U24-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

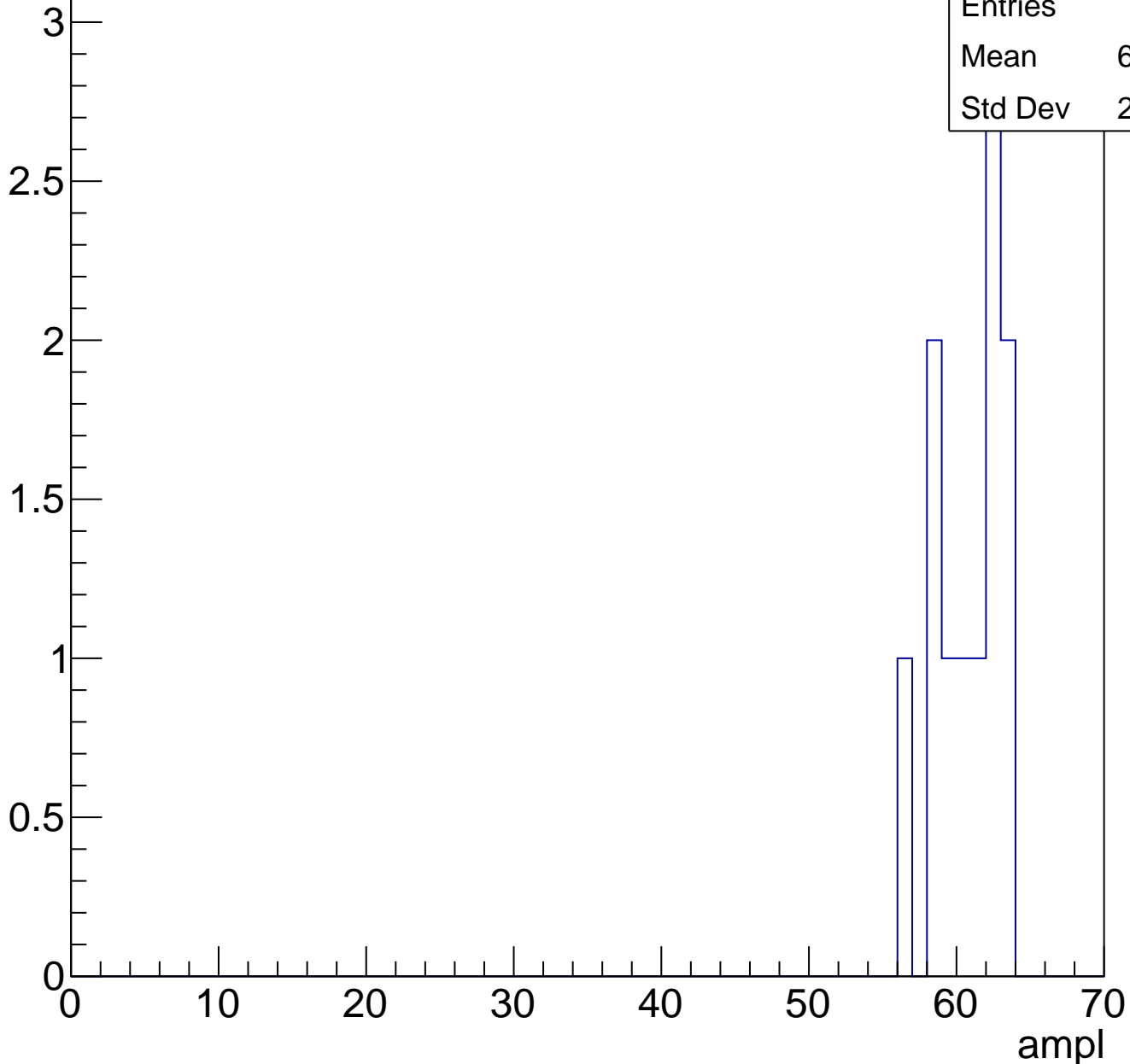
Entries	44
Mean	57.84
Std Dev	9.487



B1L103S, U24-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch113, adc0

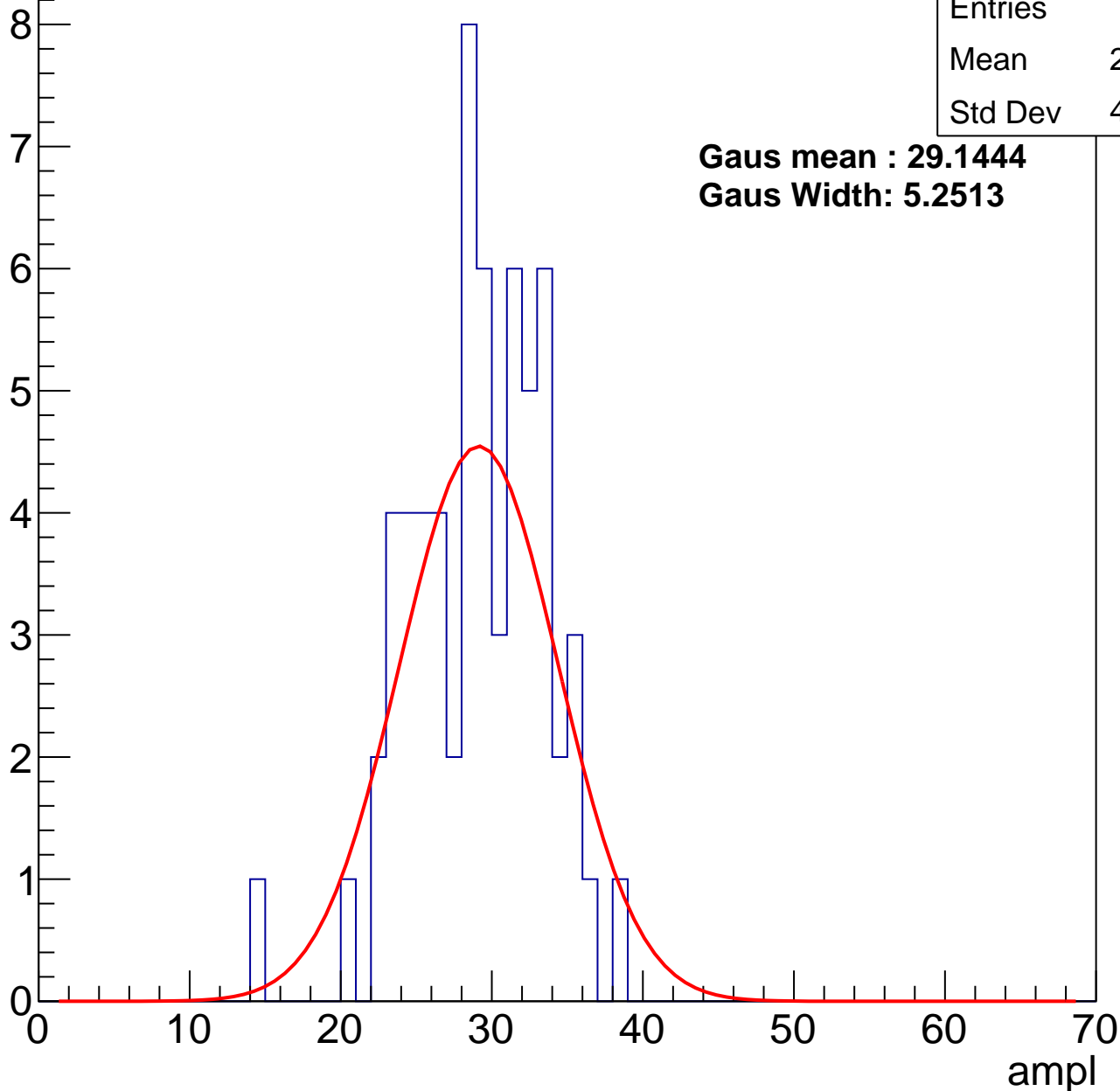
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.62
Std Dev	4.384

Gaus mean : 29.1444

Gaus Width: 5.2513



B1L103S, U24-ch113, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	36.89
Std Dev	4.068

Gaus mean : 36.3822

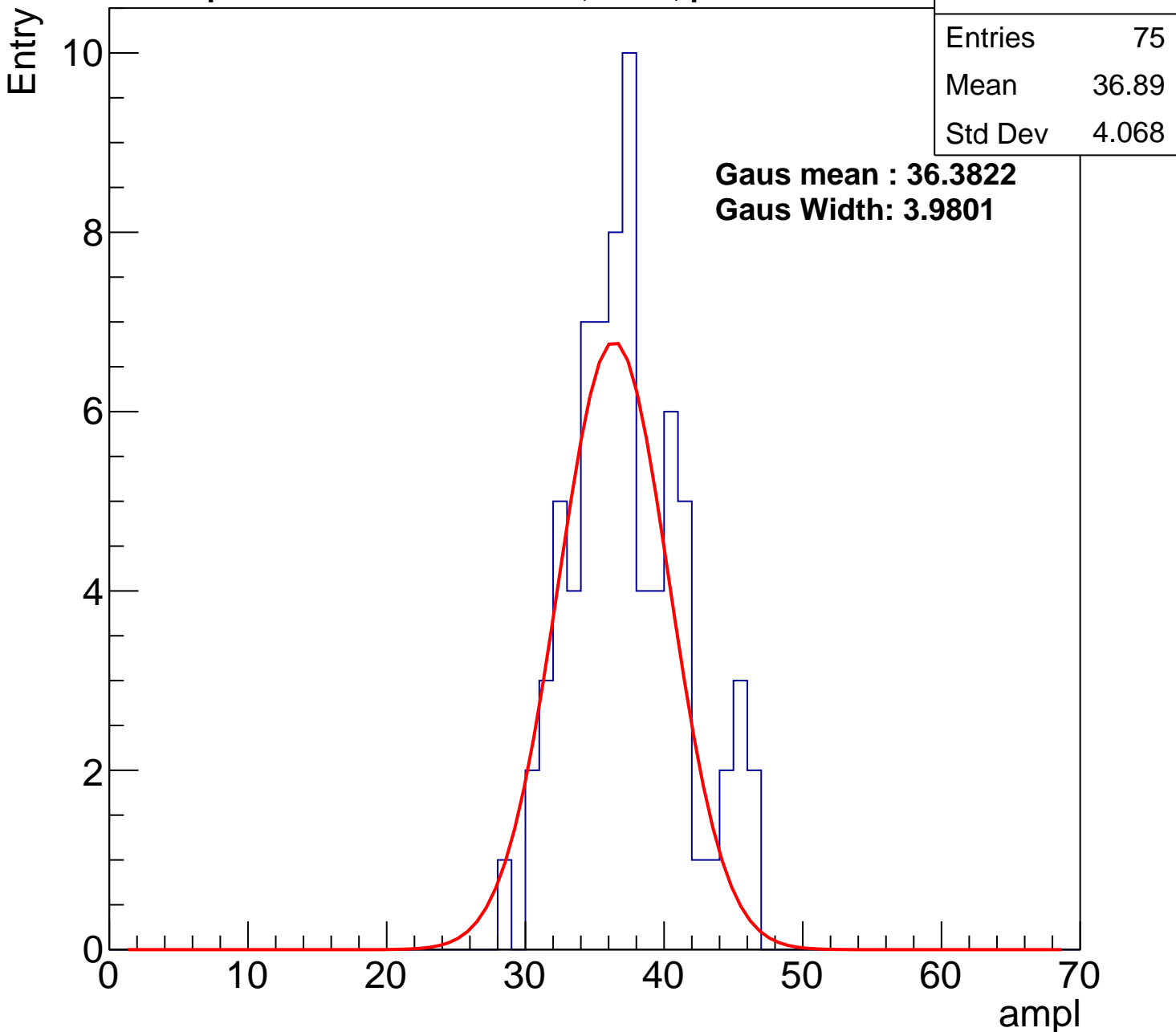
Gaus Width: 3.9801

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch113, adc2

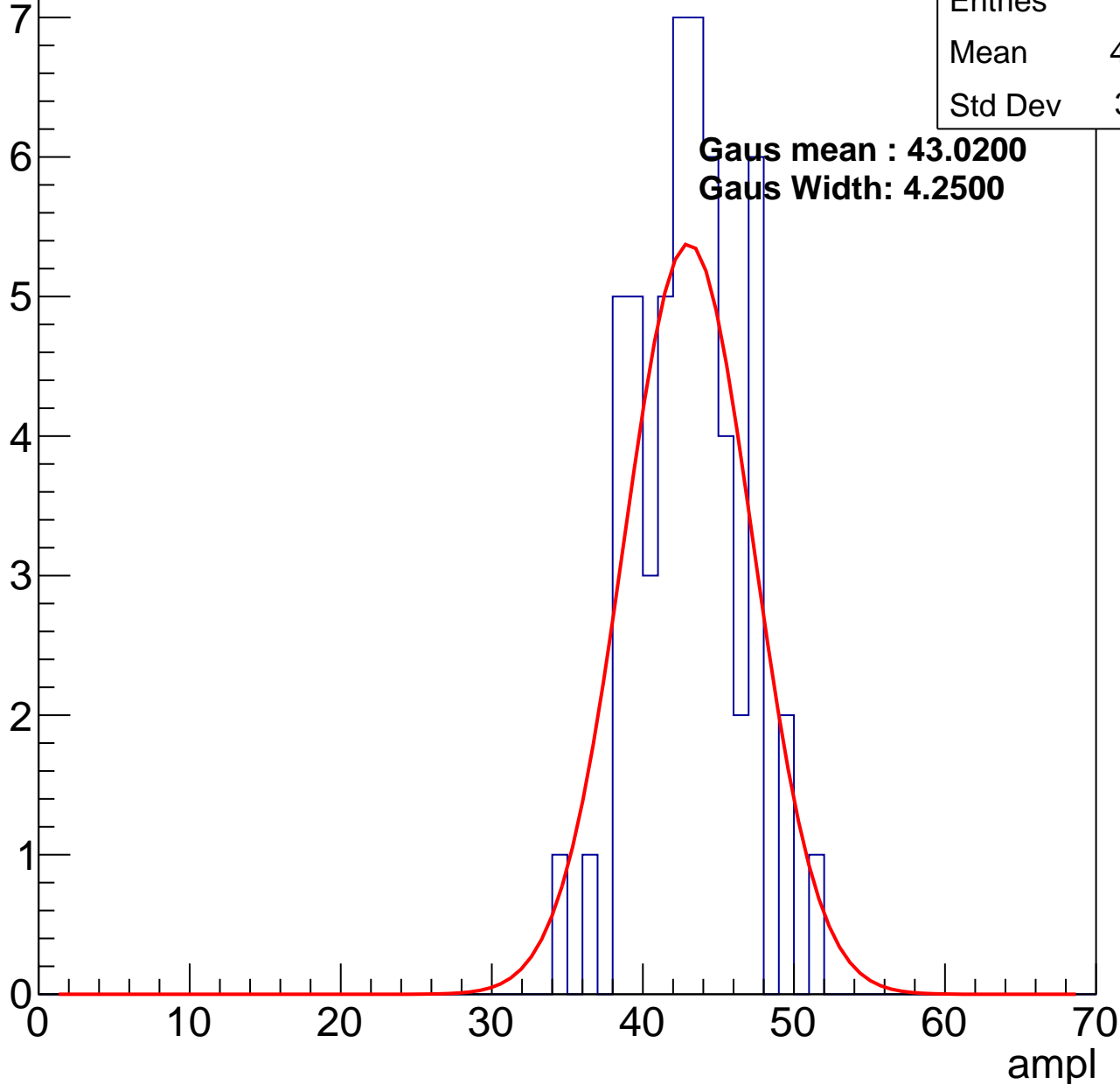
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.58
Std Dev	3.441

Gaus mean : 43.0200

Gaus Width: 4.2500

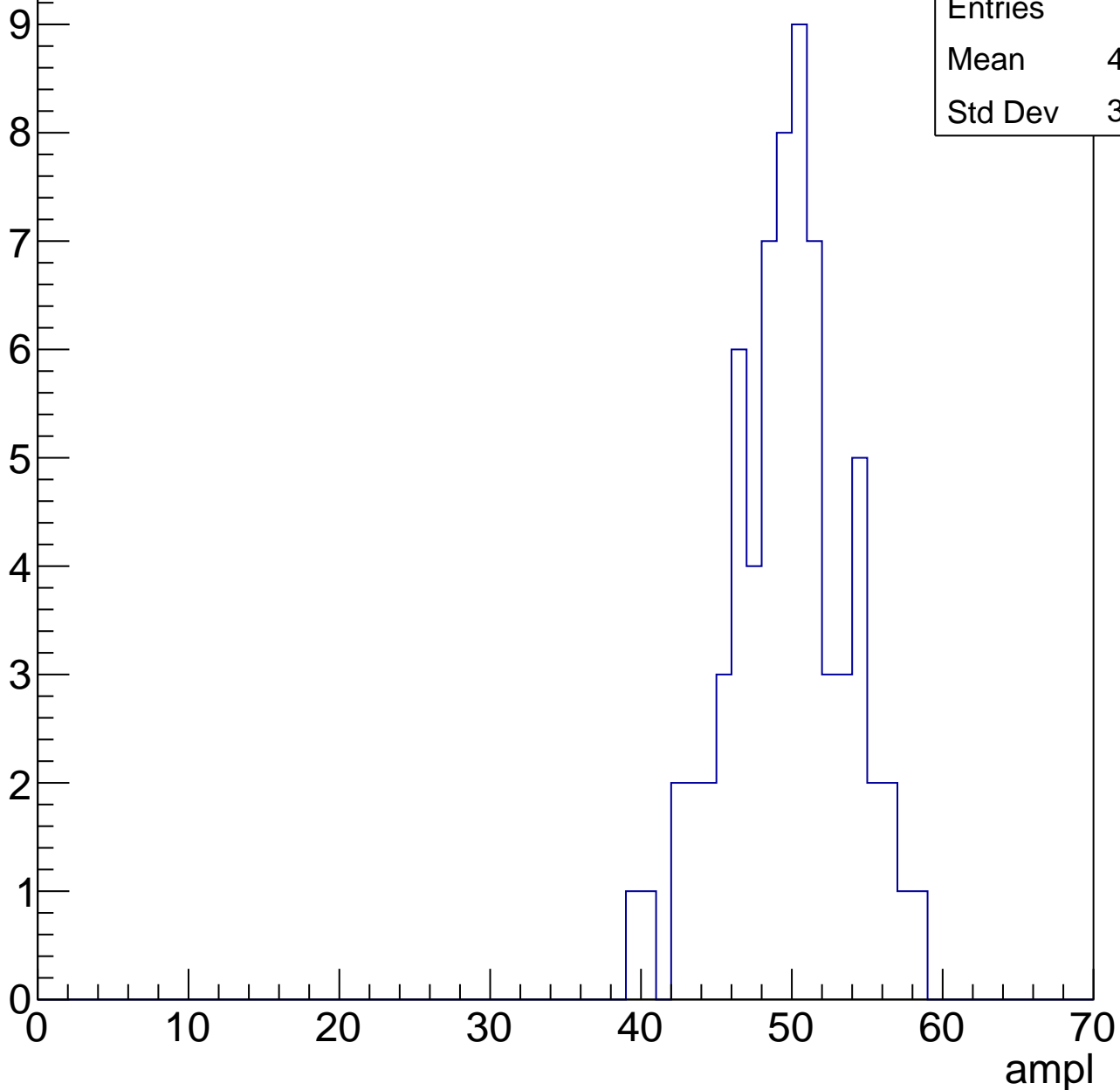


B1L103S, U24-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.17
Std Dev	3.949

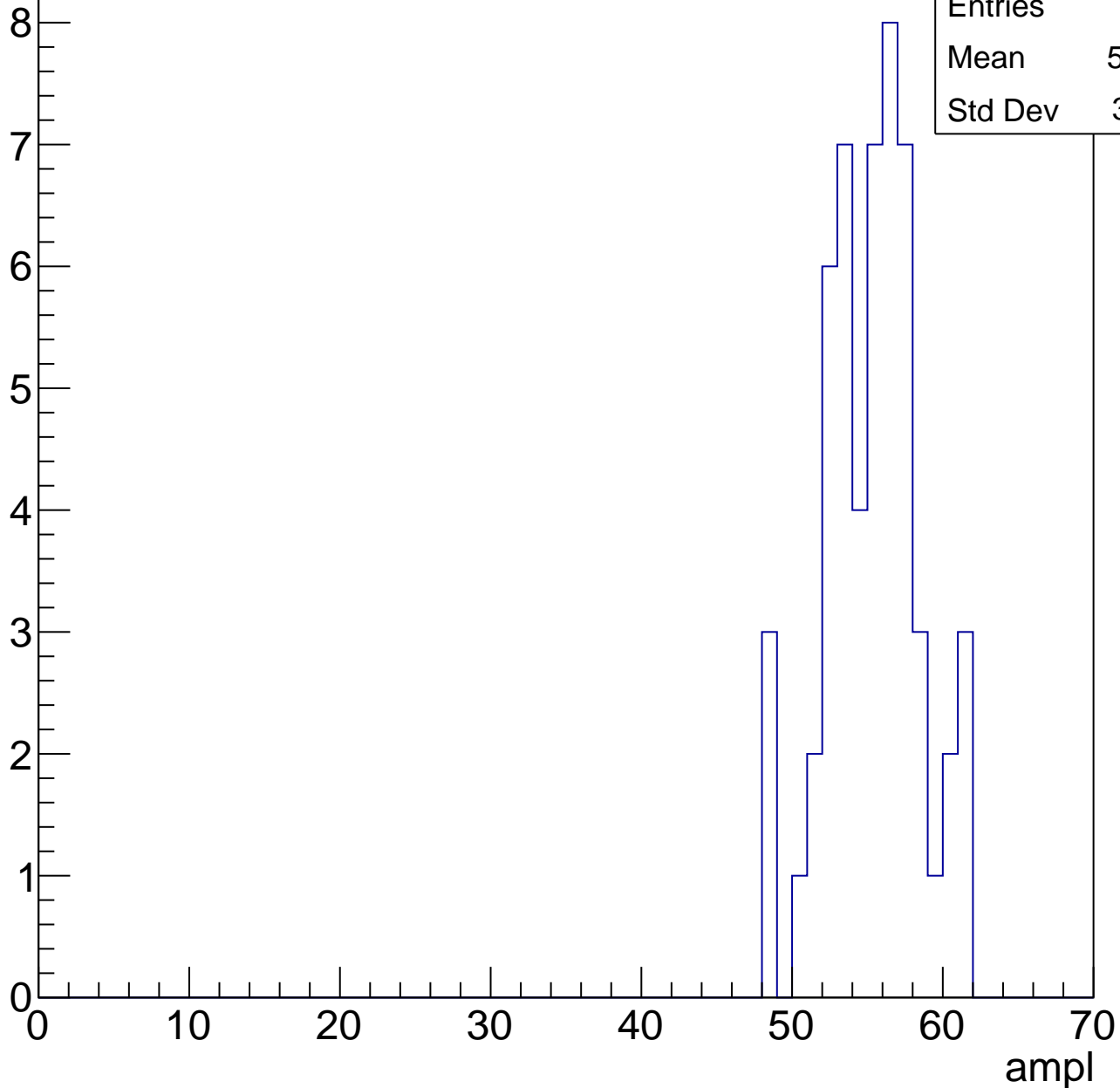


B1L103S, U24-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.87
Std Dev	3.121

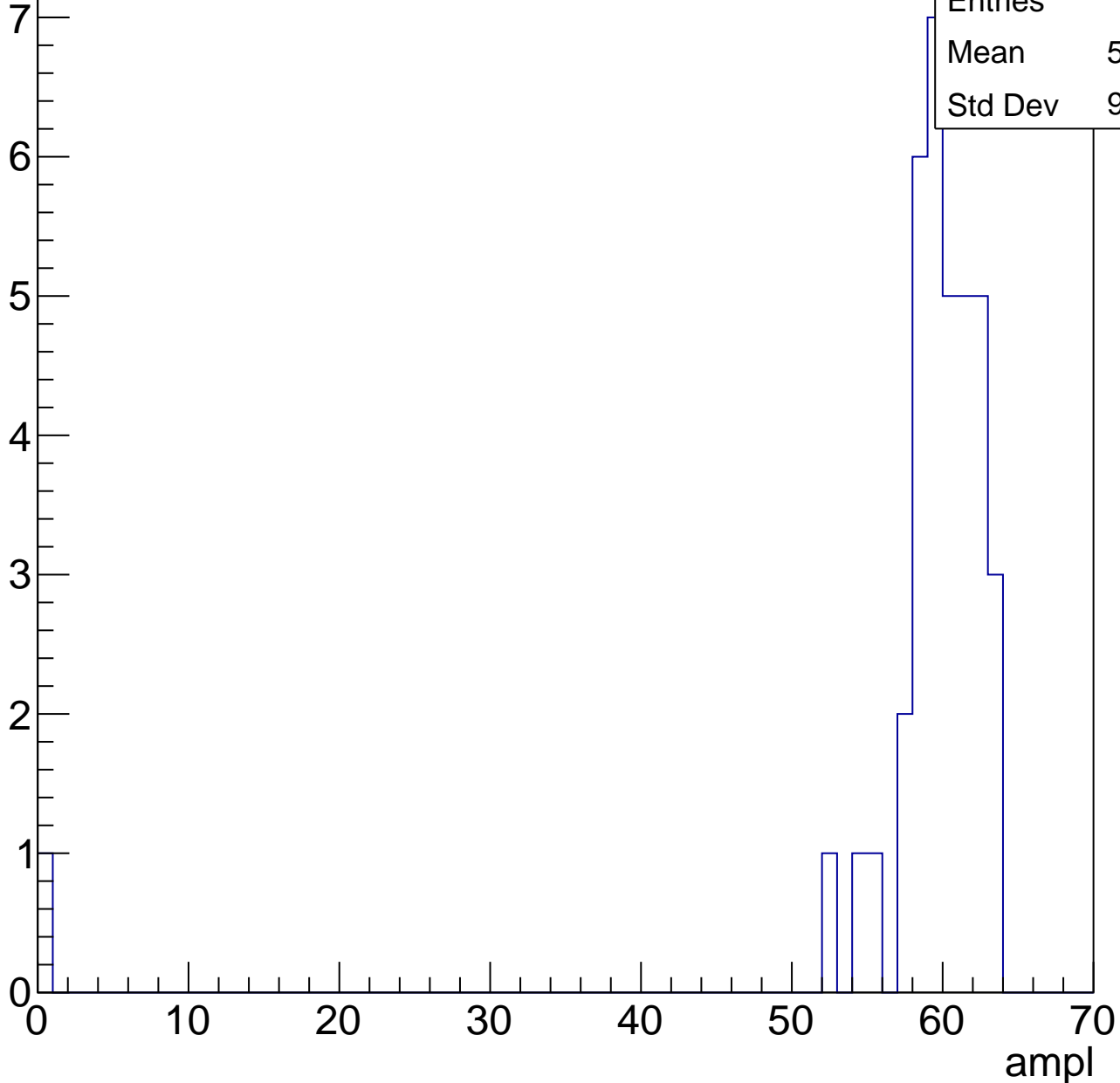


B1L103S, U24-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	57.84
Std Dev	9.936

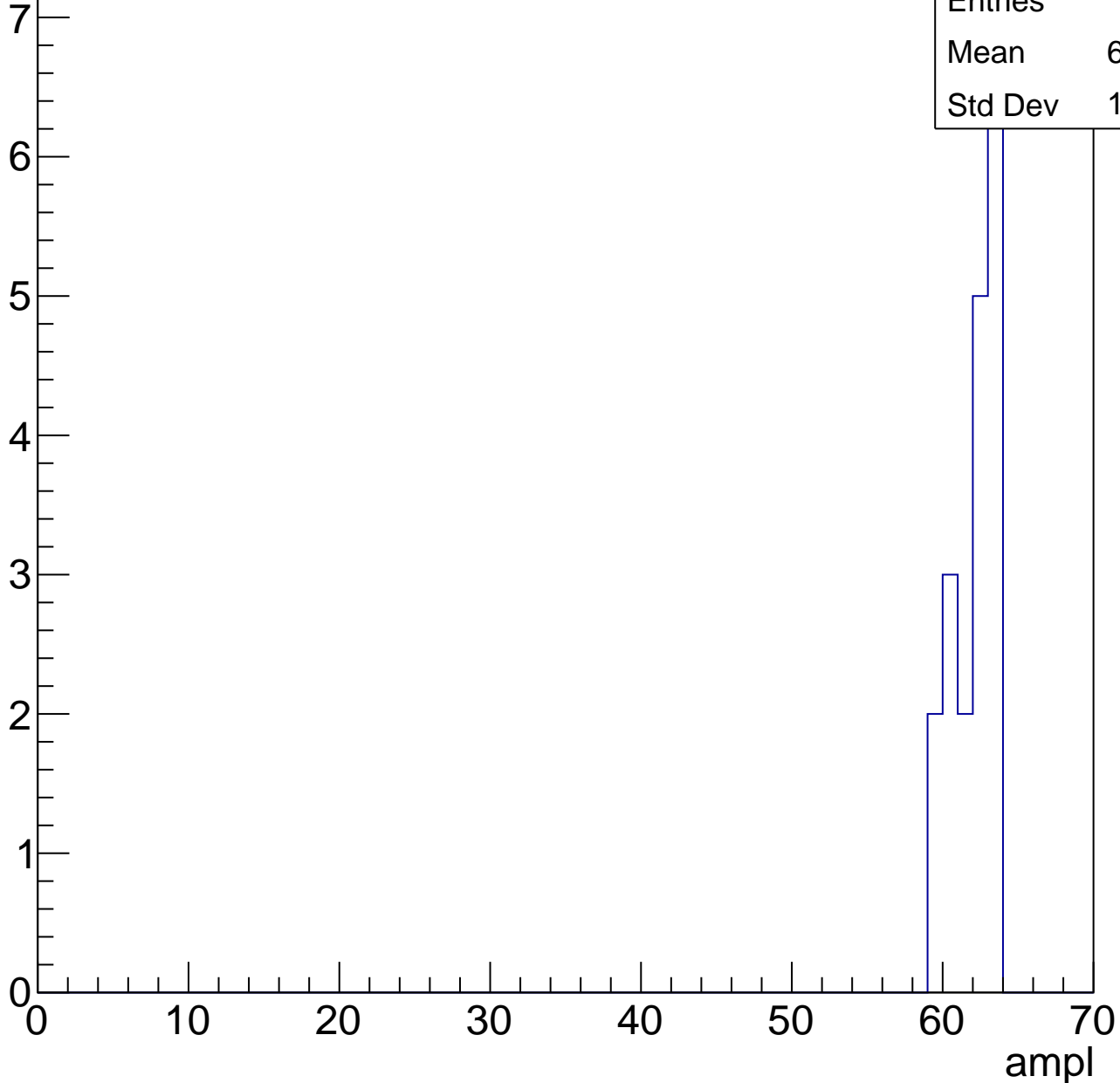


B1L103S, U24-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	61.63
Std Dev	1.385



B1L103S, U24-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch114, adc0

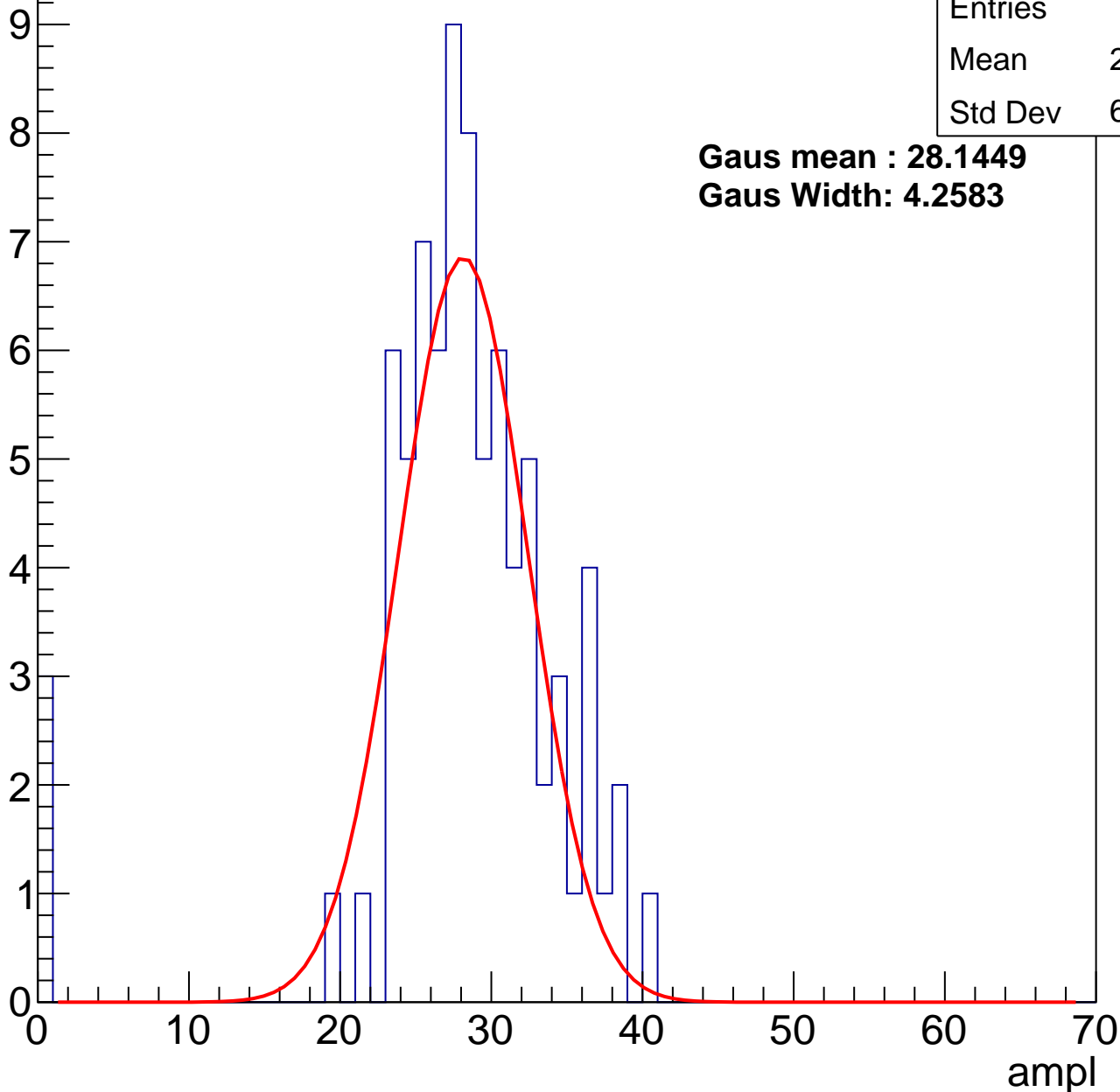
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	27.56
Std Dev	6.915

Gaus mean : 28.1449

Gaus Width: 4.2583



B1L103S, U24-ch114, adc1

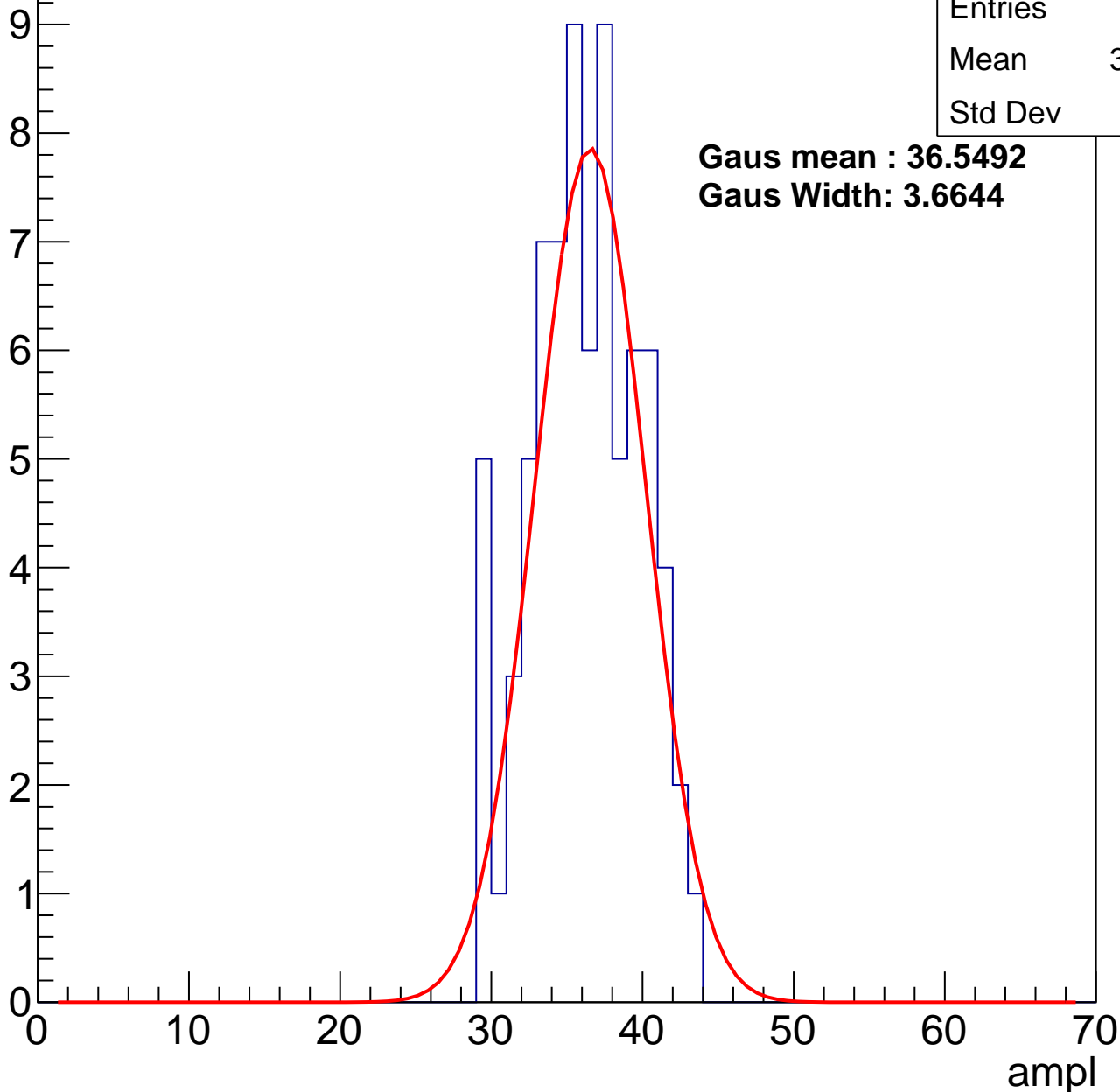
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.74
Std Dev	3.5

Gaus mean : 36.5492

Gaus Width: 3.6644



B1L103S, U24-ch114, adc2

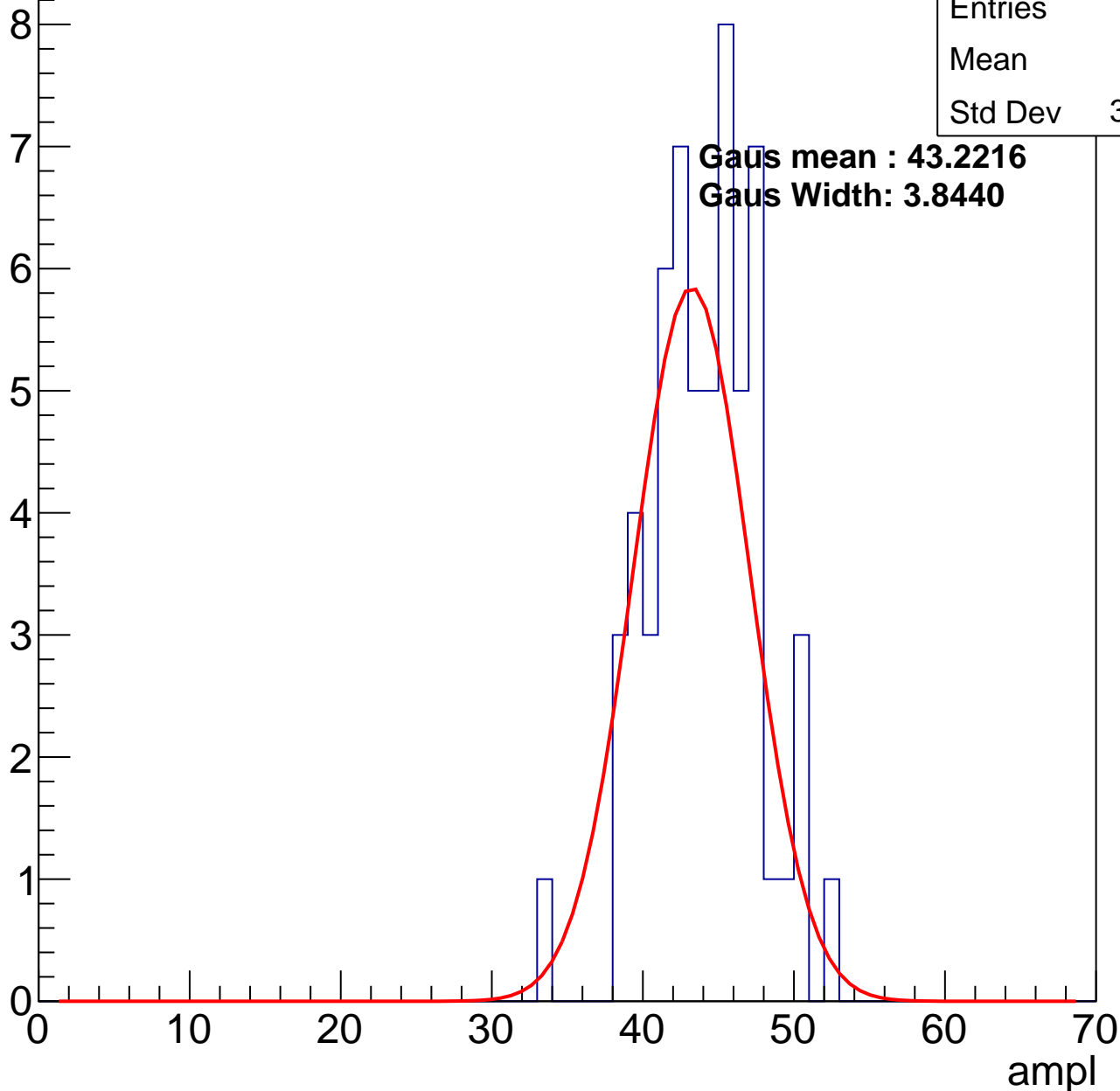
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.6
Std Dev	3.555

Gaus mean : 43.2216

Gaus Width: 3.8440

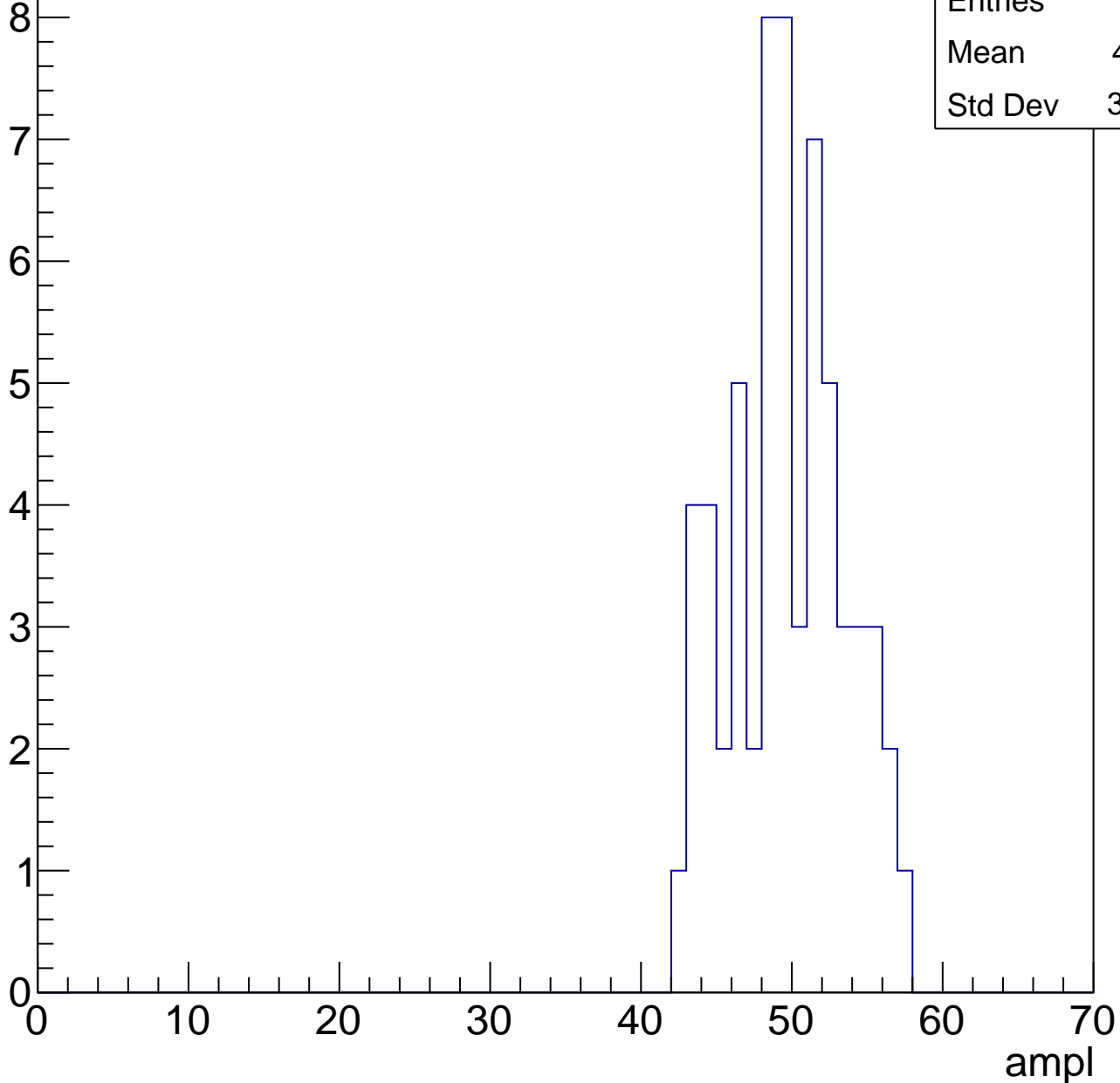


B1L103S, U24-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.21
Std Dev	3.738

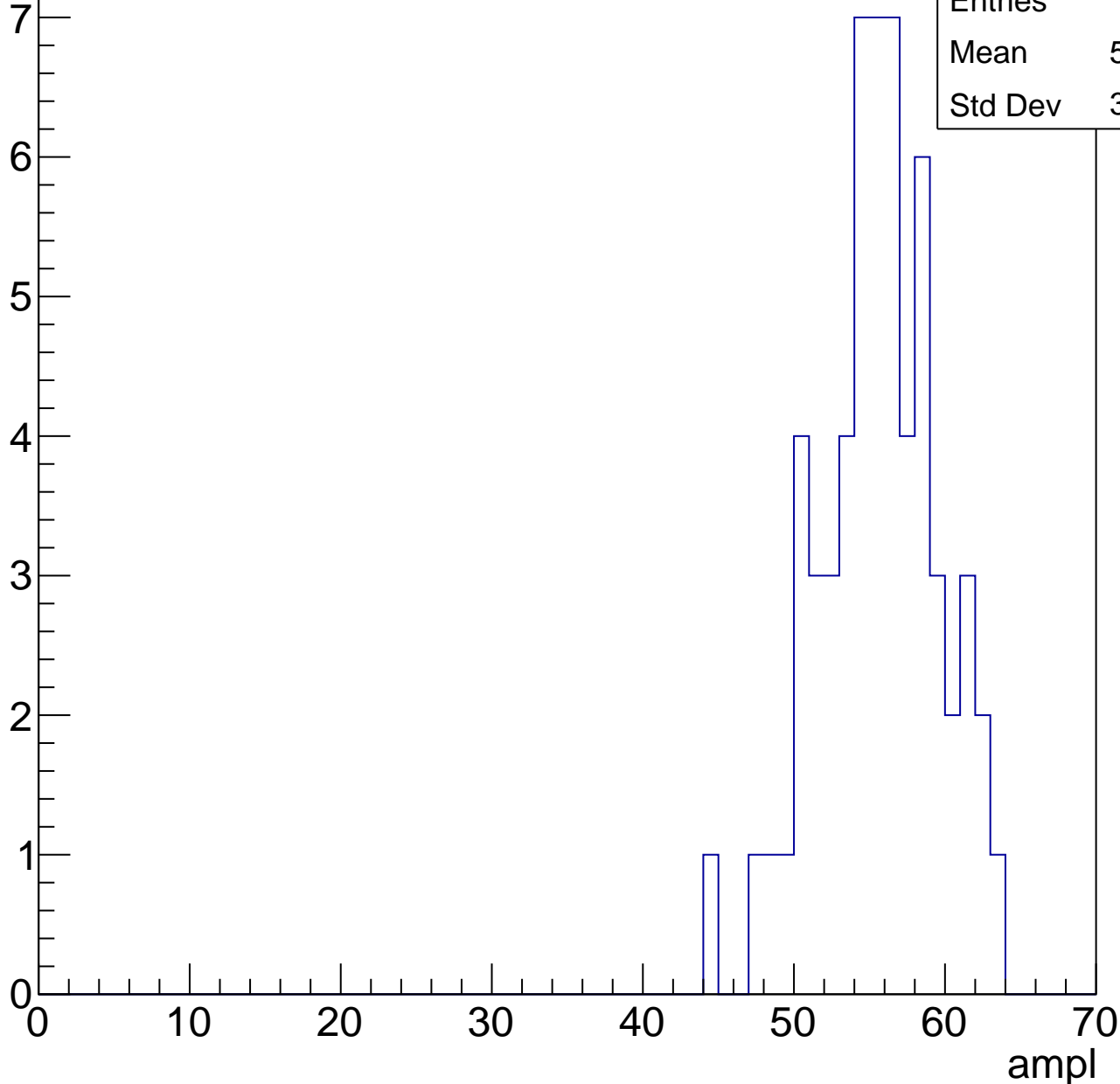


B1L103S, U24-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

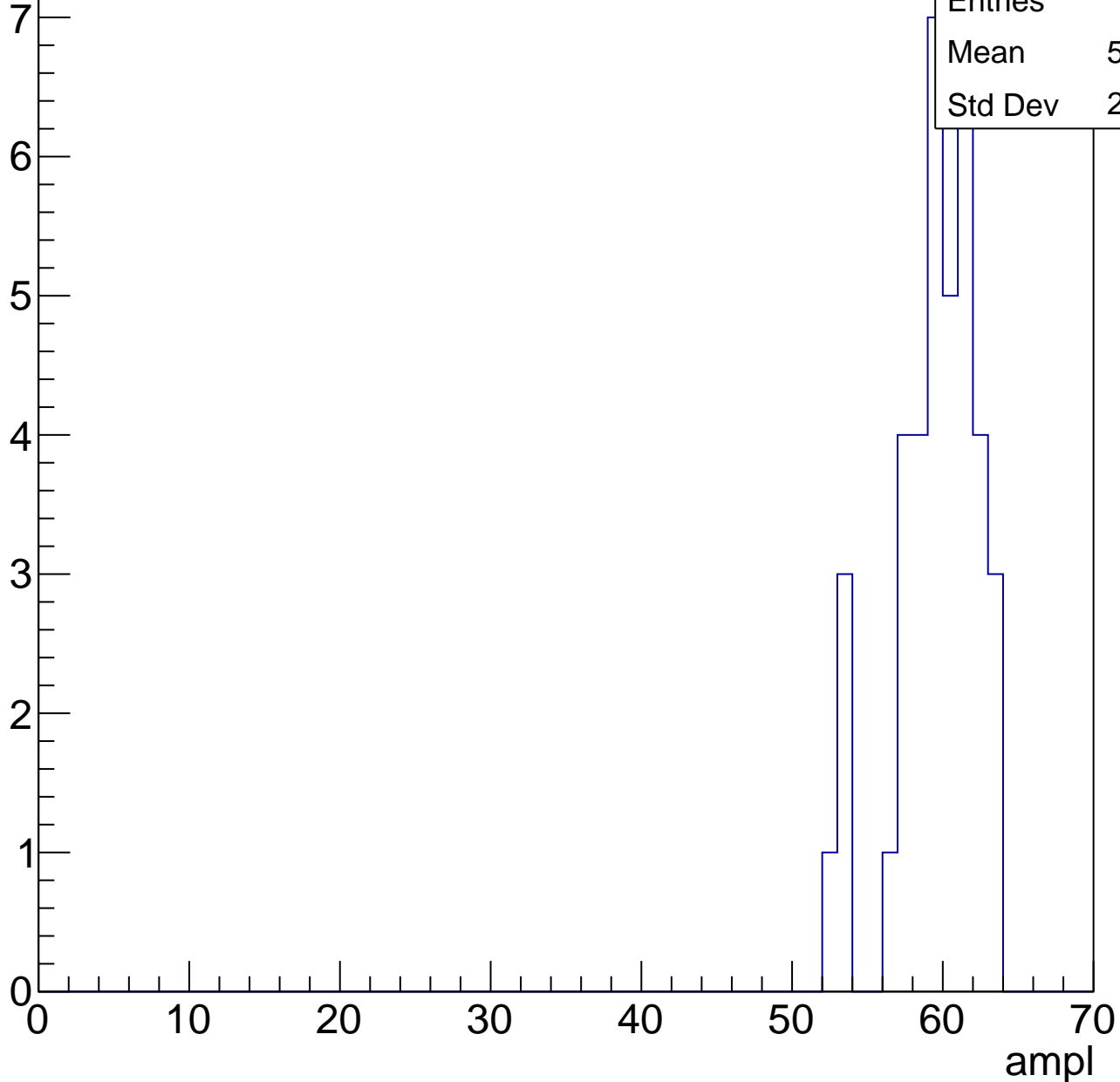
Entries	60
Mean	55.12
Std Dev	3.899



B1L103S, U24-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

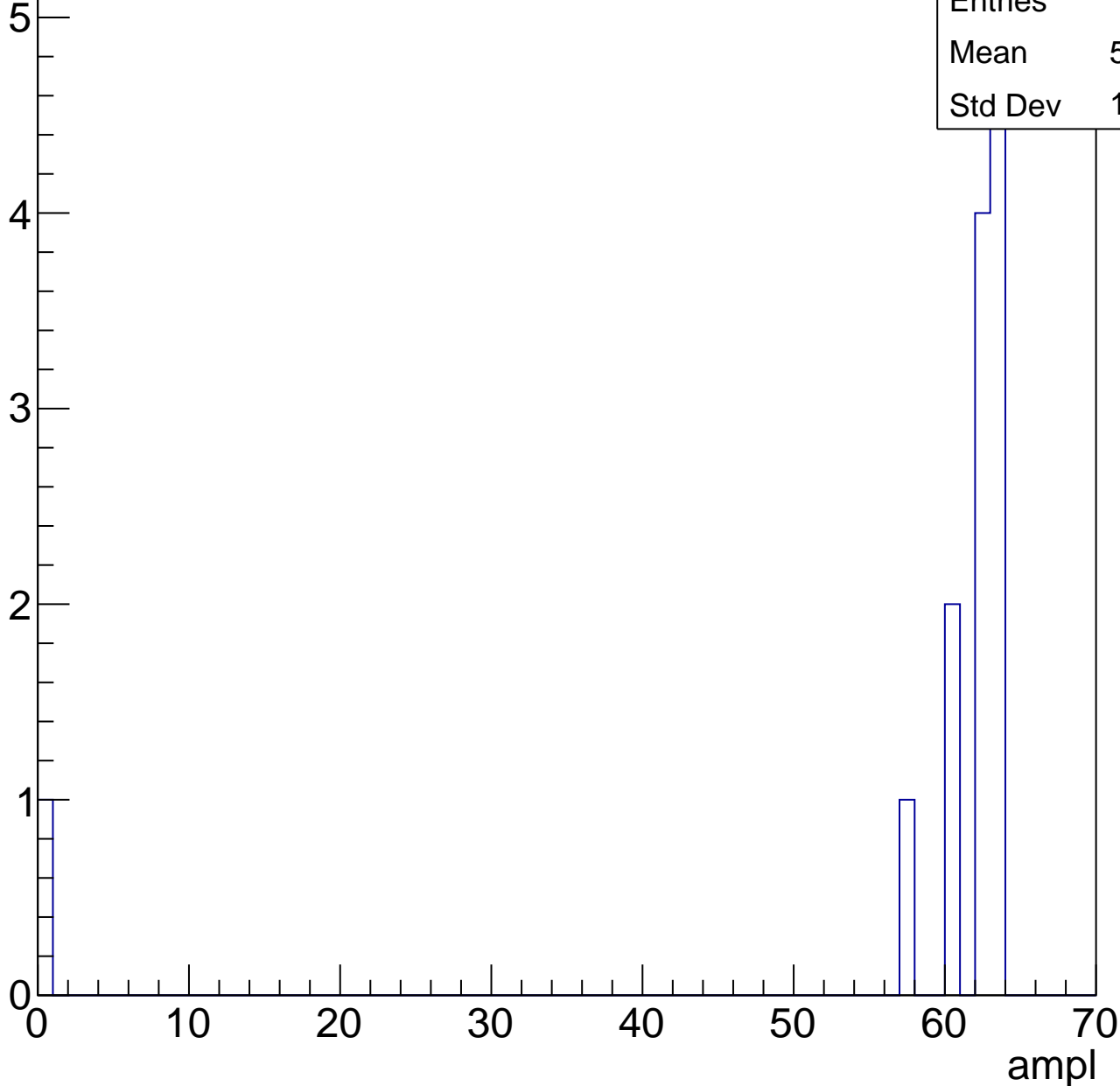


B1L103S, U24-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

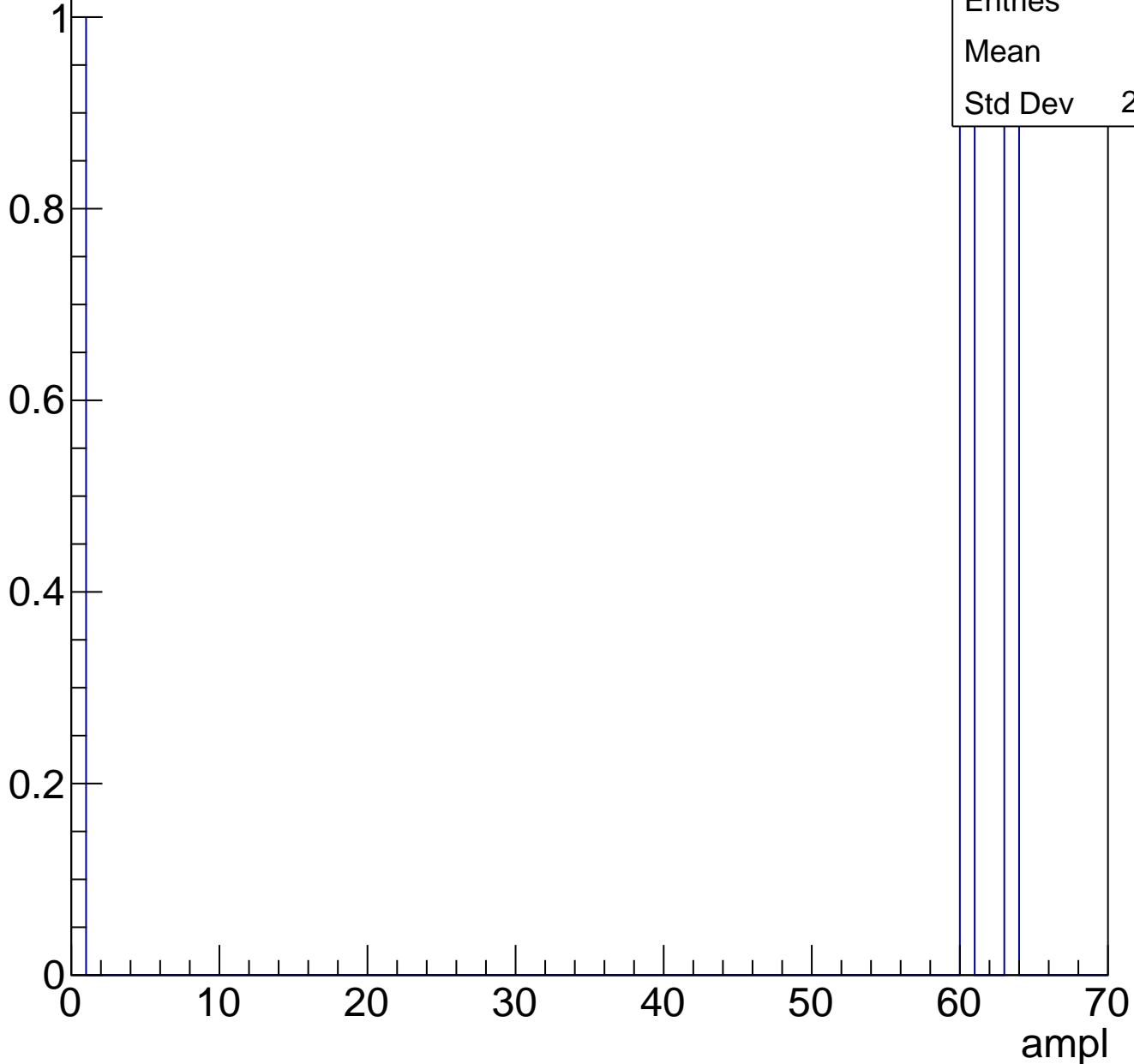
Entries	13
Mean	56.92
Std Dev	16.52



B1L103S, U24-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch115, adc0

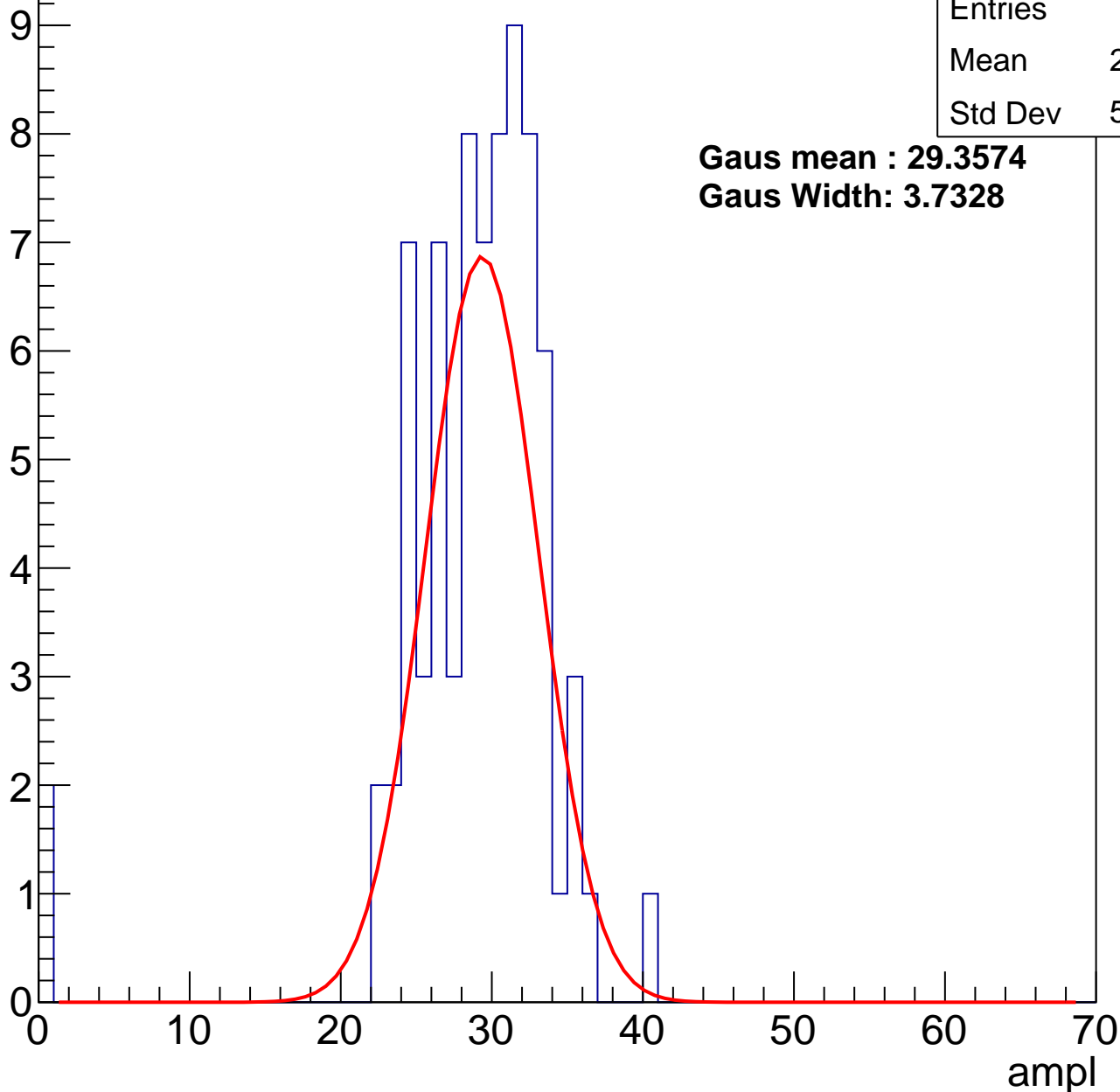
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	28.35
Std Dev	5.817

Gaus mean : 29.3574

Gaus Width: 3.7328



B1L103S, U24-ch115, adc1

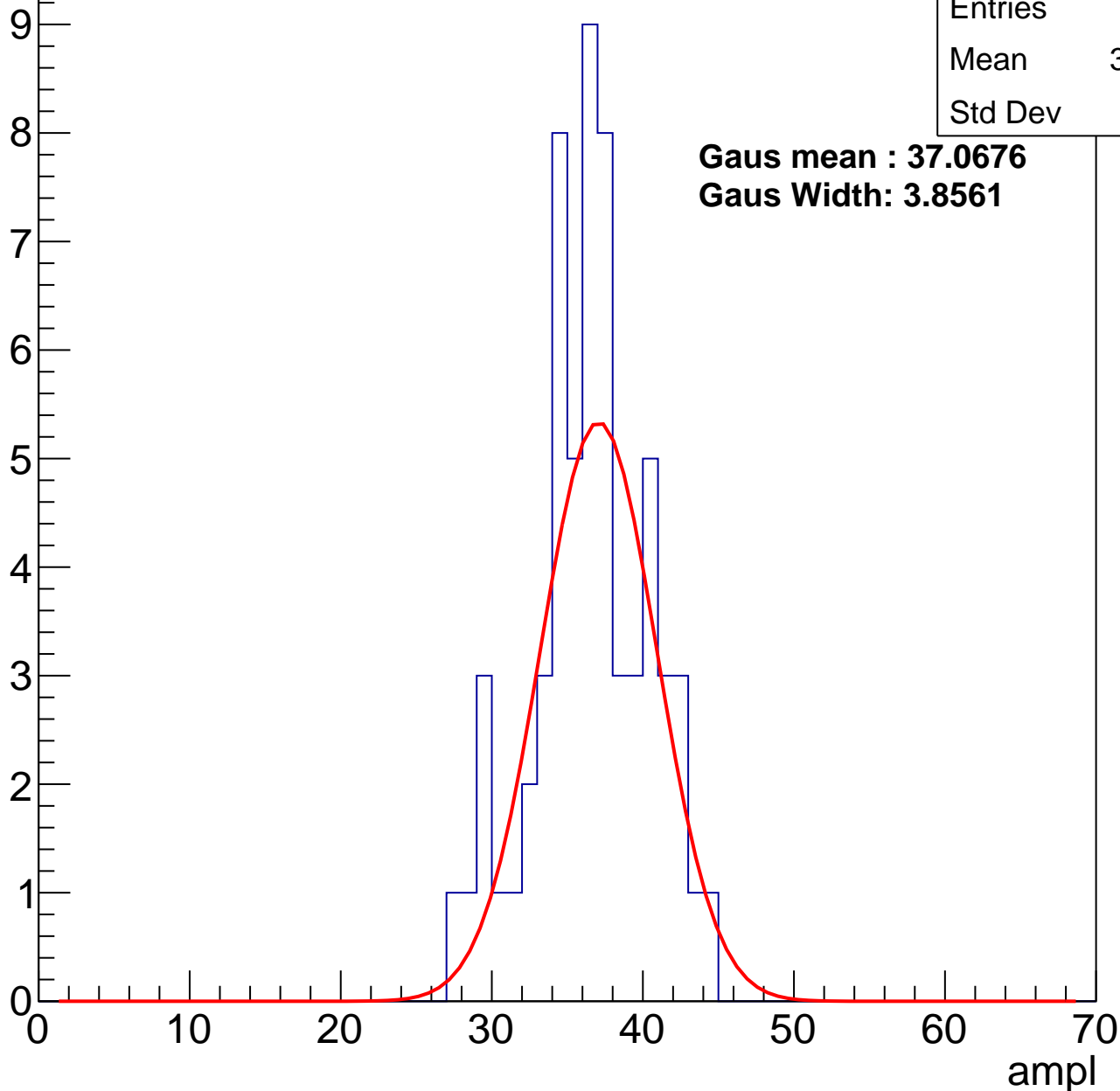
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.07
Std Dev	3.78

Gaus mean : 37.0676

Gaus Width: 3.8561



B1L103S, U24-ch115, adc2

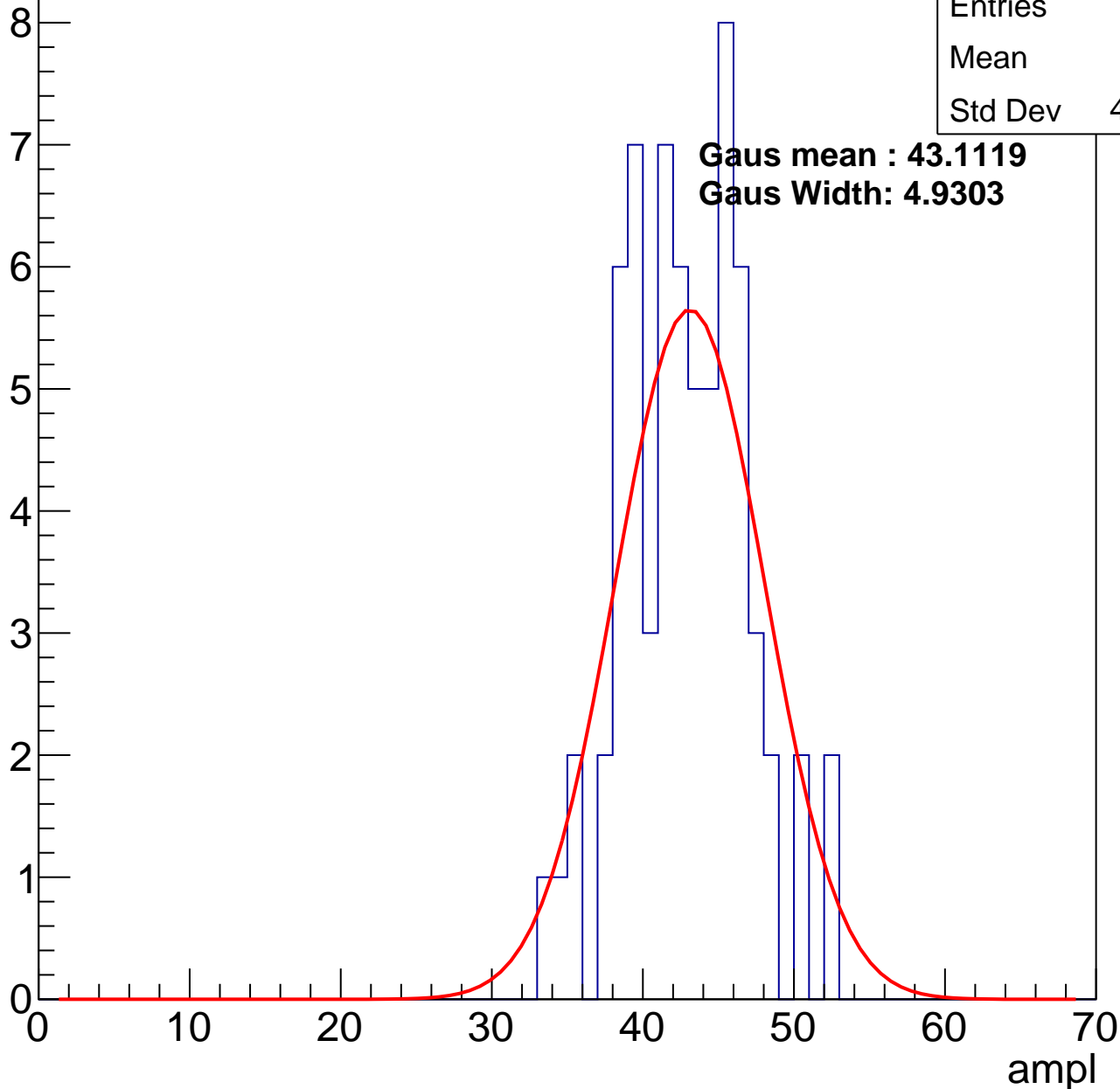
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.4
Std Dev	4.073

Gaus mean : 43.1119

Gaus Width: 4.9303

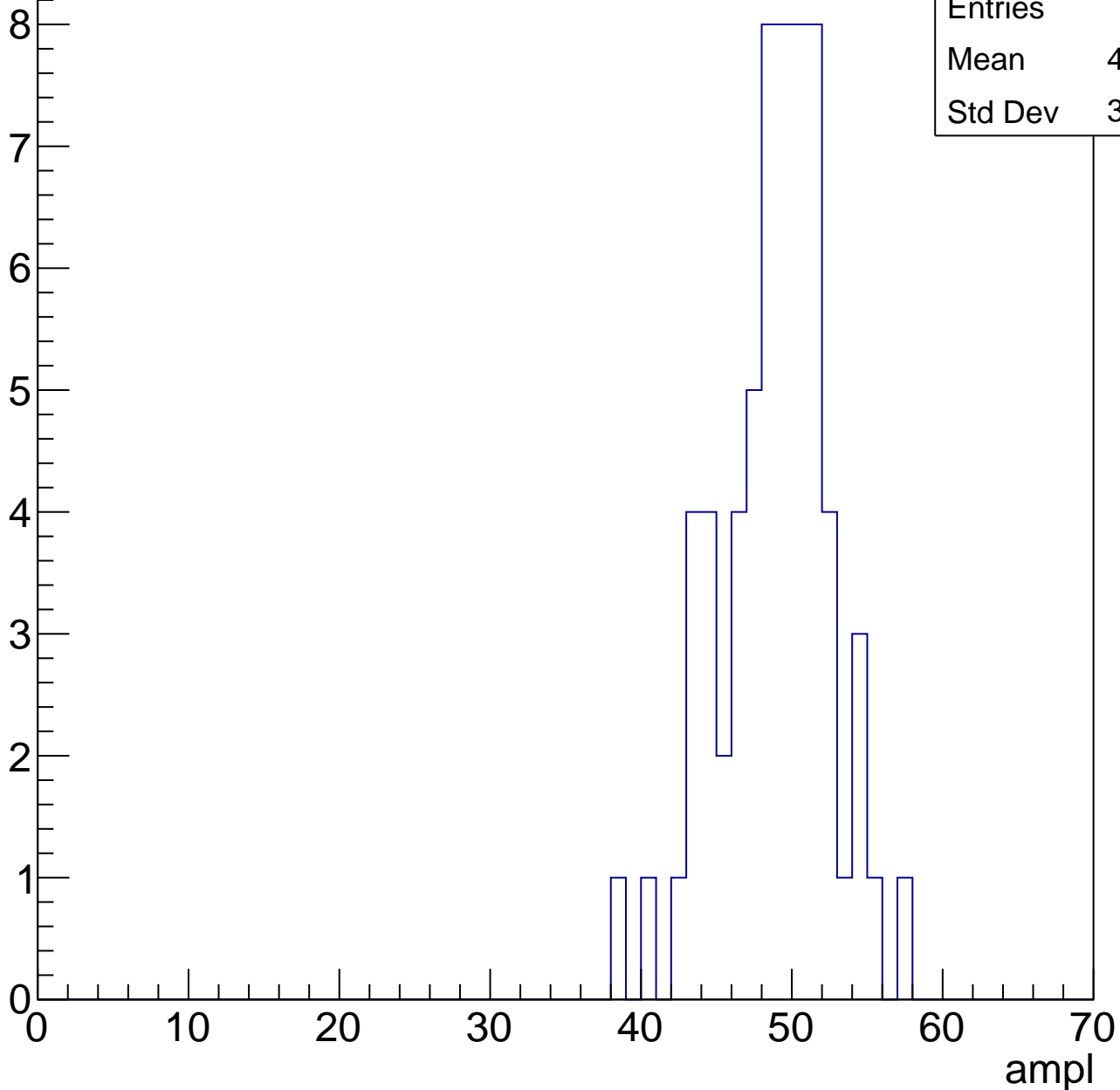


B1L103S, U24-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	48.38
Std Dev	3.608

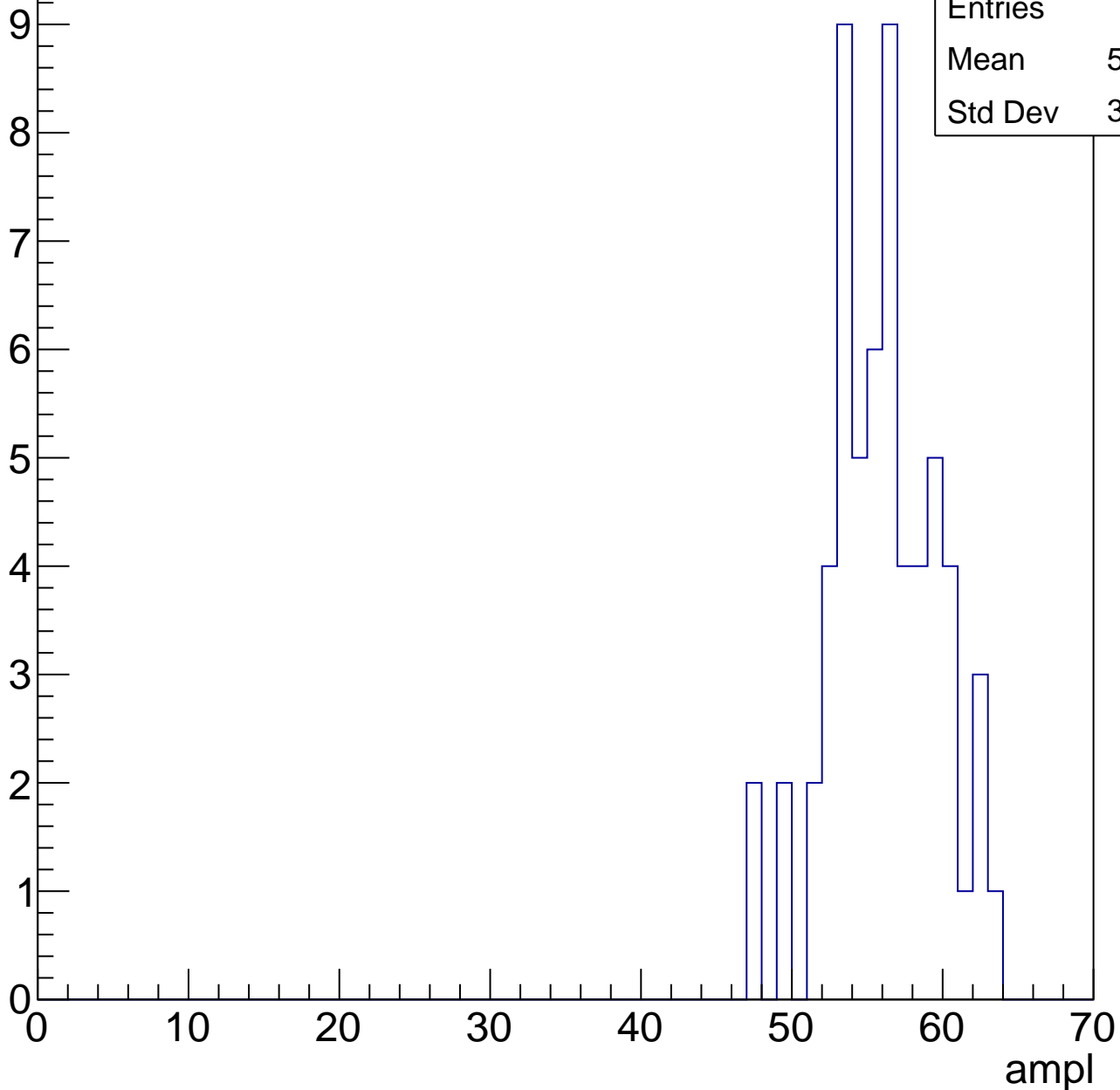


B1L103S, U24-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

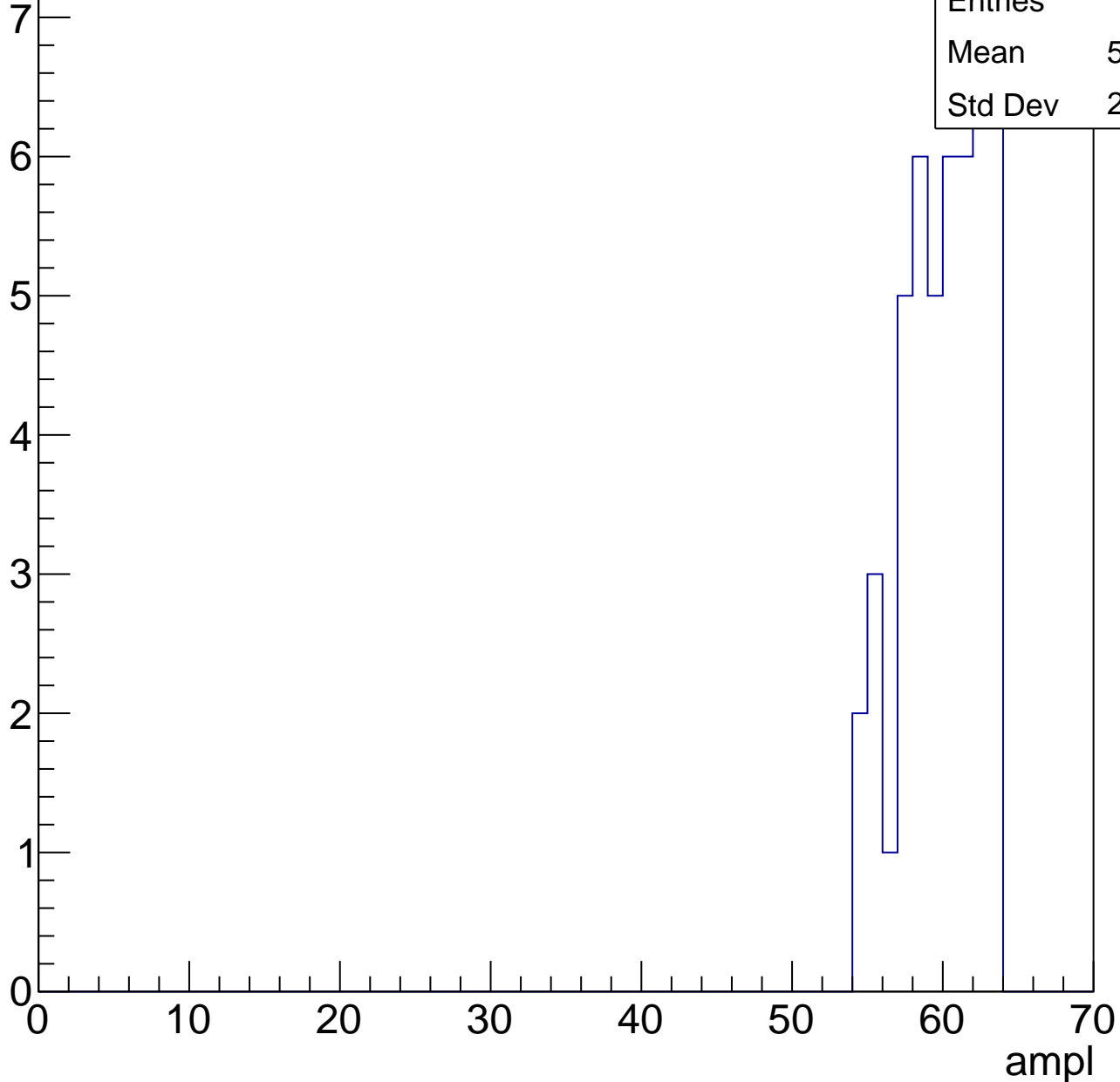
Entries	61
Mean	55.54
Std Dev	3.574



B1L103S, U24-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

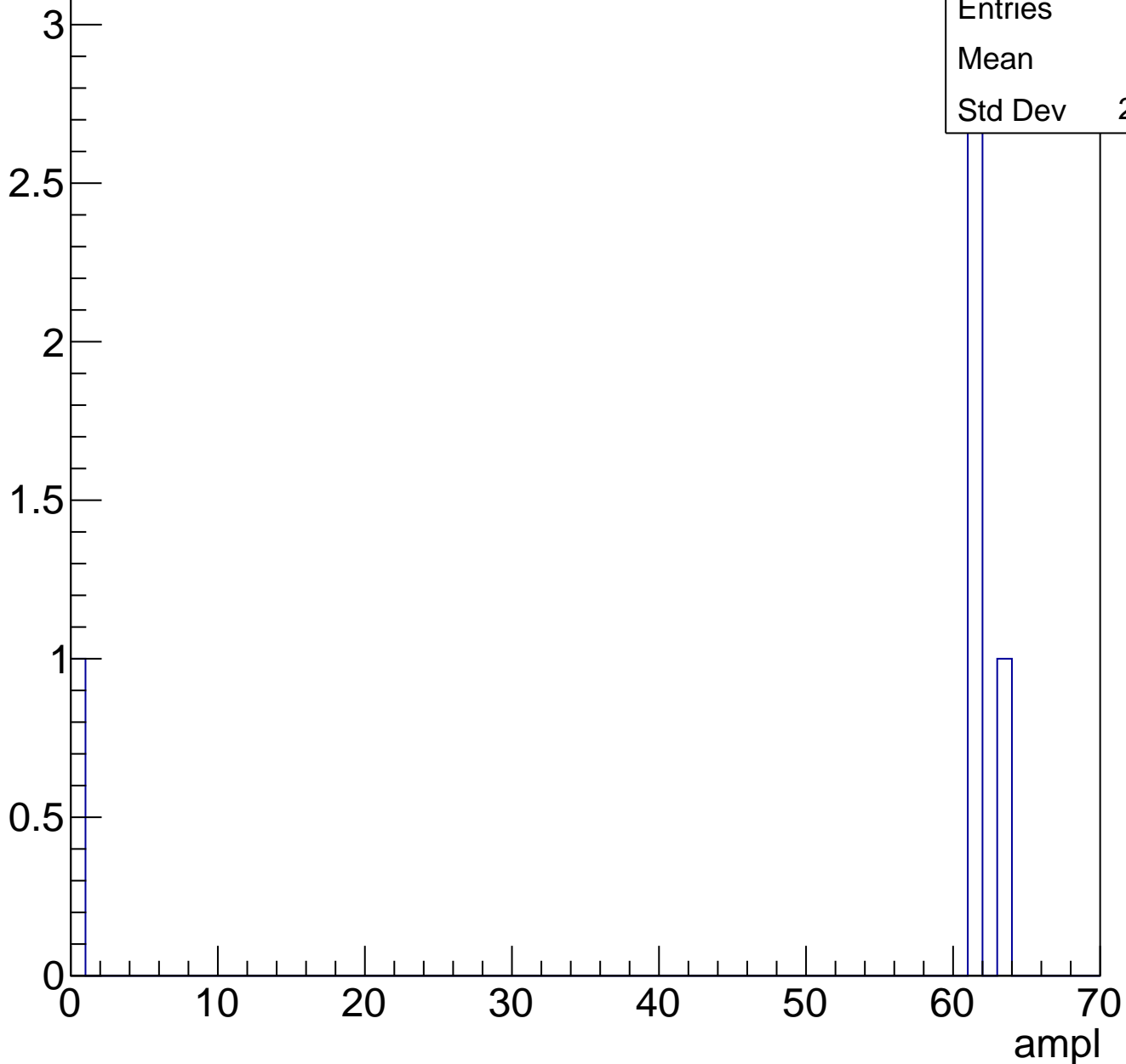


Entries	48
Mean	59.54
Std Dev	2.598

B1L103S, U24-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch115, adc7

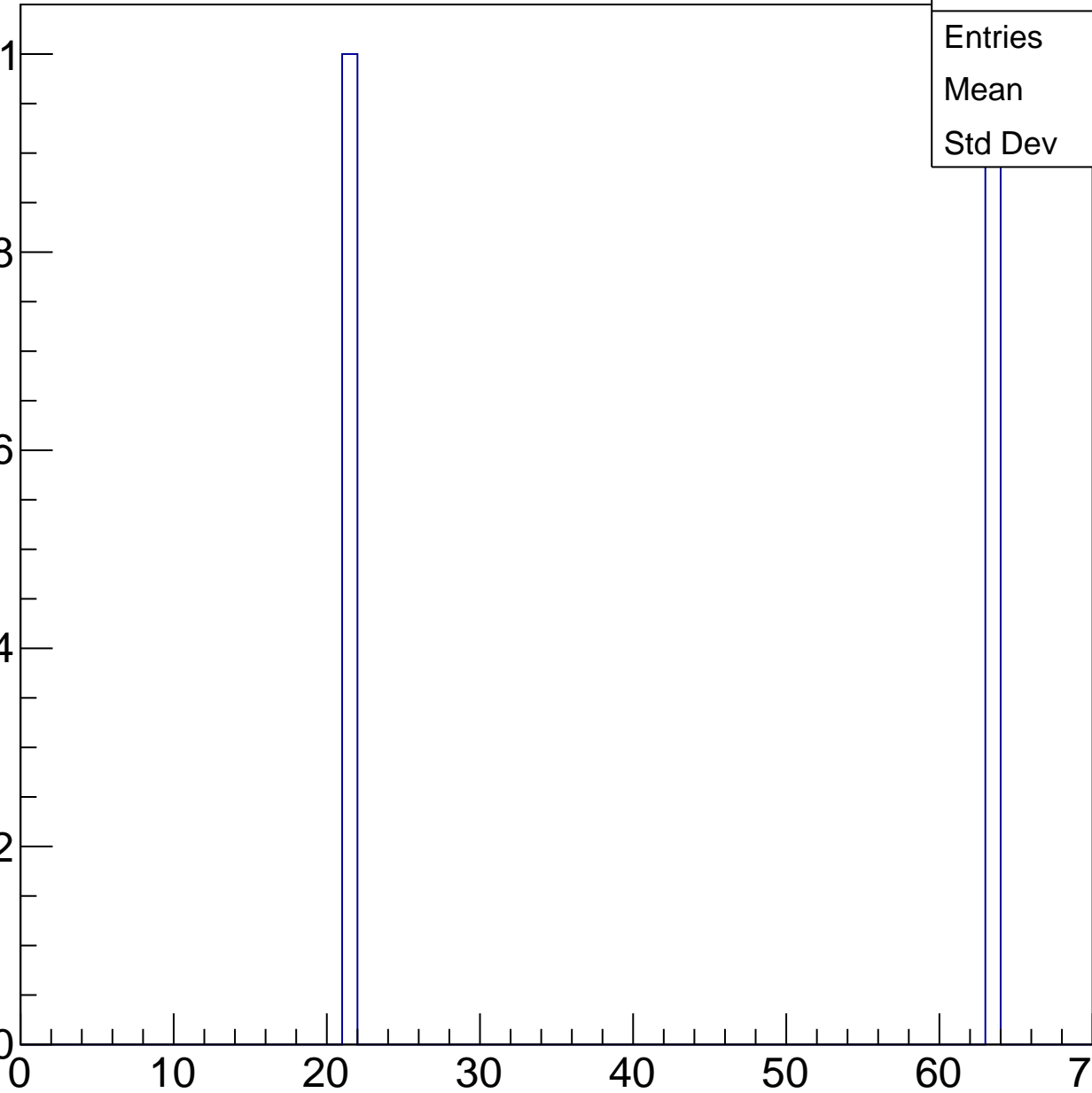
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42
Std Dev	21

ampl



B1L103S, U24-ch116, adc0

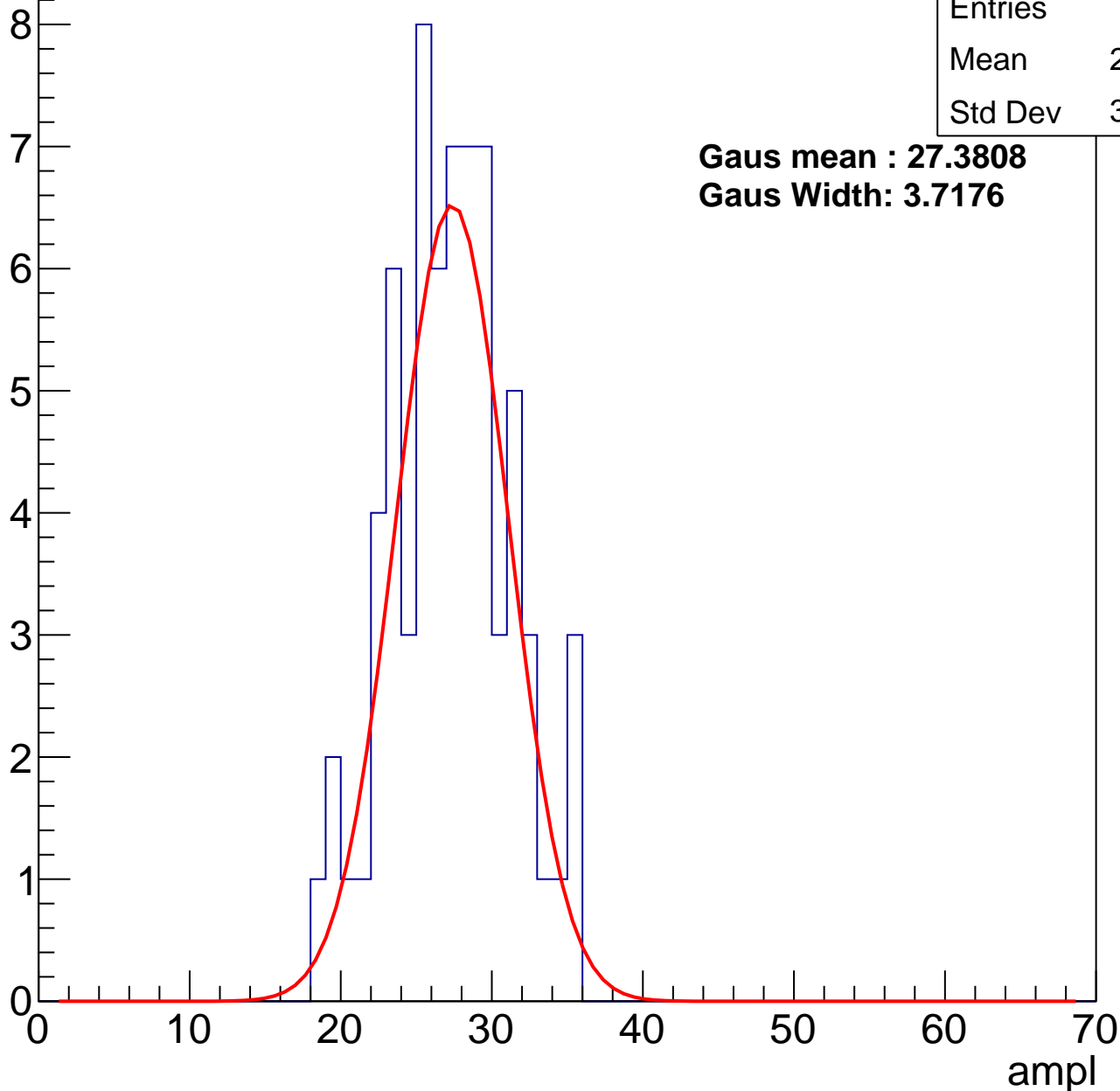
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.84
Std Dev	3.896

Gaus mean : 27.3808

Gaus Width: 3.7176



B1L103S, U24-ch116, adc1

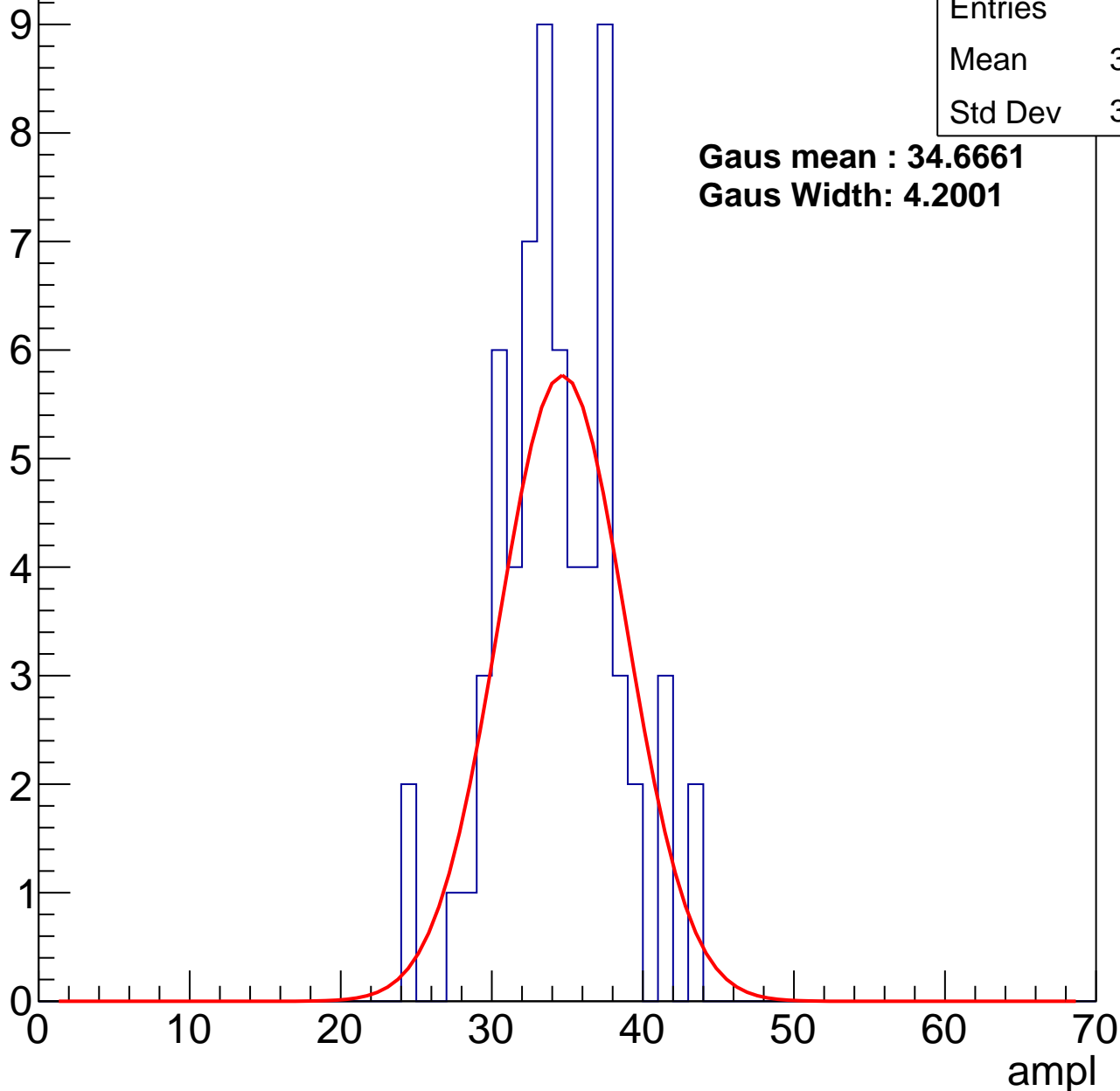
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	33.89
Std Dev	3.962

Gaus mean : 34.6661

Gaus Width: 4.2001



B1L103S, U24-ch116, adc2

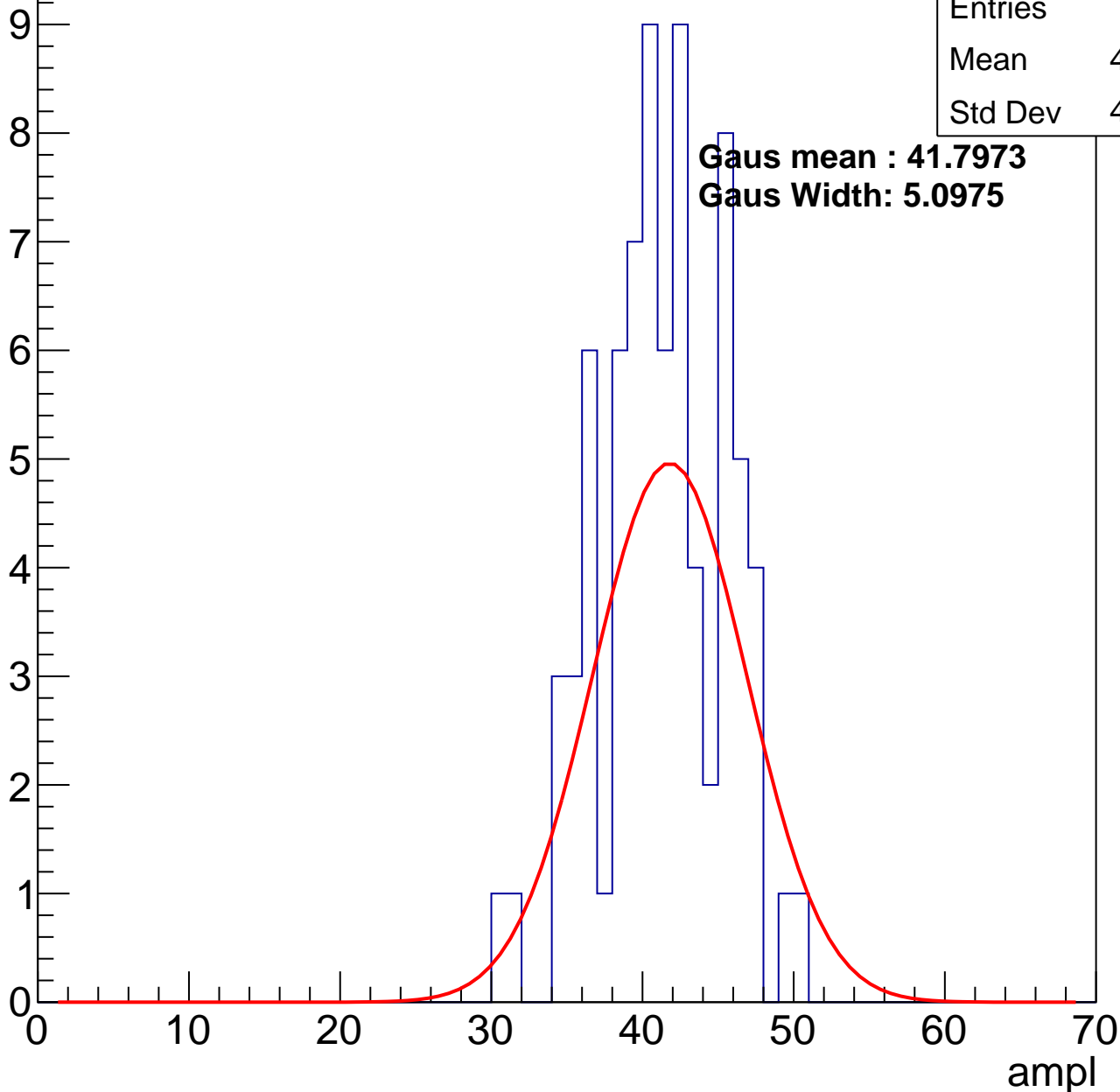
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	40.82
Std Dev	4.133

Gaus mean : 41.7973

Gaus Width: 5.0975



B1L103S, U24-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

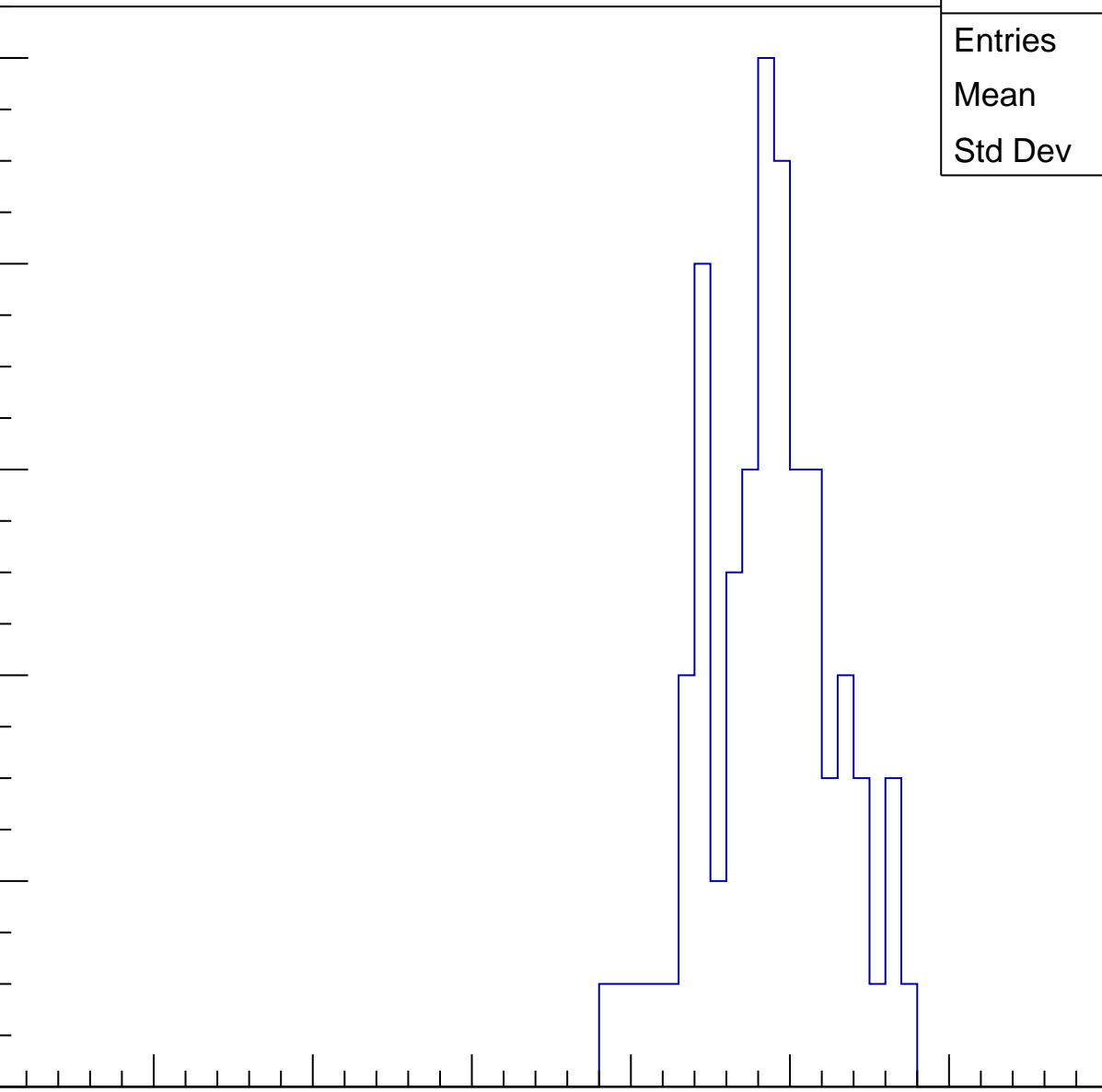
Entries	76
Mean	48.2
Std Dev	4.078

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

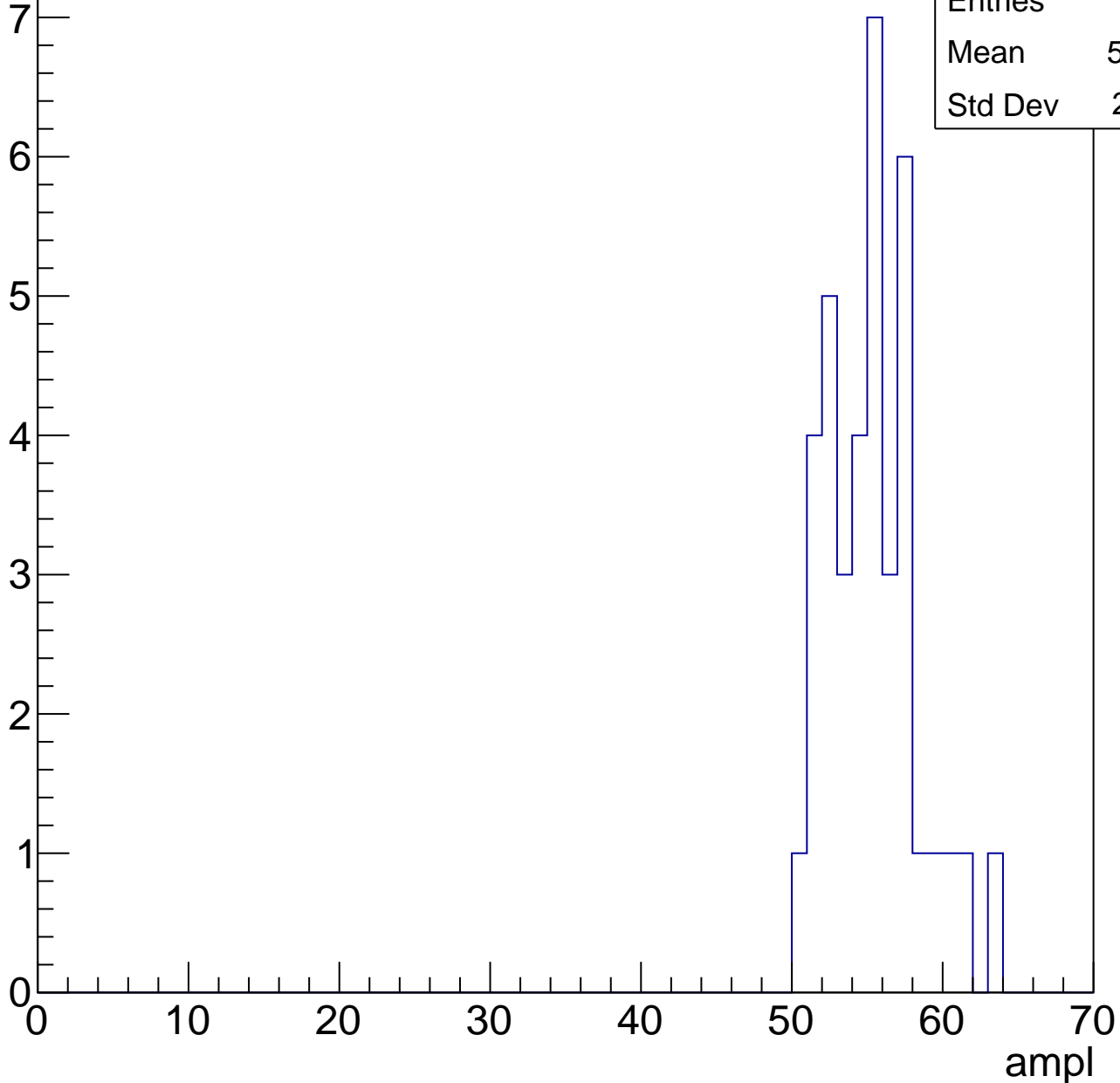


B1L103S, U24-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	54.87
Std Dev	2.931

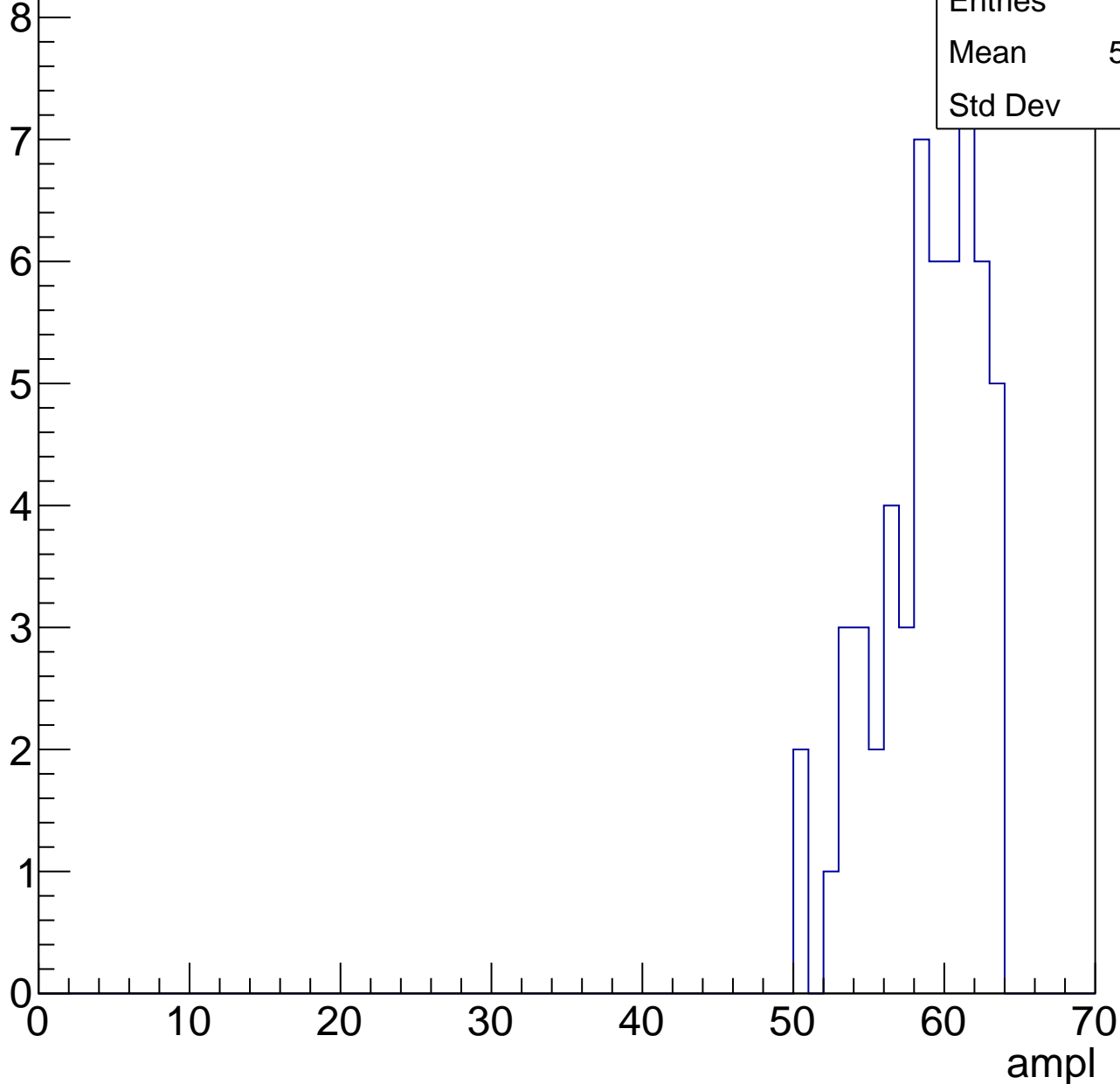


B1L103S, U24-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	58.45
Std Dev	3.37



B1L103S, U24-ch116, adc6

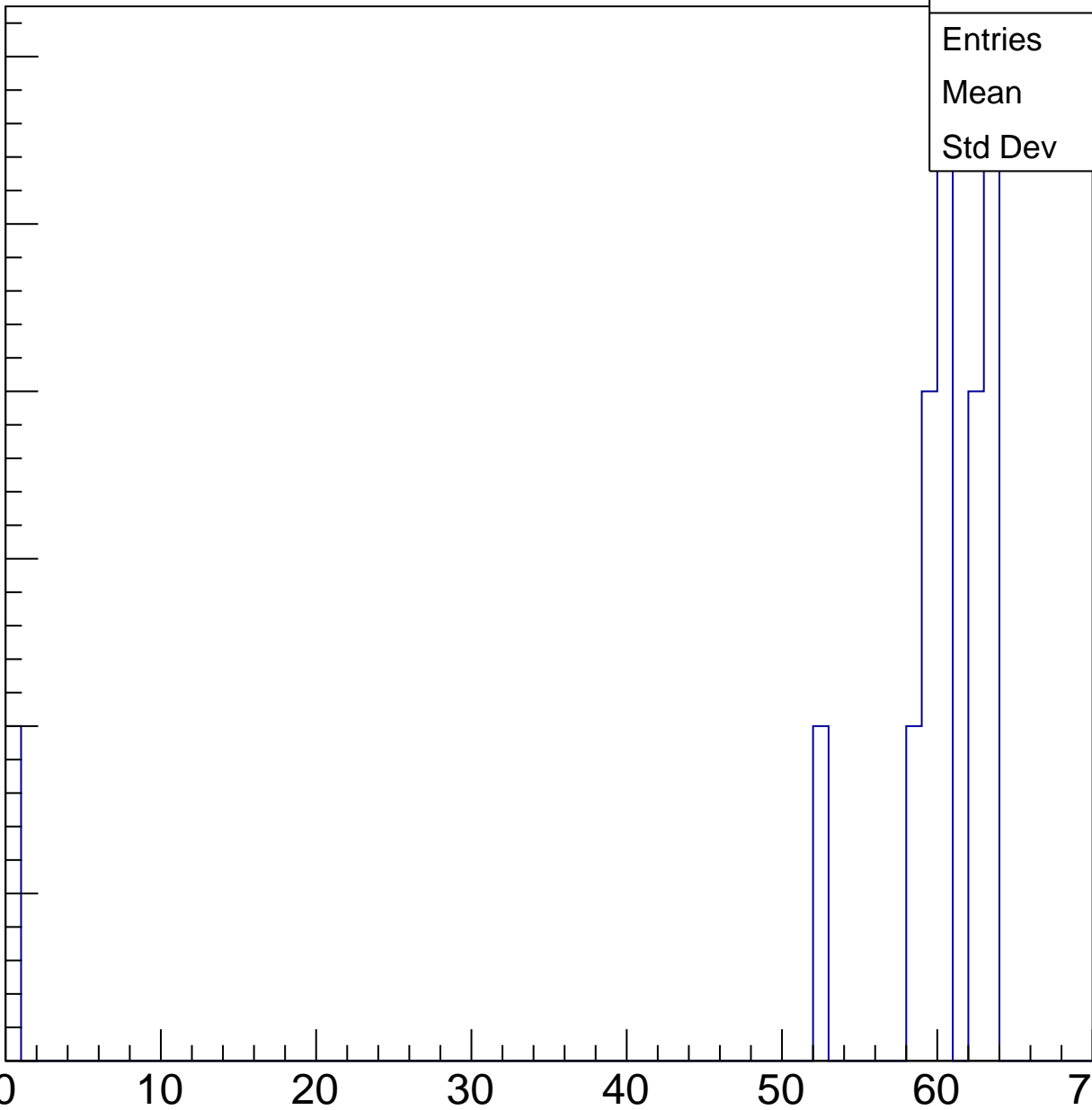
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	13
Mean	55.46
Std Dev	16.26

ampl



B1L103S, U24-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

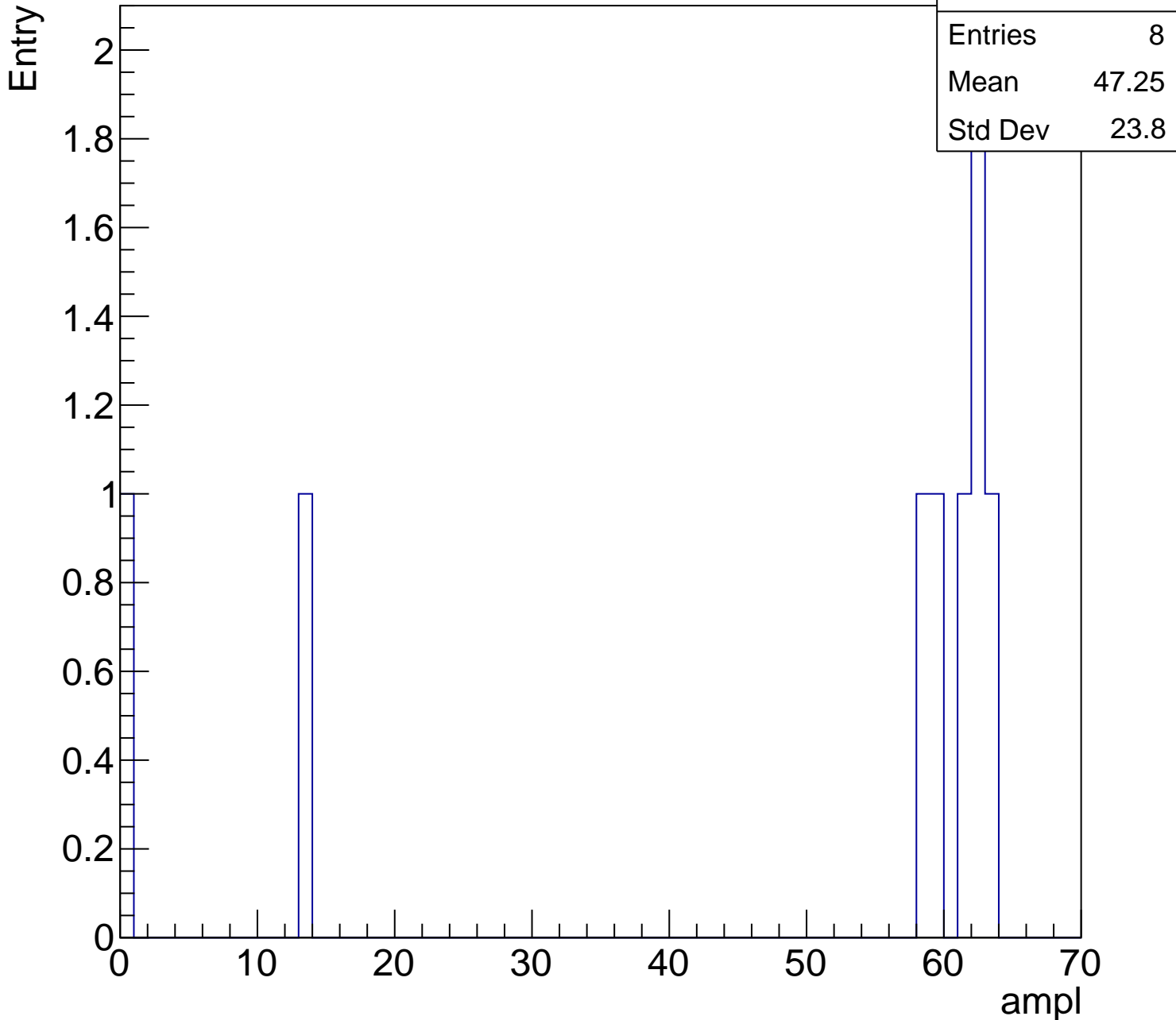
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	47.25
Std Dev	23.8

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch117, adc0

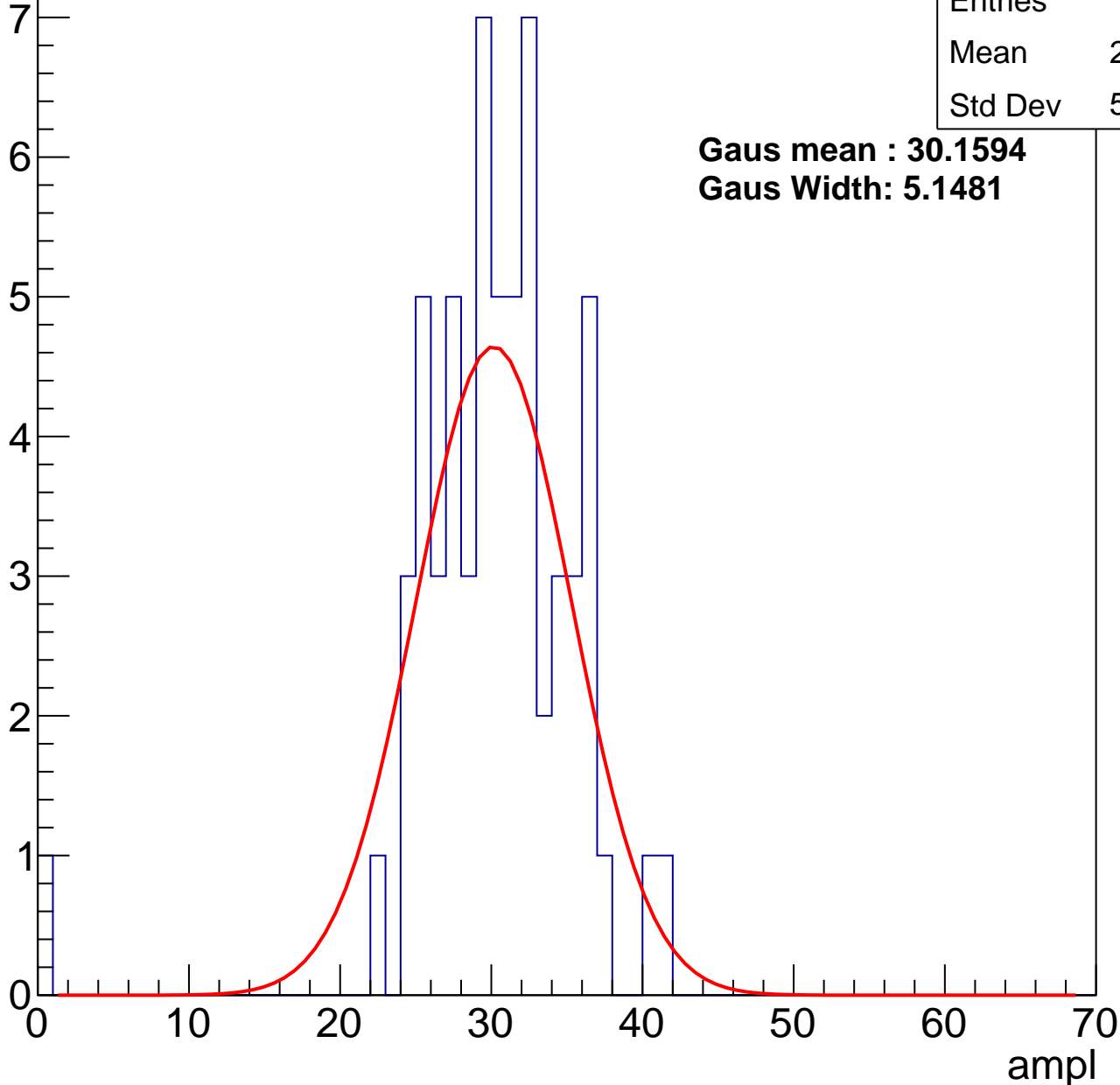
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	29.82
Std Dev	5.626

Gaus mean : 30.1594

Gaus Width: 5.1481



B1L103S, U24-ch117, adc1

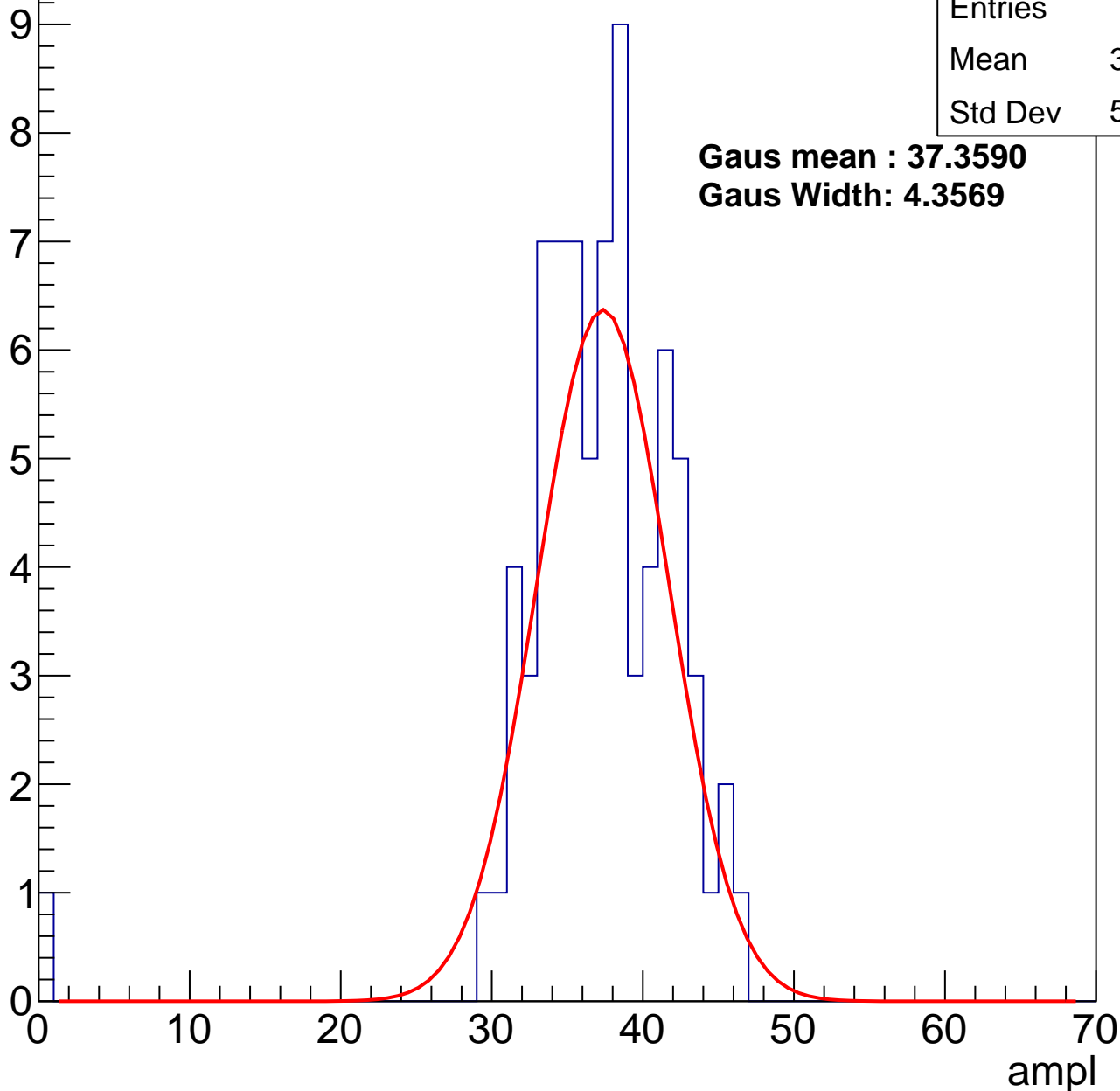
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.57
Std Dev	5.742

Gaus mean : 37.3590

Gaus Width: 4.3569

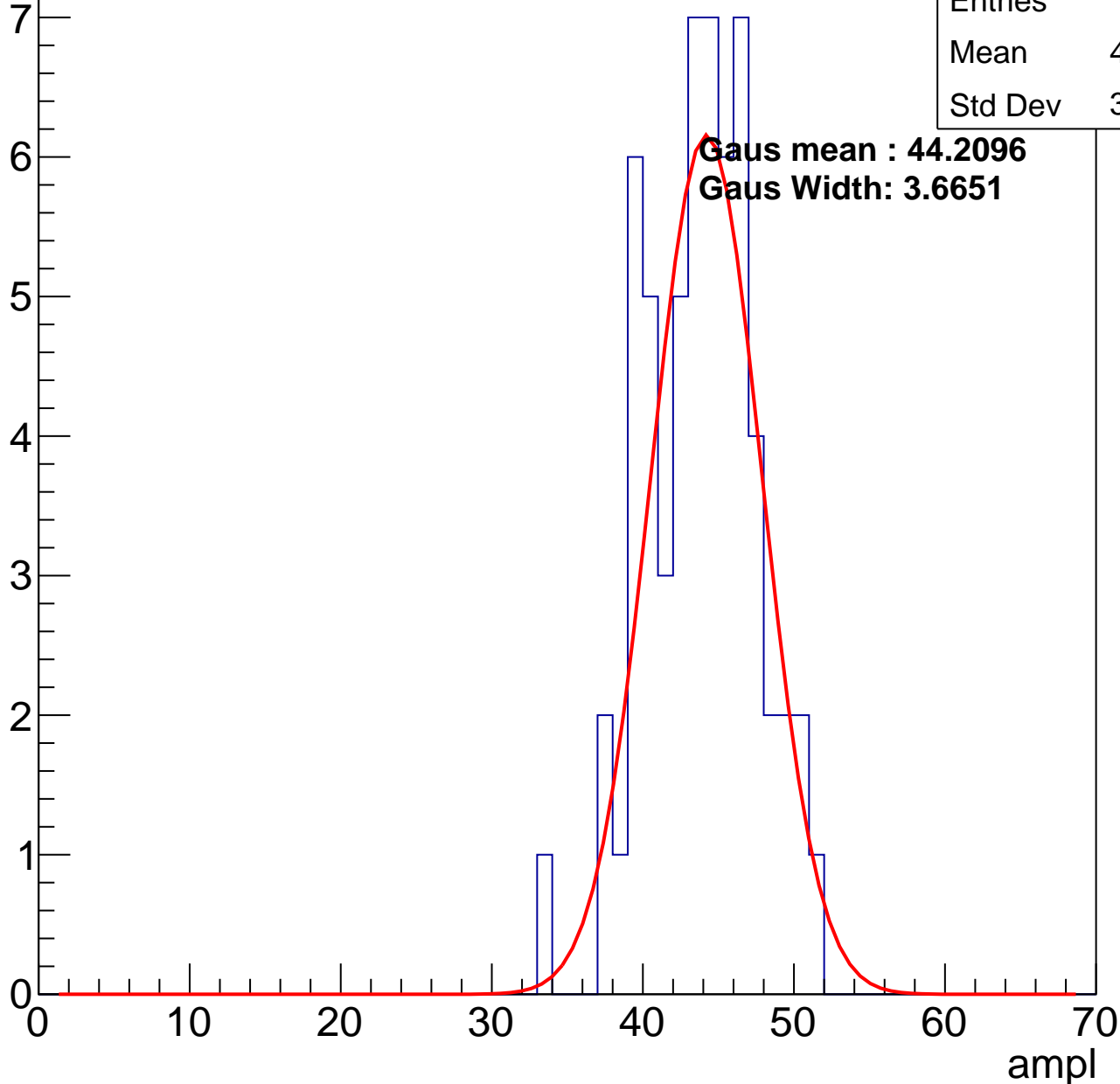


B1L103S, U24-ch117, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

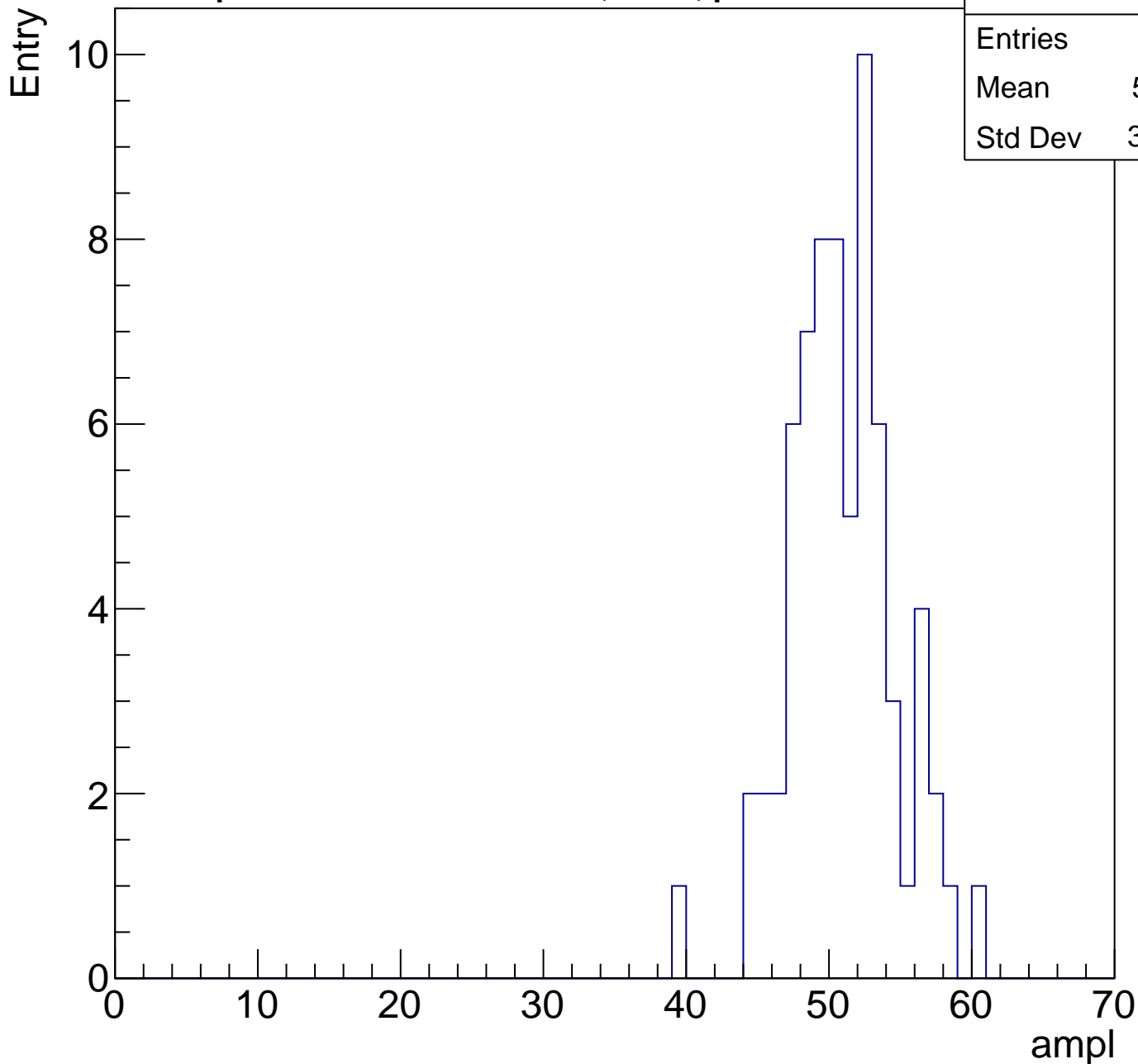
Entries	61
Mean	43.38
Std Dev	3.595



B1L103S, U24-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	50.51
Std Dev	3.662

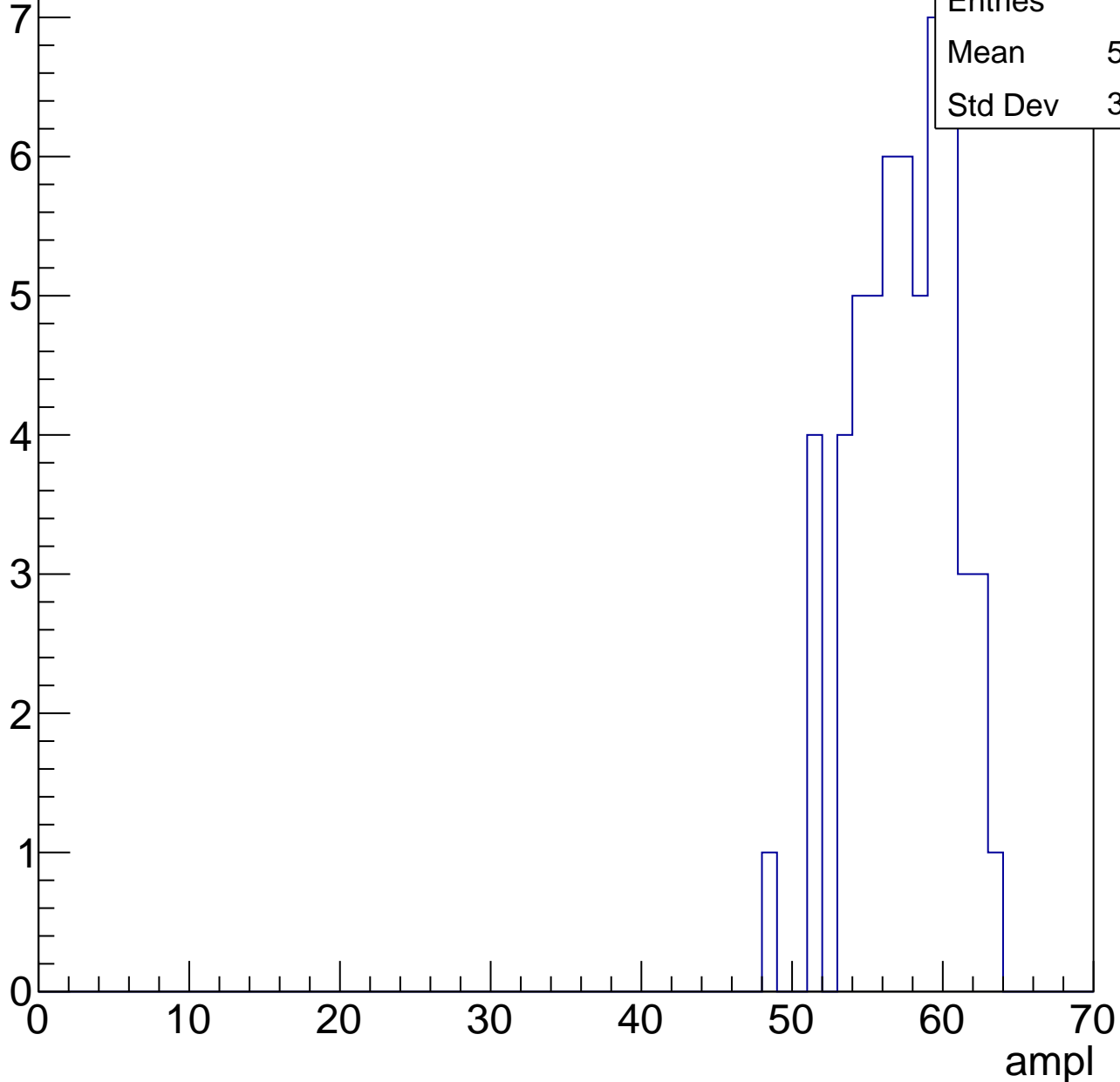


B1L103S, U24-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.88
Std Dev	3.282

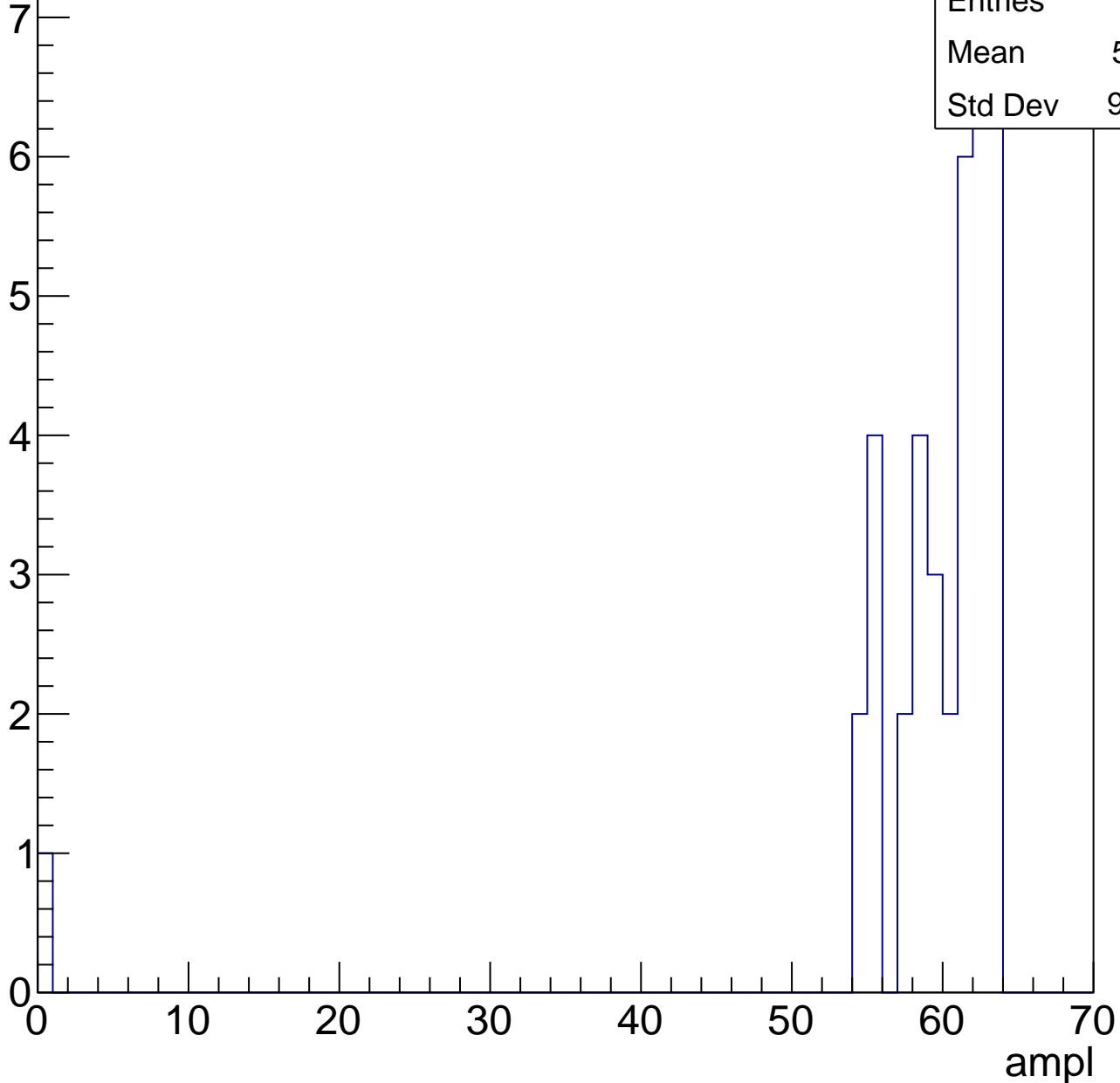


B1L103S, U24-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.21
Std Dev	9.979



B1L103S, U24-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch118, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	27.31
Std Dev	4.833

Gaus mean : 28.5500

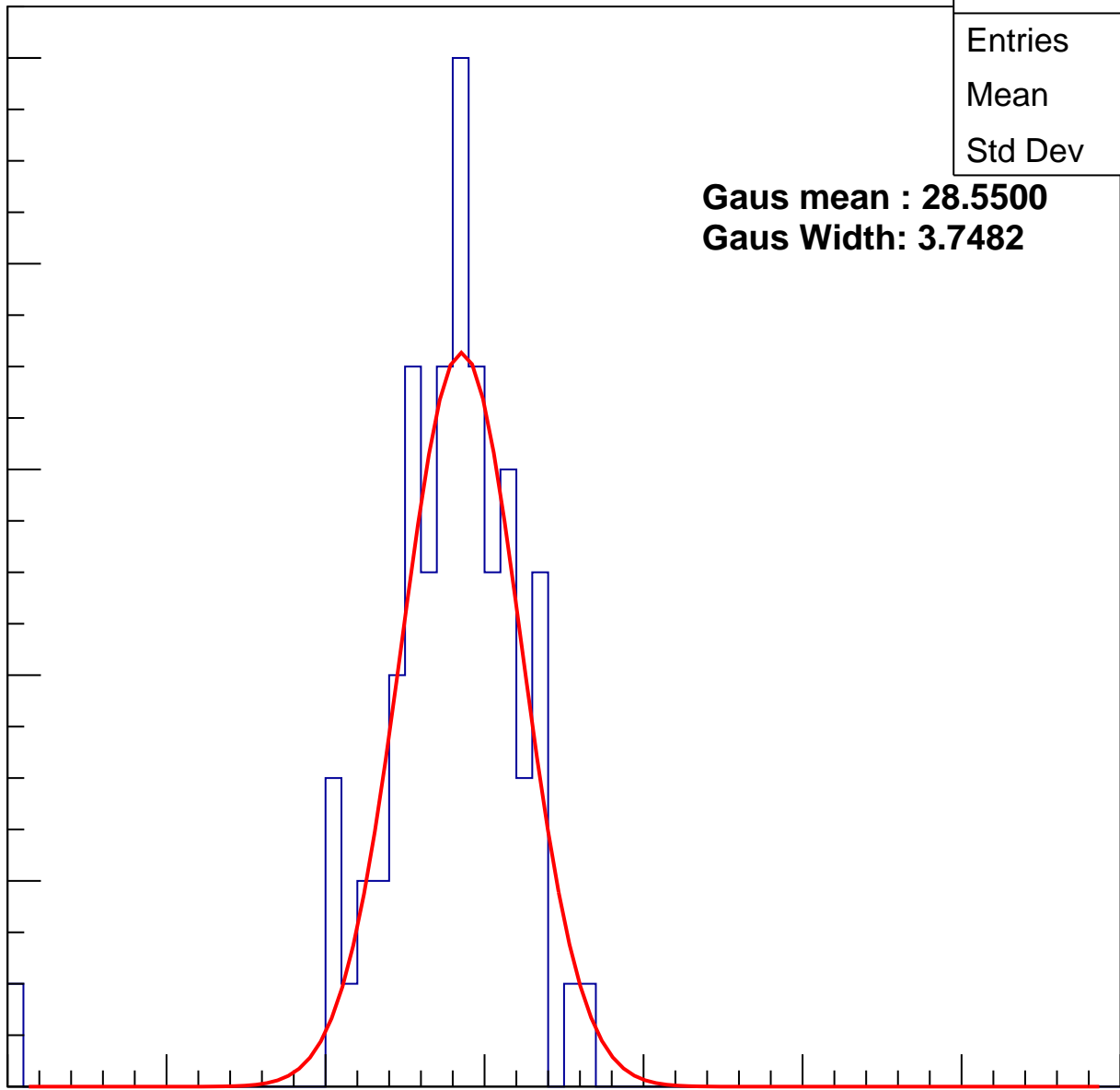
Gaus Width: 3.7482

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch118, adc1

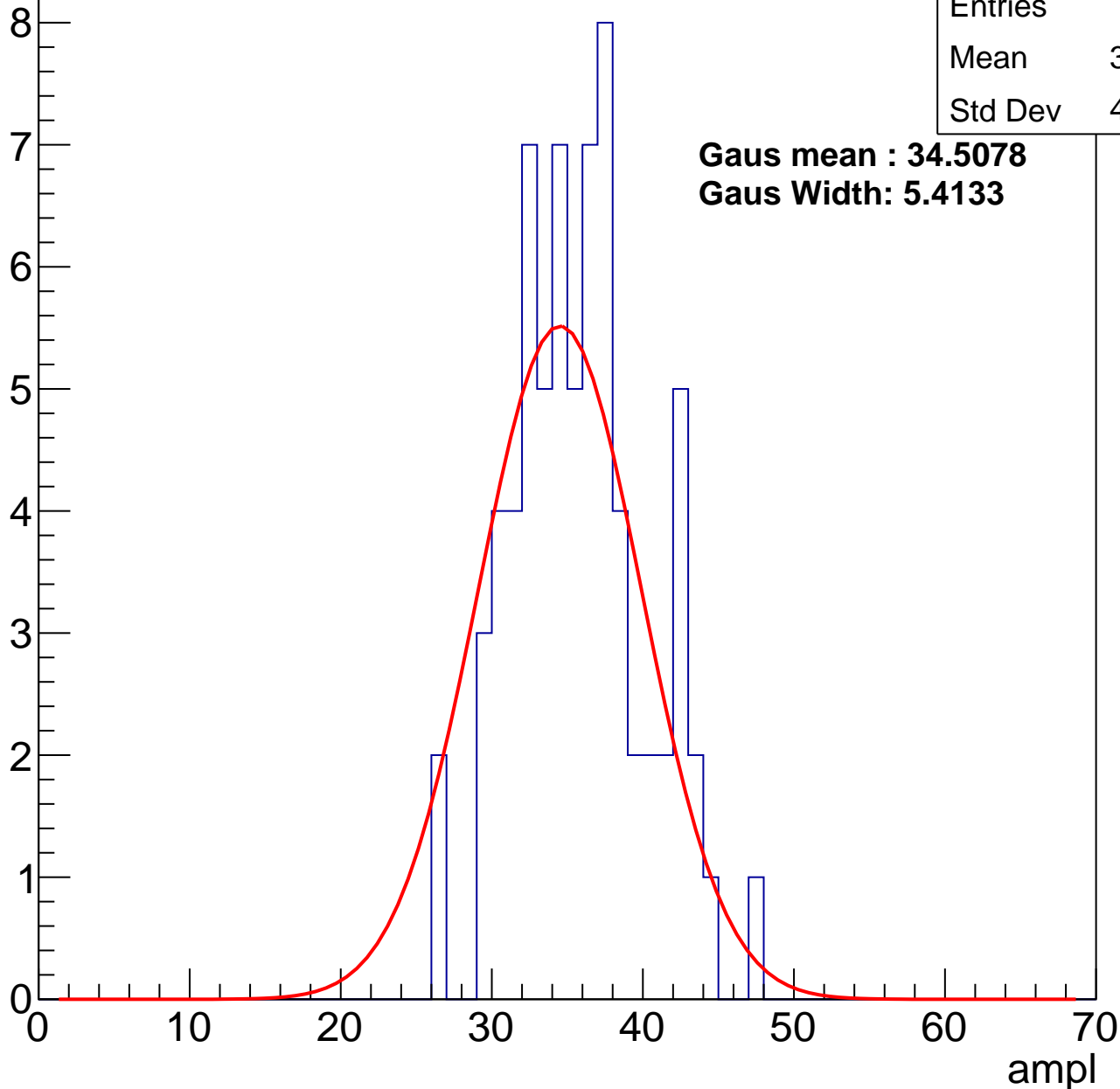
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.38
Std Dev	4.346

Gaus mean : 34.5078

Gaus Width: 5.4133



B1L103S, U24-ch118, adc2

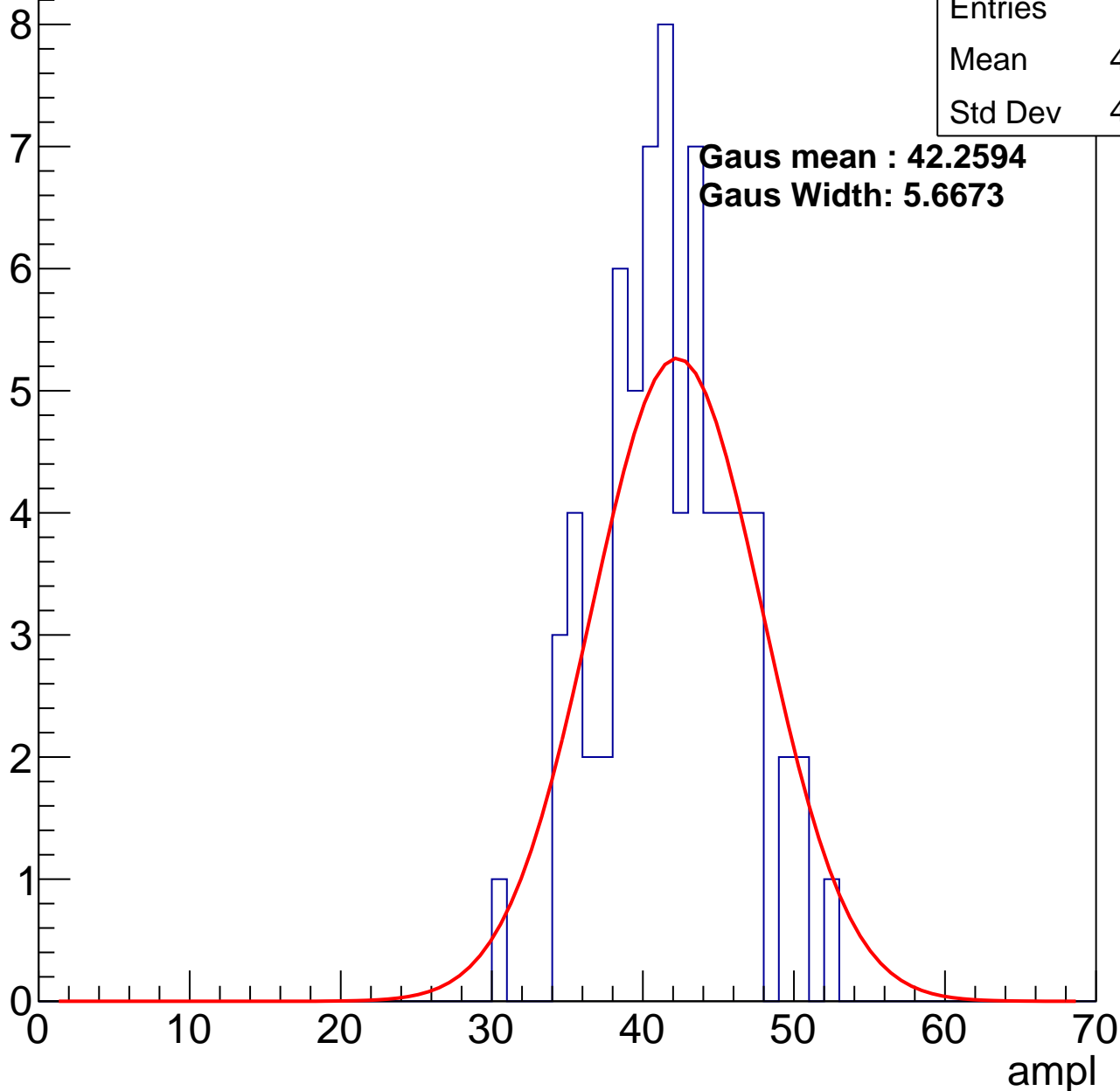
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.37
Std Dev	4.395

Gaus mean : 42.2594

Gaus Width: 5.6673

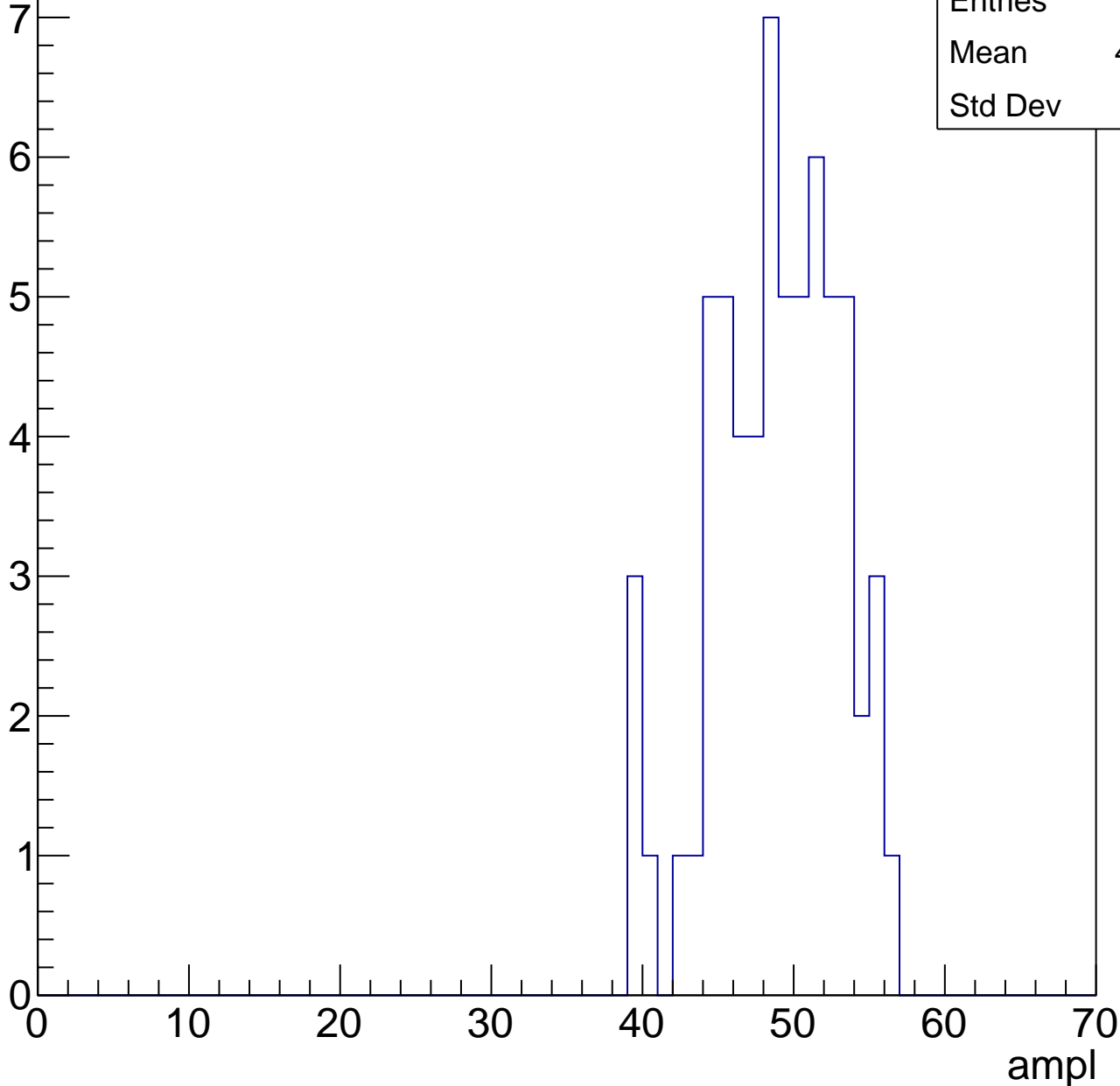


B1L103S, U24-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.41
Std Dev	4.12

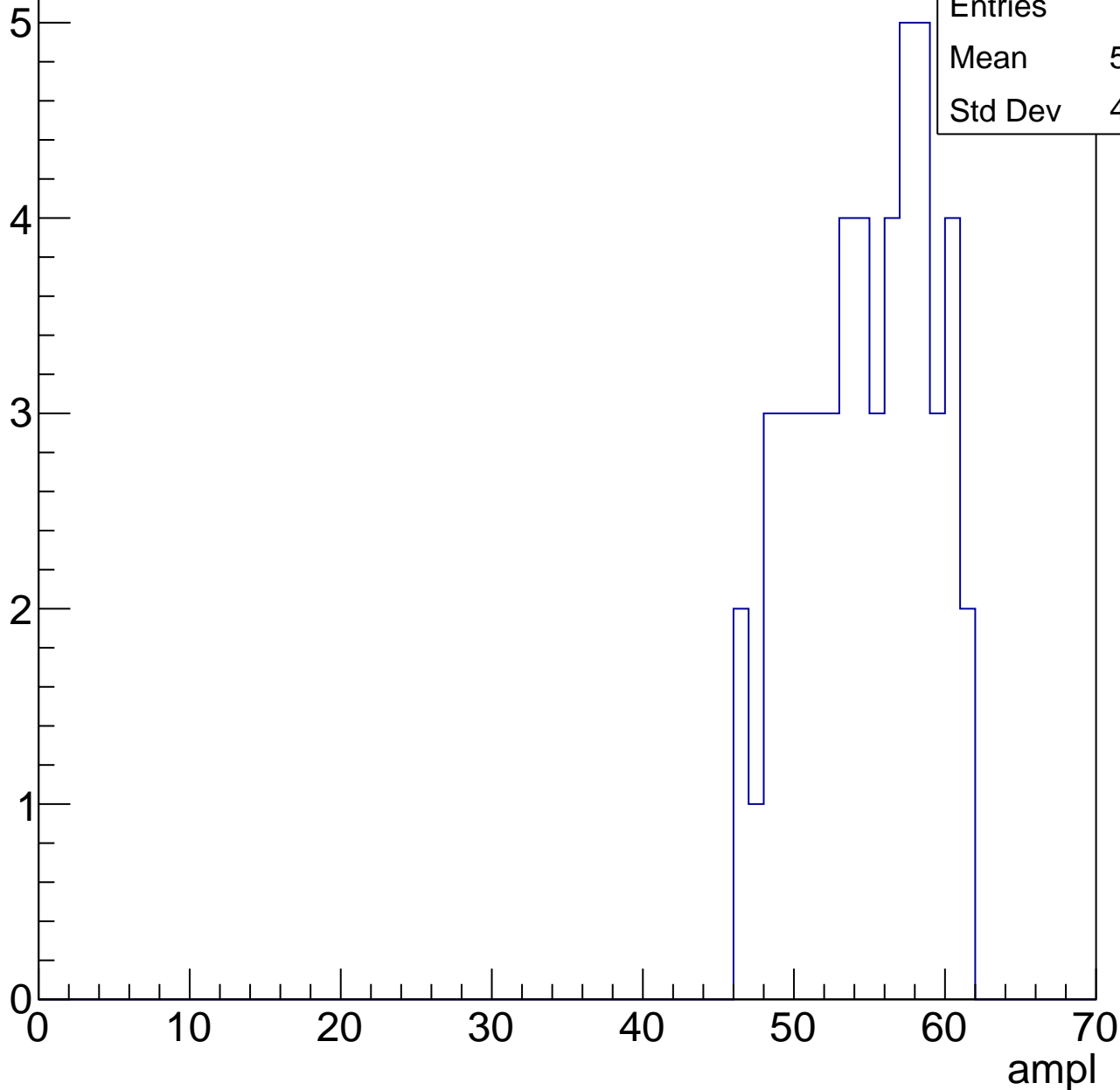


B1L103S, U24-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.23
Std Dev	4.182

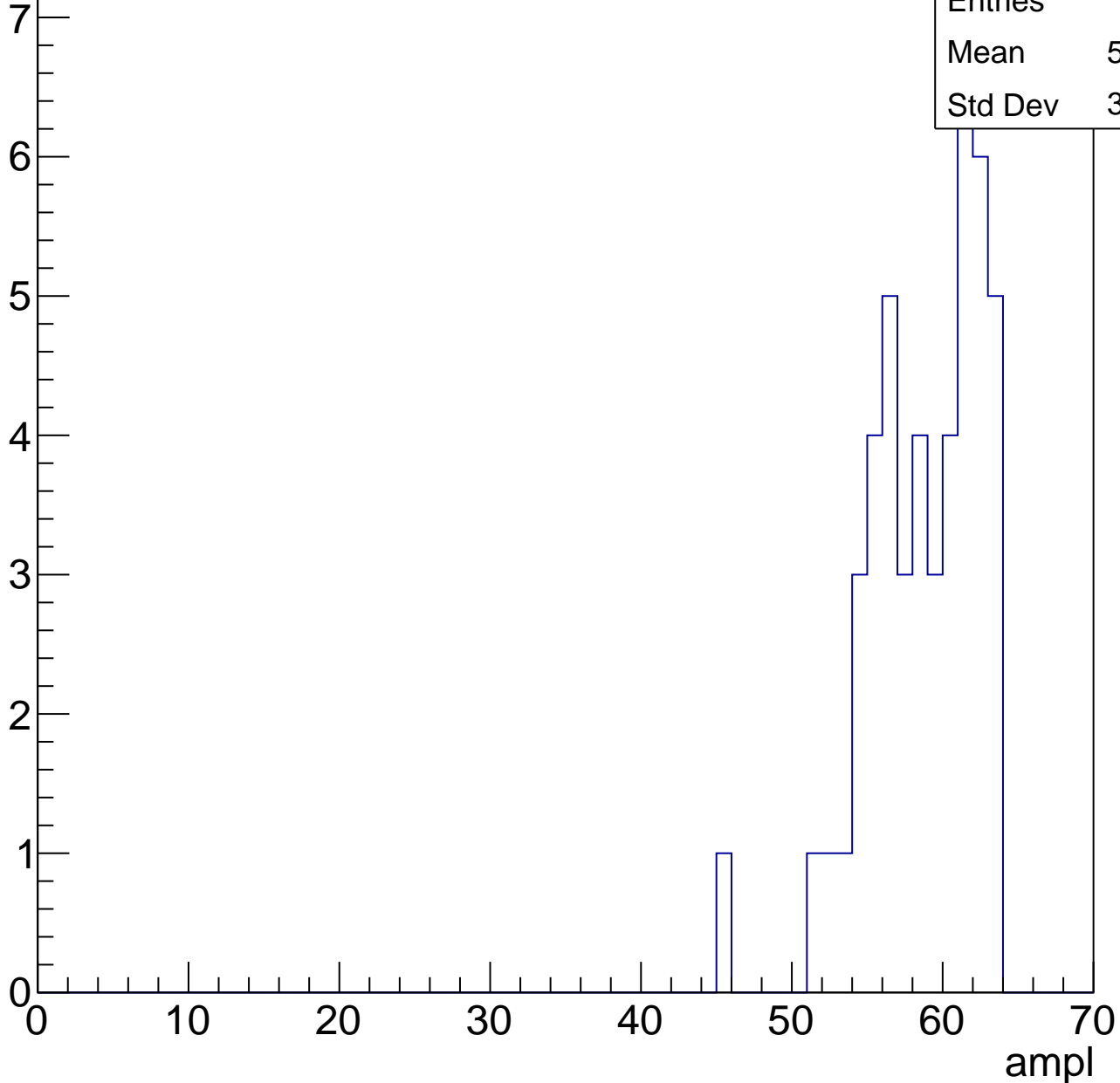


B1L103S, U24-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.27
Std Dev	3.779

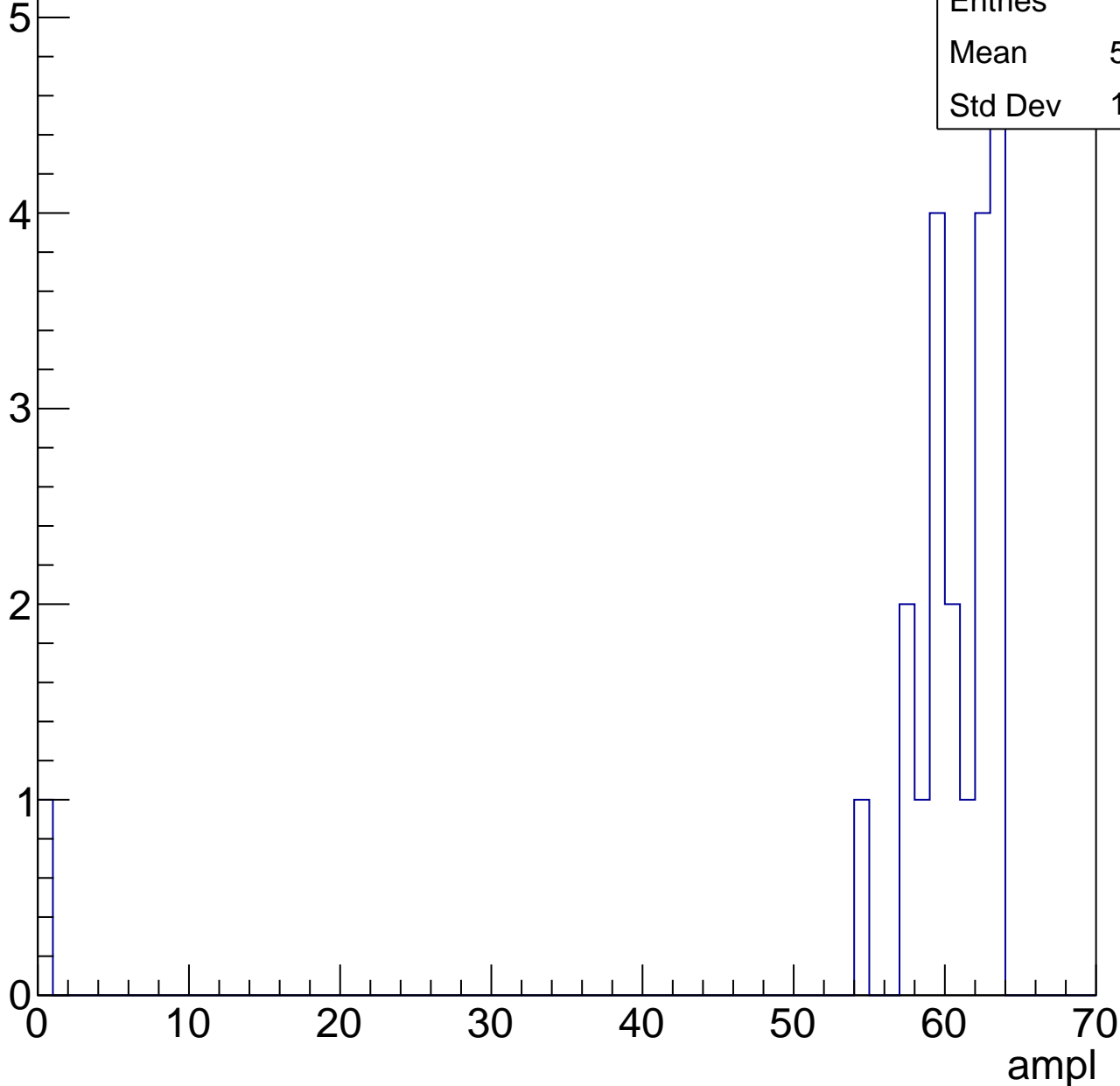


B1L103S, U24-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	57.43
Std Dev	13.07



B1L103S, U24-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch119, adc0

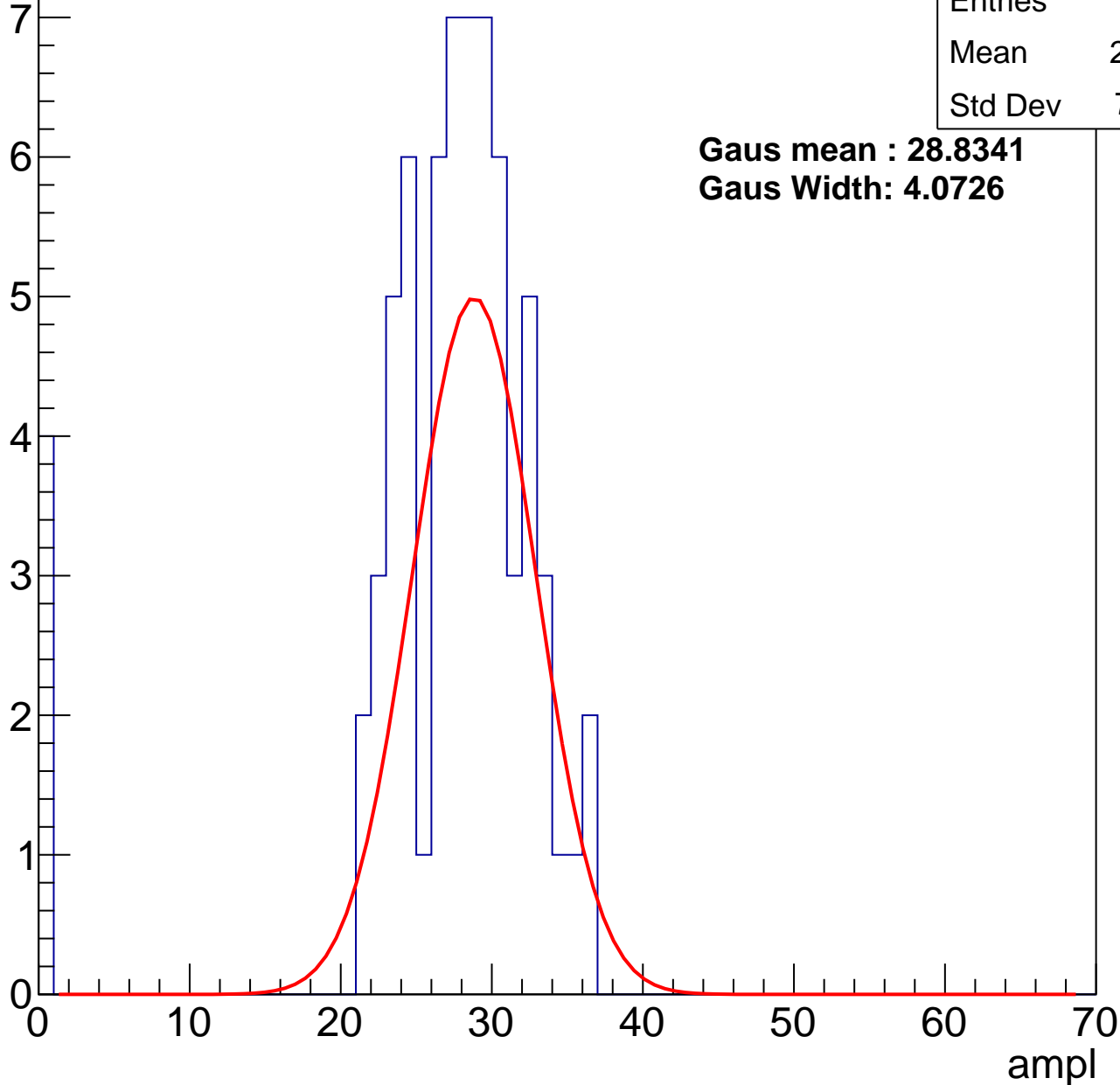
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.22
Std Dev	7.431

Gaus mean : 28.8341

Gaus Width: 4.0726



B1L103S, U24-ch119, adc1

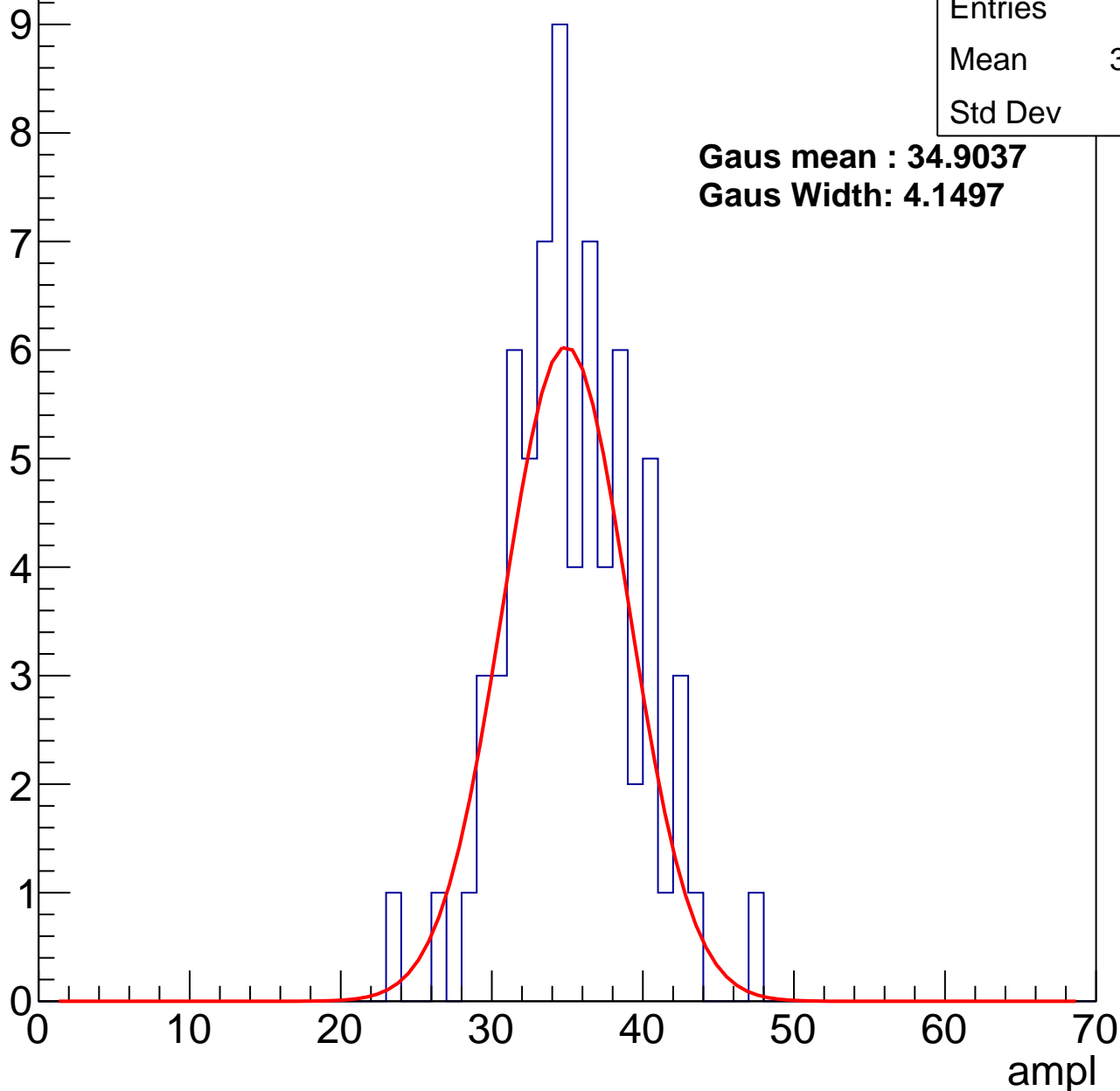
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.86
Std Dev	4.24

Gaus mean : 34.9037

Gaus Width: 4.1497



B1L103S, U24-ch119, adc2

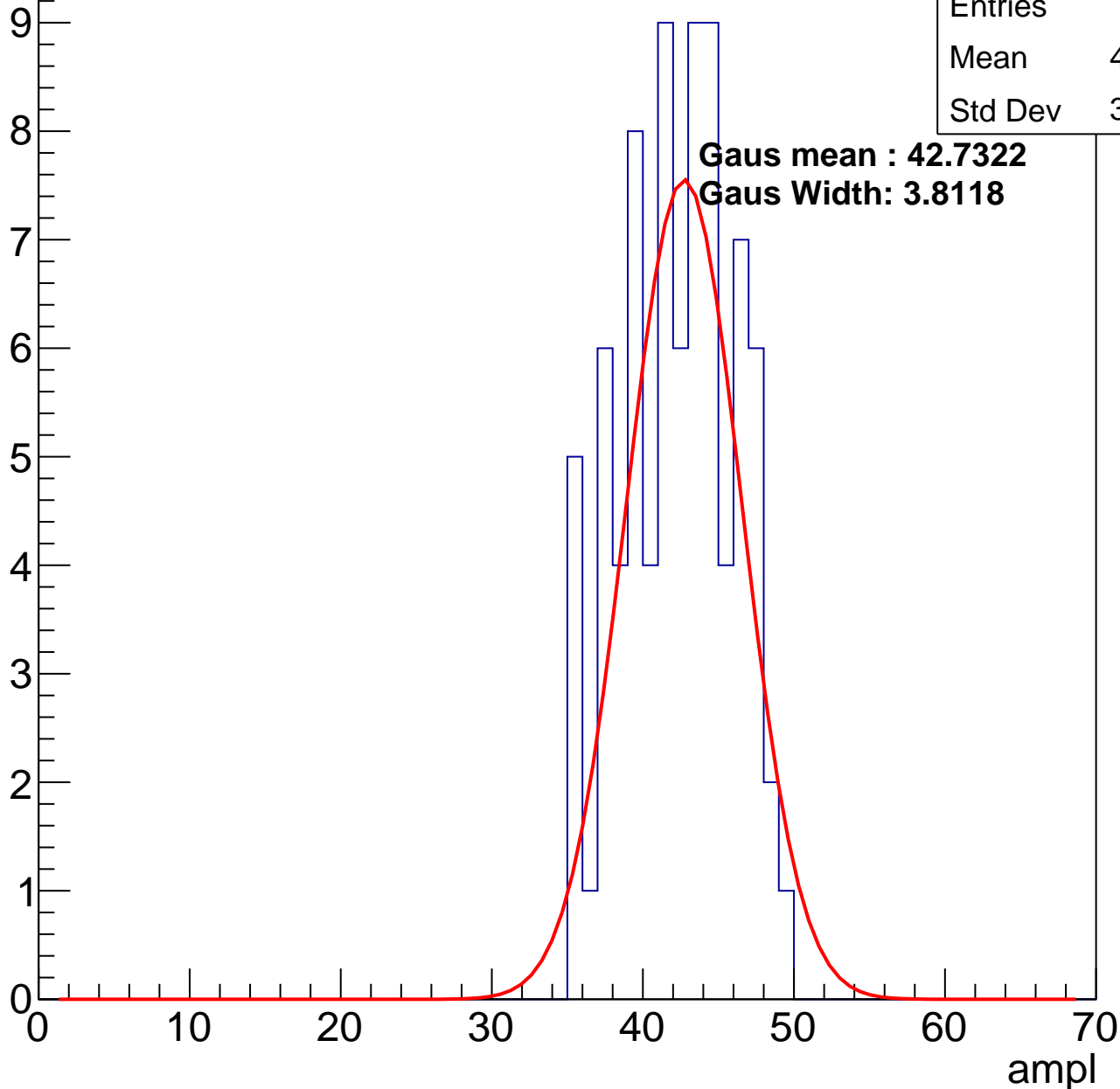
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	41.85
Std Dev	3.608

Gaus mean : 42.7322

Gaus Width: 3.8118

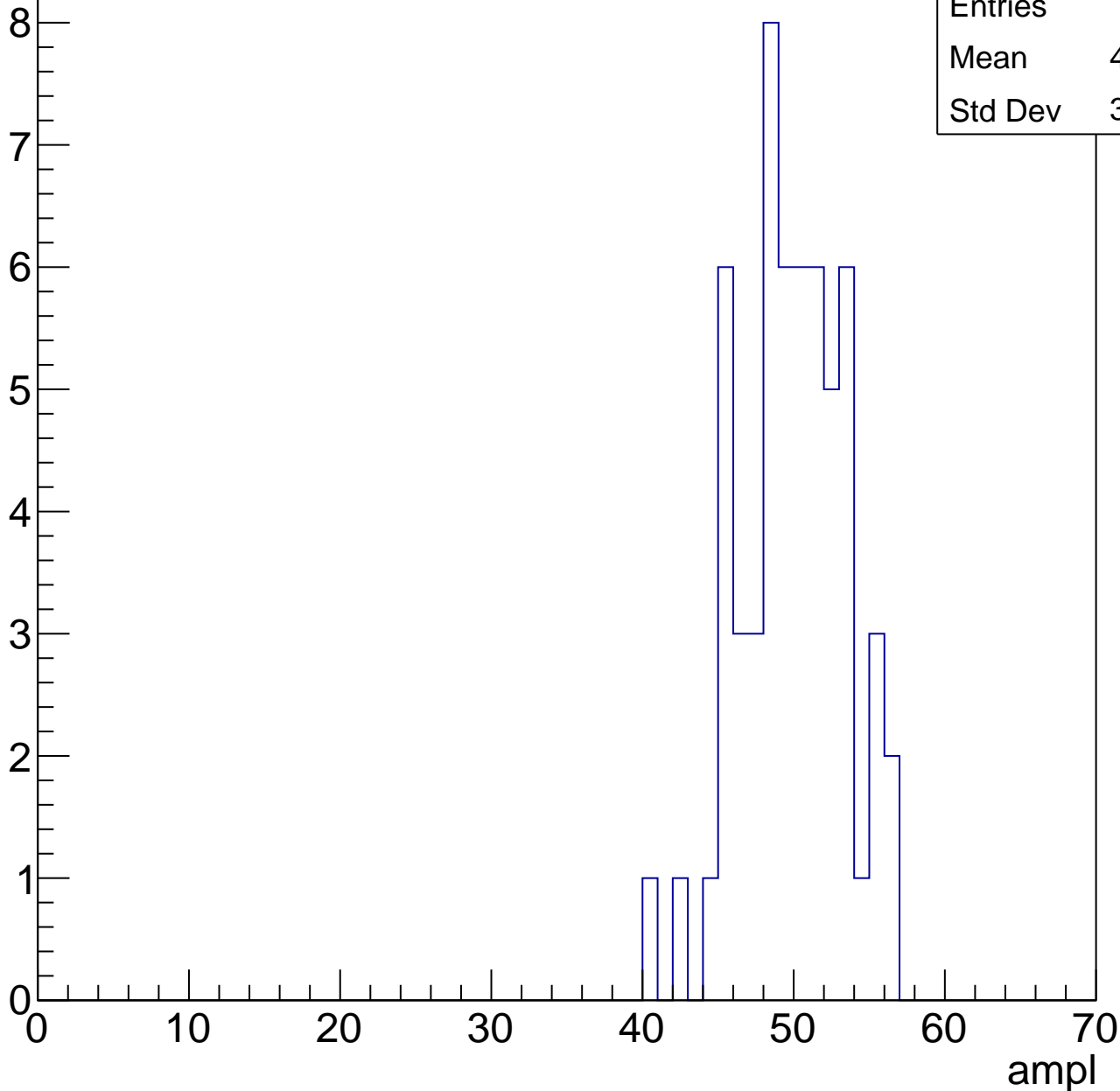


B1L103S, U24-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.45
Std Dev	3.445

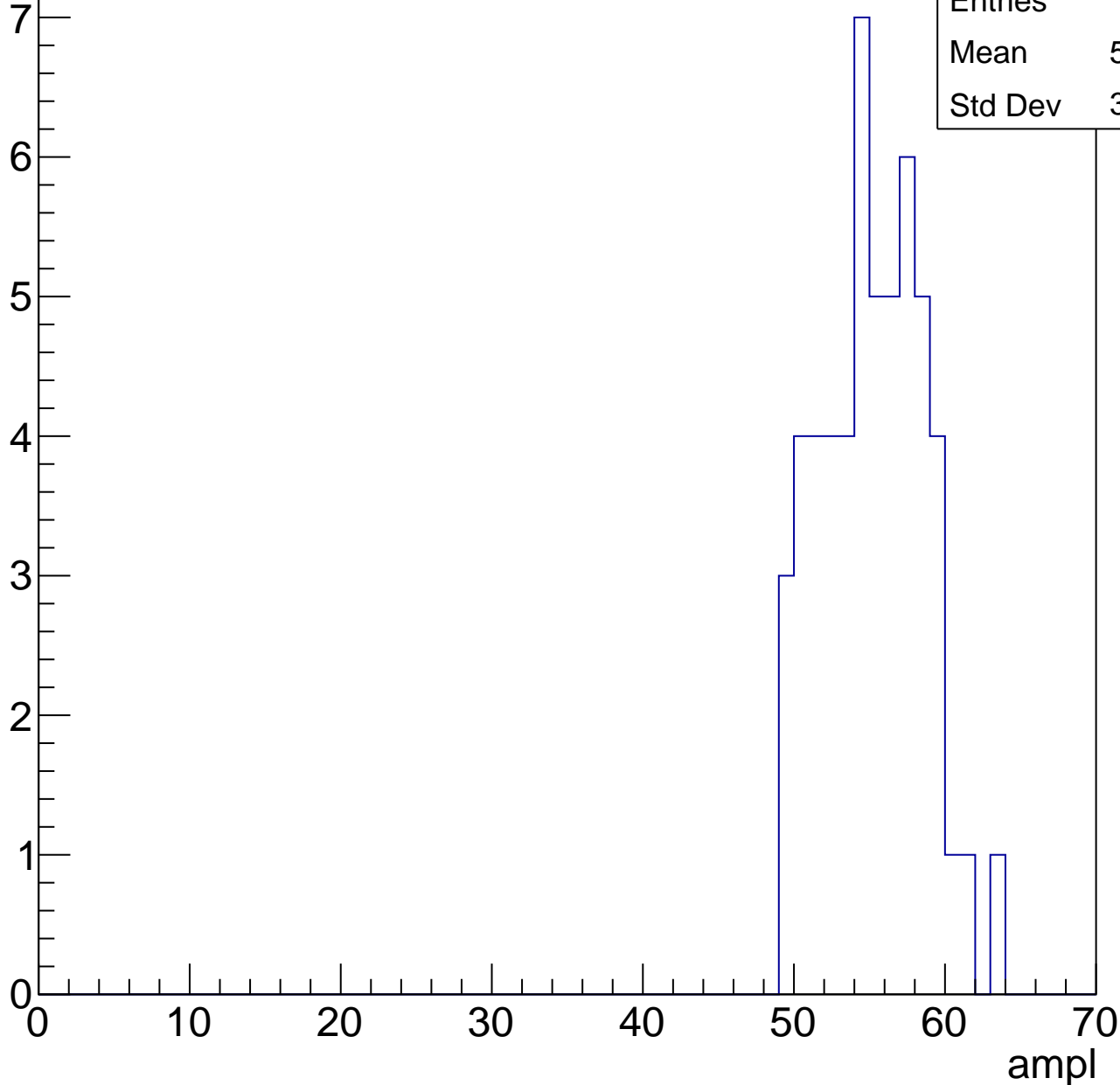


B1L103S, U24-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.74
Std Dev	3.306

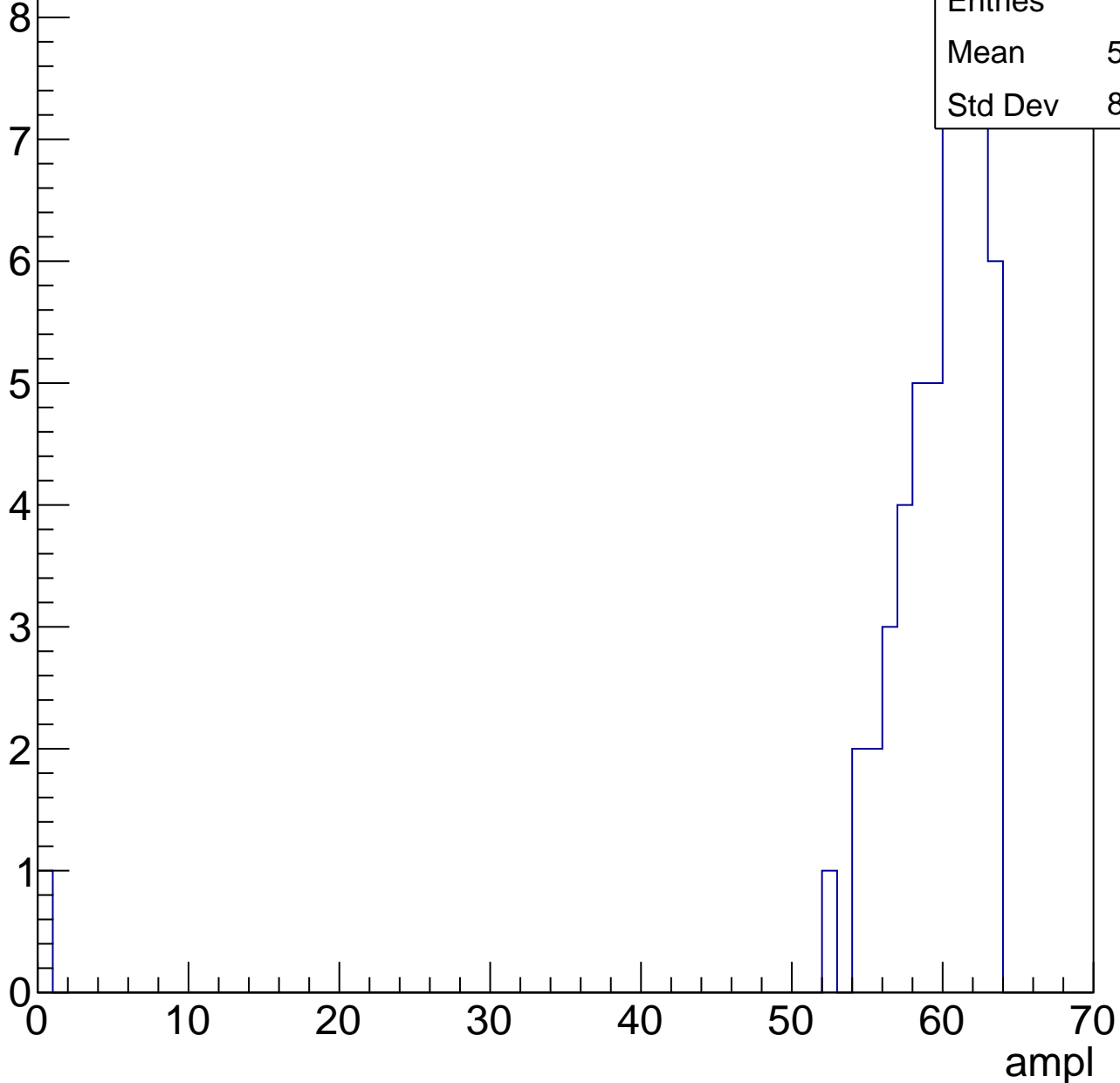


B1L103S, U24-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

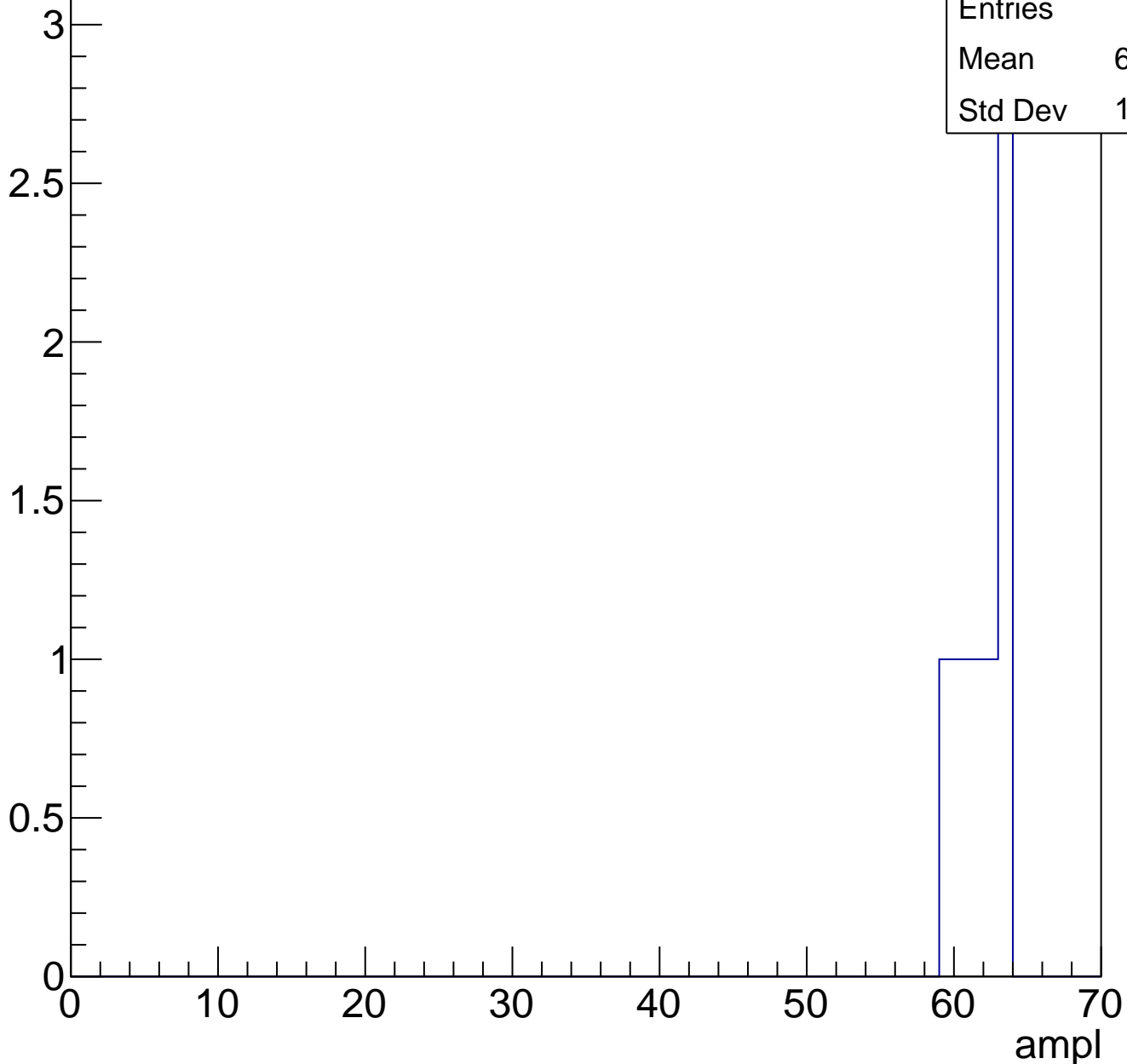
Entries	53
Mean	58.36
Std Dev	8.519



B1L103S, U24-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch120, adc0

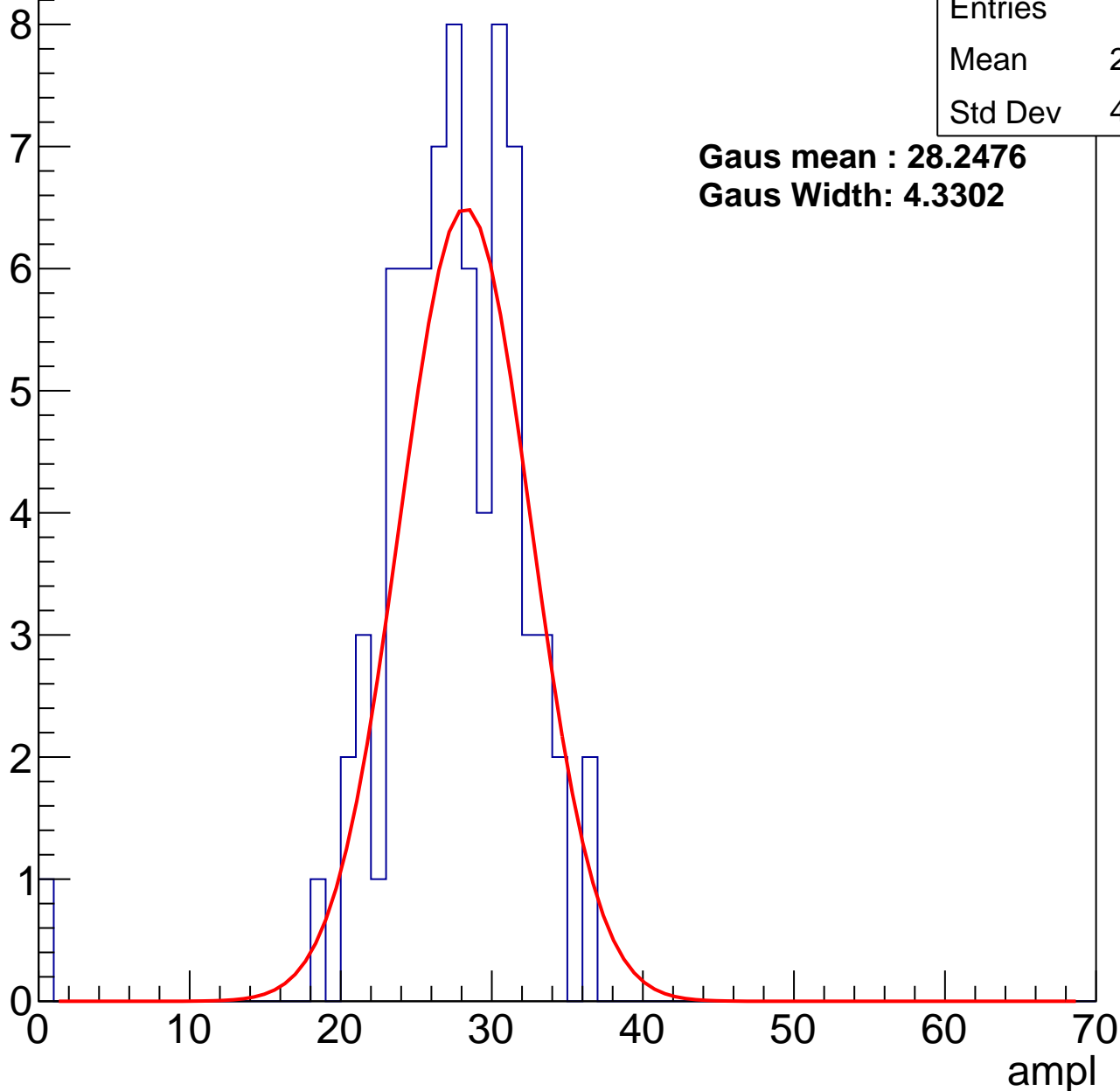
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	26.96
Std Dev	4.962

Gaus mean : 28.2476

Gaus Width: 4.3302



B1L103S, U24-ch120, adc1

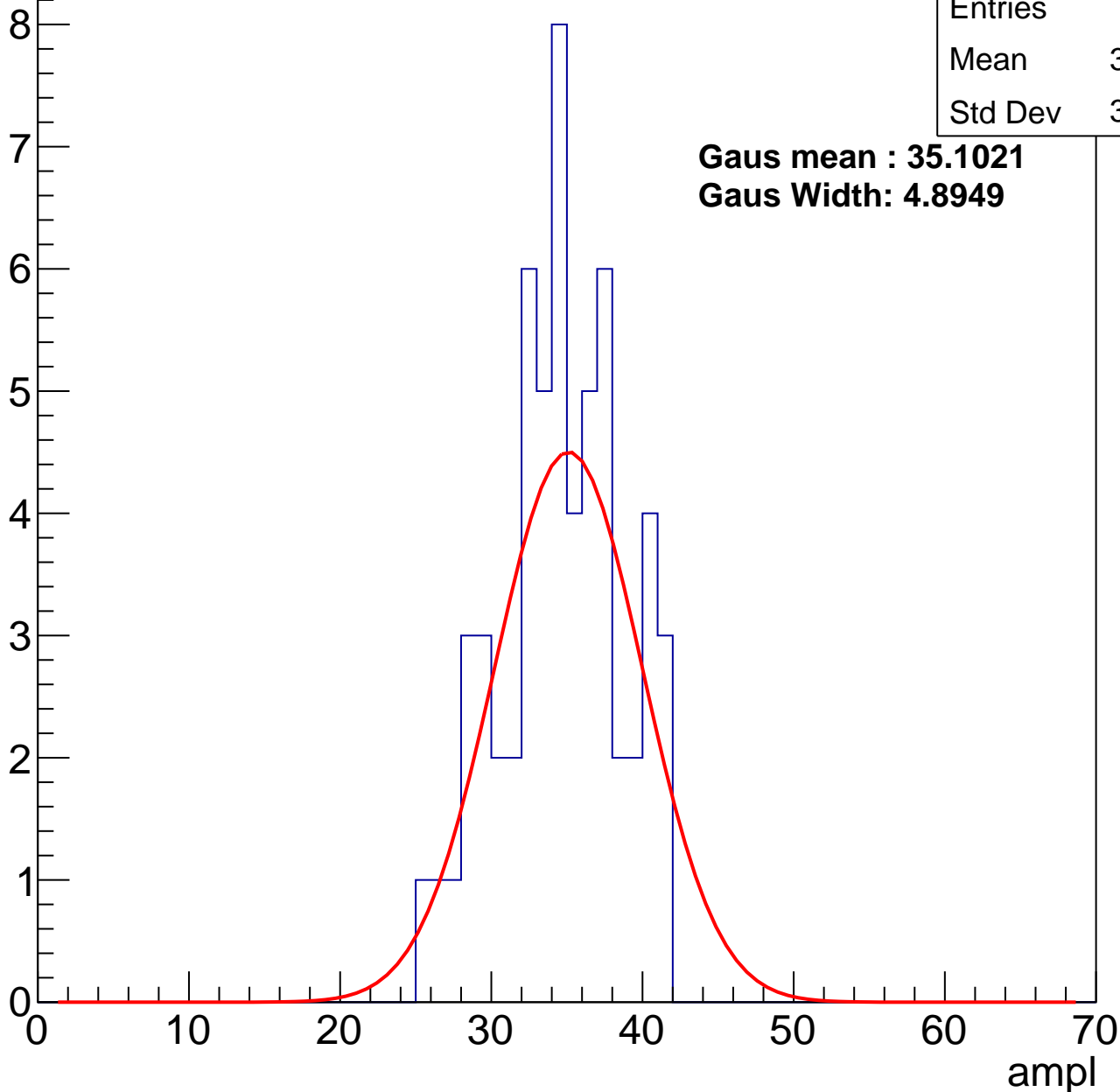
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	34.12
Std Dev	3.953

Gaus mean : 35.1021

Gaus Width: 4.8949



B1L103S, U24-ch120, adc2

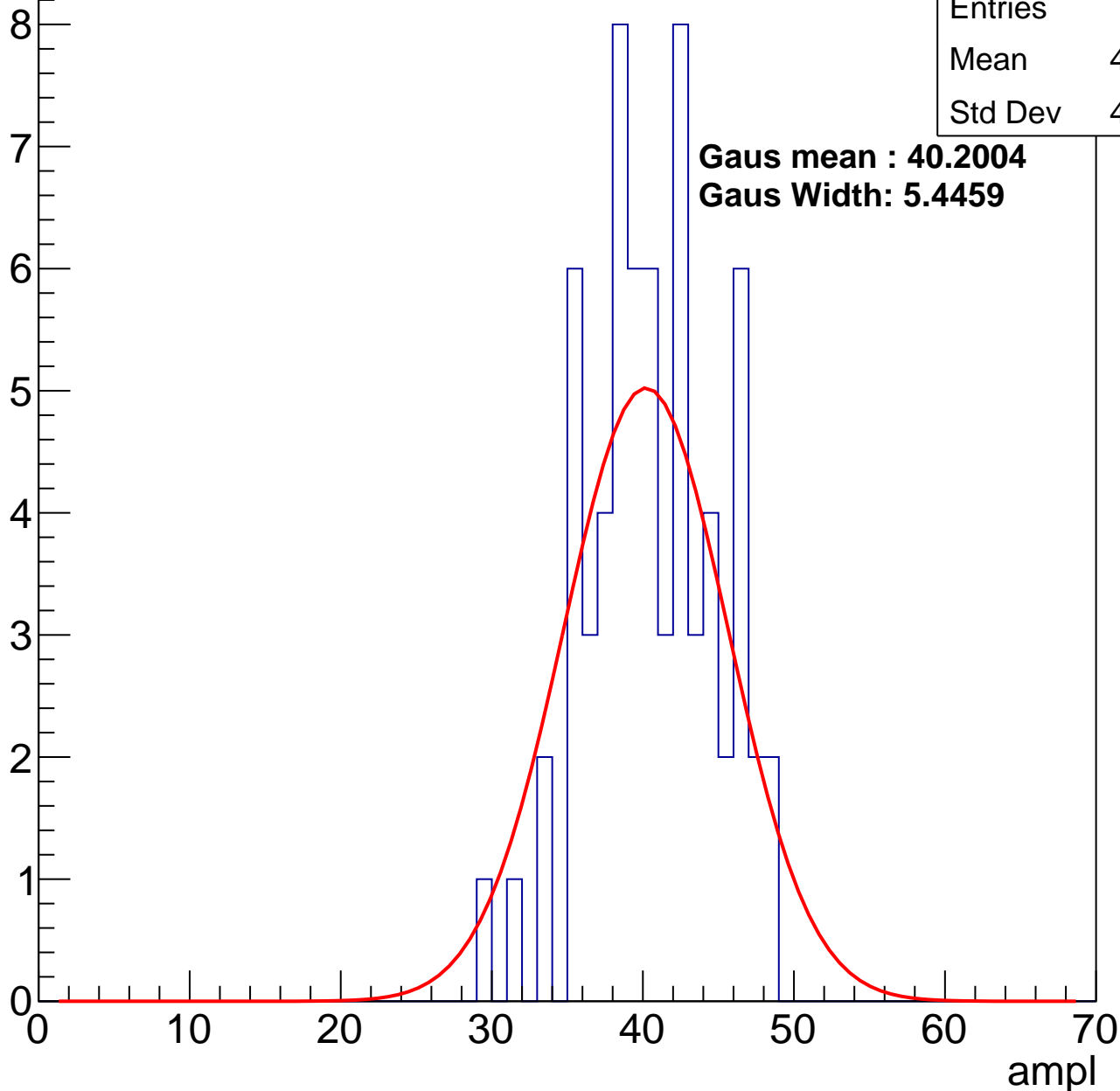
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	40.15
Std Dev	4.212

Gaus mean : 40.2004

Gaus Width: 5.4459

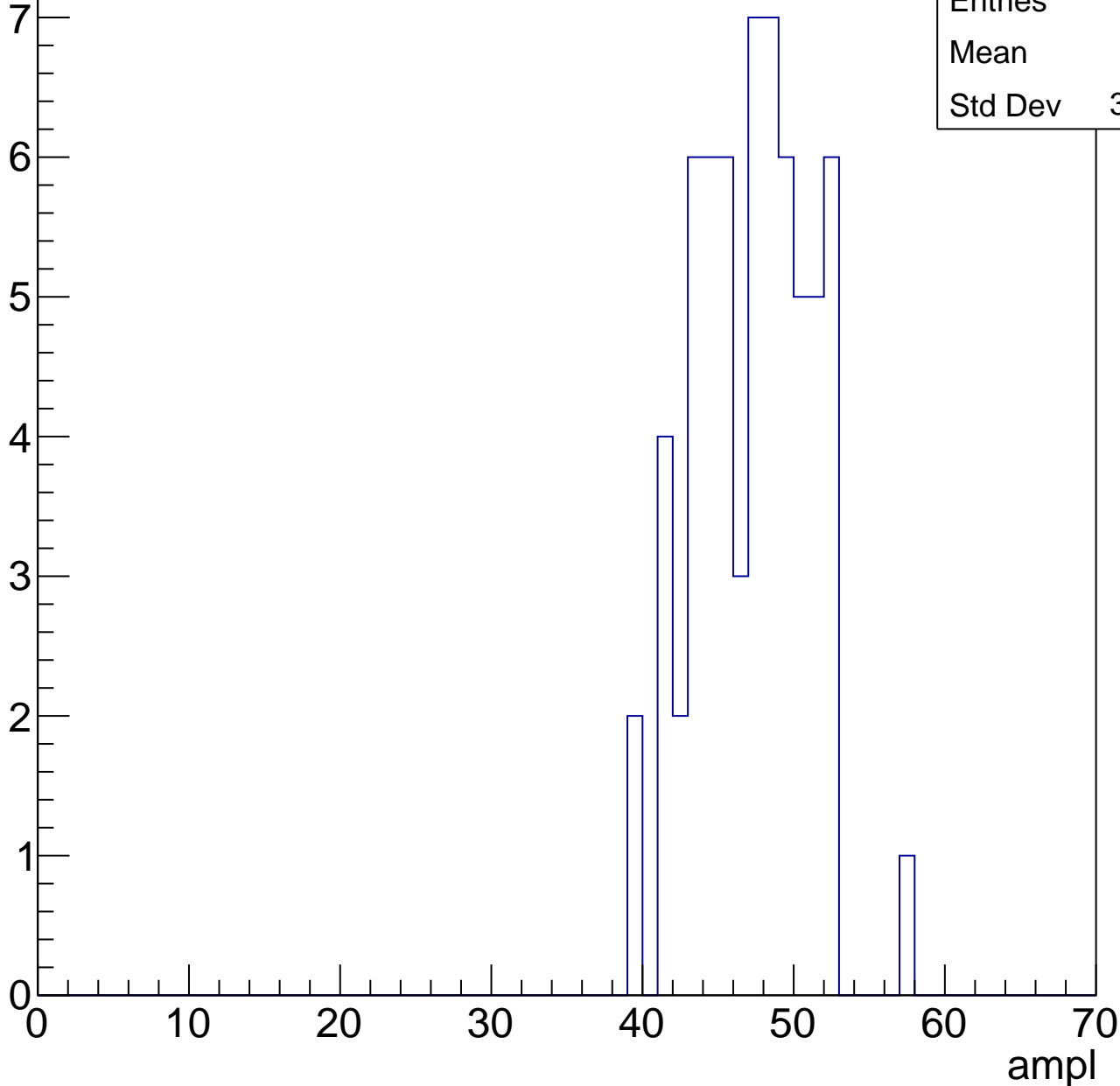


B1L103S, U24-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	46.8
Std Dev	3.698

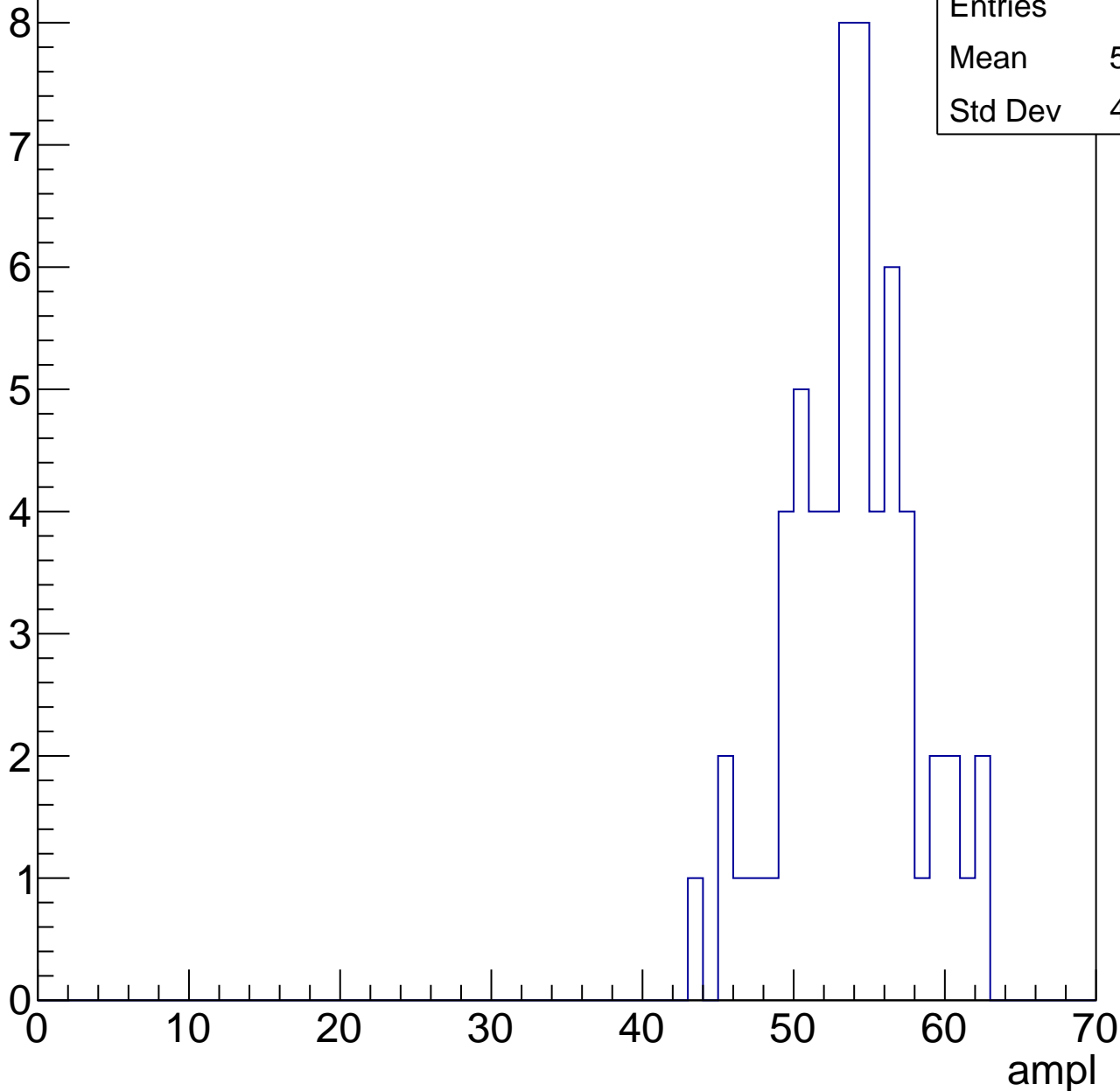


B1L103S, U24-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	53.33
Std Dev	4.096

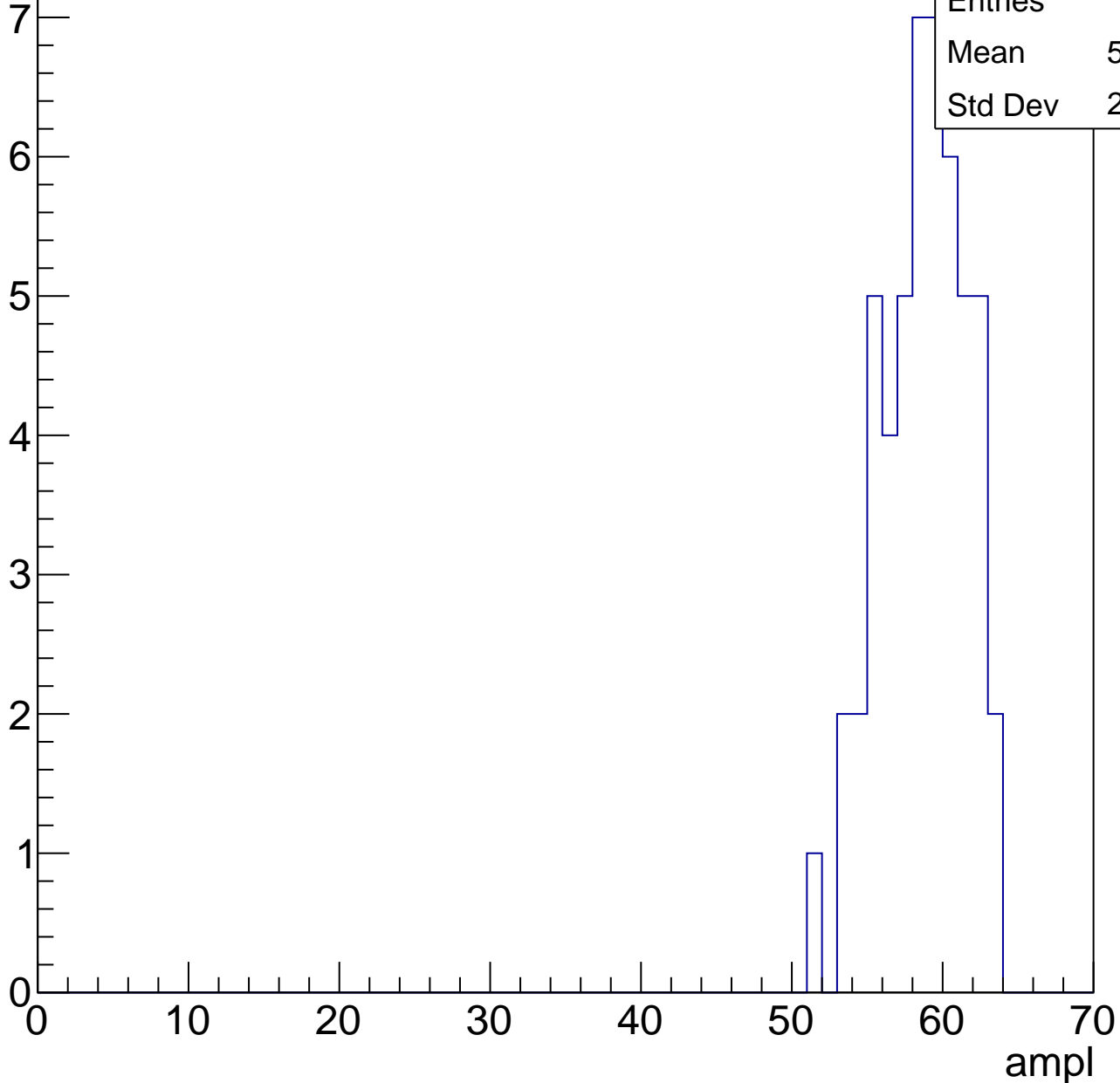


B1L103S, U24-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.22
Std Dev	2.803

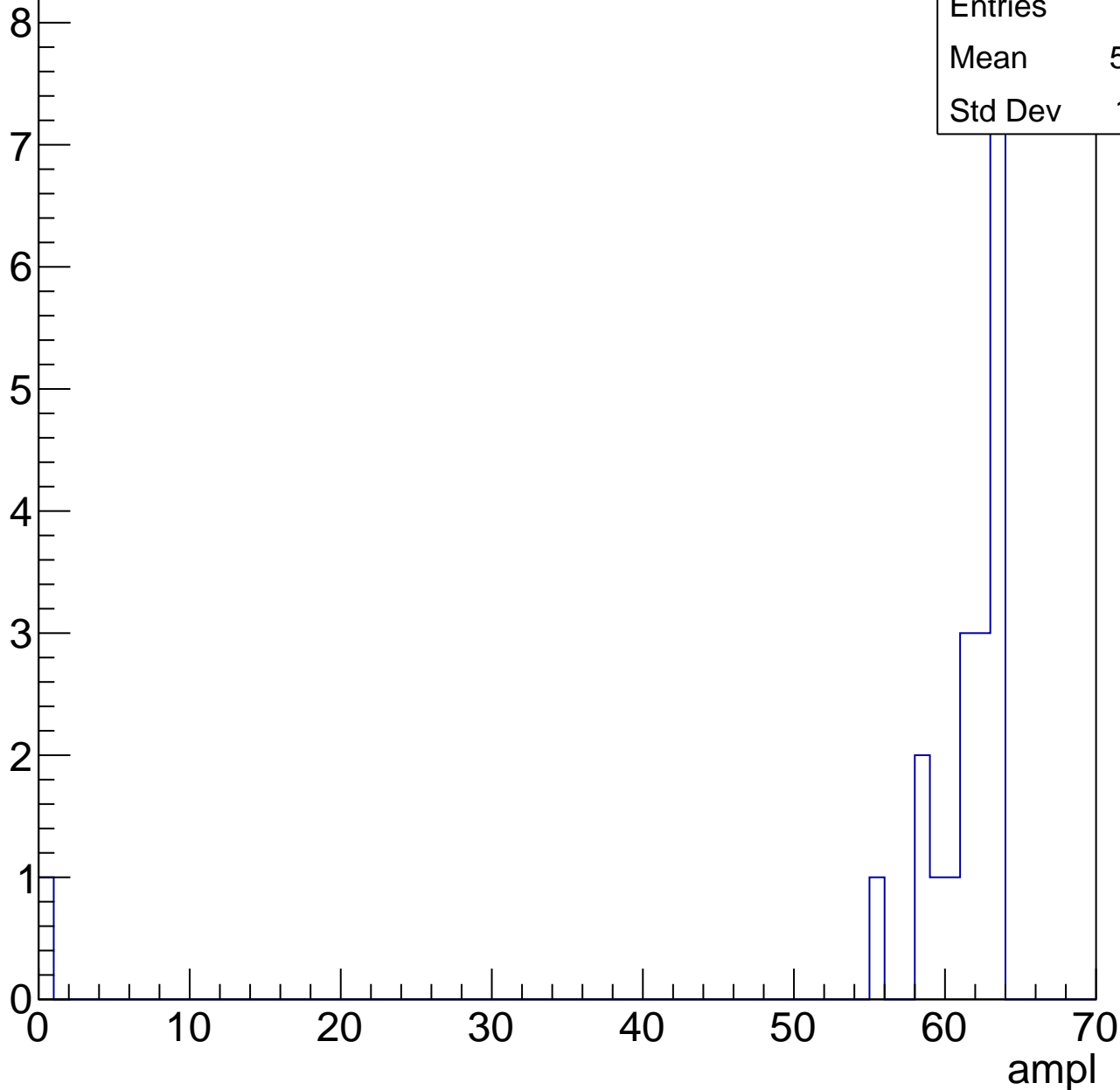


B1L103S, U24-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

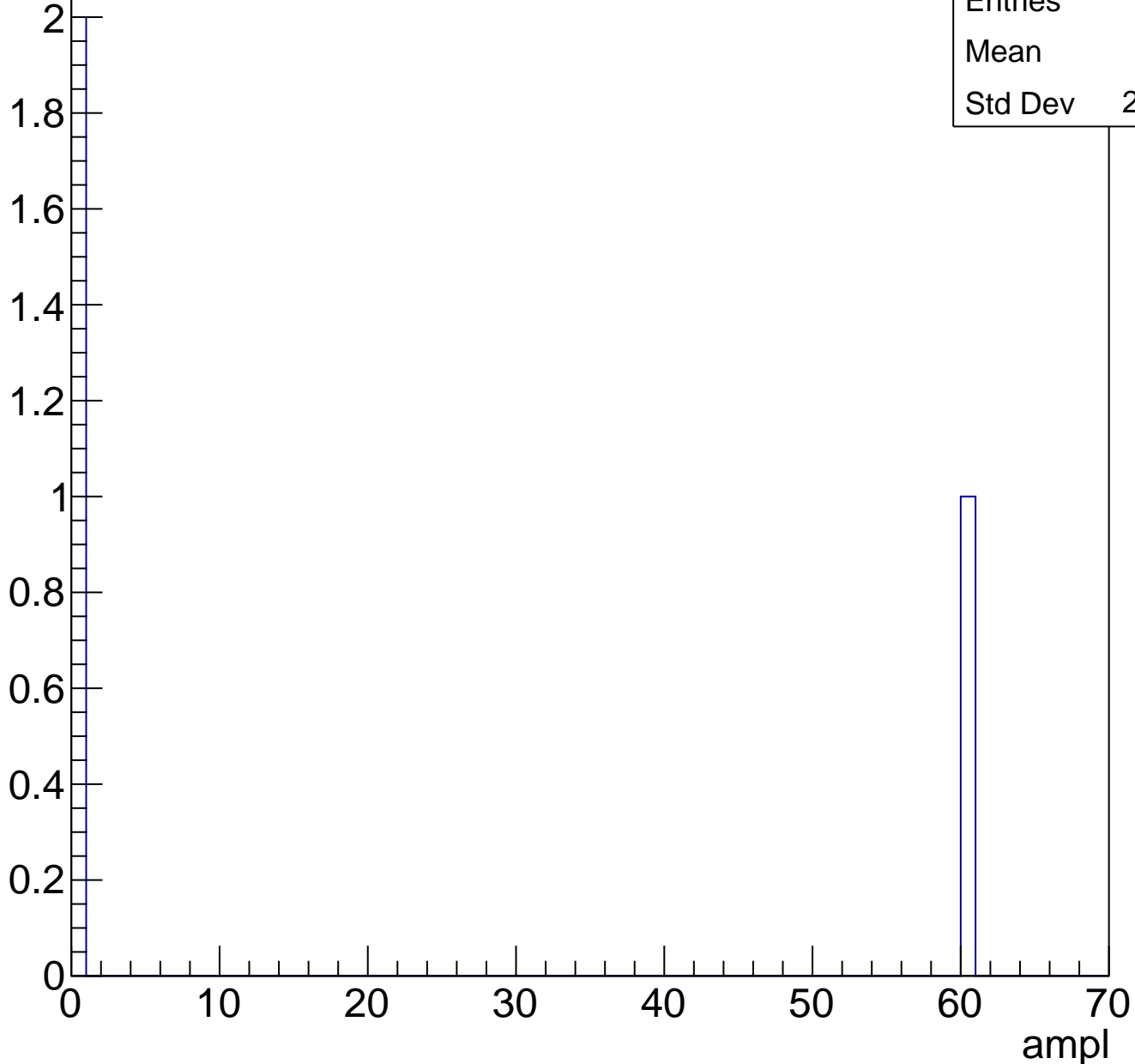
Entries	20
Mean	58.15
Std Dev	13.51



B1L103S, U24-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20
Std Dev	28.28

B1L103S, U24-ch121, adc0

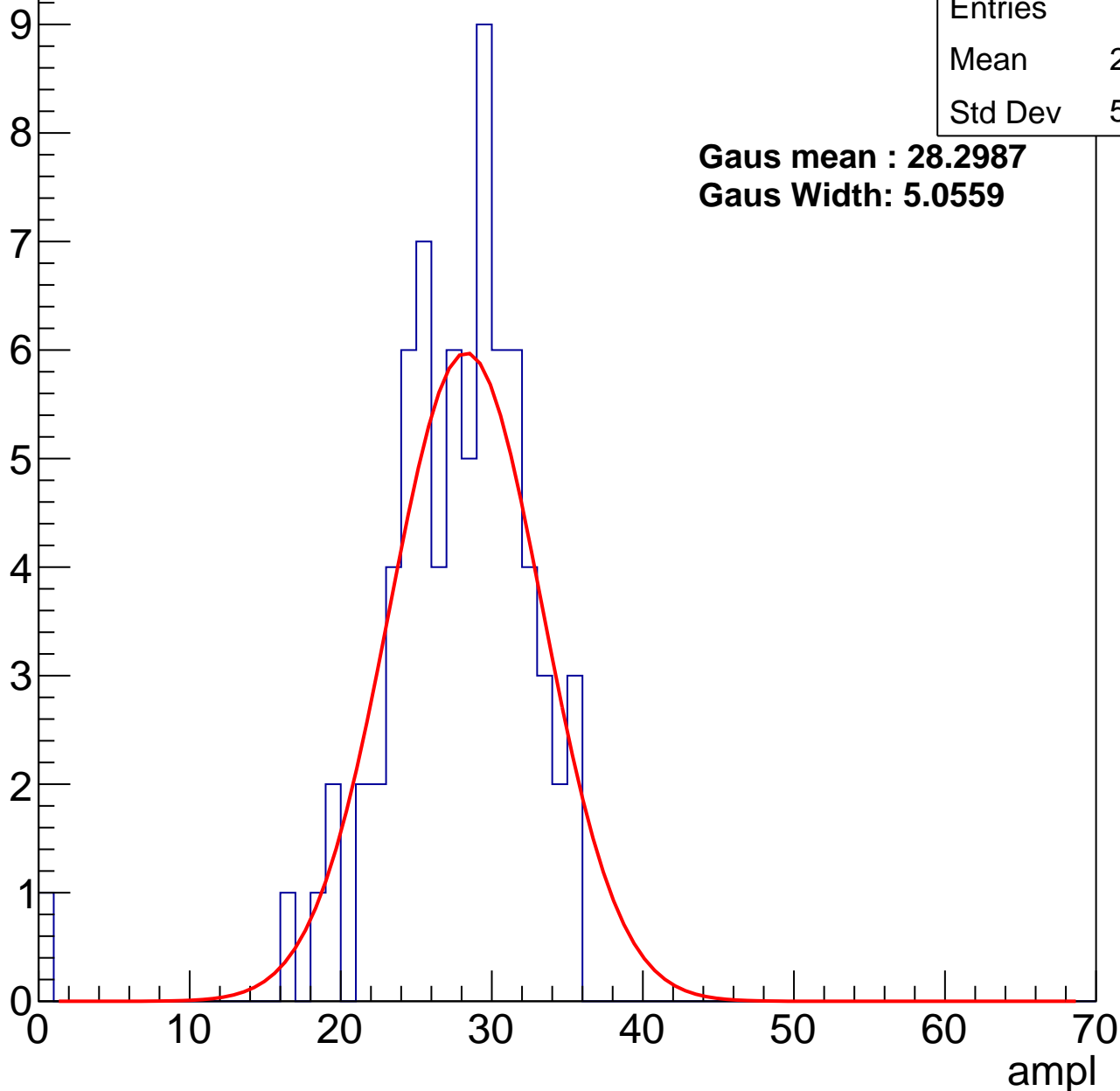
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.05
Std Dev	5.224

Gaus mean : 28.2987

Gaus Width: 5.0559



B1L103S, U24-ch121, adc1

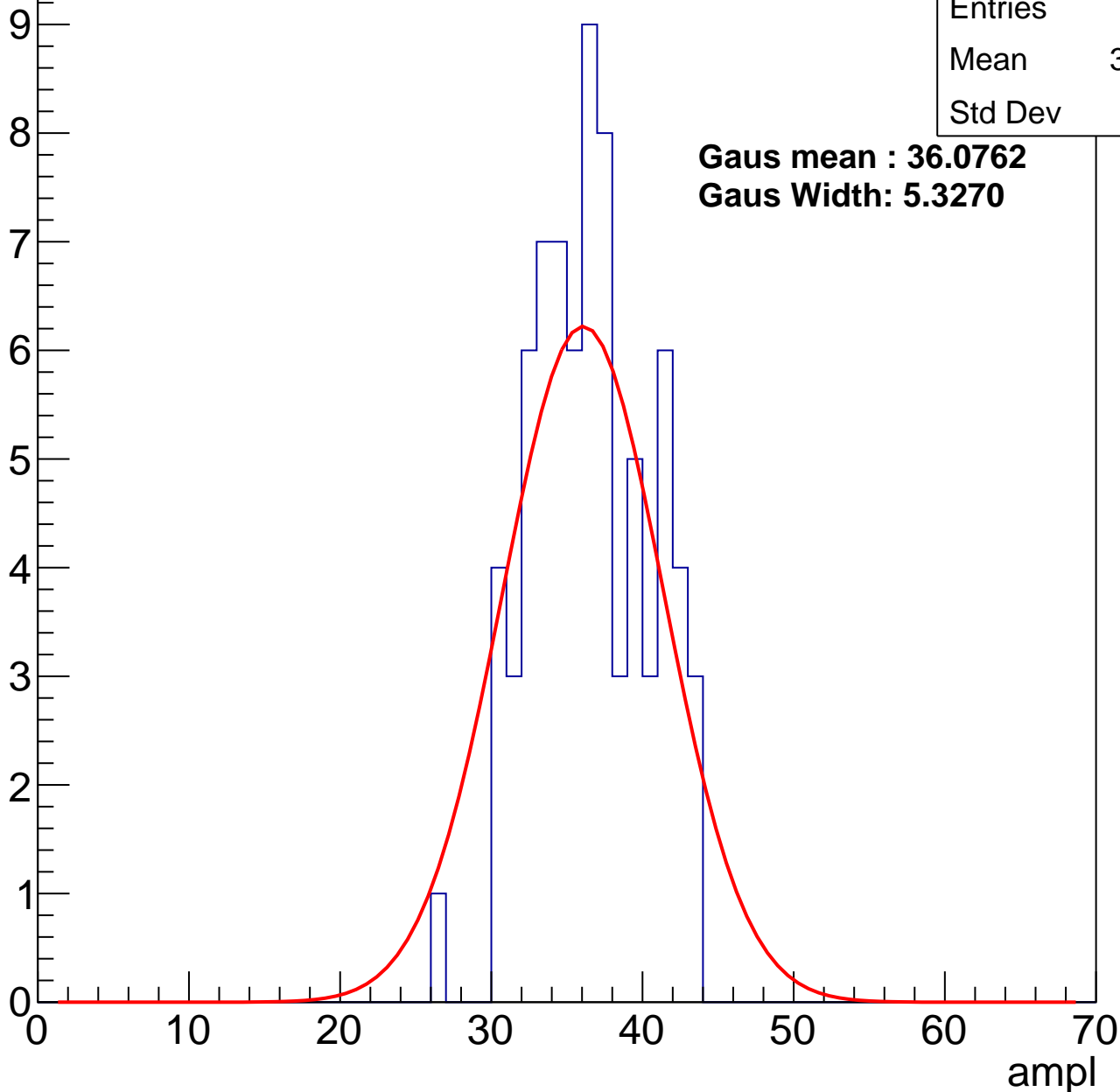
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.03
Std Dev	3.77

Gaus mean : 36.0762

Gaus Width: 5.3270

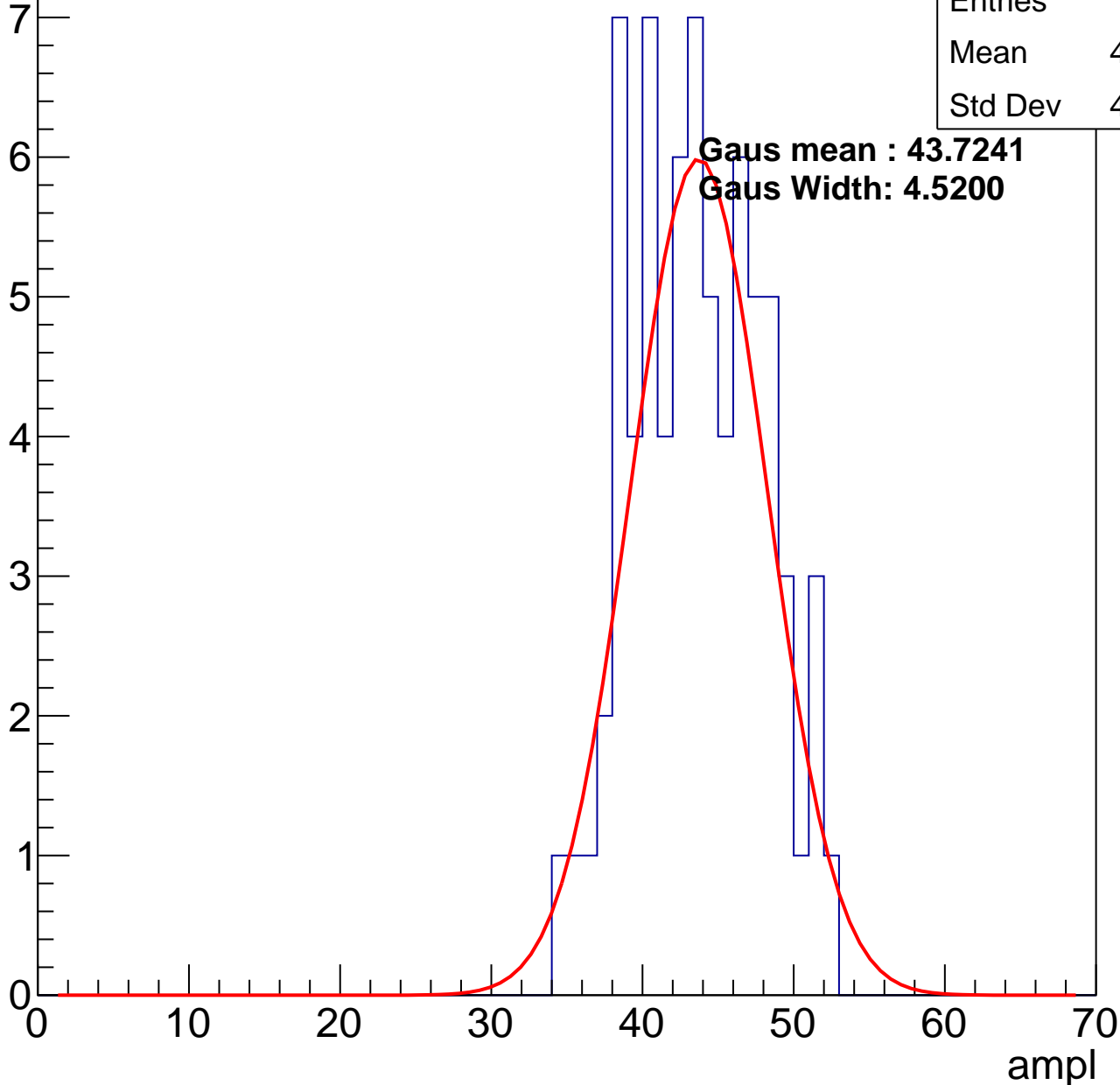


B1L103S, U24-ch121, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.16
Std Dev	4.217

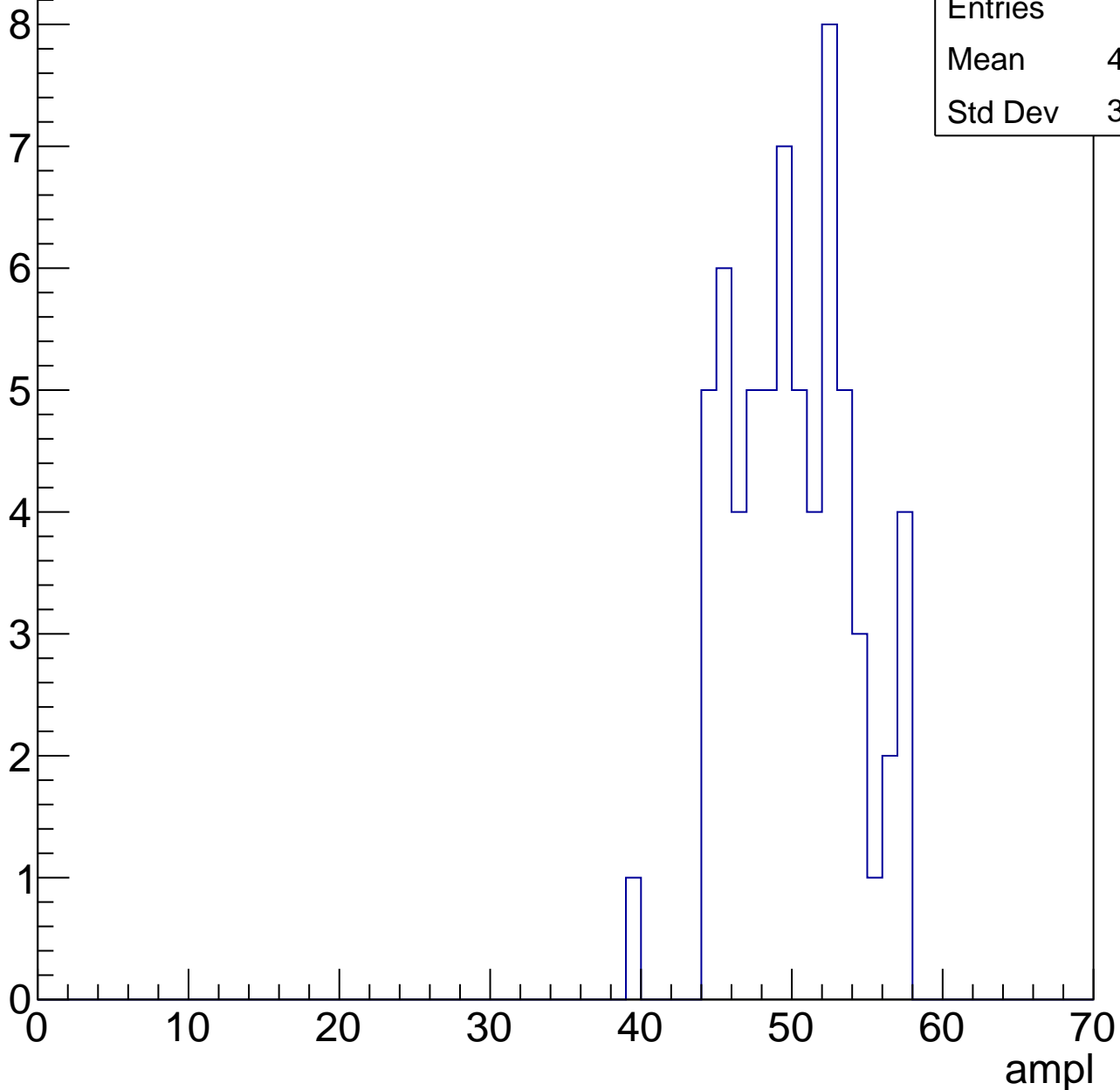


B1L103S, U24-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.58
Std Dev	3.918

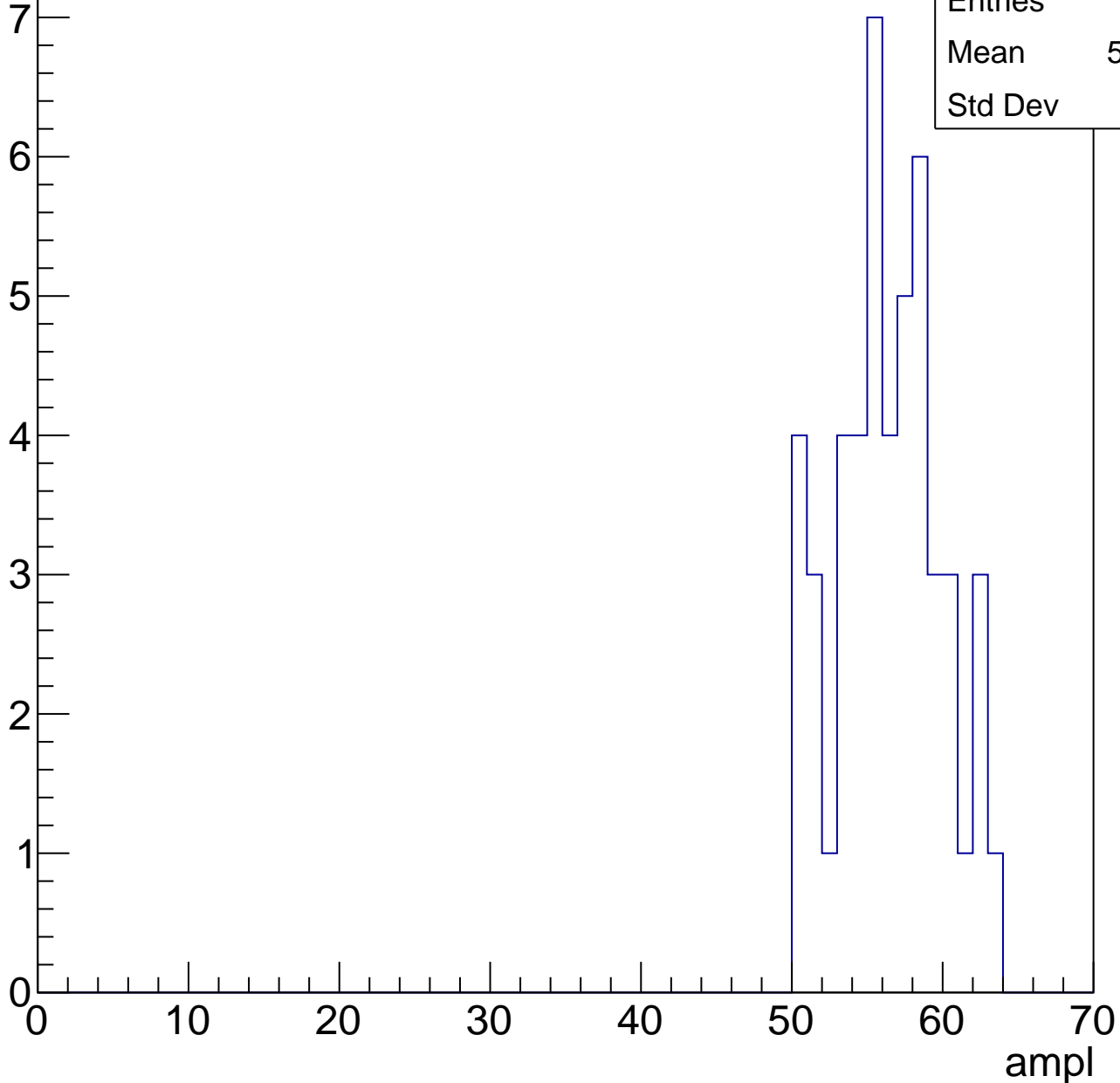


B1L103S, U24-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.96
Std Dev	3.44

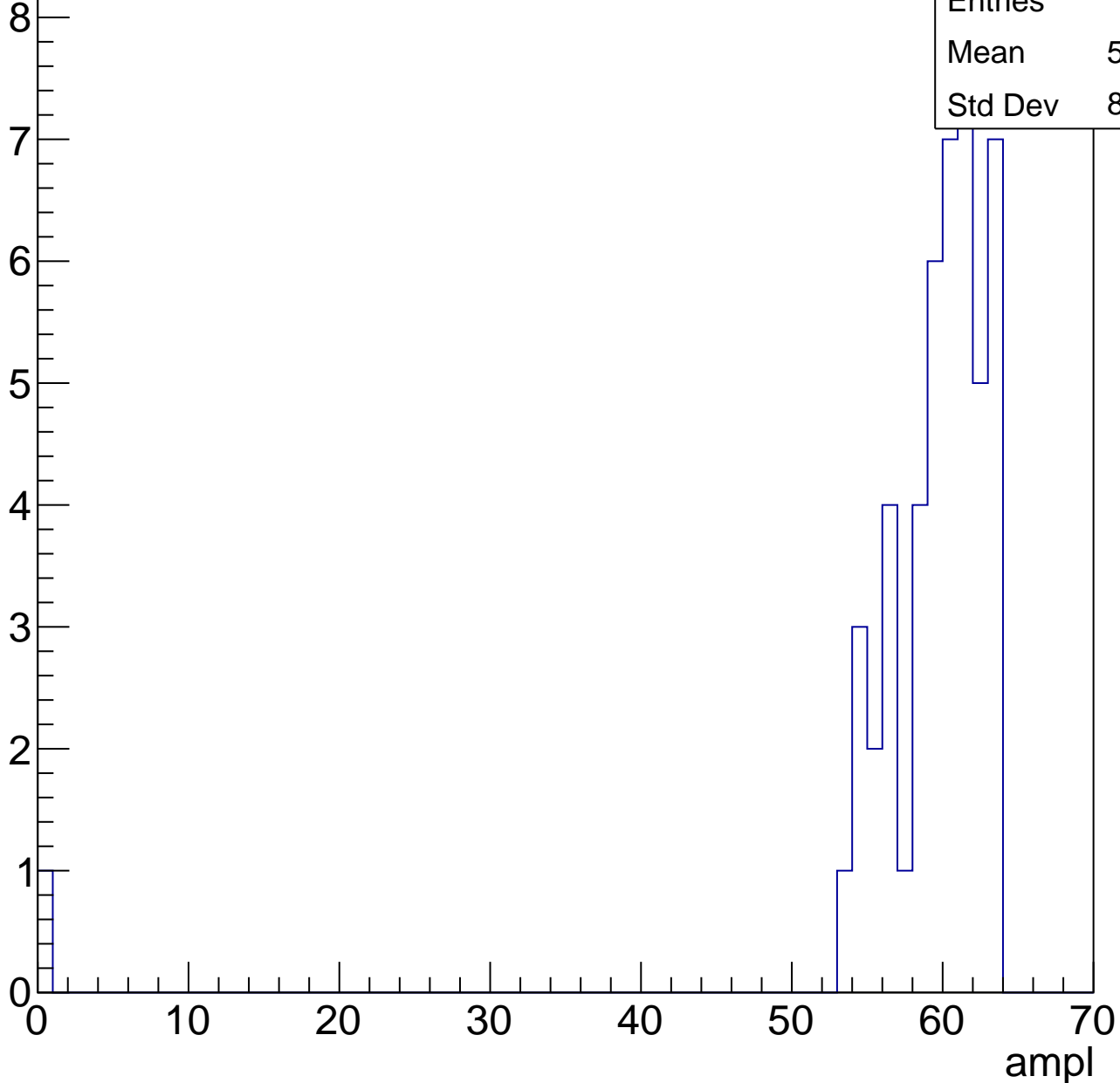


B1L103S, U24-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.18
Std Dev	8.843



B1L103S, U24-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

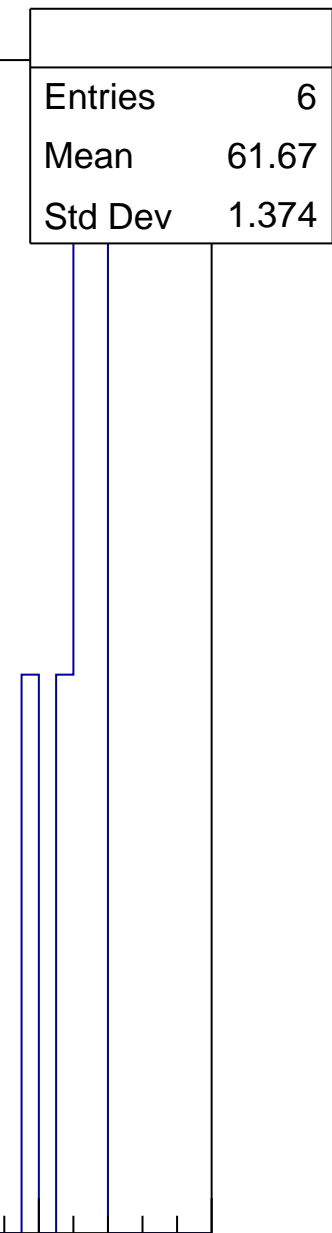
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.374

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch122, adc0

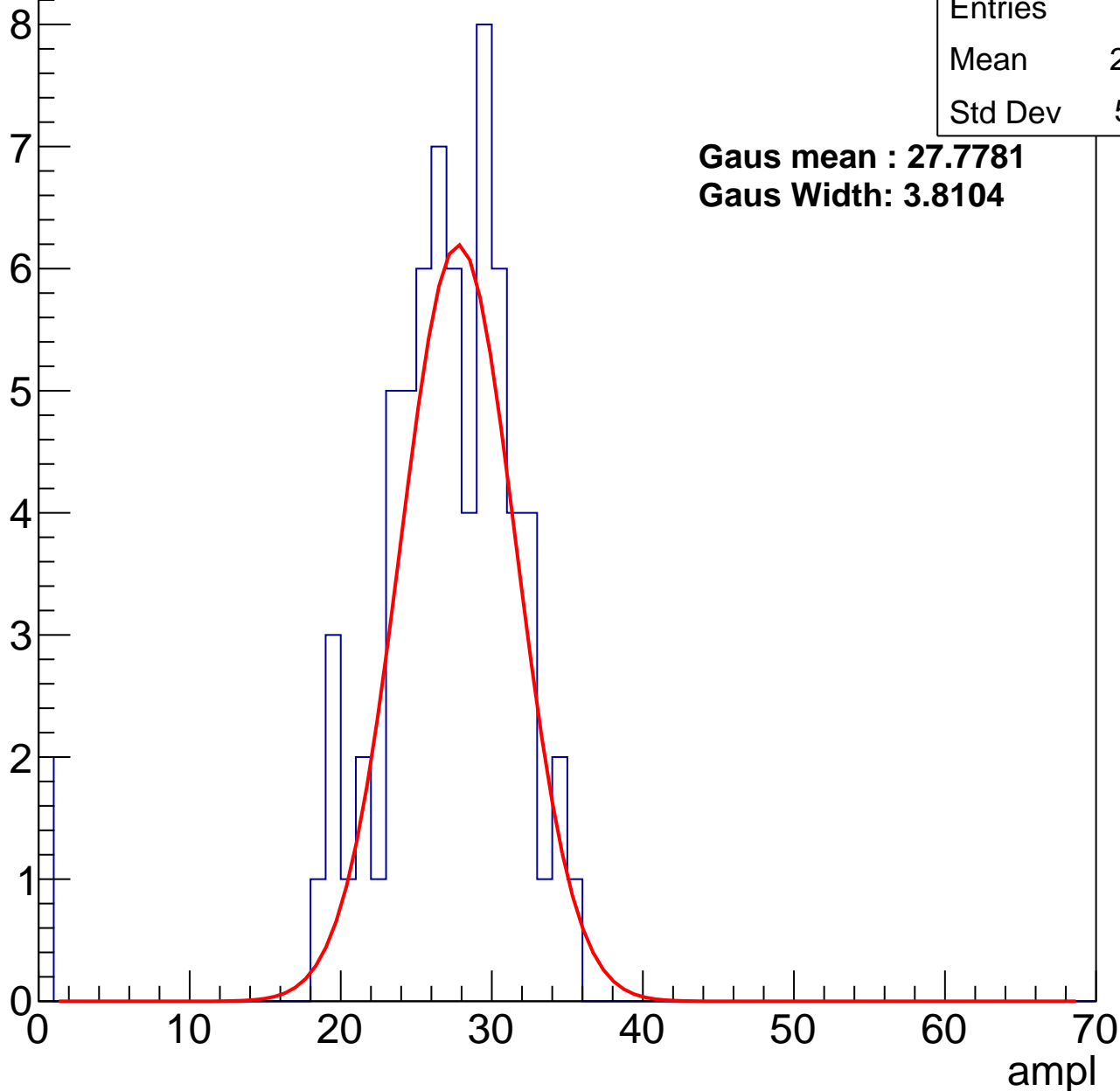
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.09
Std Dev	5.931

Gaus mean : 27.7781

Gaus Width: 3.8104



B1L103S, U24-ch122, adc1

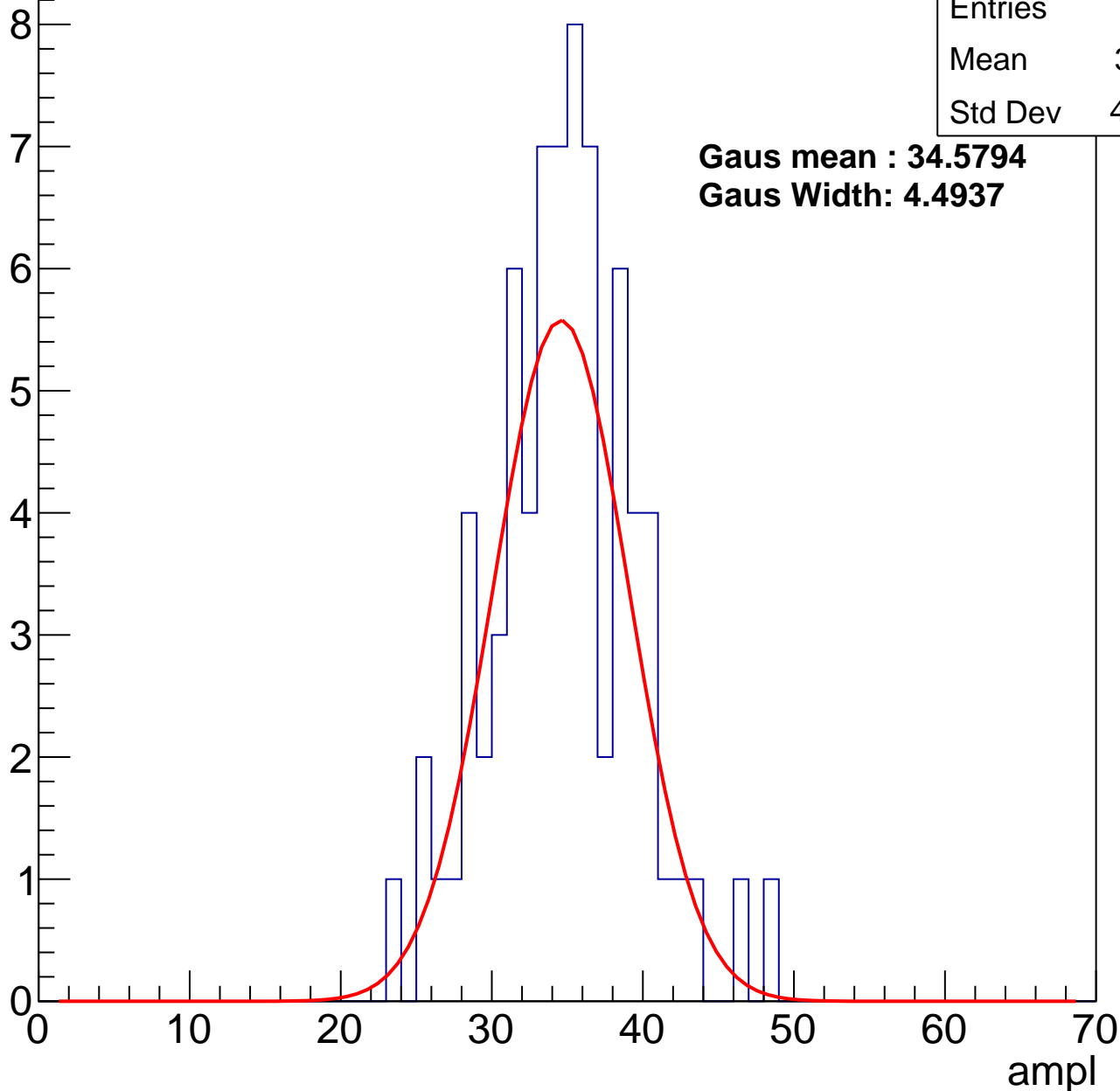
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	34.31
Std Dev	4.702

Gaus mean : 34.5794

Gaus Width: 4.4937



B1L103S, U24-ch122, adc2

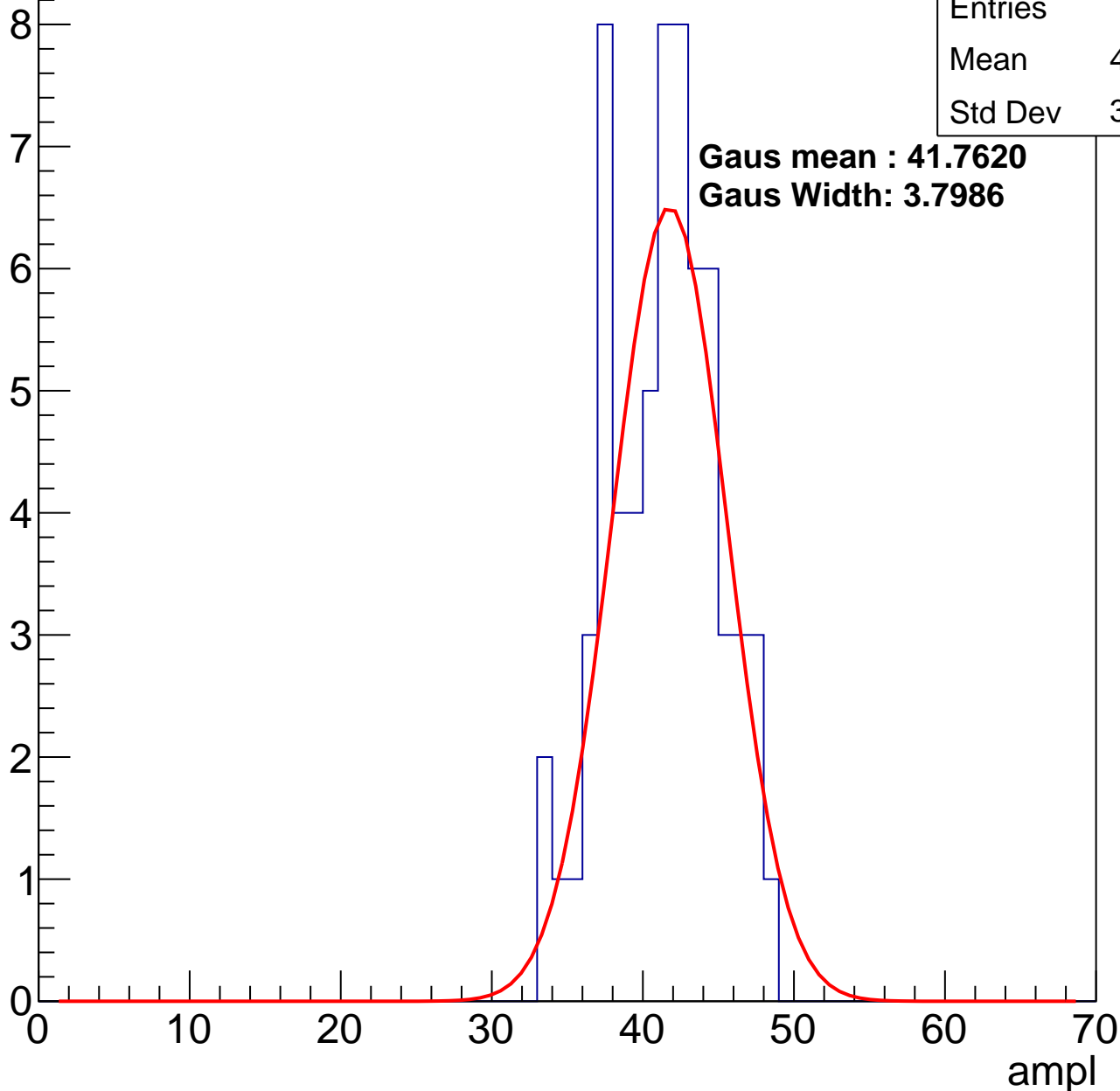
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	40.83
Std Dev	3.562

Gaus mean : 41.7620

Gaus Width: 3.7986

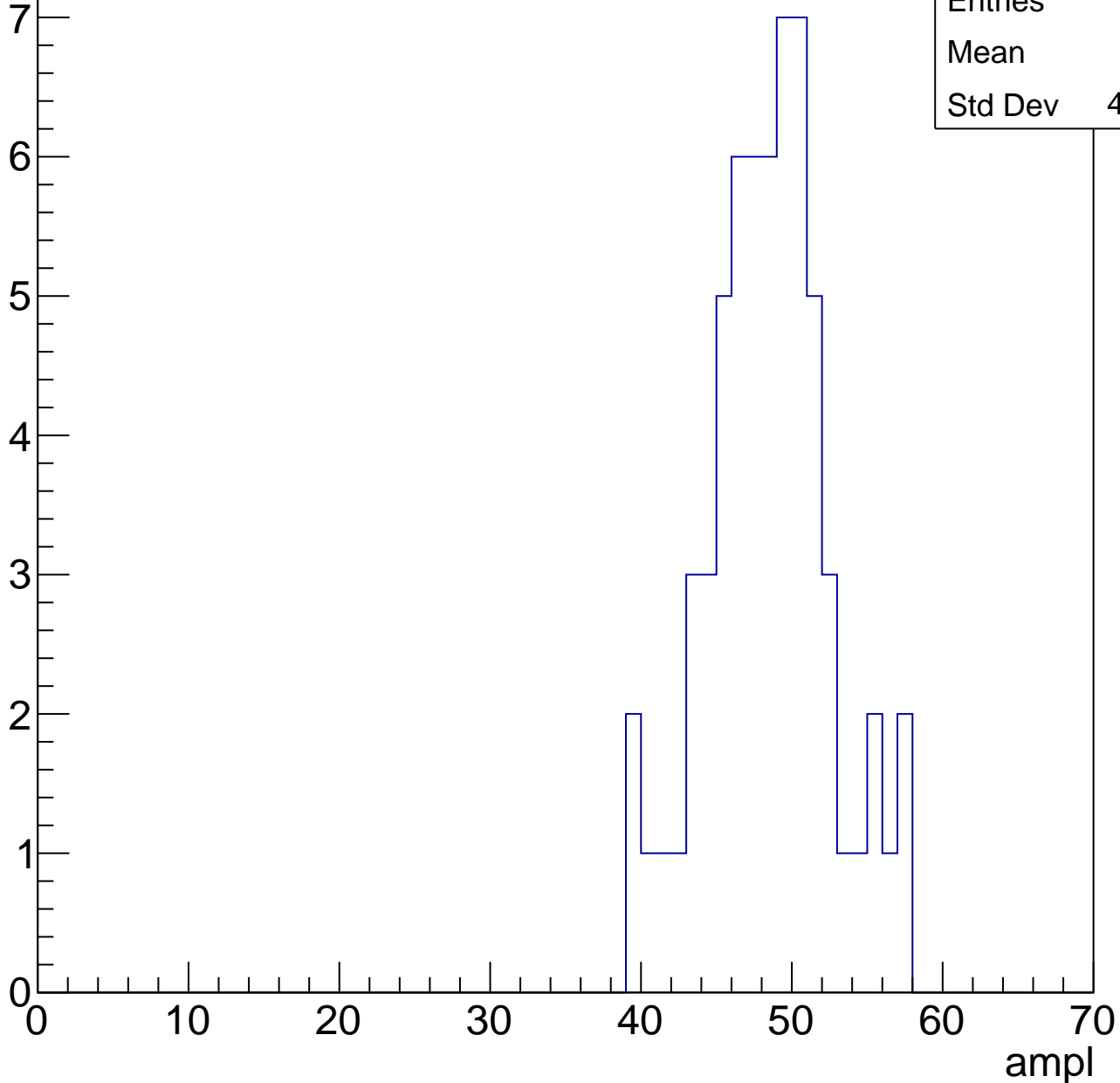


B1L103S, U24-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48
Std Dev	4.028

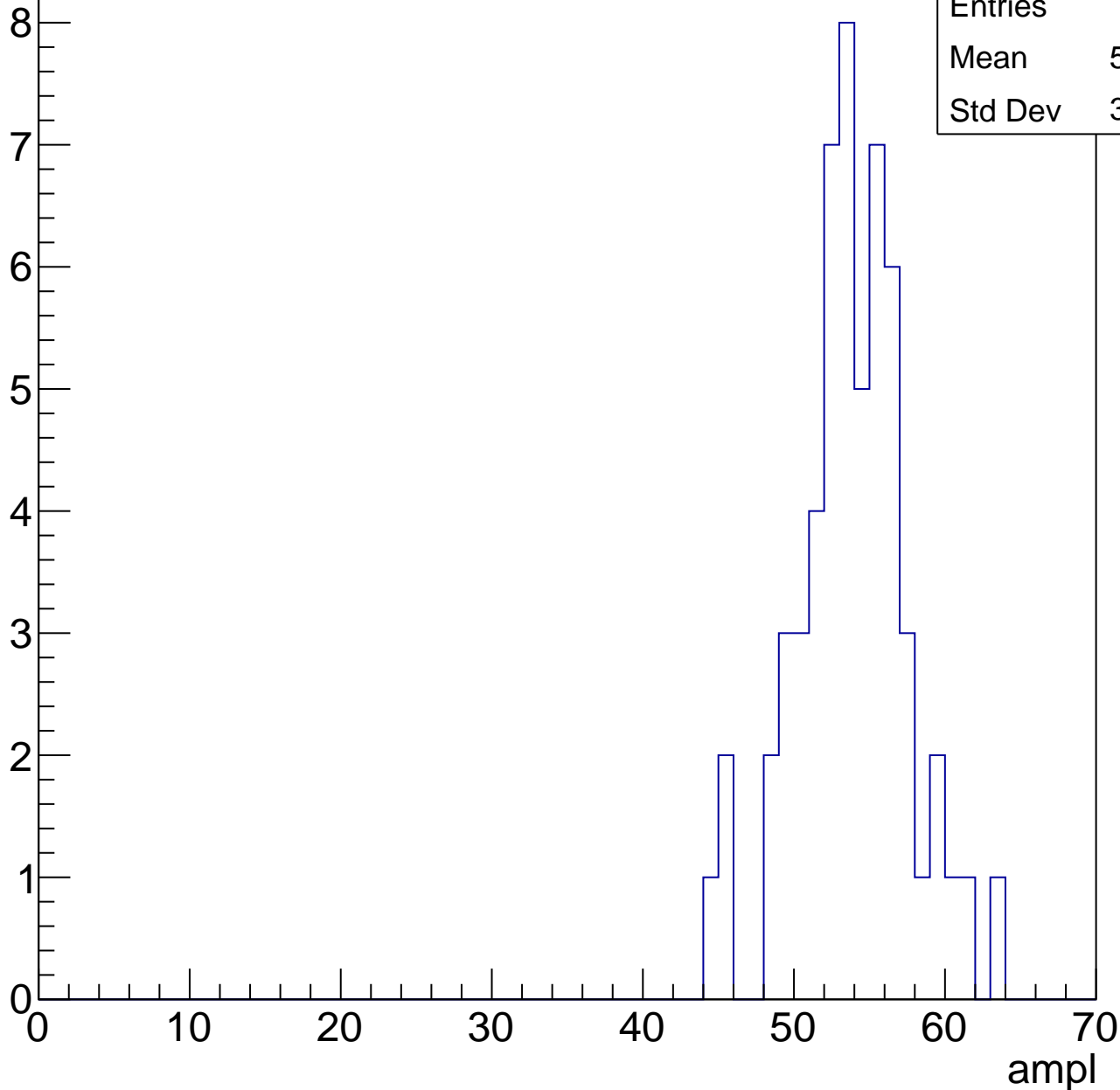


B1L103S, U24-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	53.35
Std Dev	3.735

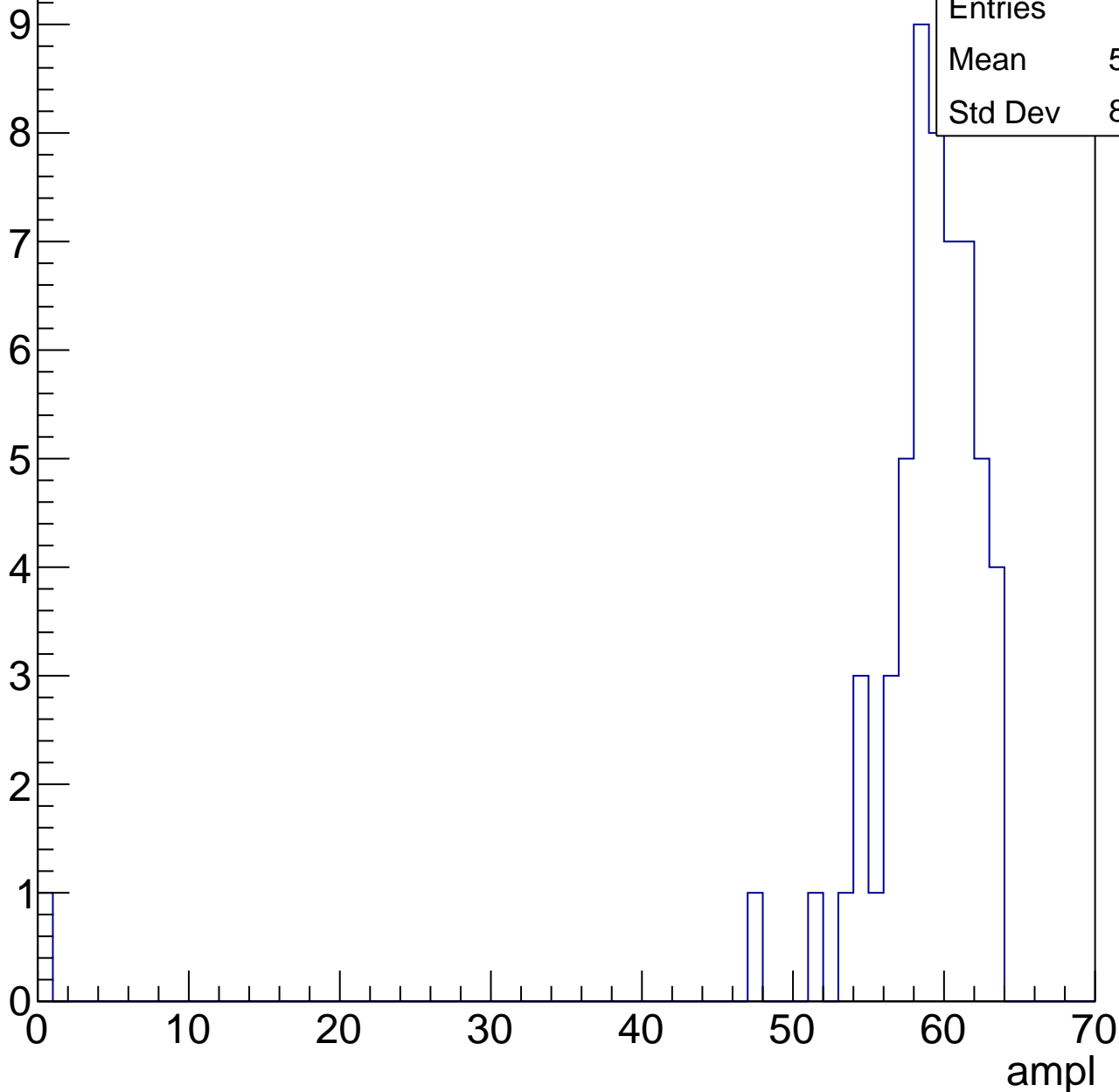


B1L103S, U24-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	57.57
Std Dev	8.347

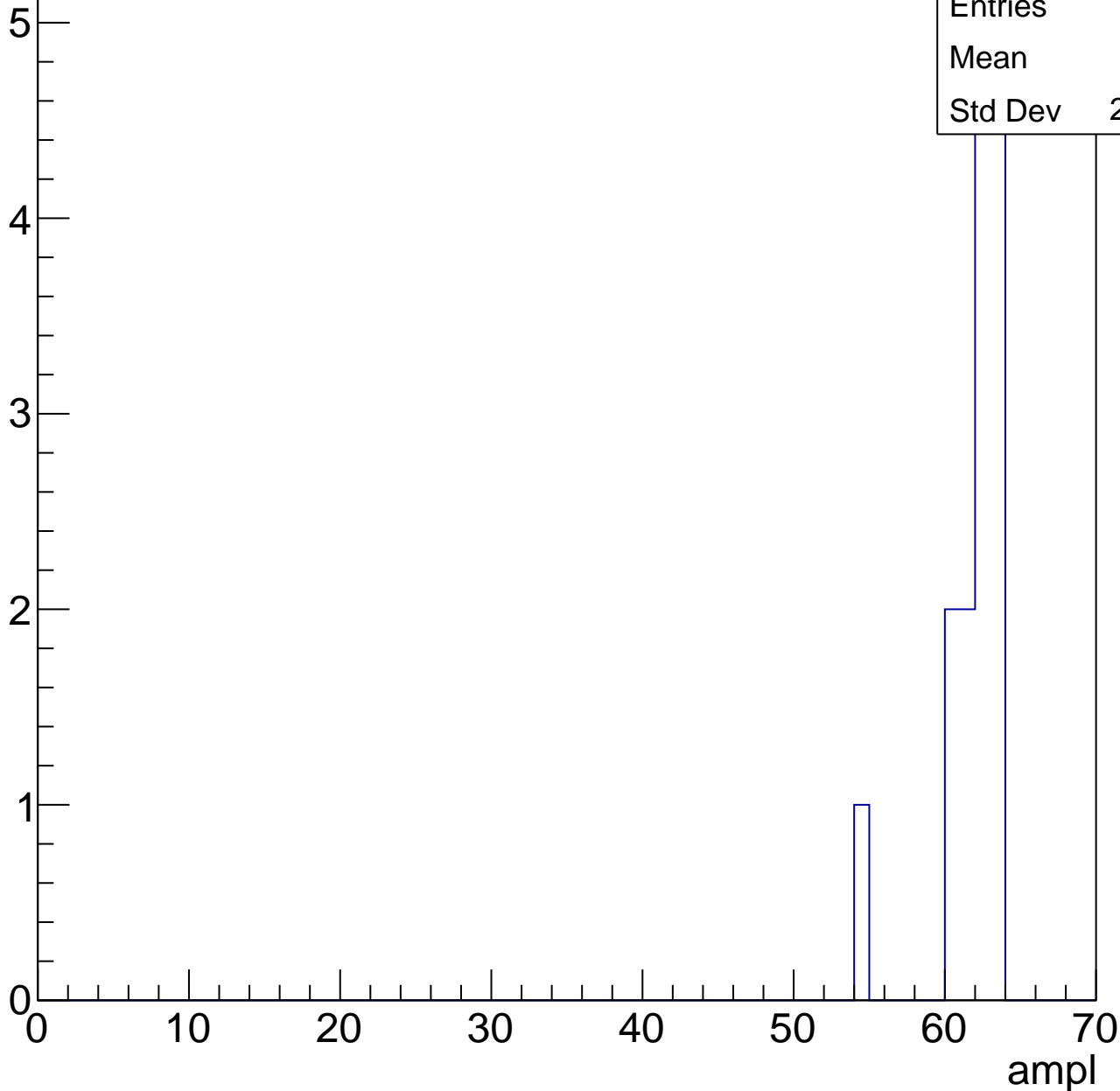


B1L103S, U24-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.4
Std Dev	2.215



B1L103S, U24-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch123, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	27.26
Std Dev	4.806

Gaus mean : 28.2677

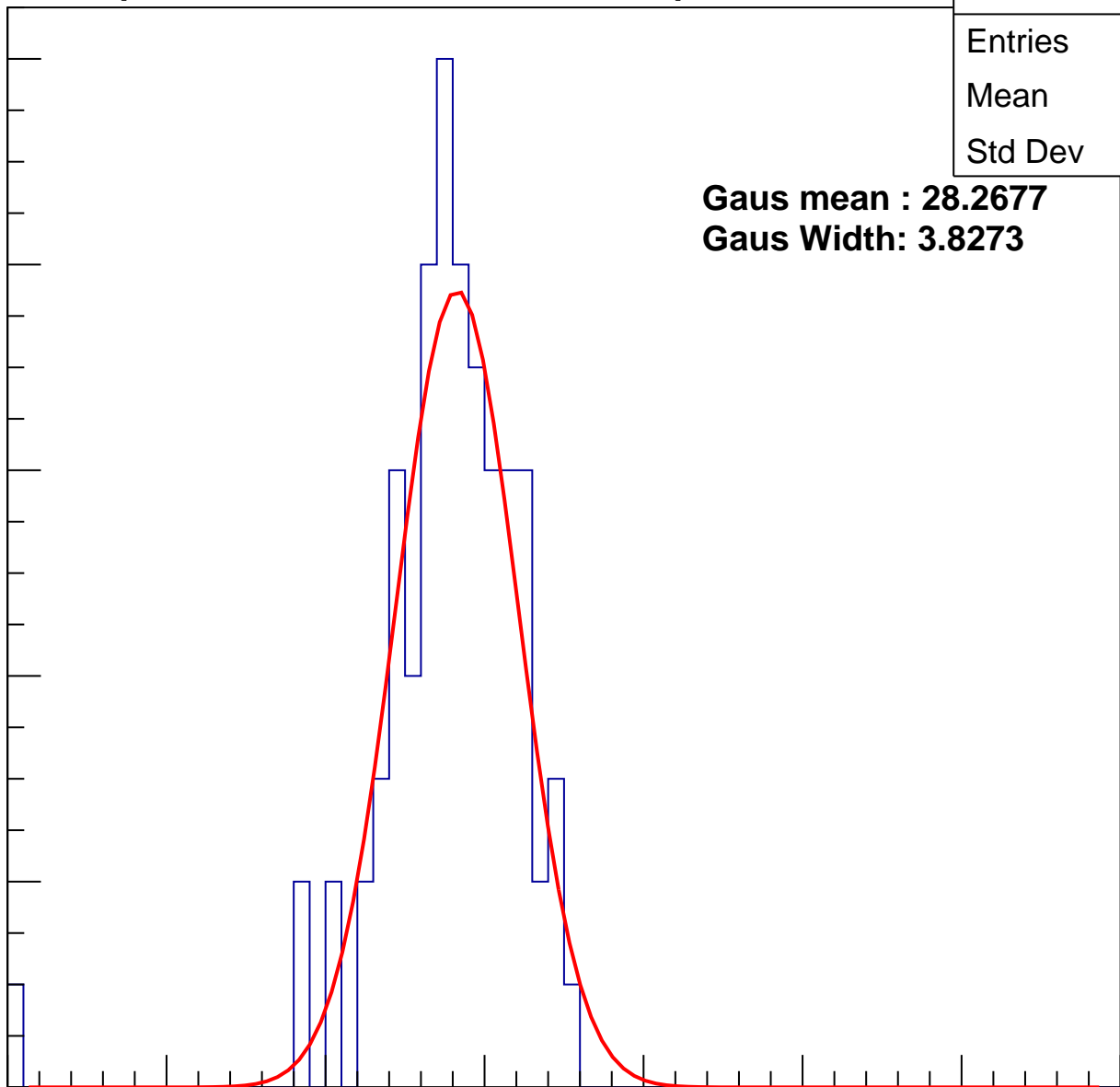
Gaus Width: 3.8273

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch123, adc1

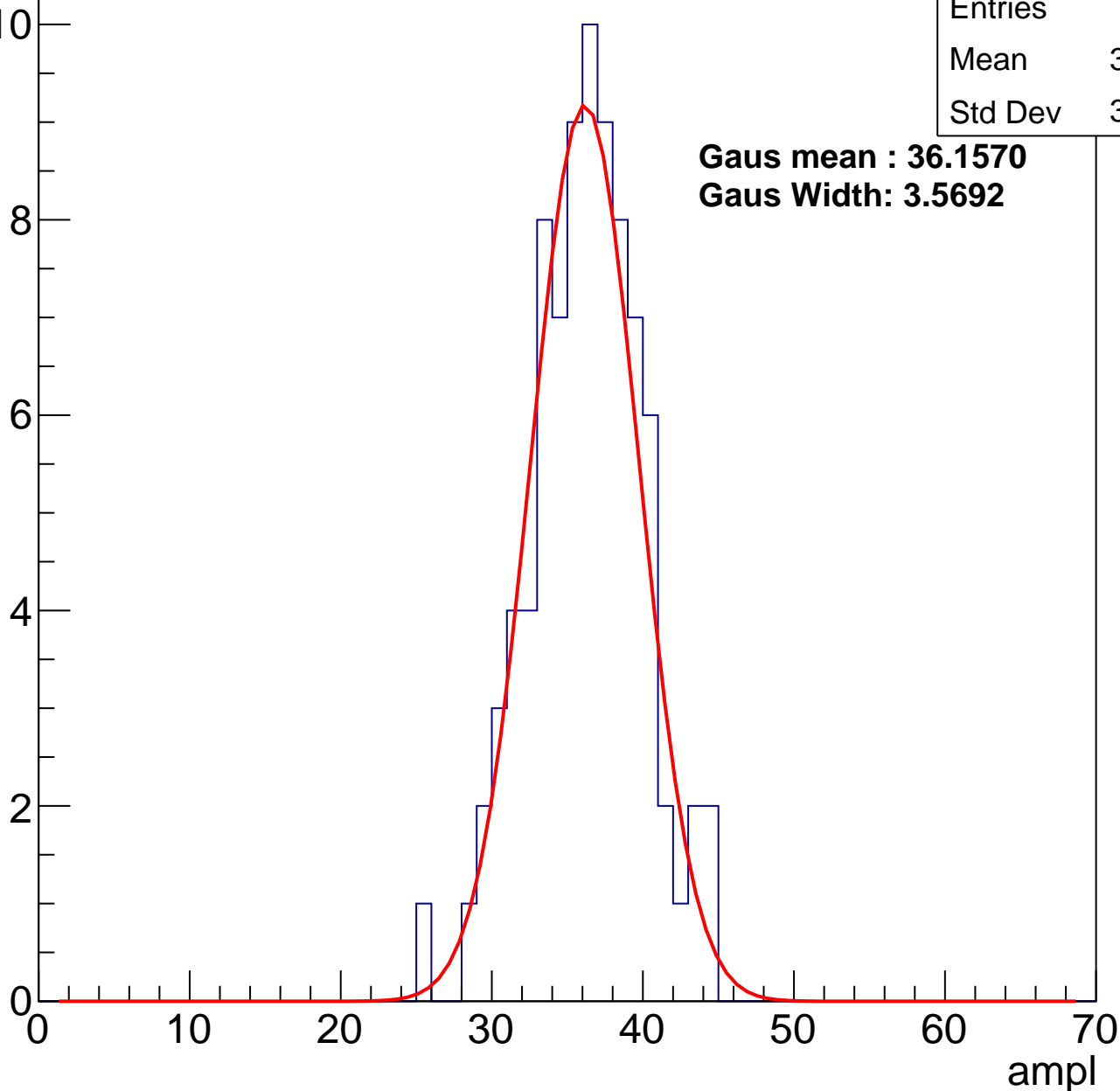
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	35.79
Std Dev	3.692

Gaus mean : 36.1570

Gaus Width: 3.5692



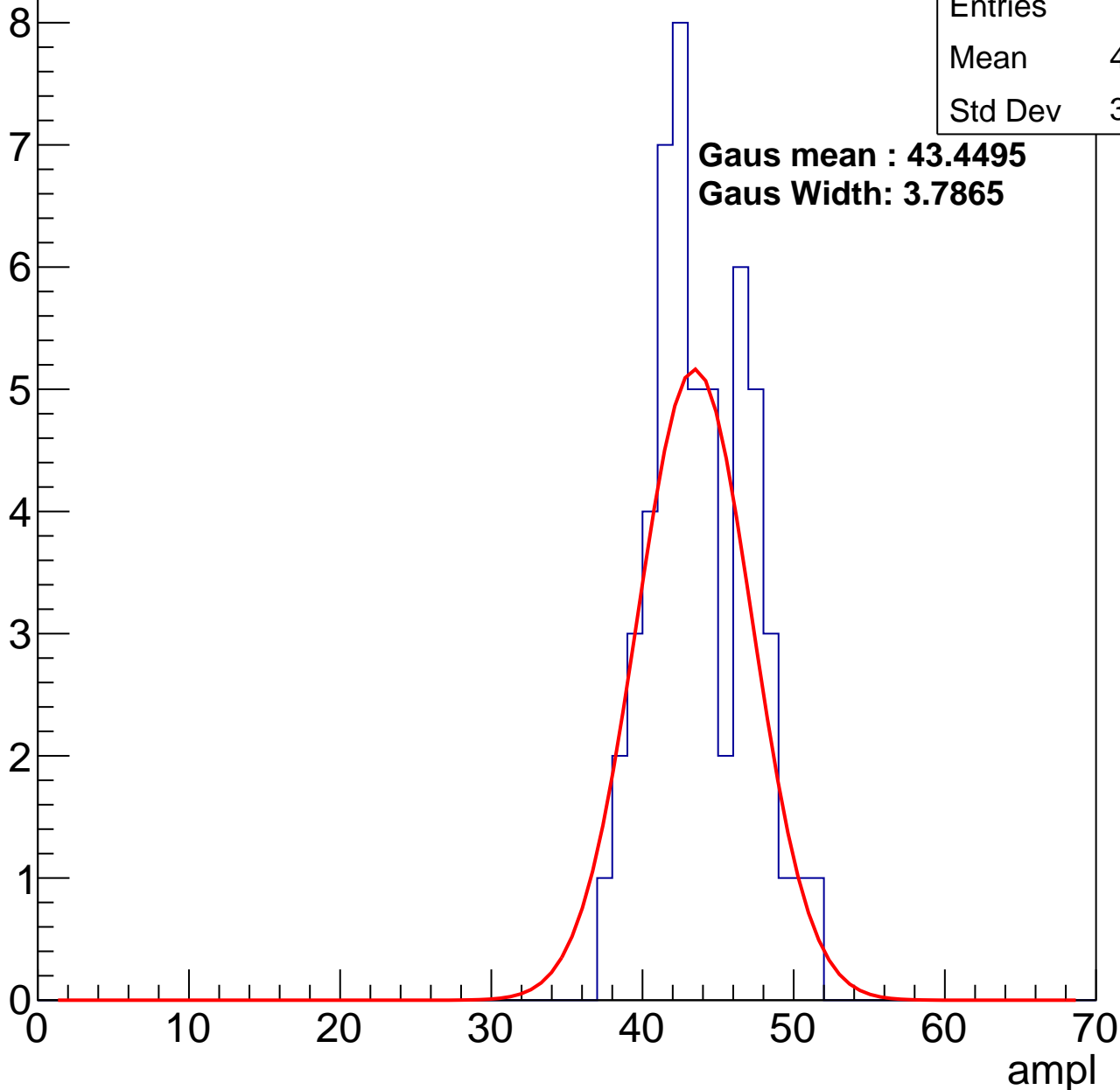
B1L103S, U24-ch123, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	43.39
Std Dev	3.246

Gaus mean : 43.4495
Gaus Width: 3.7865

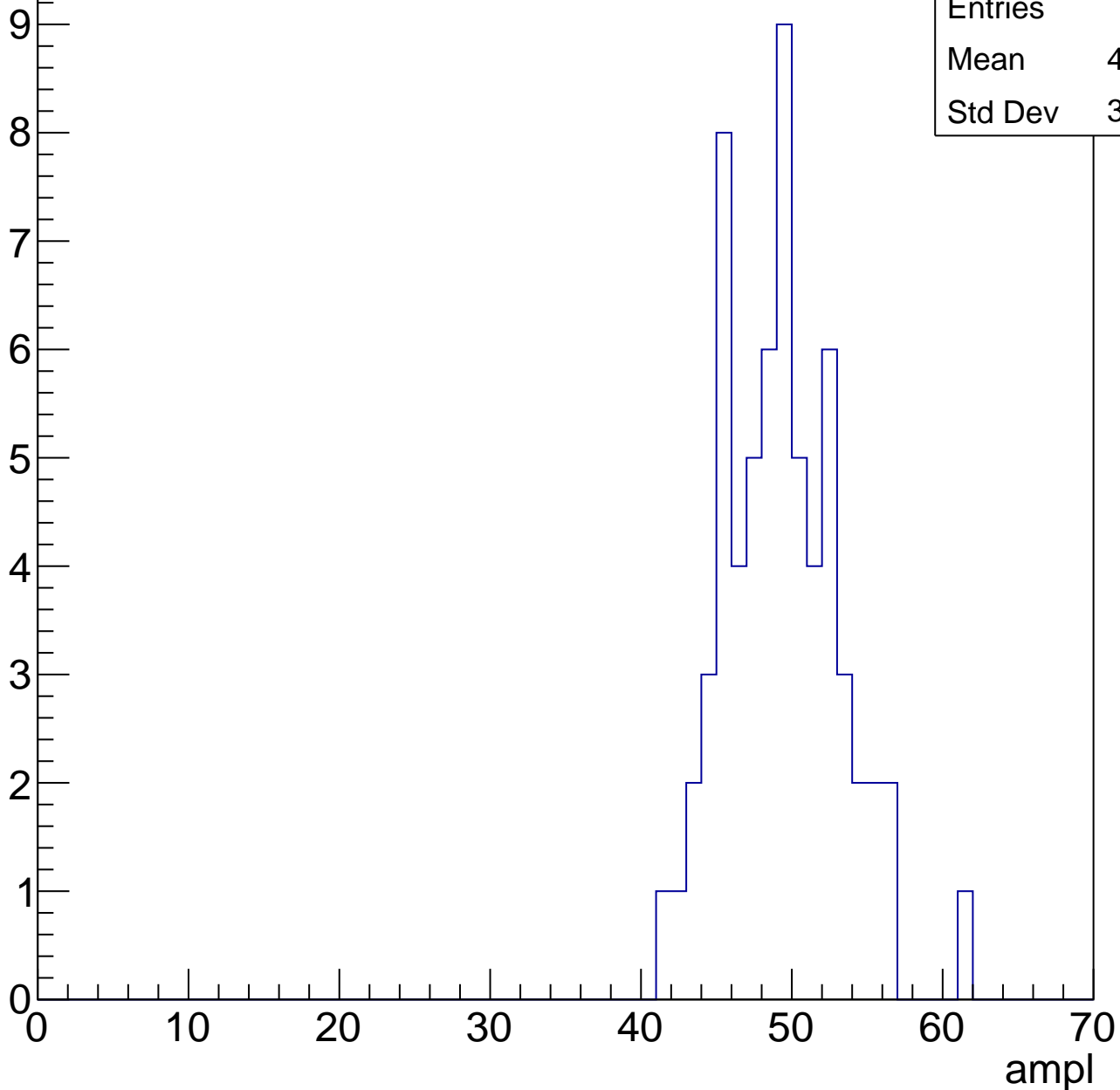


B1L103S, U24-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	48.83
Std Dev	3.822

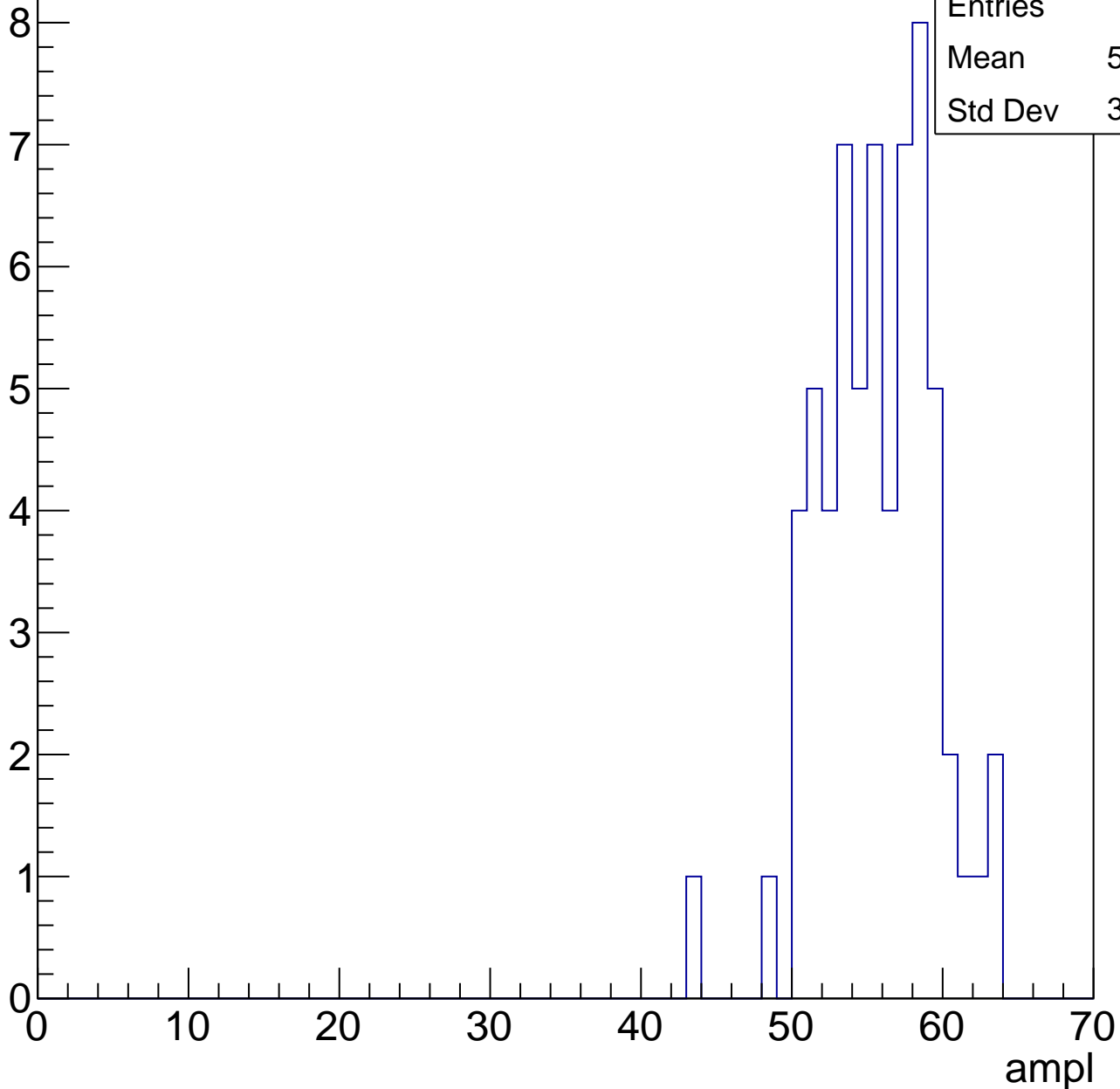


B1L103S, U24-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.17
Std Dev	3.723

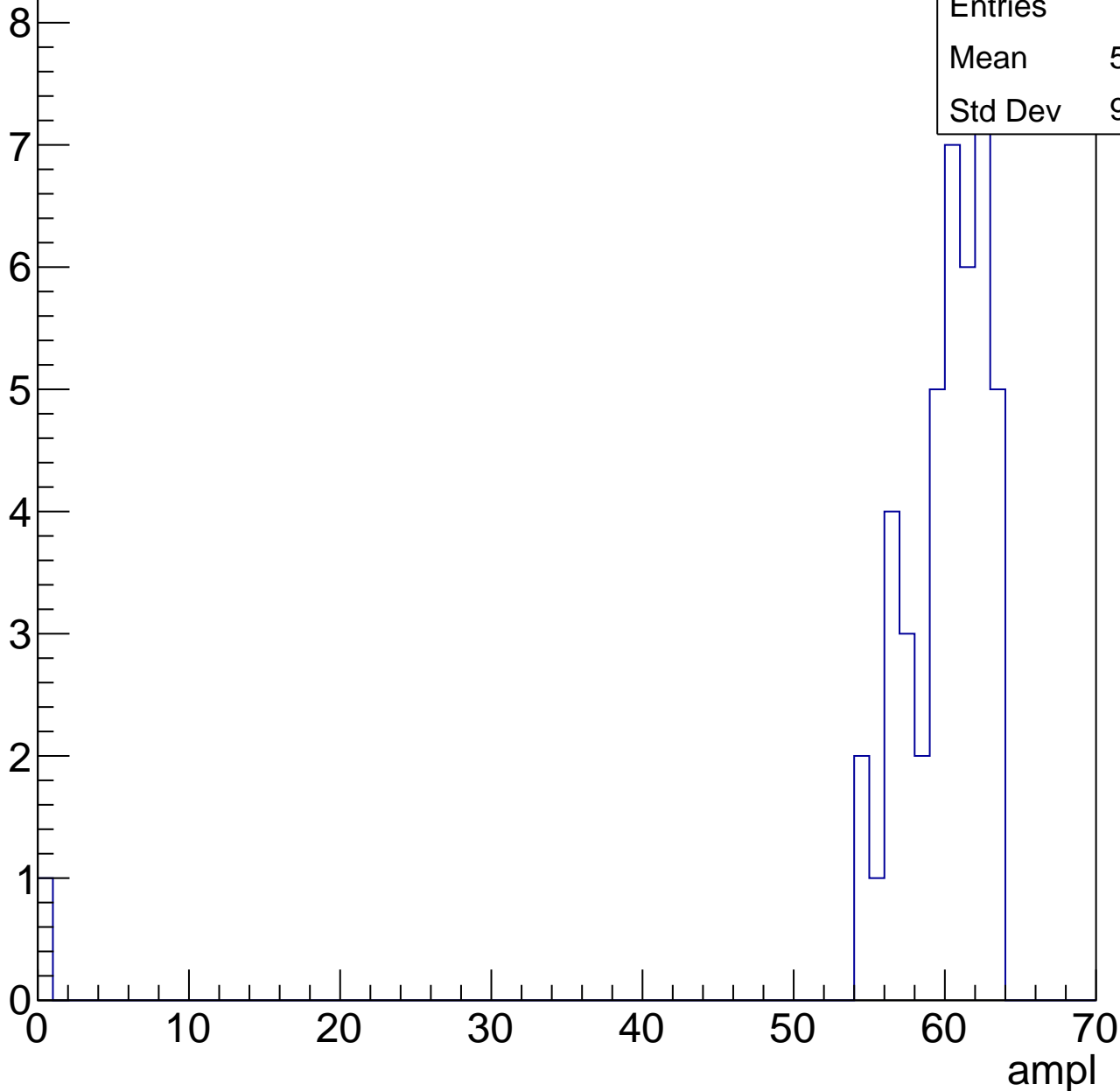


B1L103S, U24-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.32
Std Dev	9.244



B1L103S, U24-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

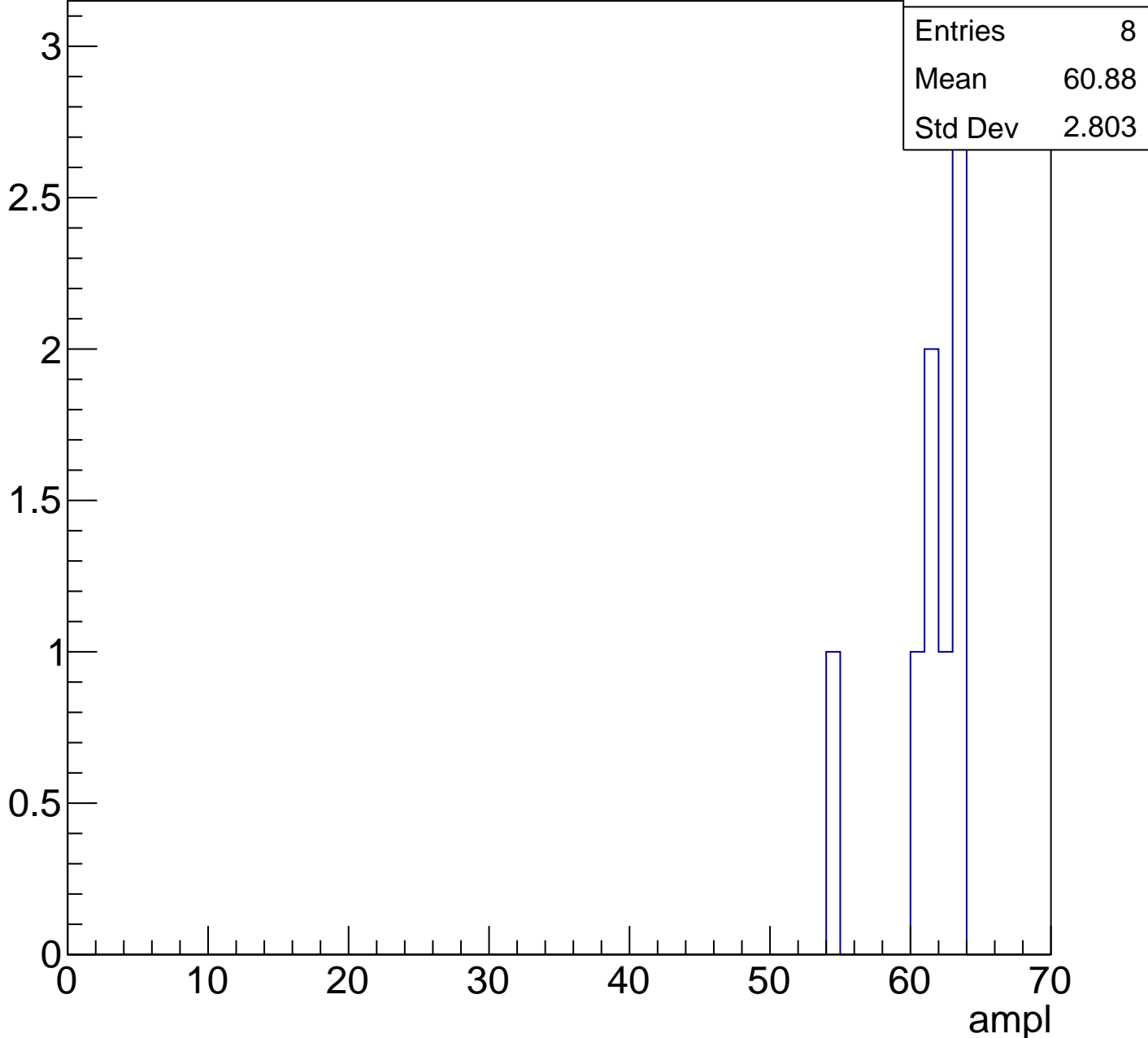
8

Mean

60.88

Std Dev

2.803



B1L103S, U24-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch124, adc0

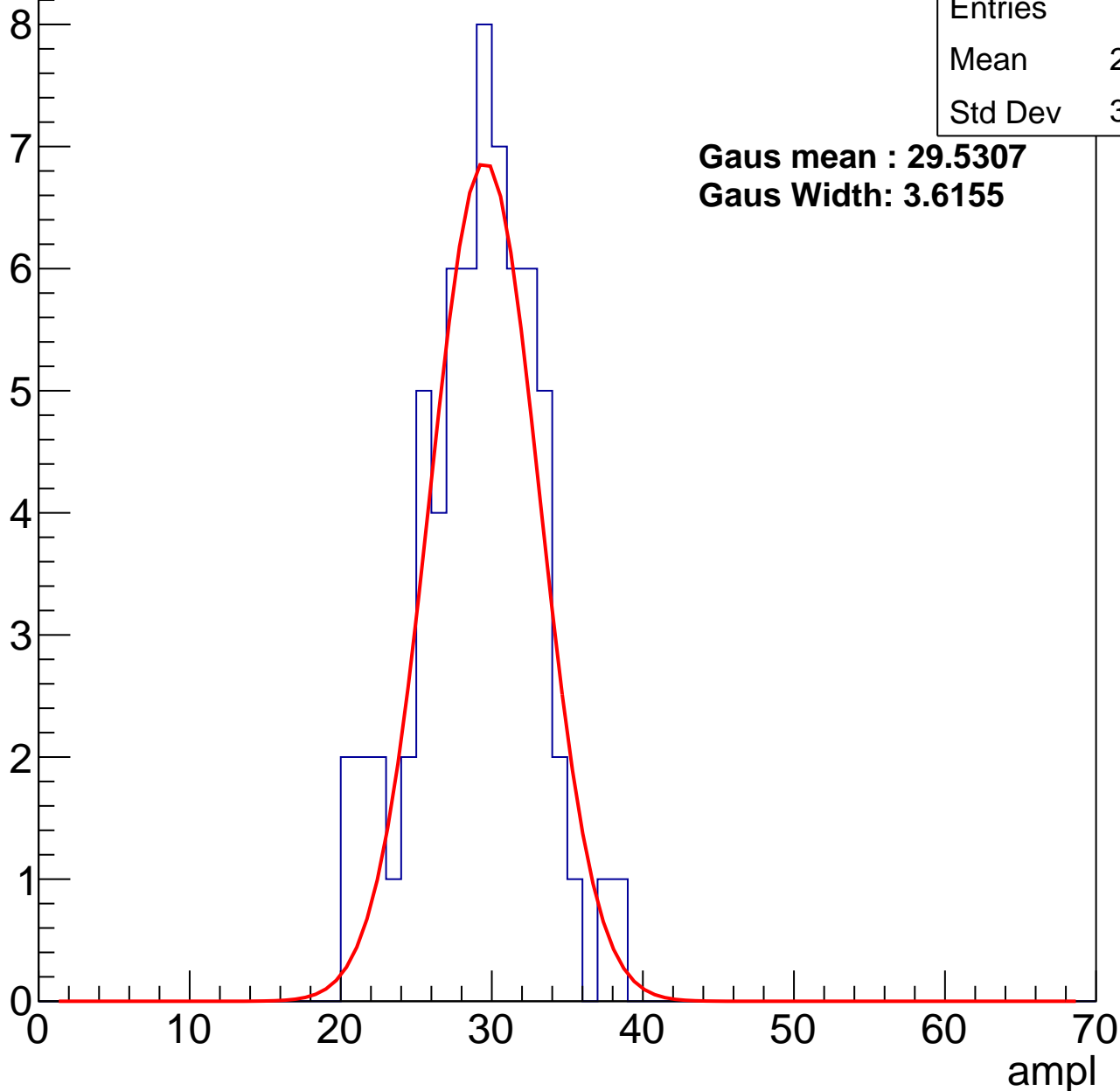
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.64
Std Dev	3.878

Gaus mean : 29.5307

Gaus Width: 3.6155



B1L103S, U24-ch124, adc1

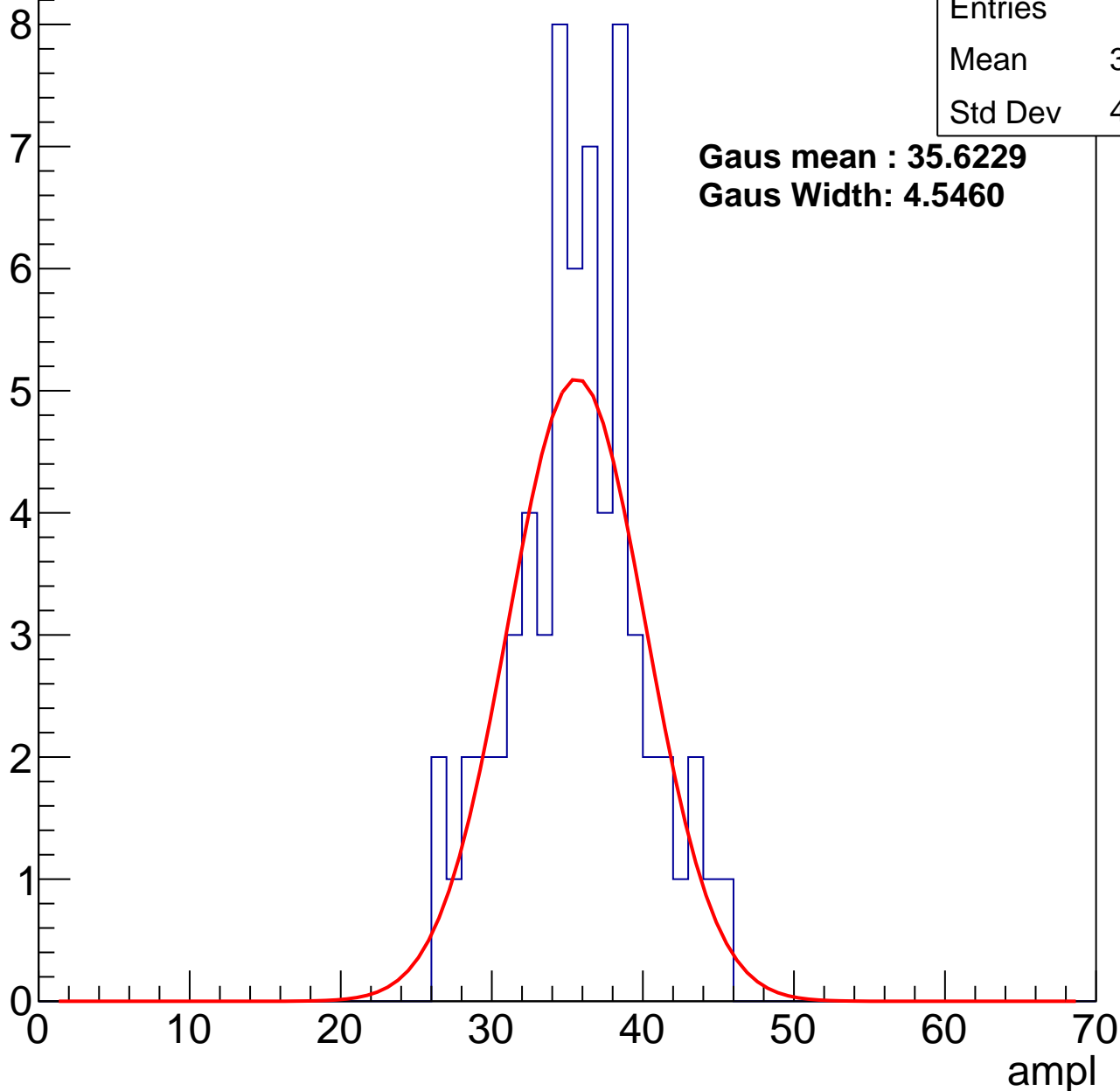
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.23
Std Dev	4.275

Gaus mean : 35.6229

Gaus Width: 4.5460



B1L103S, U24-ch124, adc2

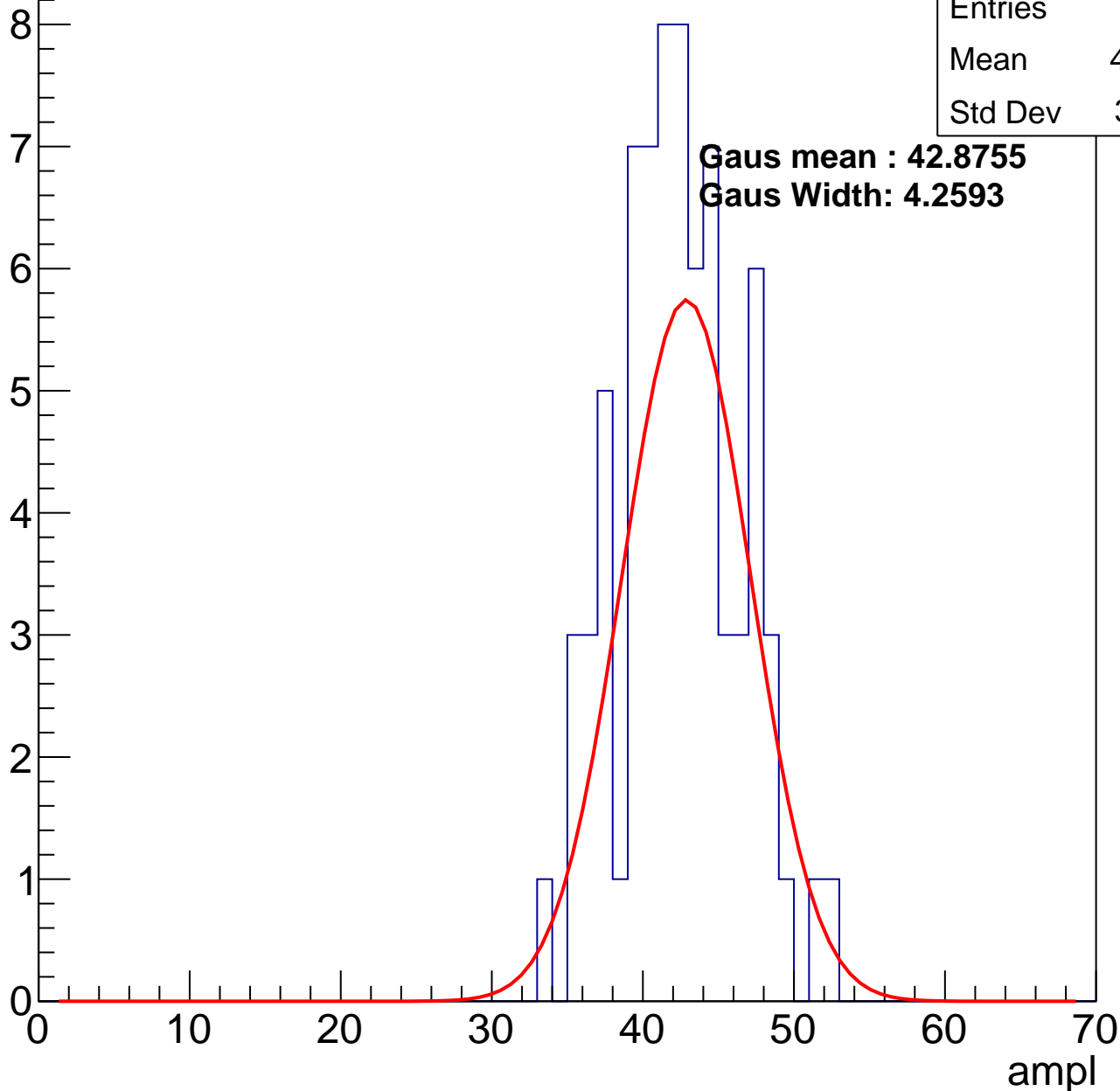
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	41.93
Std Dev	3.981

Gaus mean : 42.8755

Gaus Width: 4.2593

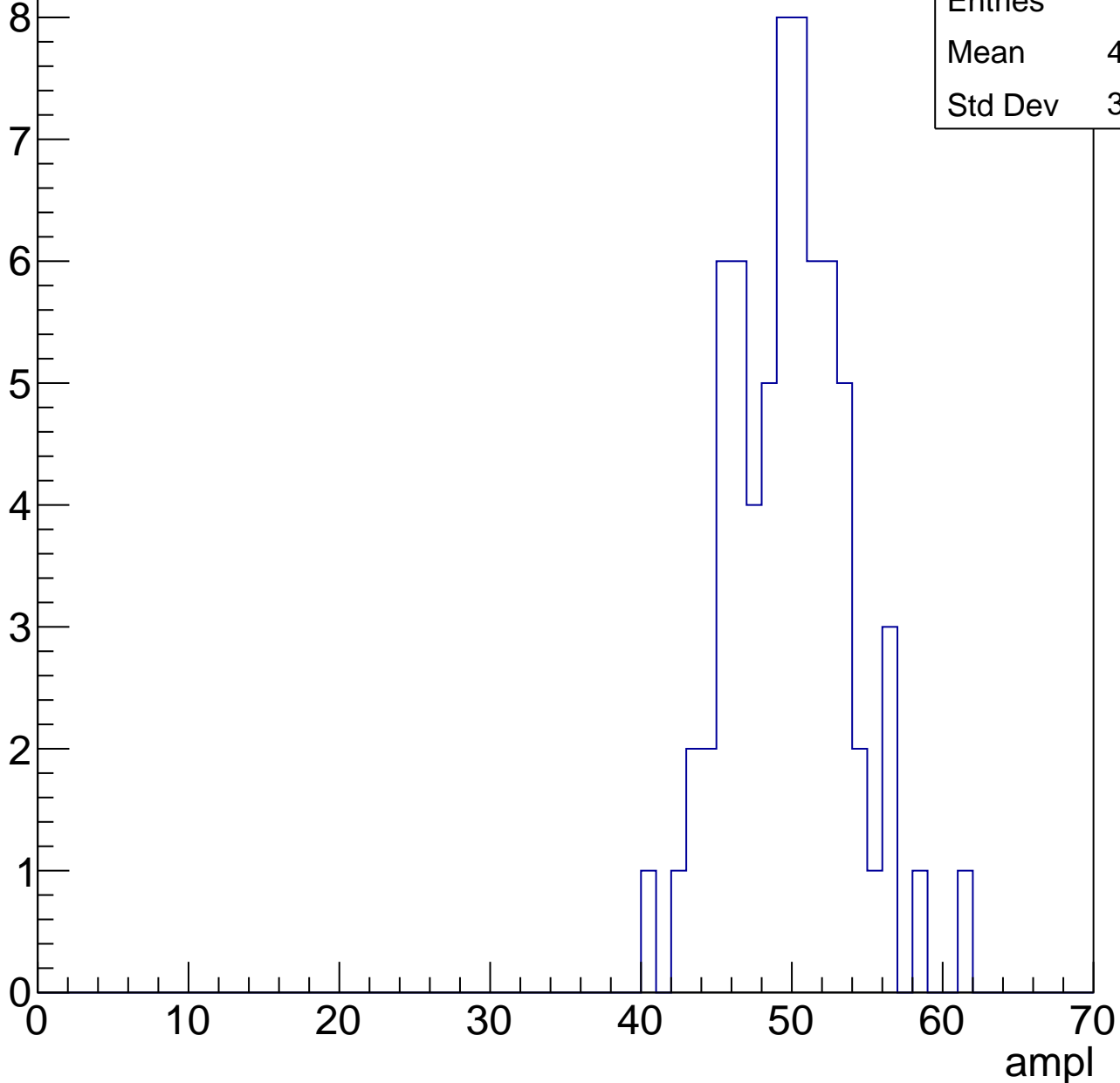


B1L103S, U24-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.34
Std Dev	3.909

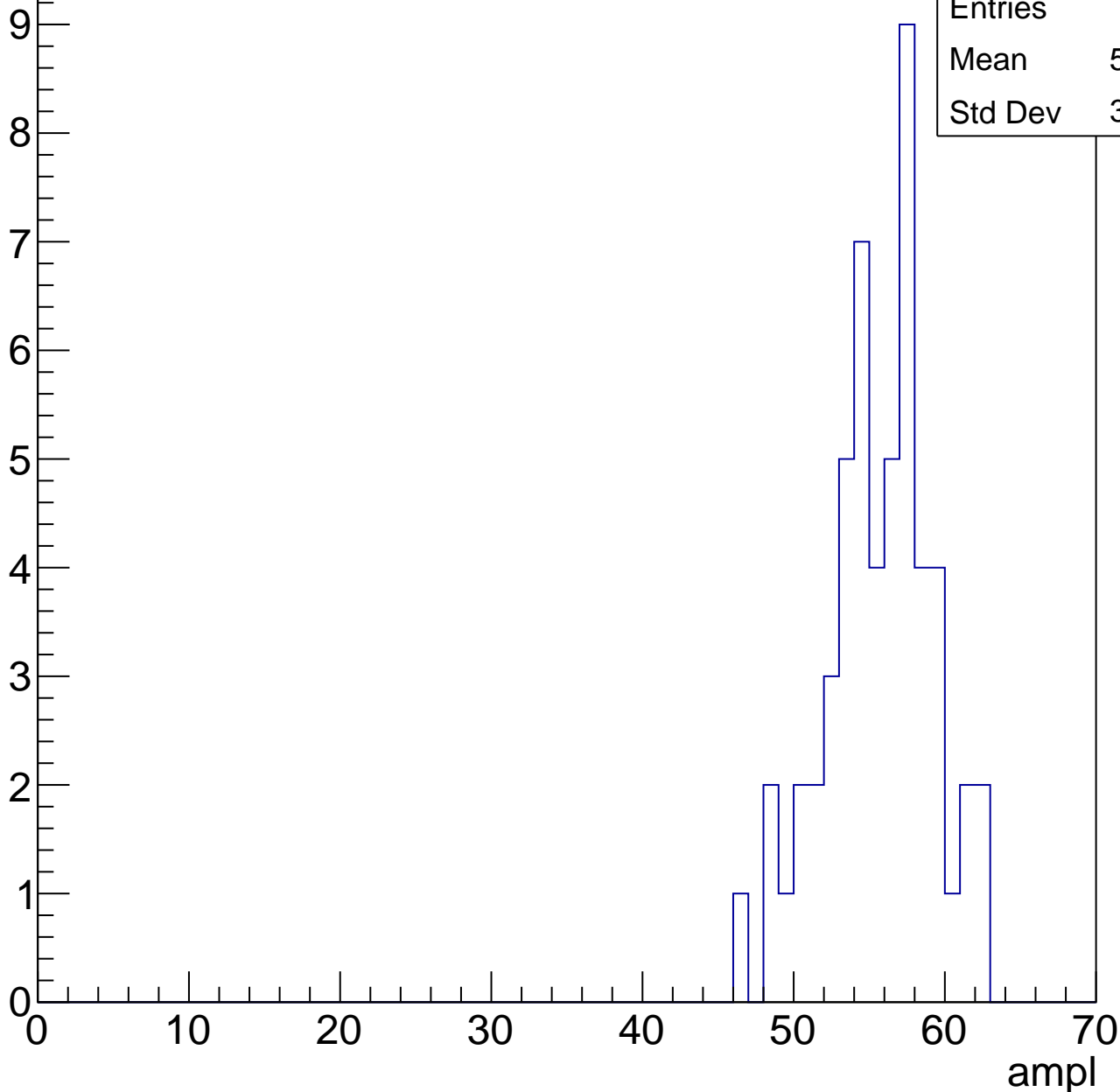


B1L103S, U24-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.17
Std Dev	3.563

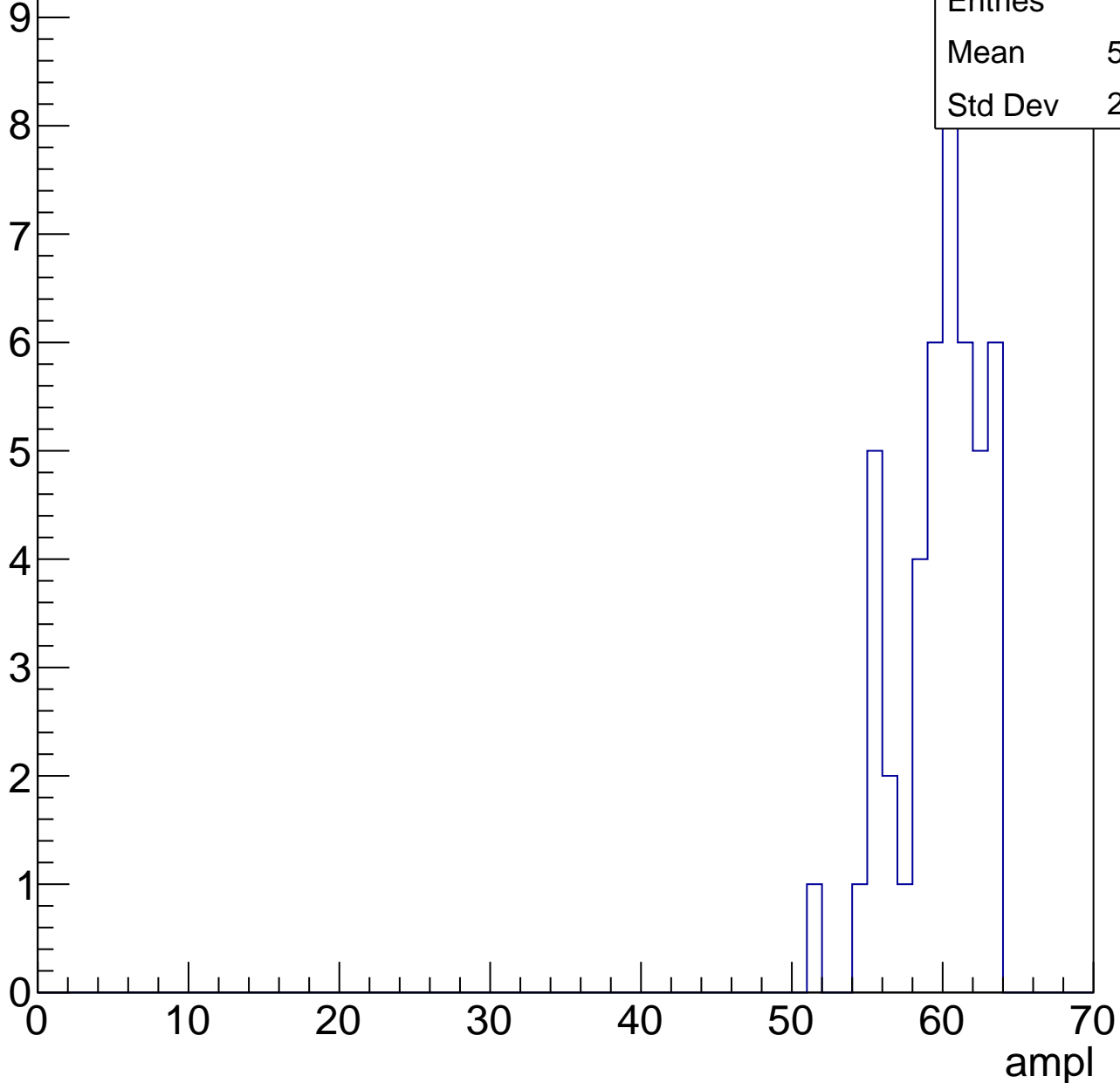


B1L103S, U24-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

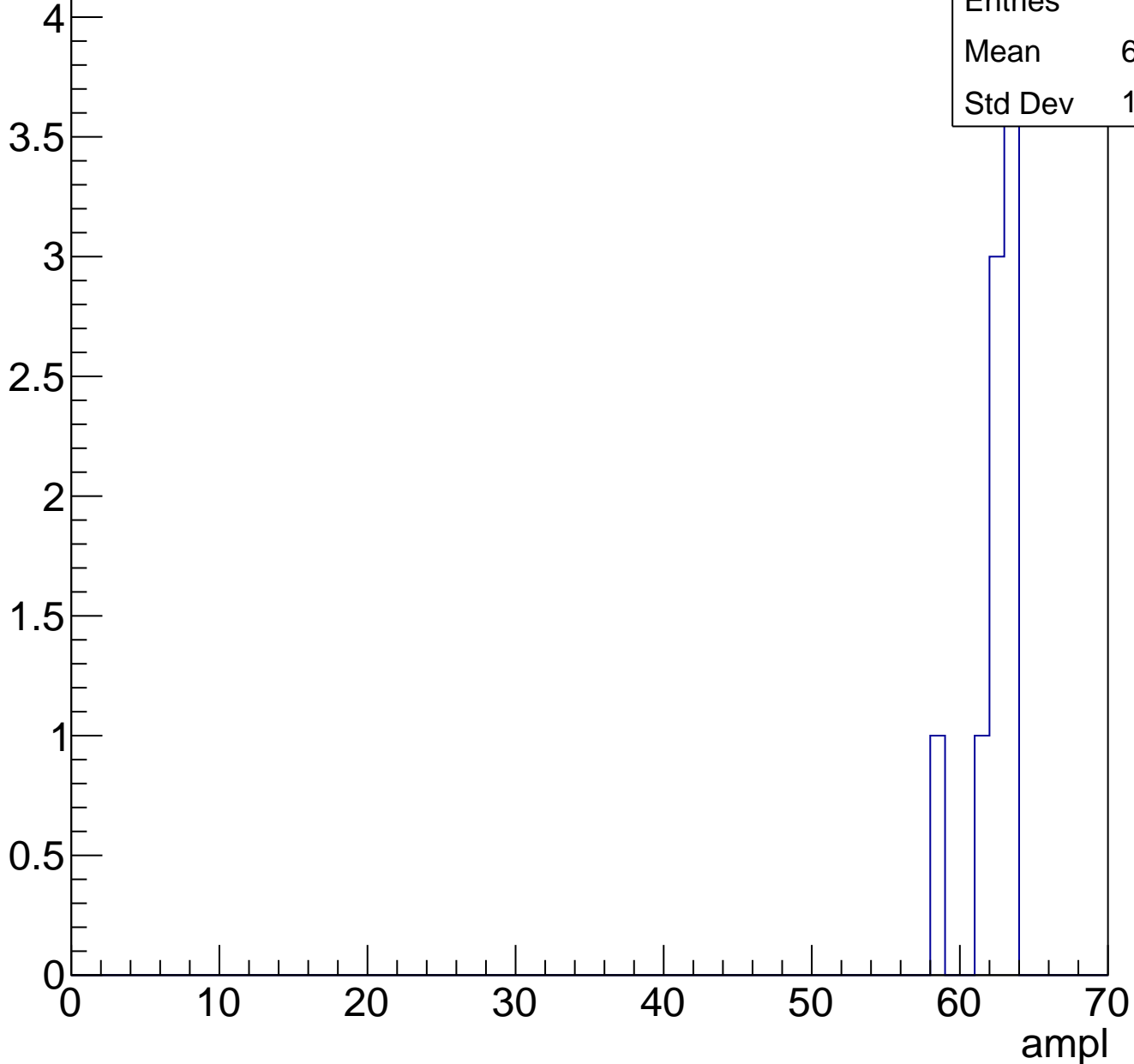
Entries	46
Mean	59.33
Std Dev	2.813



B1L103S, U24-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch125, adc0

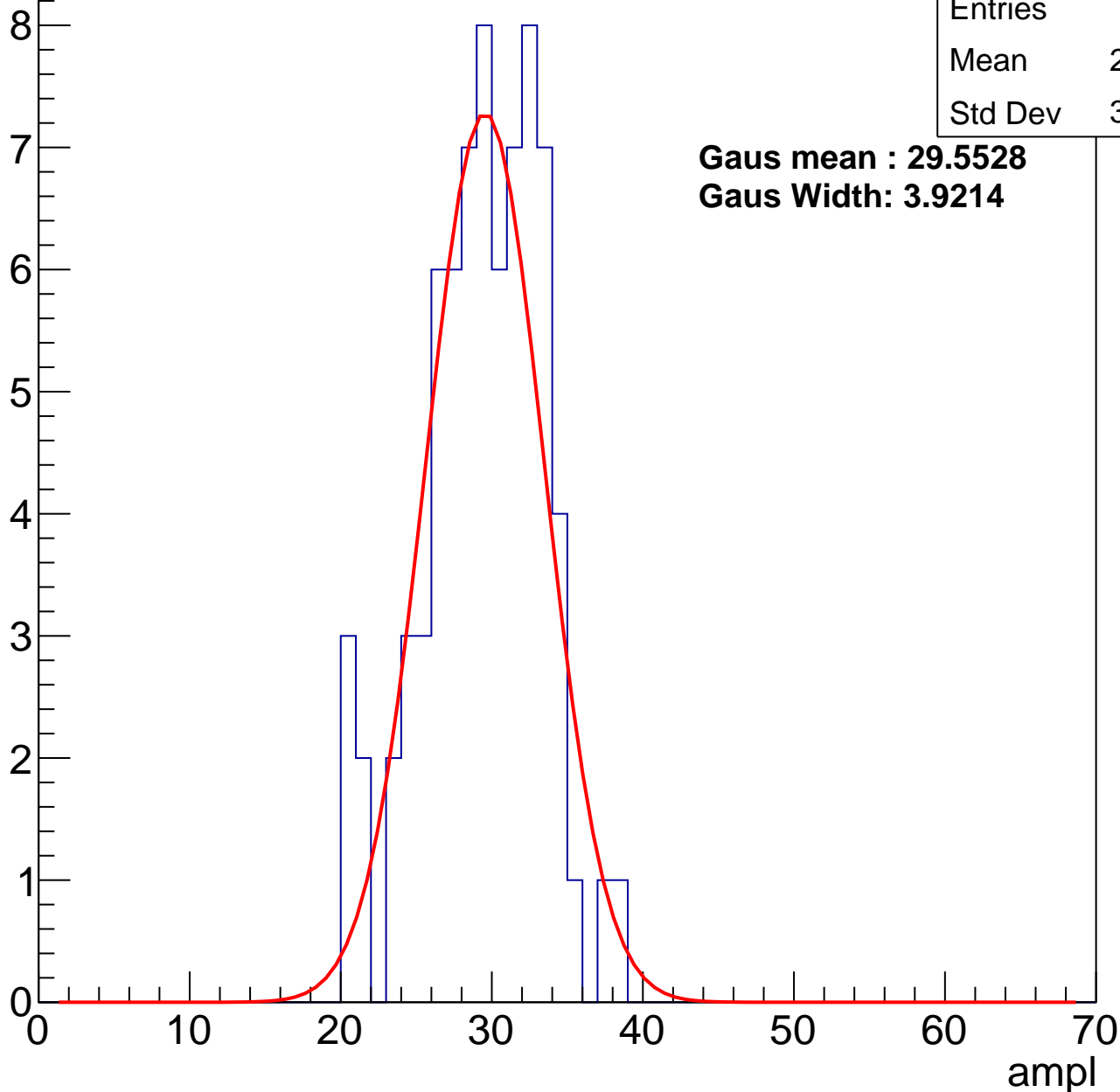
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.95
Std Dev	3.915

Gaus mean : 29.5528

Gaus Width: 3.9214



B1L103S, U24-ch125, adc1

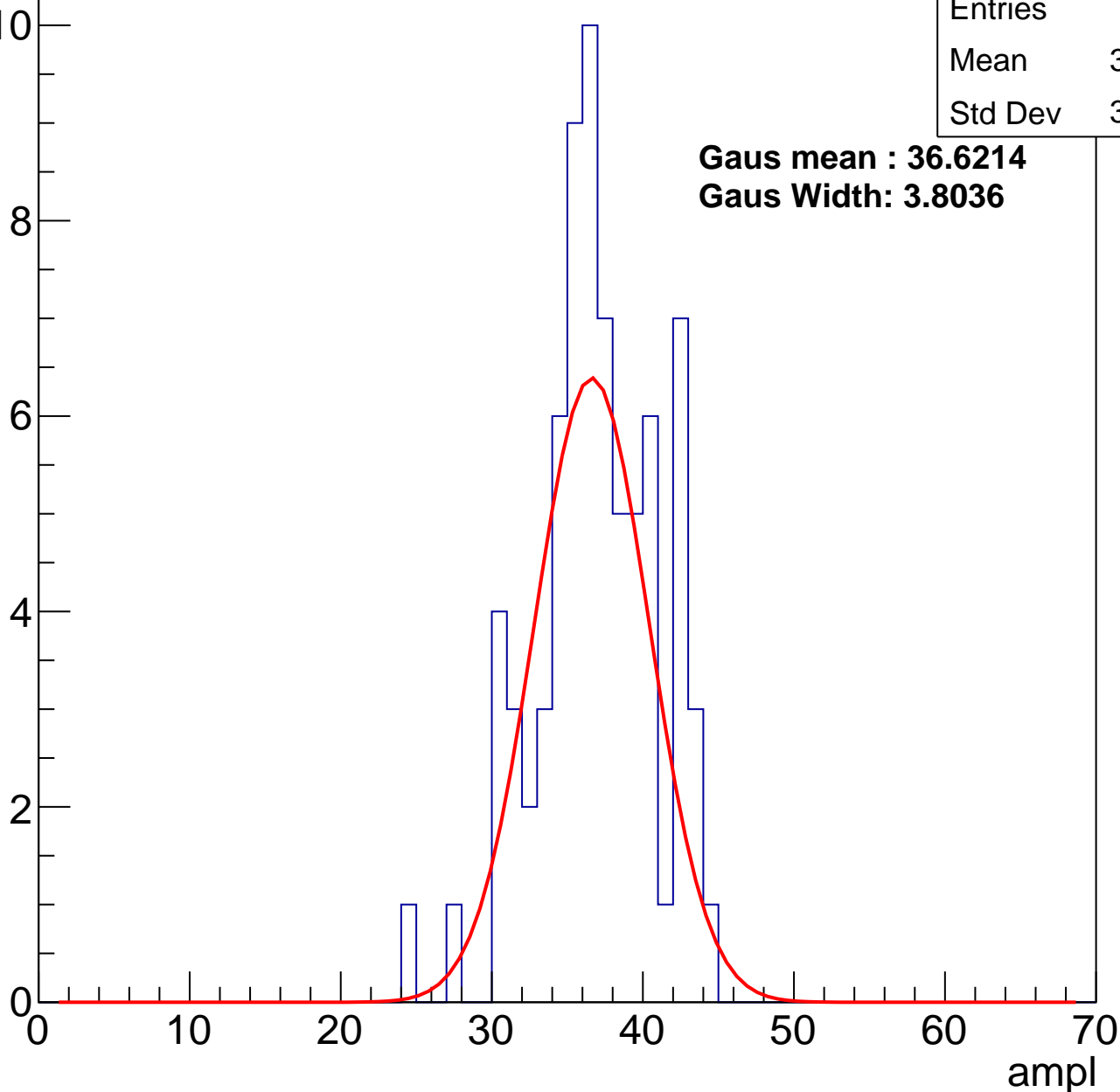
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.46
Std Dev	3.987

Gaus mean : 36.6214

Gaus Width: 3.8036



B1L103S, U24-ch125, adc2

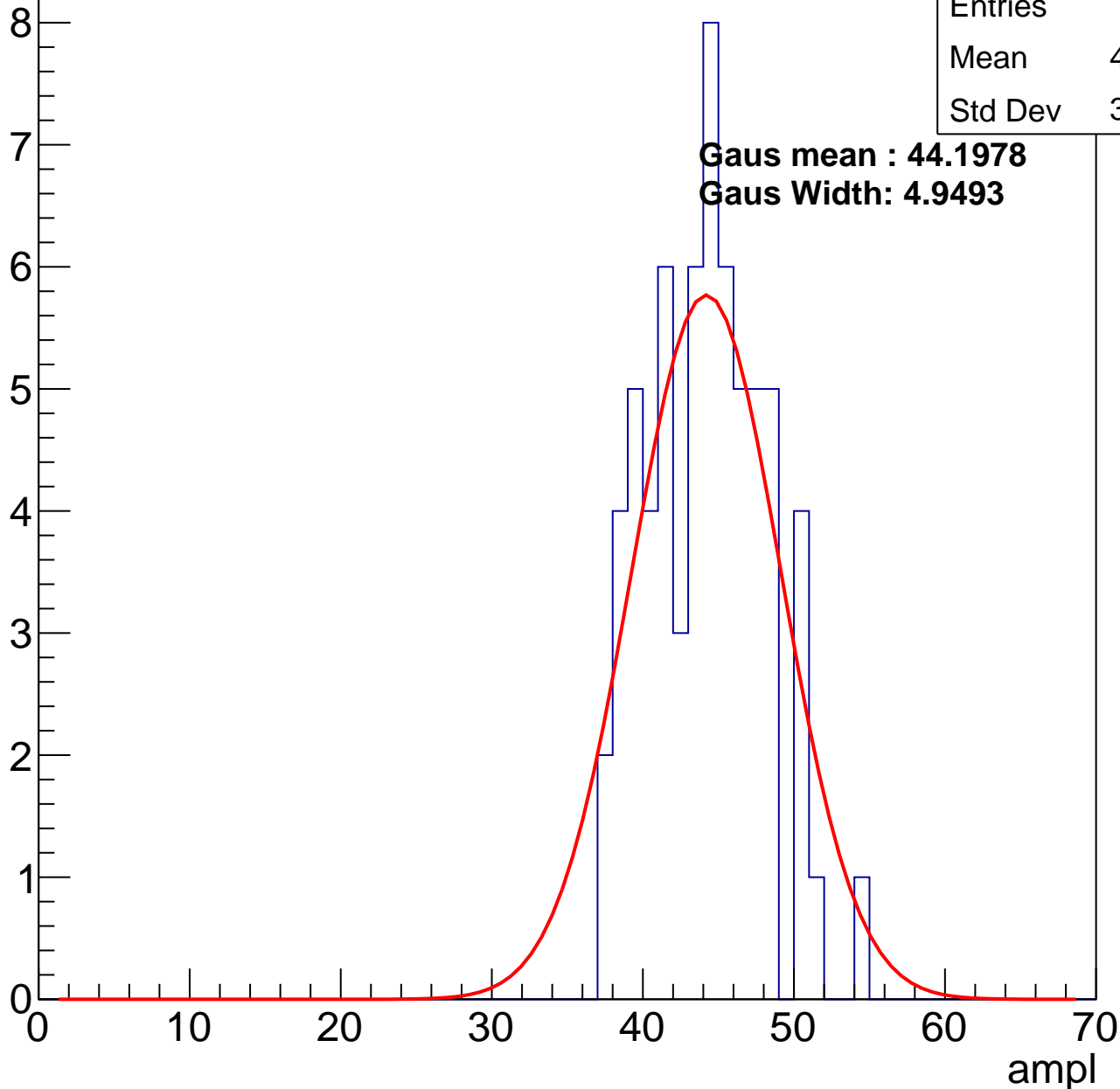
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.74
Std Dev	3.808

Gaus mean : 44.1978

Gaus Width: 4.9493

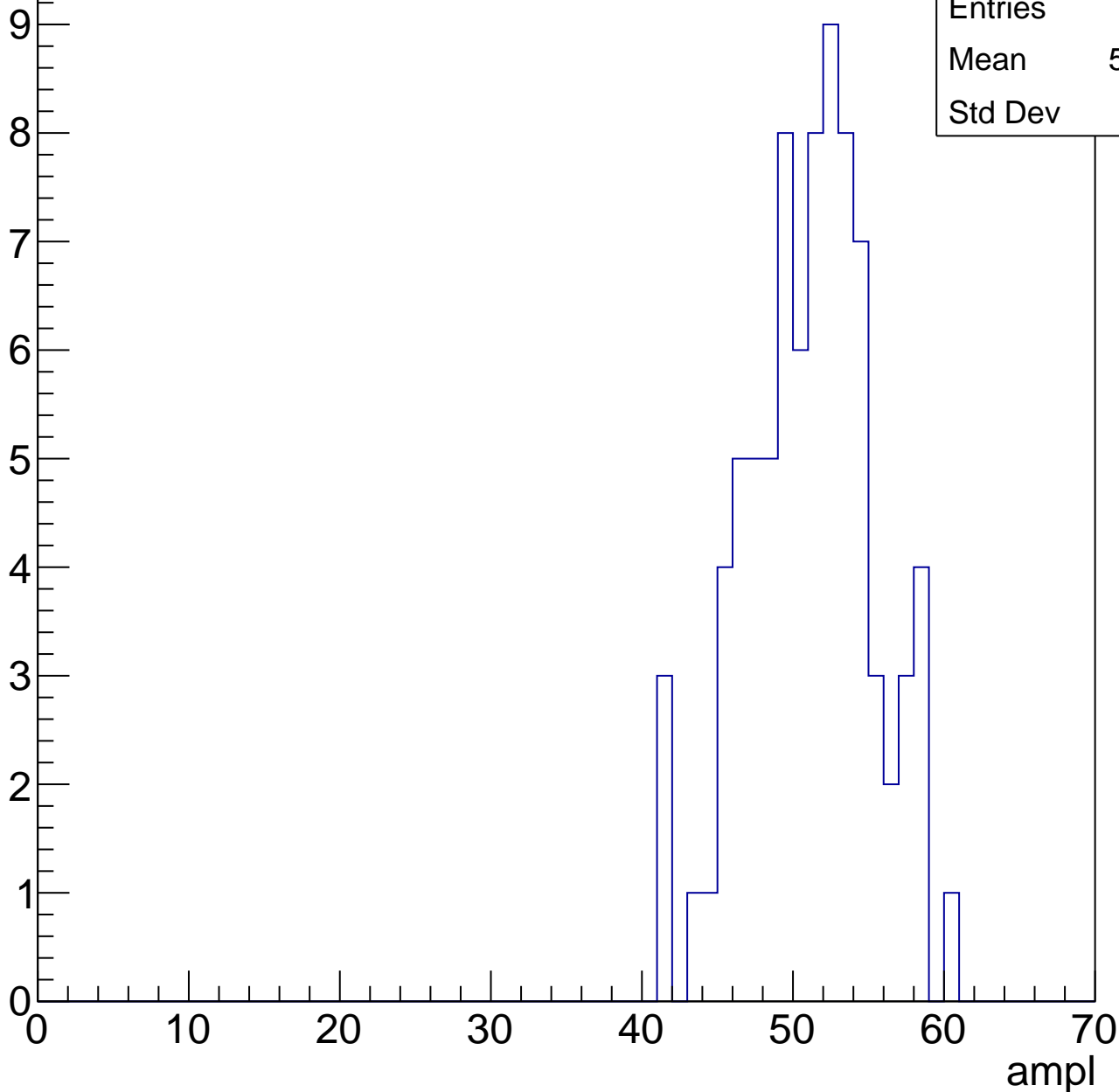


B1L103S, U24-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	50.66
Std Dev	4.14

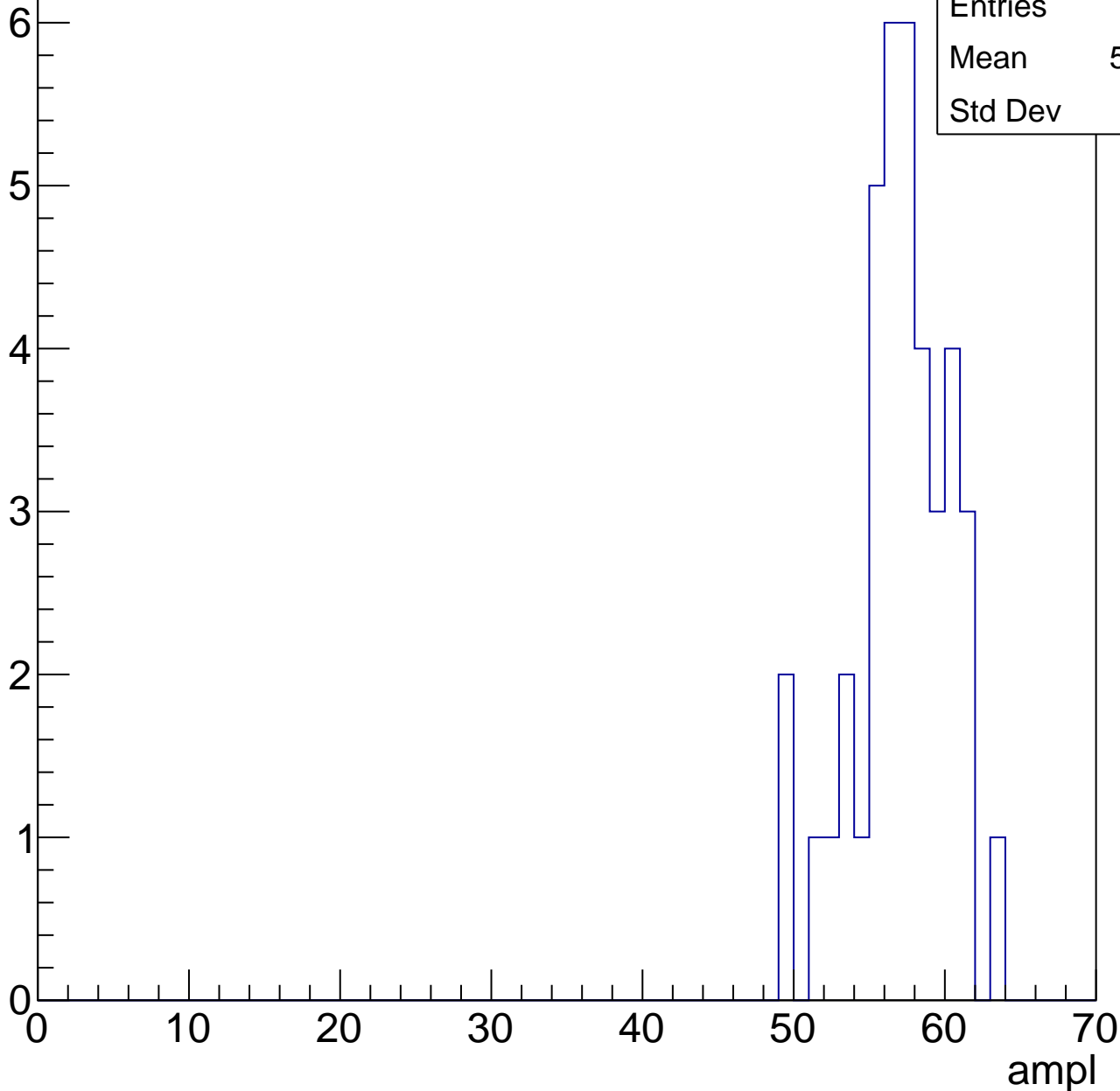


B1L103S, U24-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	56.64
Std Dev	3.15

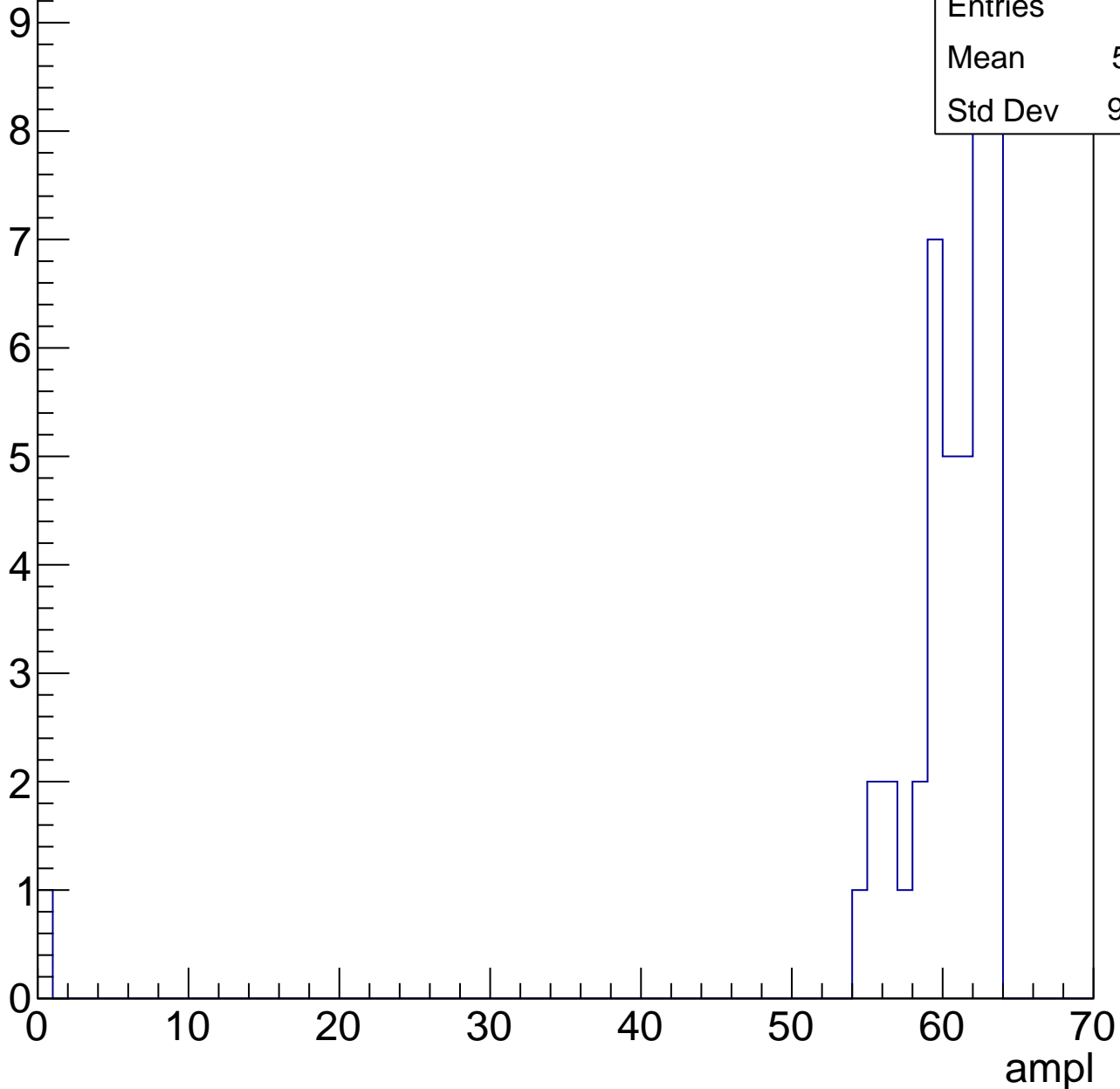


B1L103S, U24-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.81
Std Dev	9.394



B1L103S, U24-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch126, adc0

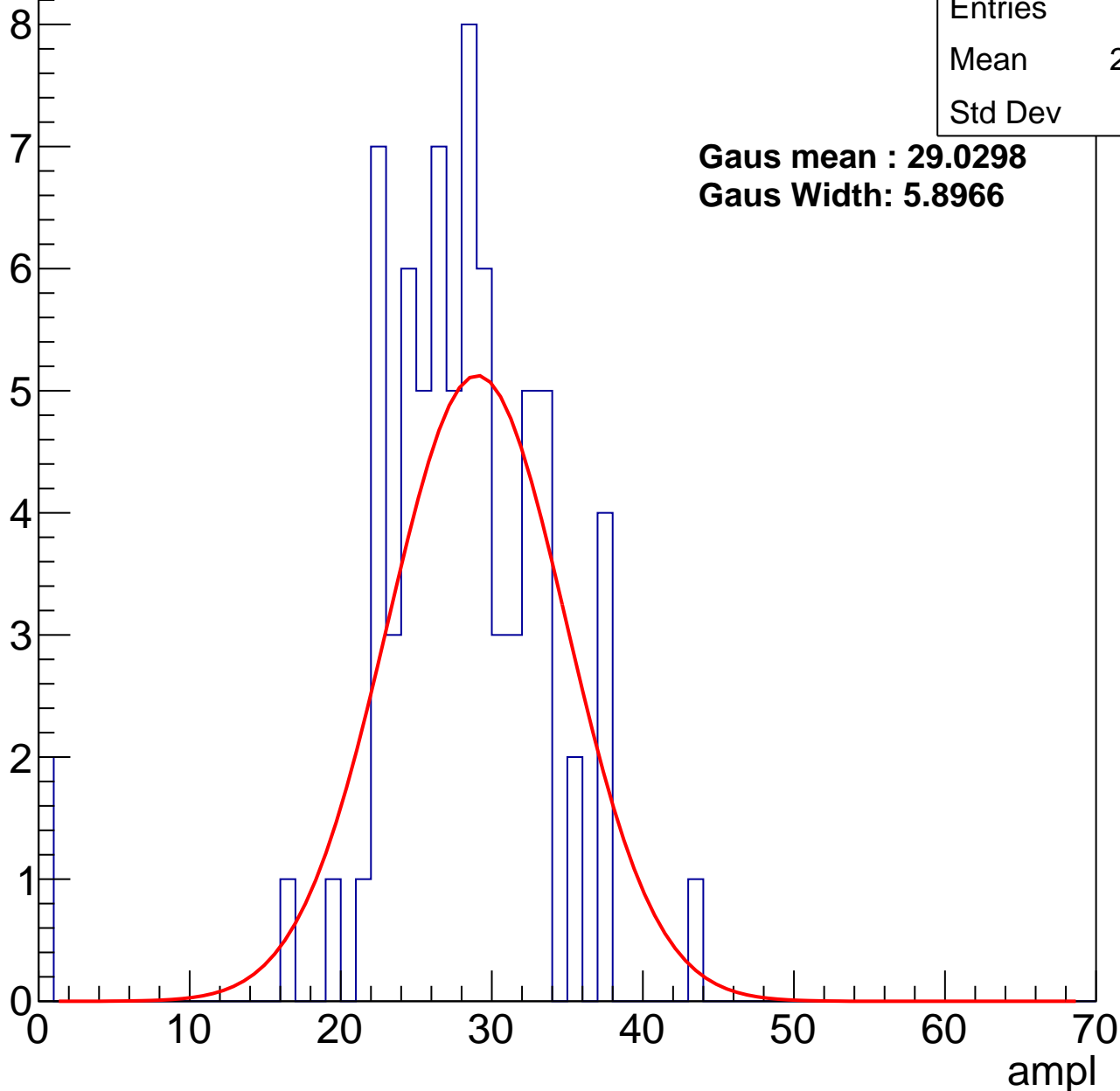
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	27.09
Std Dev	6.51

Gaus mean : 29.0298

Gaus Width: 5.8966



B1L103S, U24-ch126, adc1

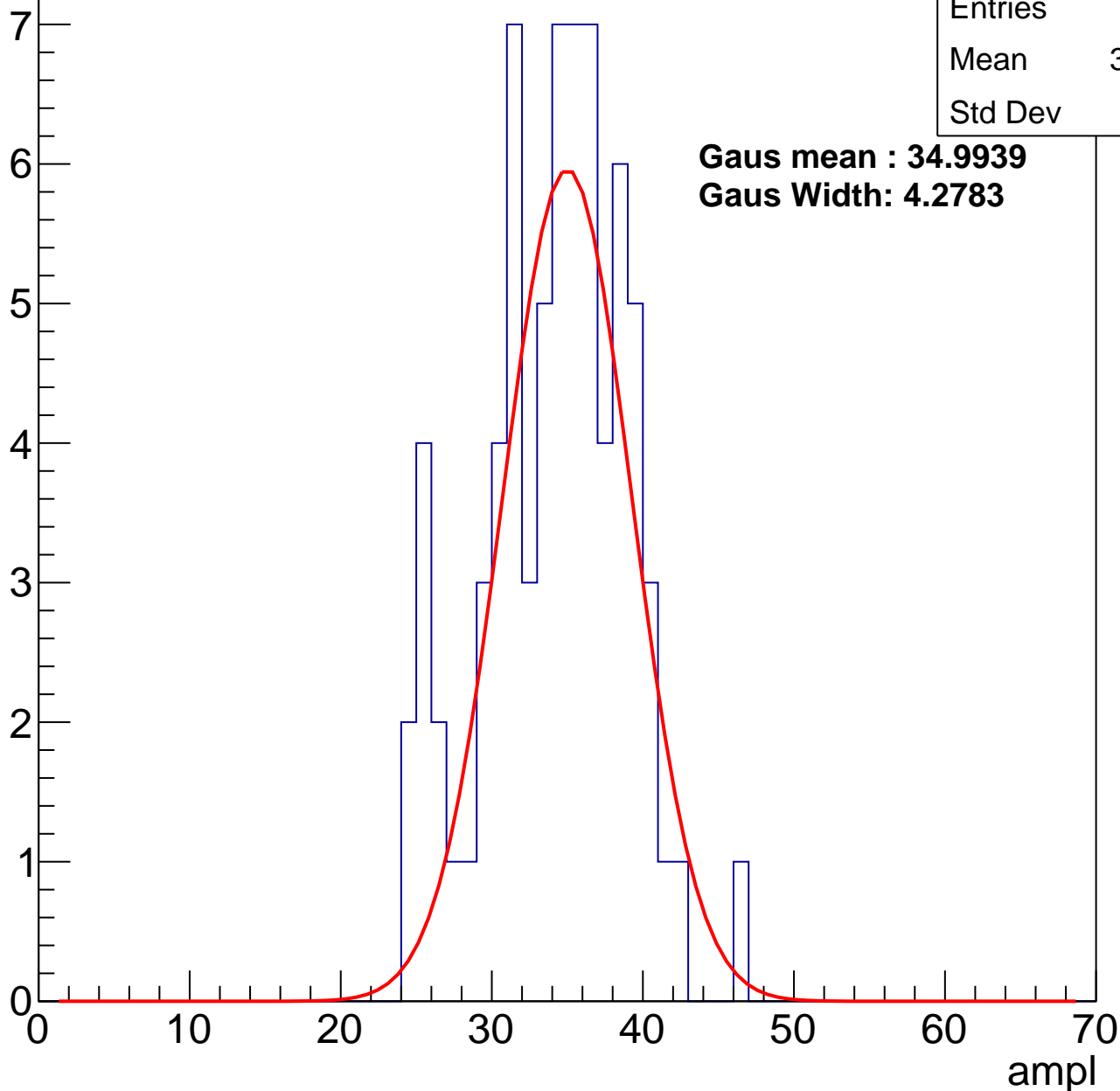
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	33.72
Std Dev	4.66

Gaus mean : 34.9939

Gaus Width: 4.2783



B1L103S, U24-ch126, adc2

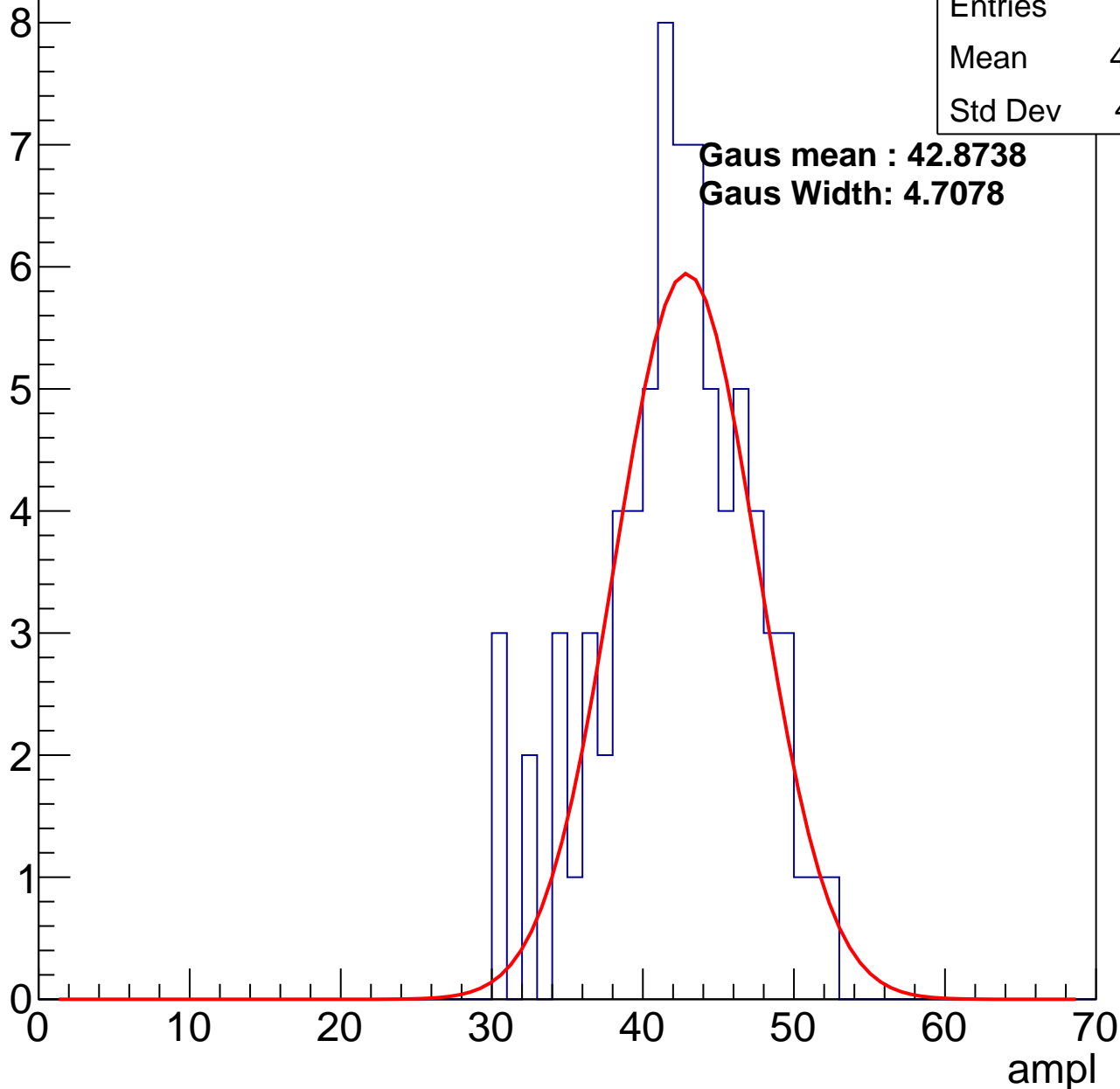
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	41.66
Std Dev	4.991

Gaus mean : 42.8738

Gaus Width: 4.7078

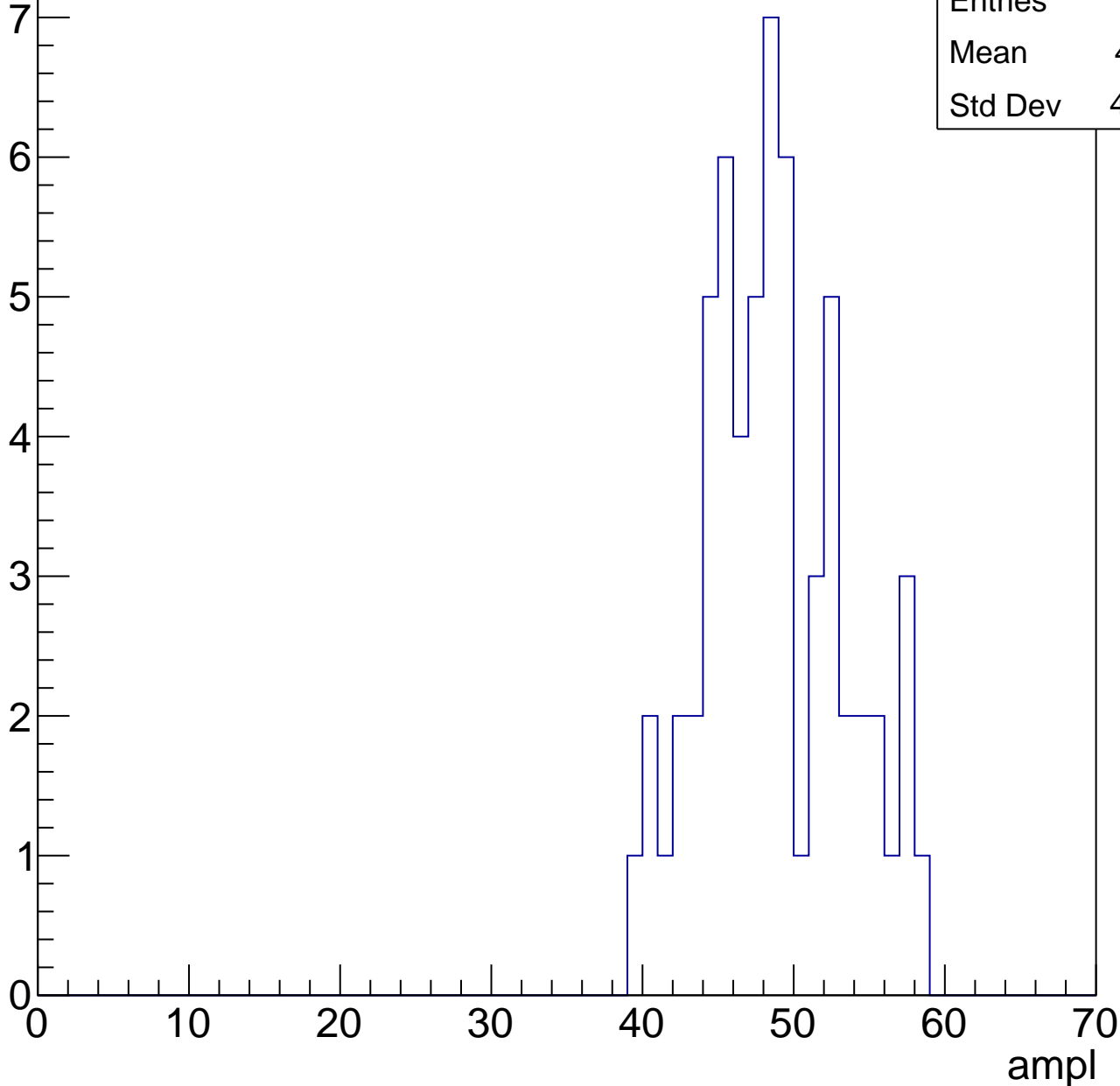


B1L103S, U24-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.21
Std Dev	4.574

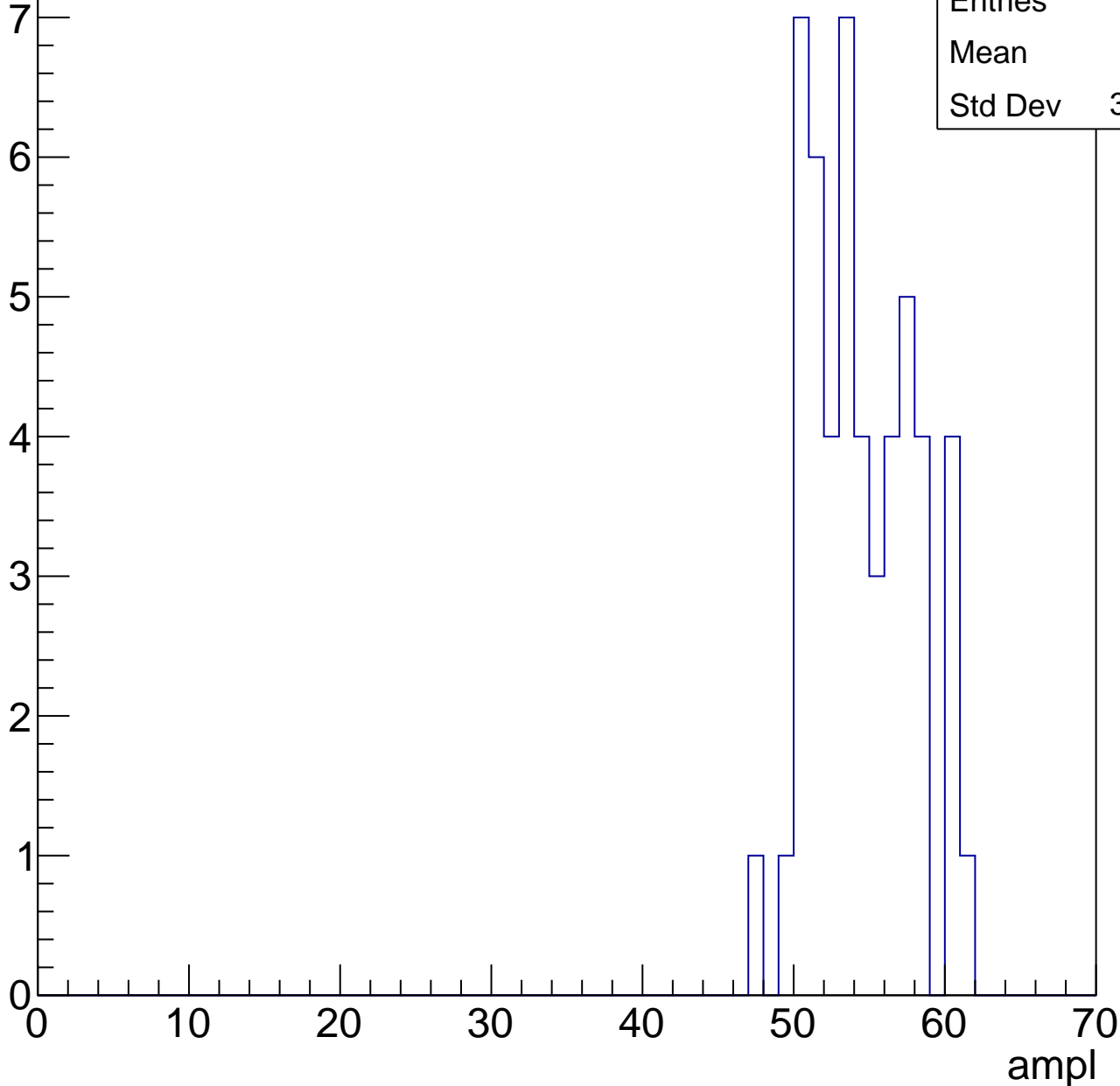


B1L103S, U24-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54
Std Dev	3.384



B1L103S, U24-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	57.85
Std Dev	8.13

Entry

10

8

6

4

2

0

0

10

20

30

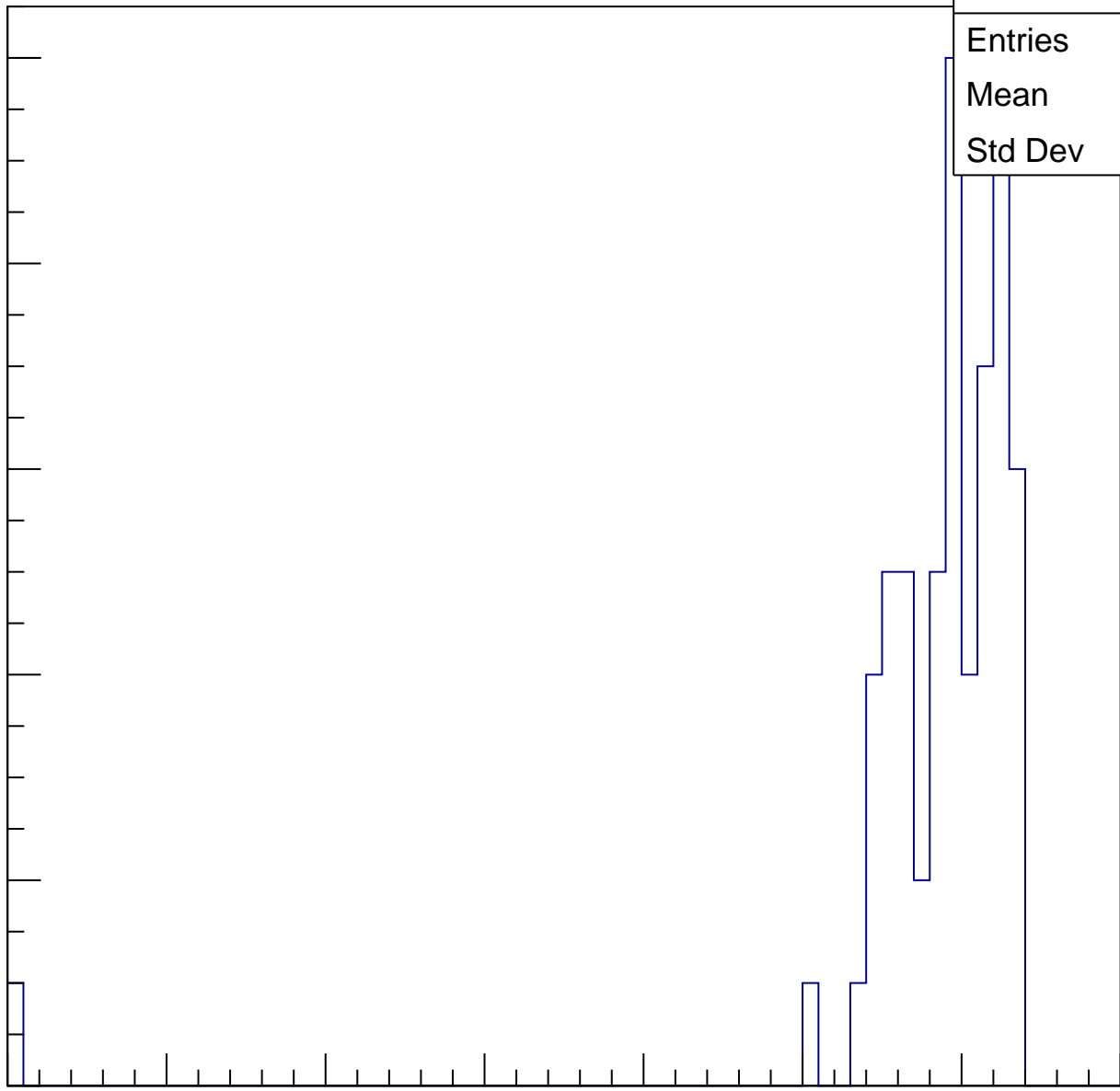
40

50

60

70

ampl



B1L103S, U24-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

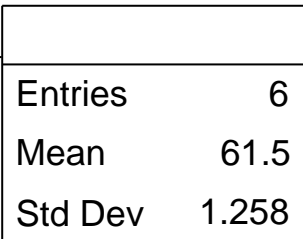
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch127, adc0

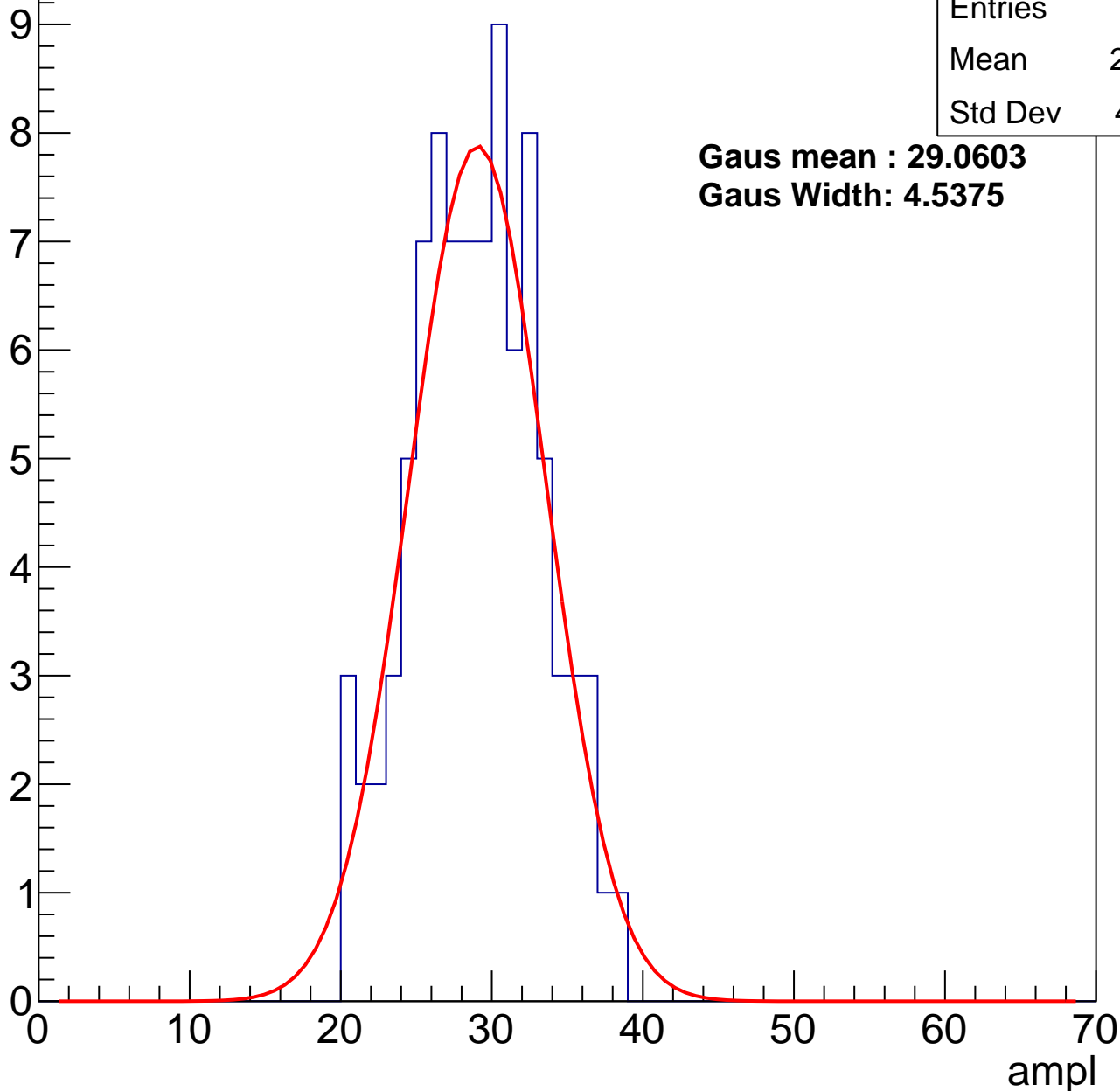
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	28.59
Std Dev	4.171

Gaus mean : 29.0603

Gaus Width: 4.5375



B1L103S, U24-ch127, adc1

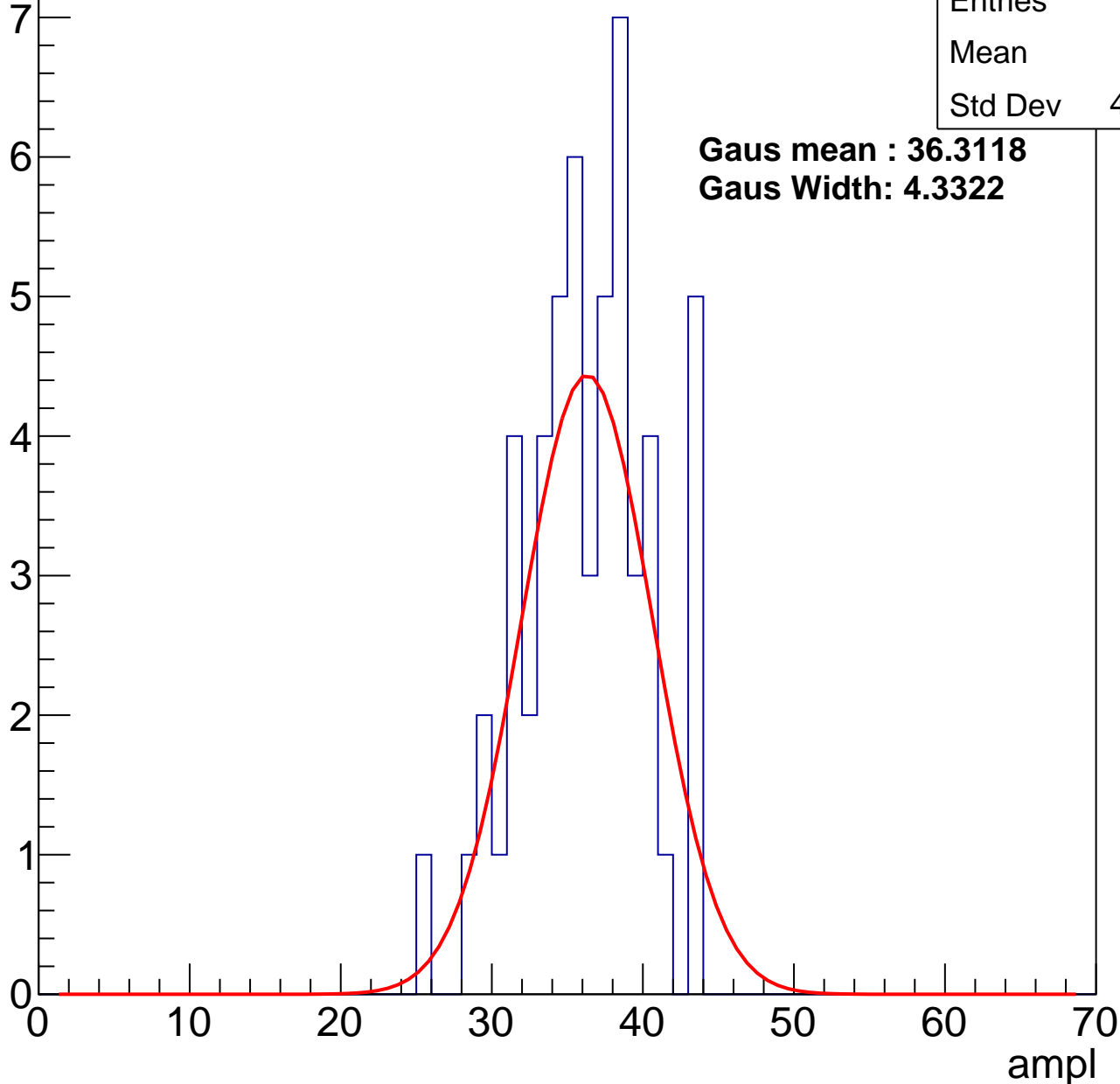
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	35.8
Std Dev	4.093

Gaus mean : 36.3118

Gaus Width: 4.3322



B1L103S, U24-ch127, adc2

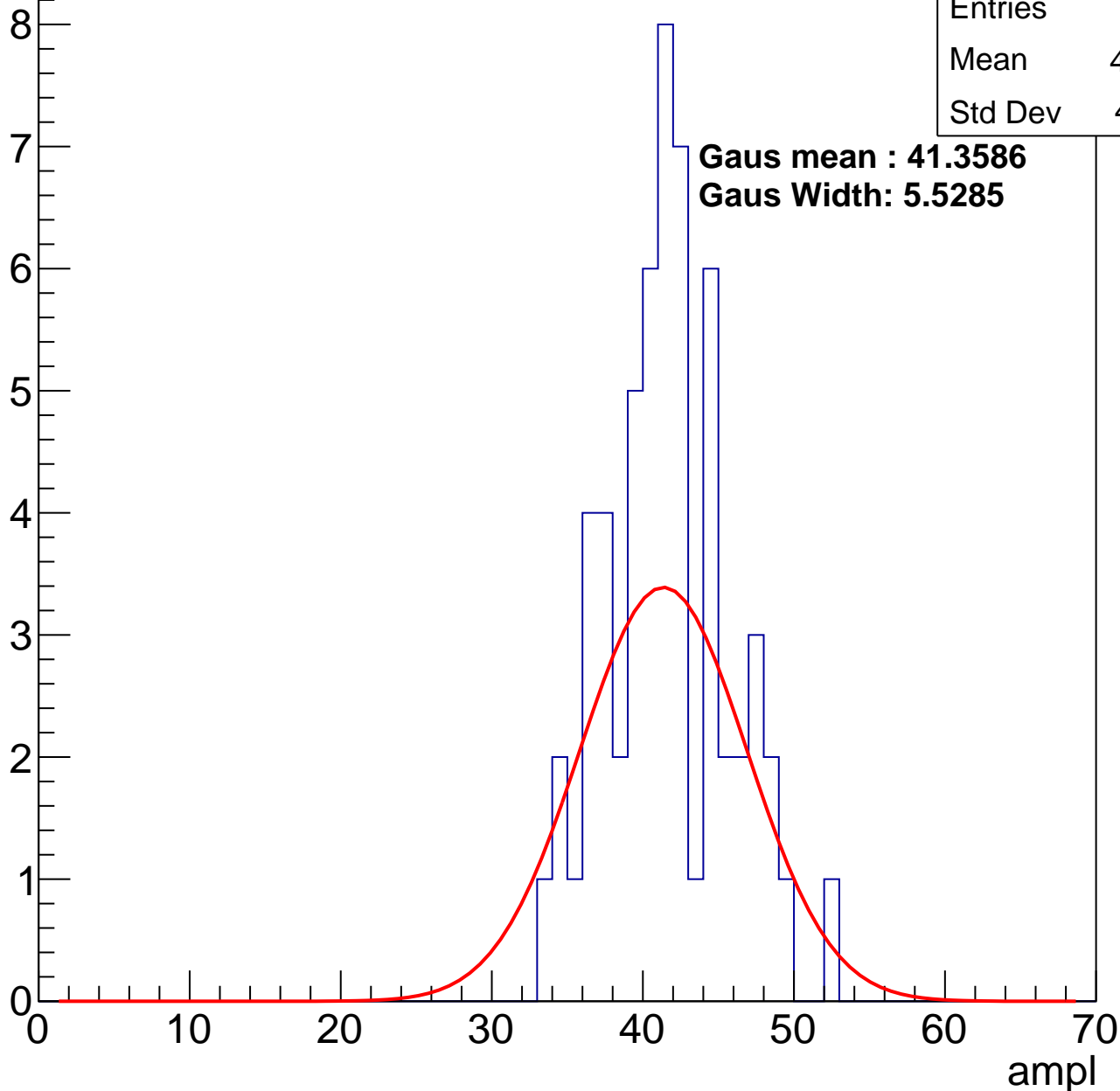
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.17
Std Dev	4.031

Gaus mean : 41.3586

Gaus Width: 5.5285

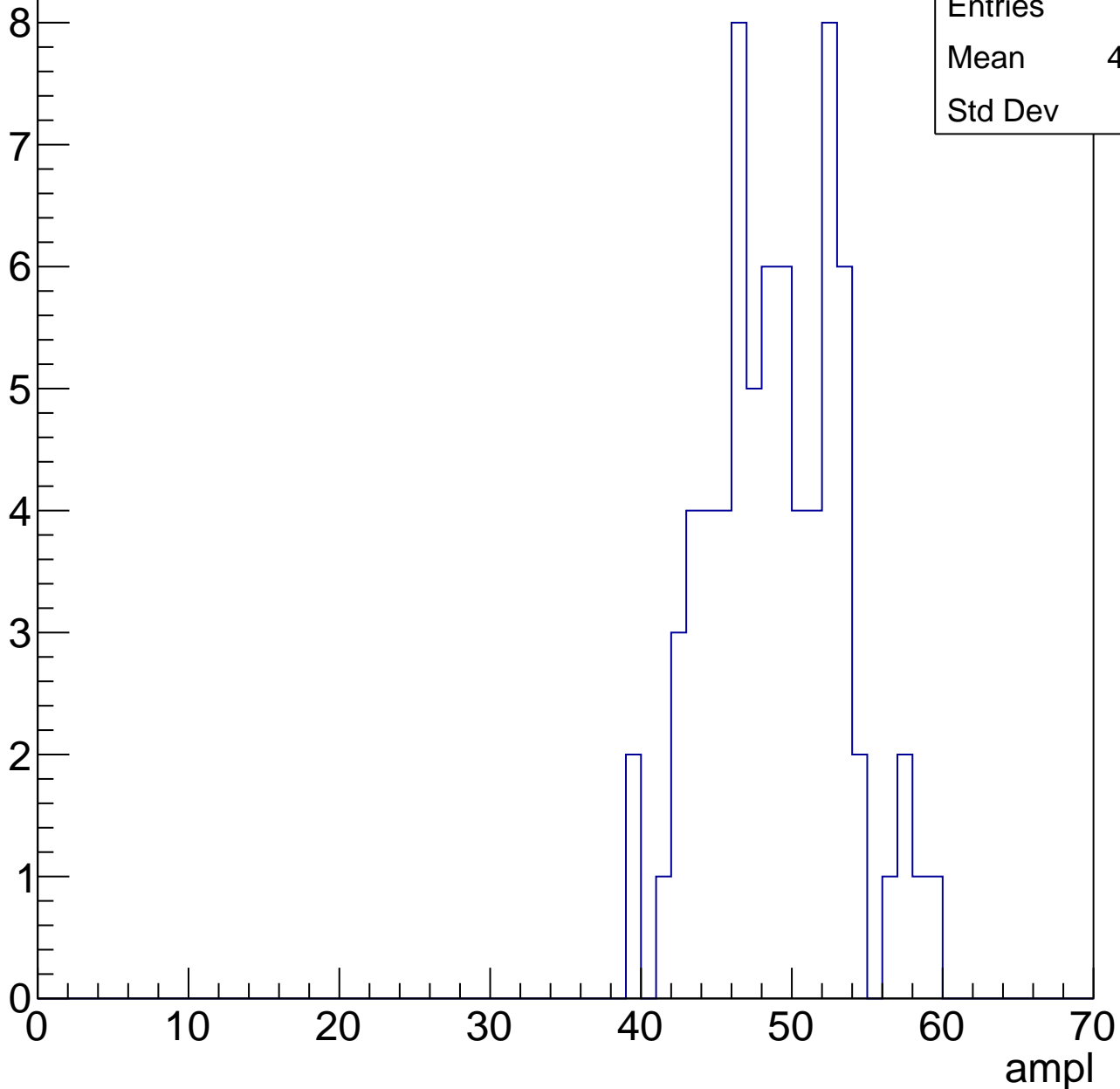


B1L103S, U24-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.49
Std Dev	4.4

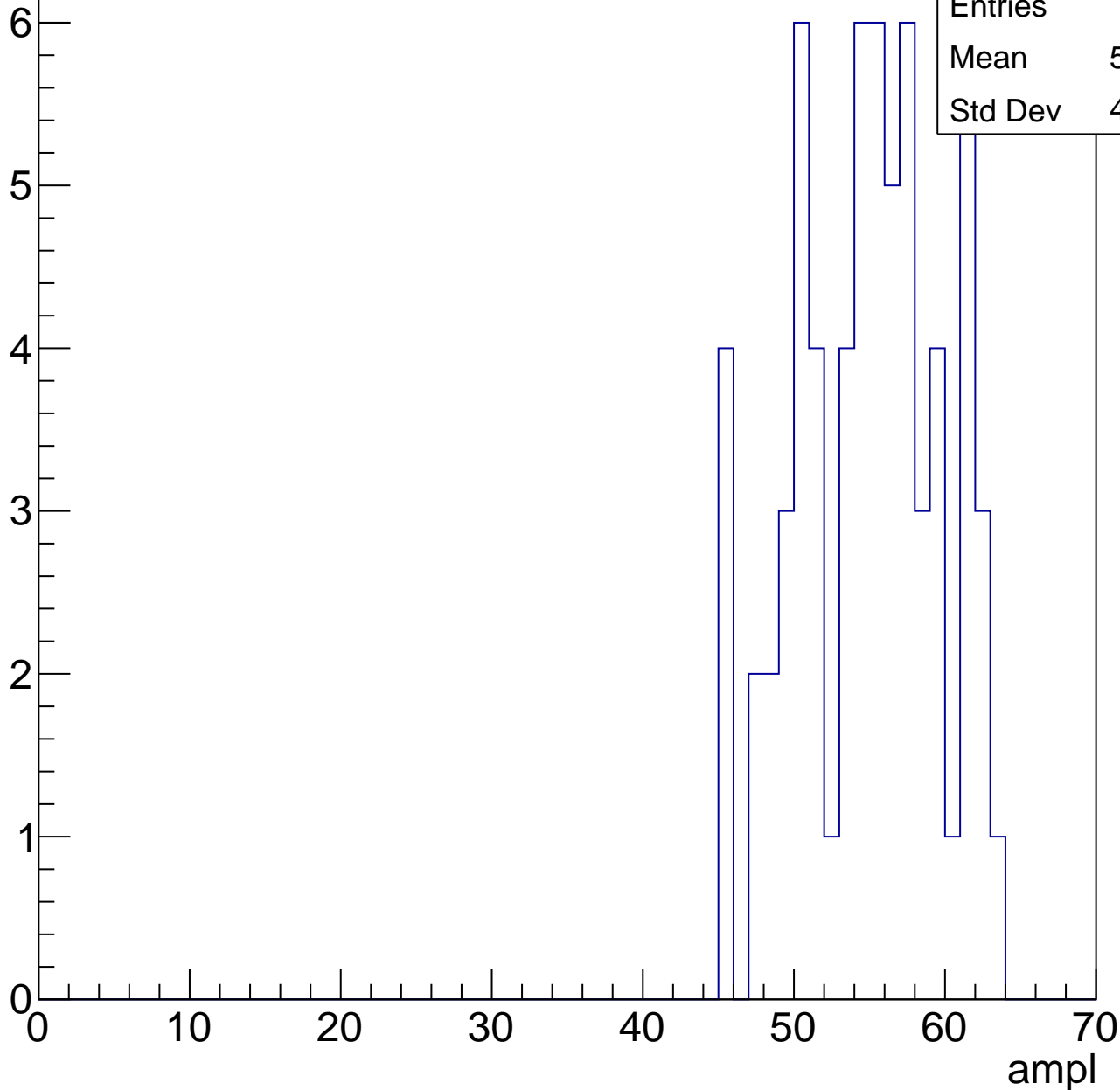


B1L103S, U24-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	54.42
Std Dev	4.776

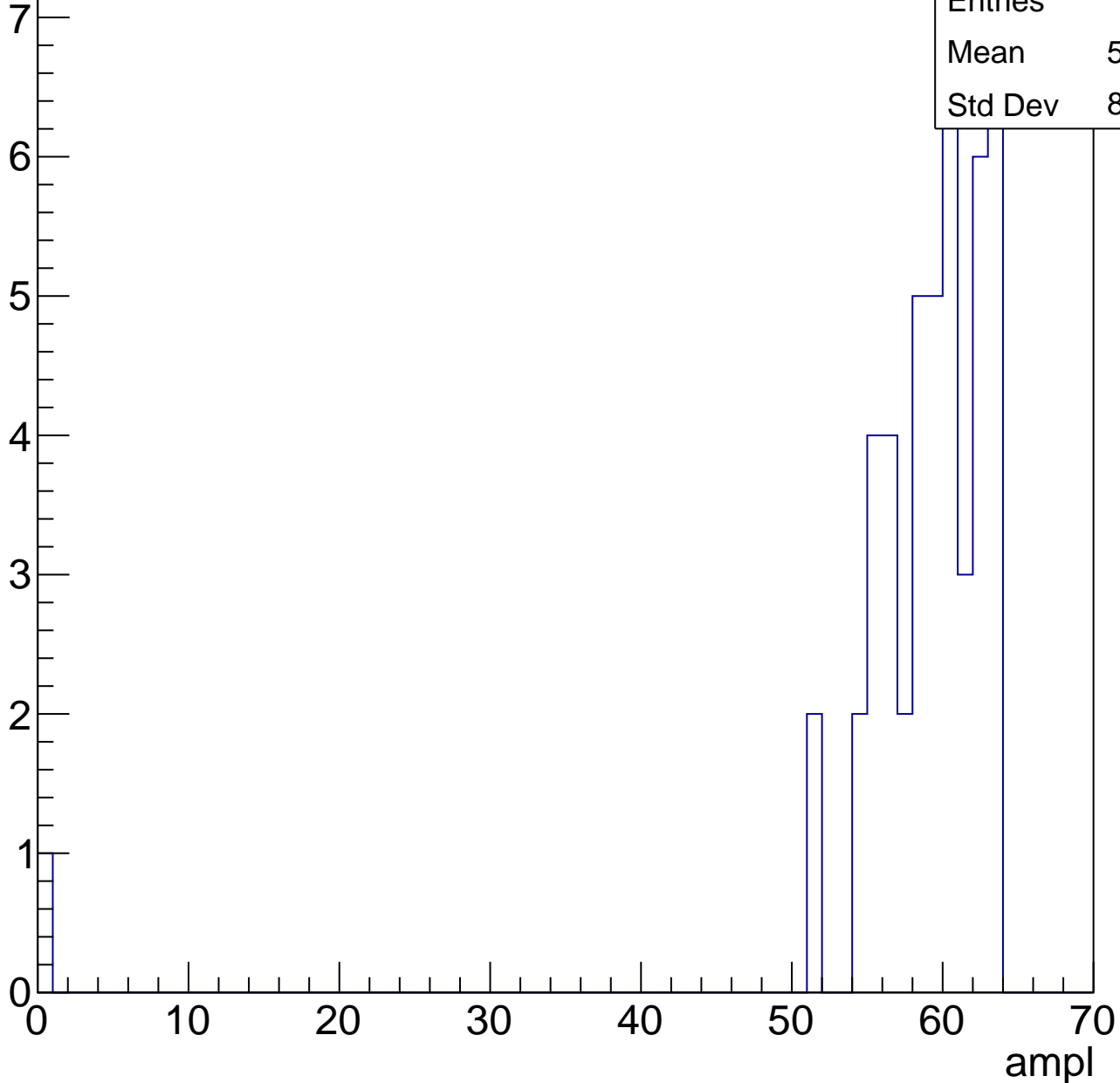


B1L103S, U24-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	57.69
Std Dev	8.984



B1L103S, U24-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

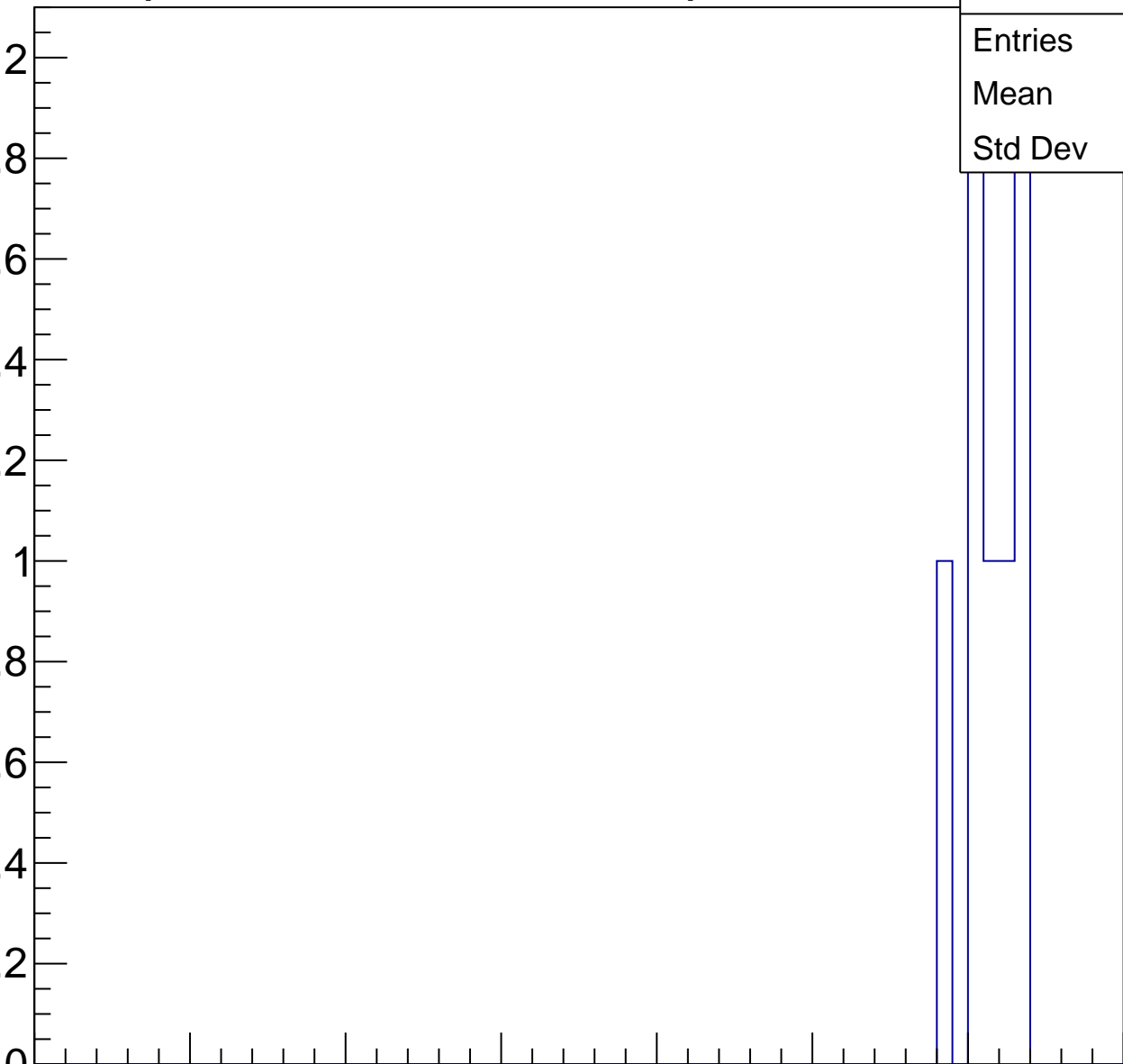
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61
Std Dev	1.69

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

