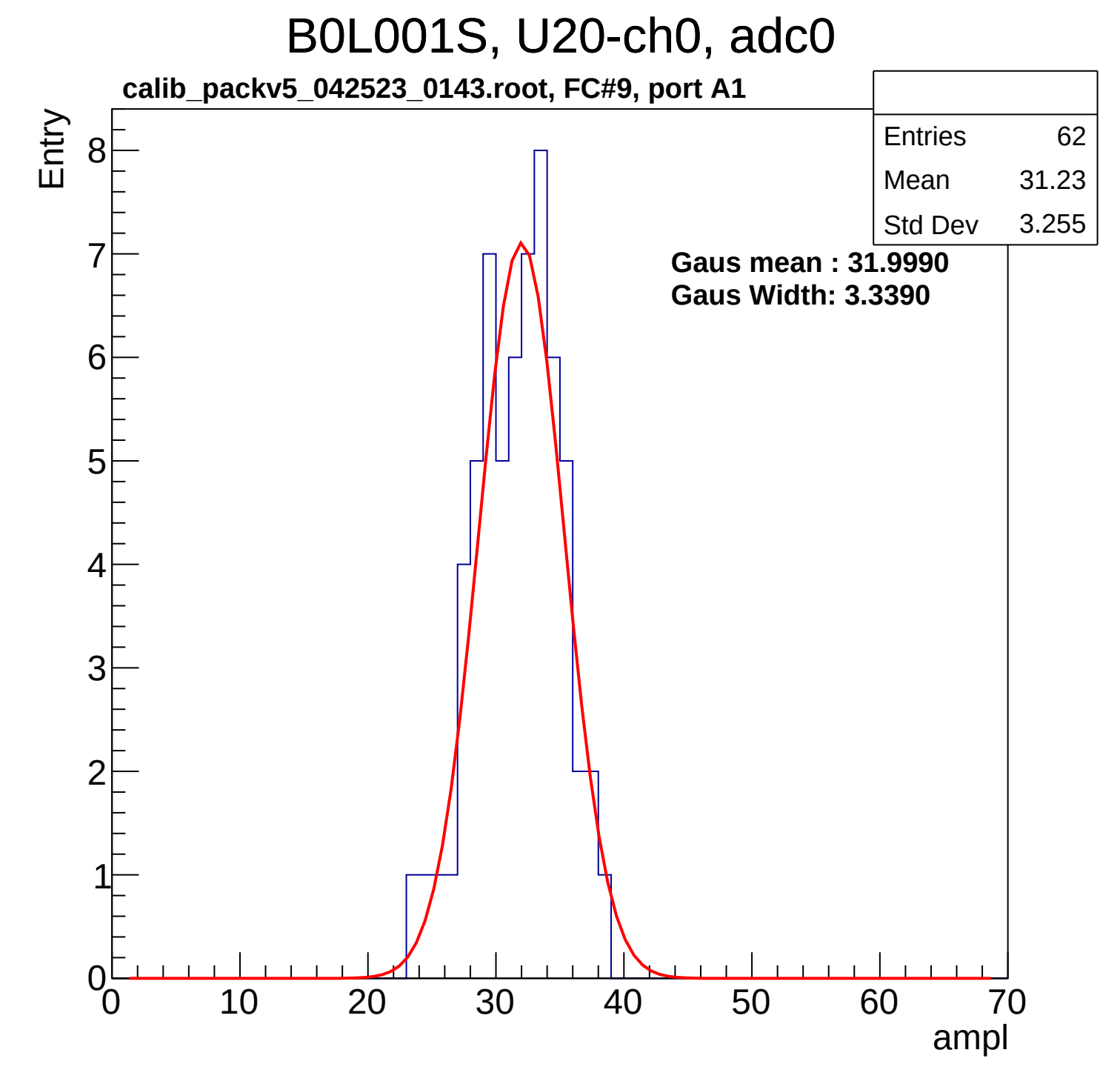
B0L001S, U20-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.23
Std Dev 3.255
Gaus mean : 31.9990
Gaus Width: 3.3390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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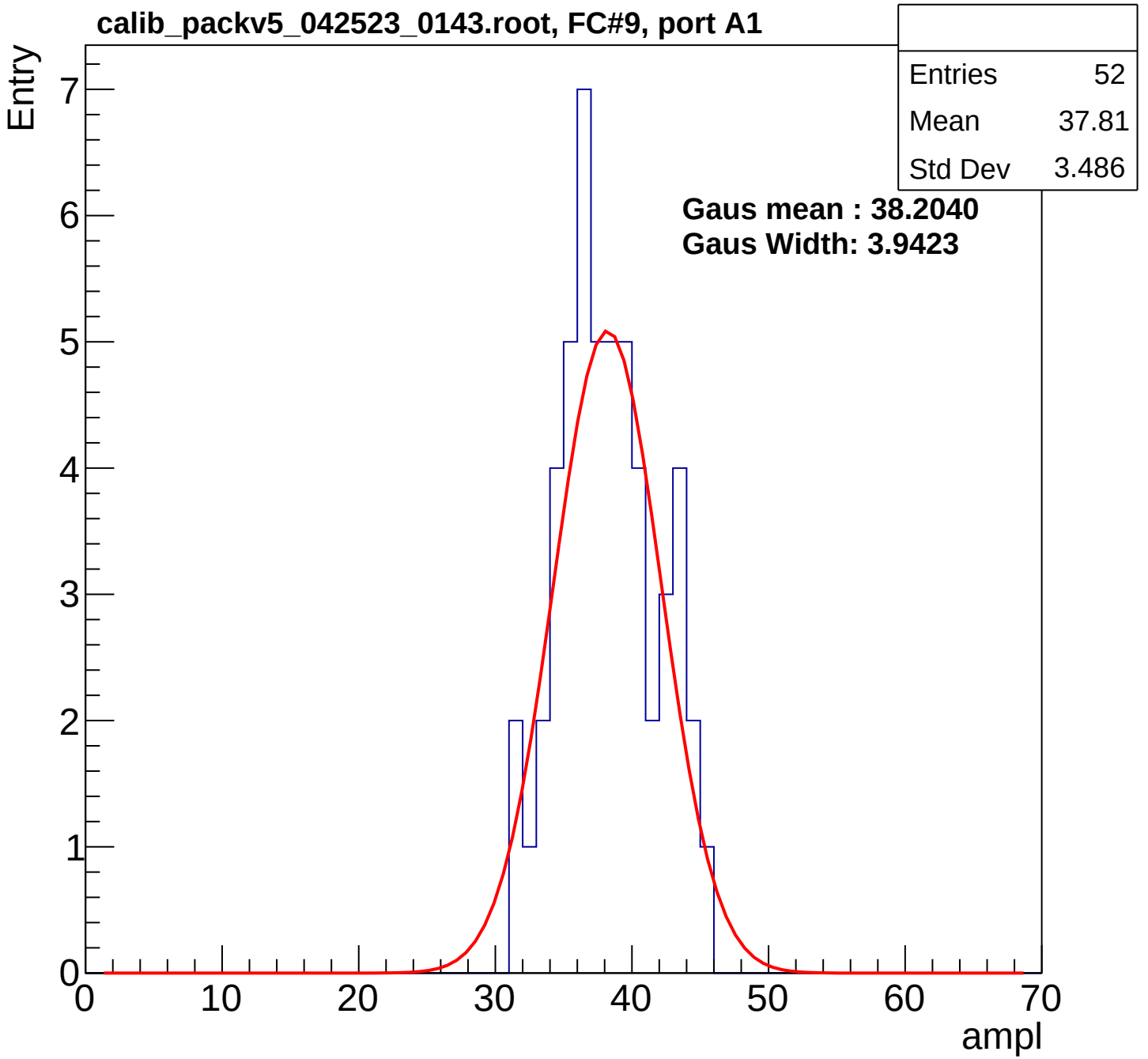
B0L001S, U20-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 37.81
Std Dev 3.486
Gaus mean : 38.2040
Gaus Width: 3.9423
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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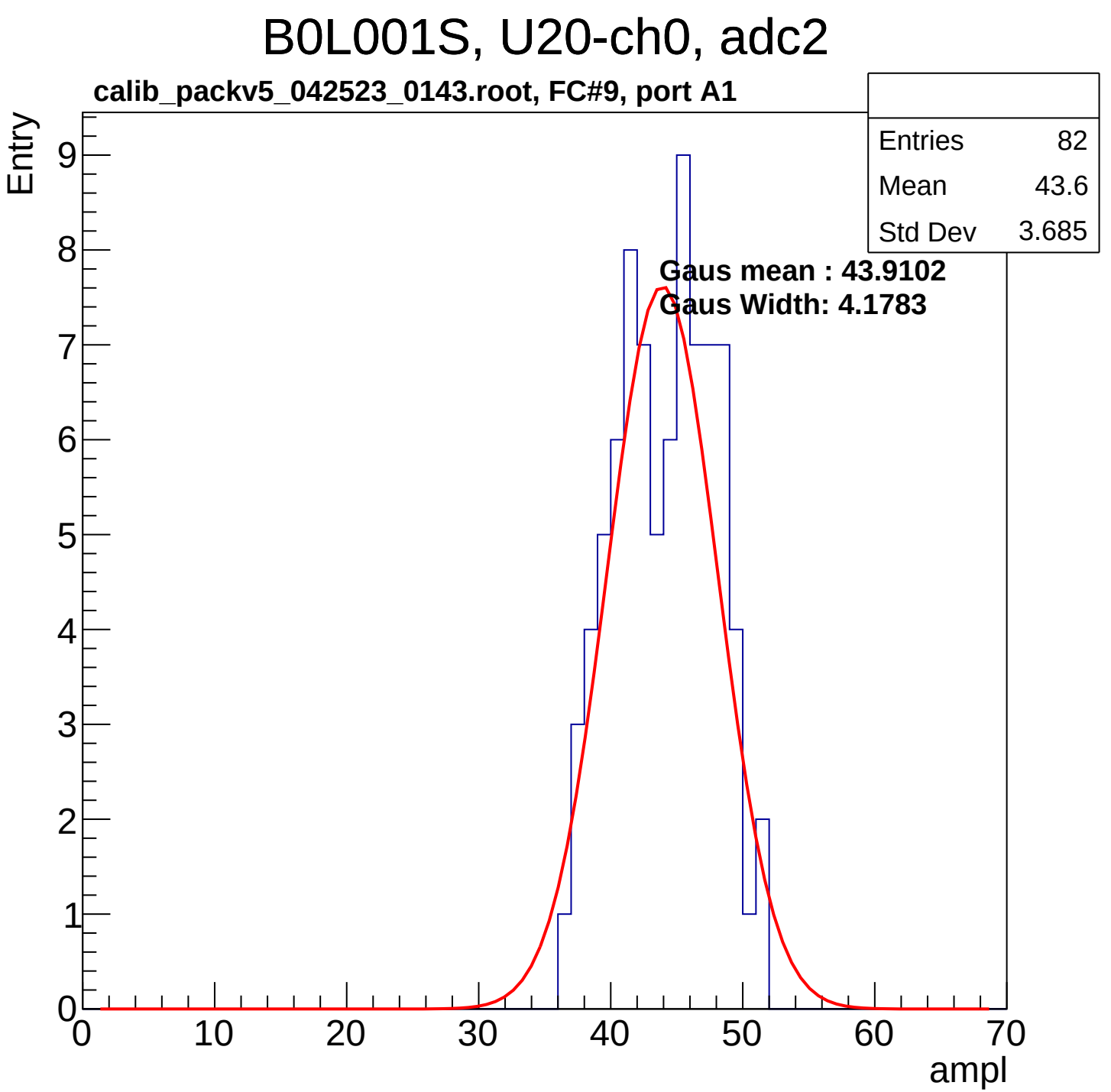
B0L001S, U20-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 43.6
Std Dev 3.685
Gaus mean : 43.9102
Gaus Width: 4.1783
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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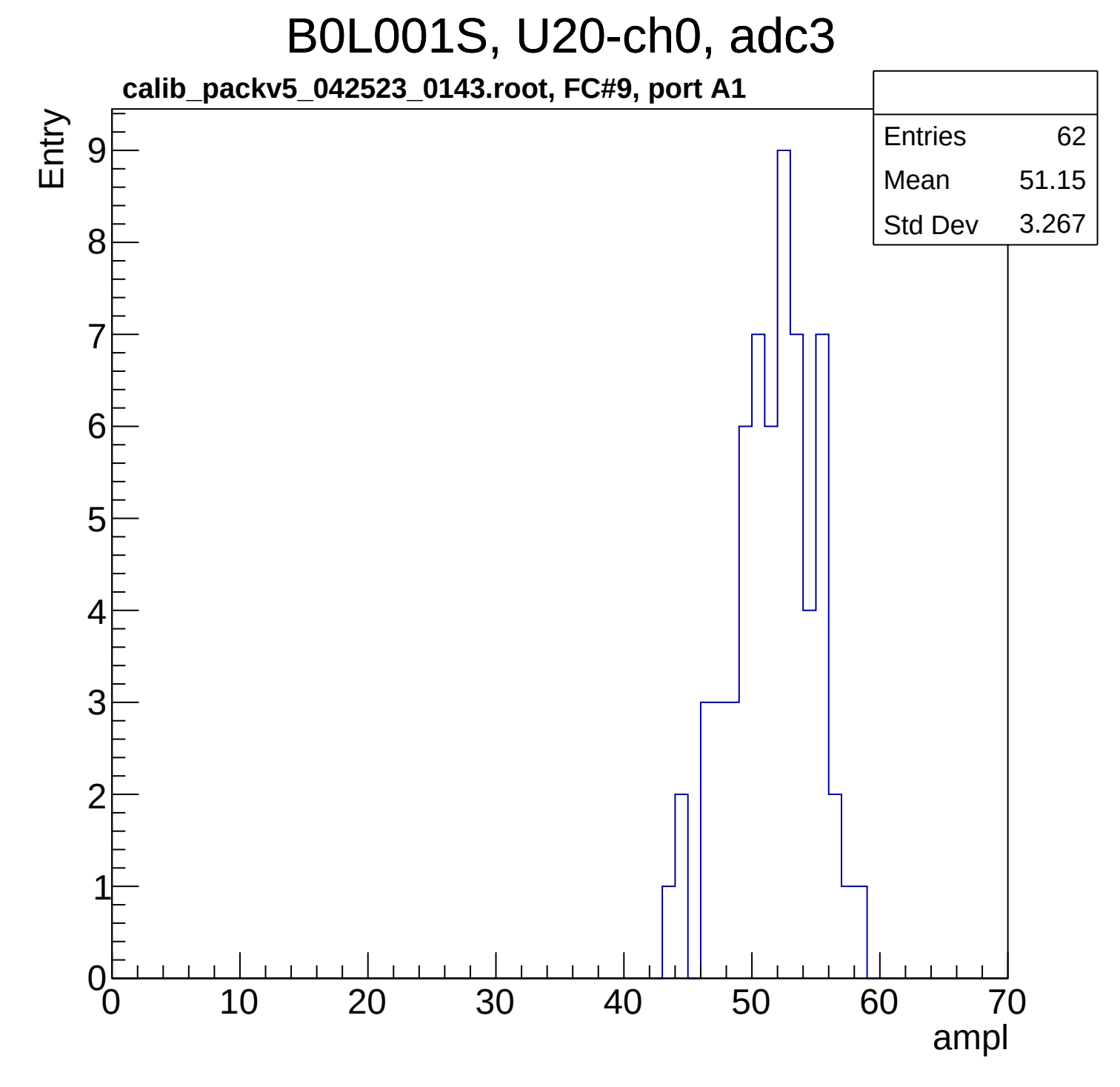
B0L001S, U20-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.15
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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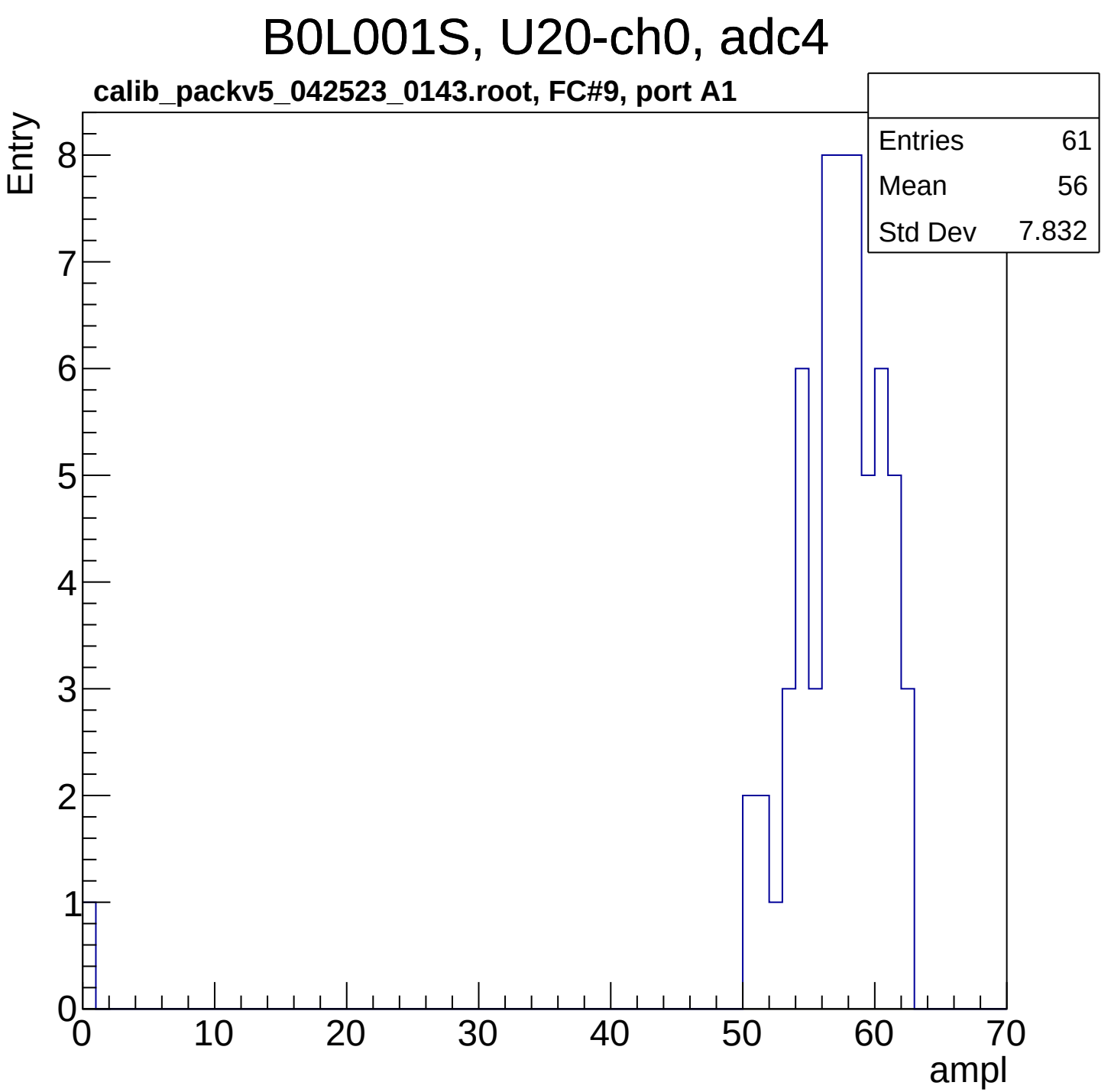
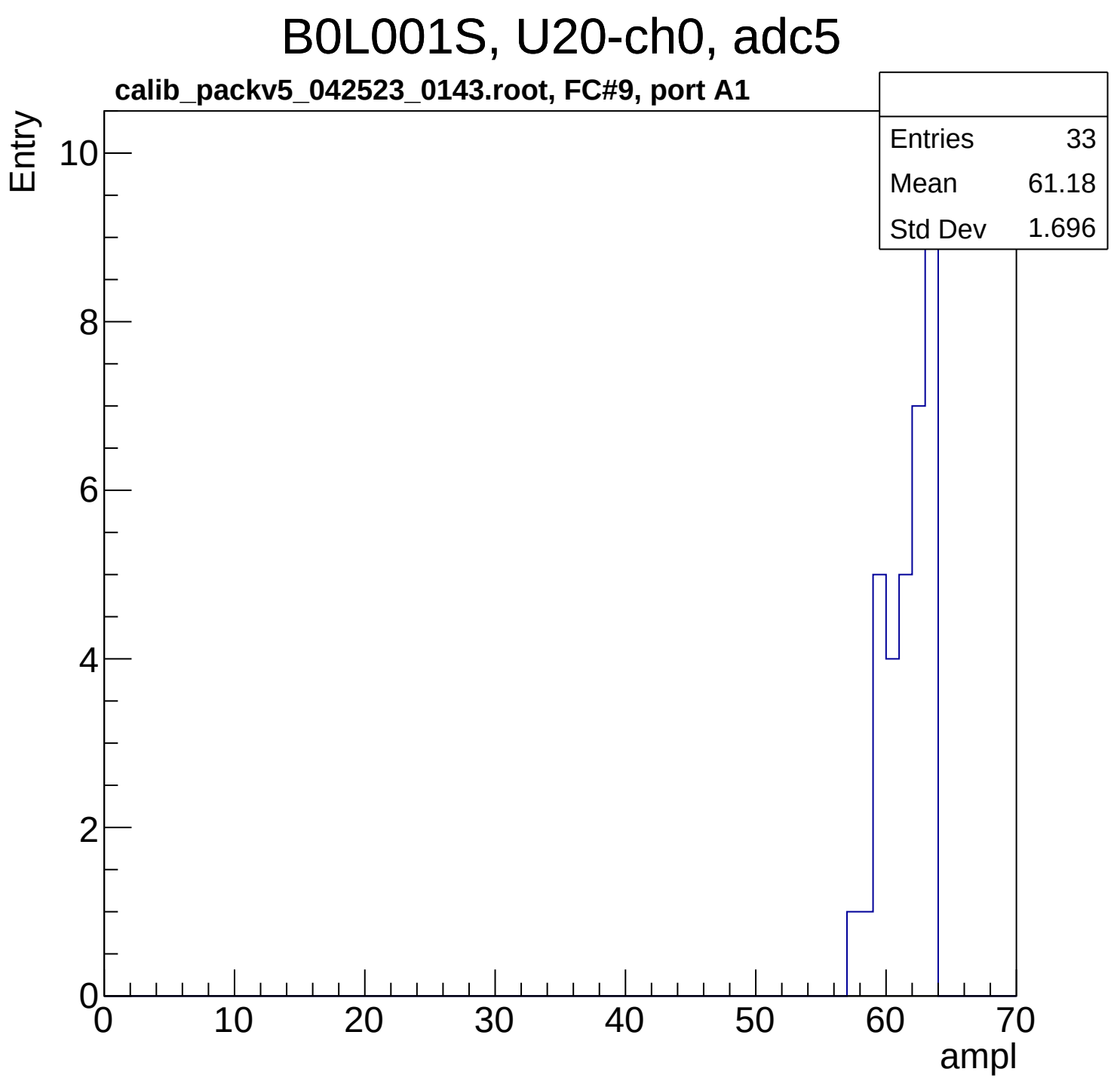
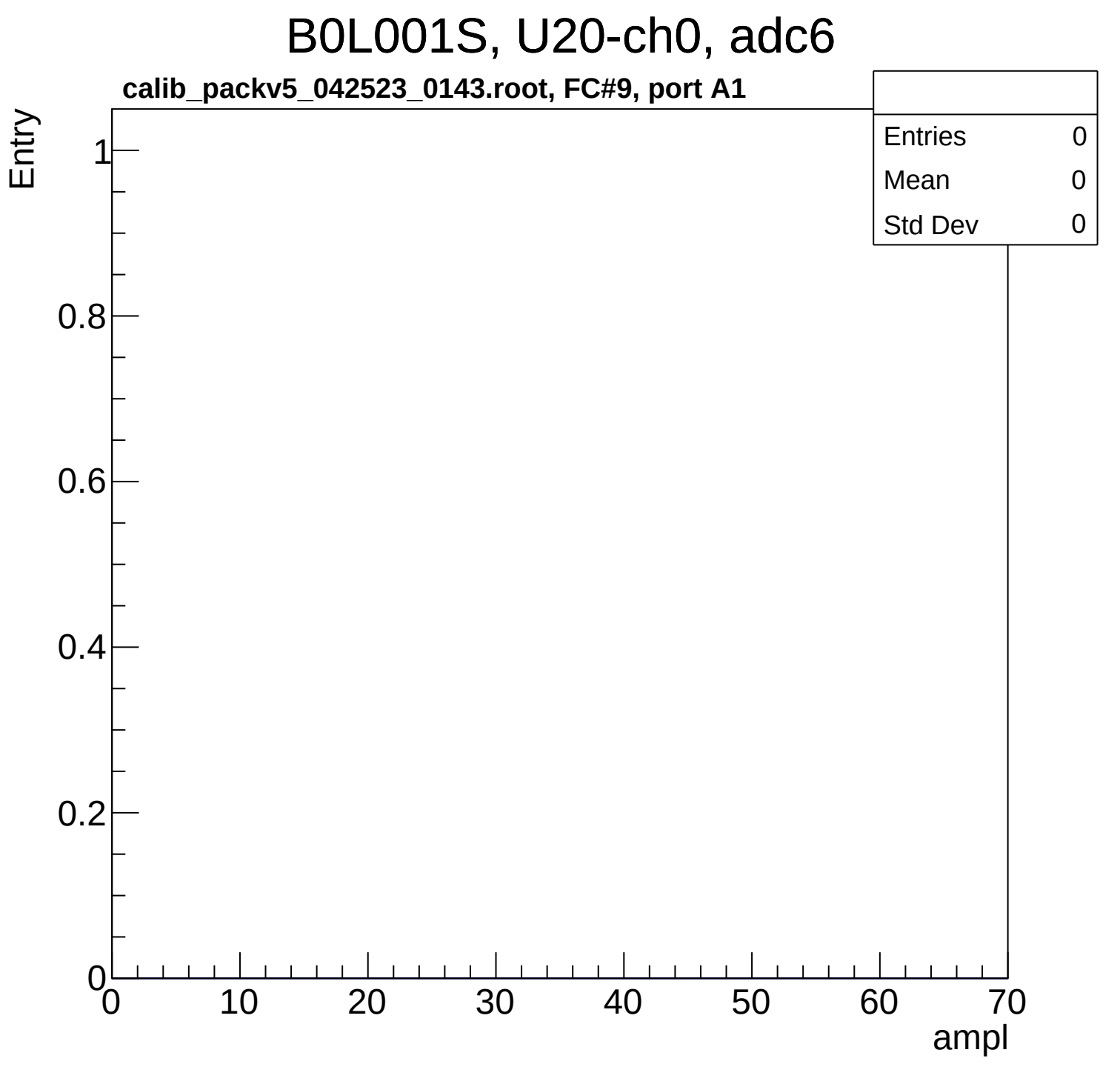
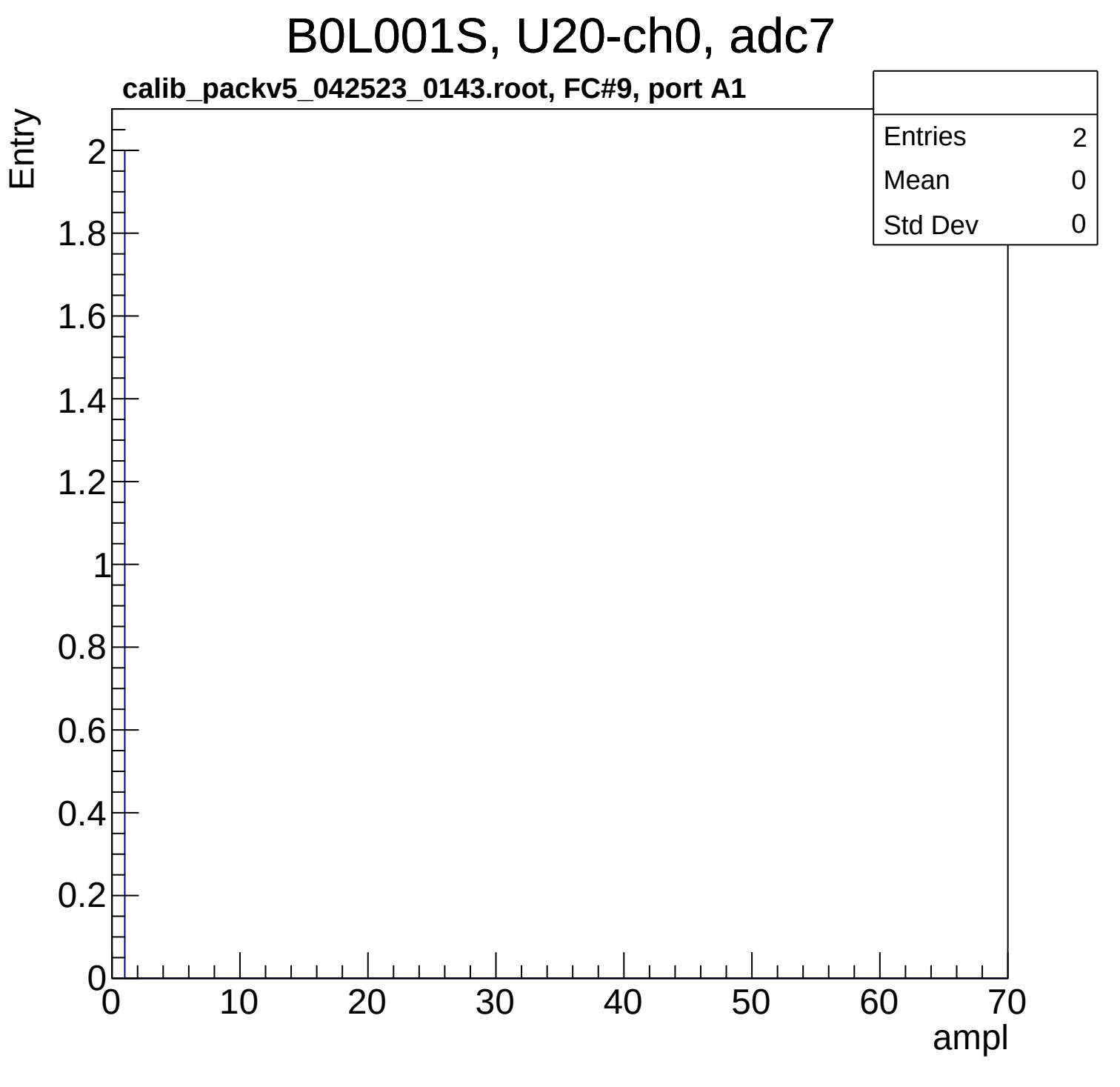
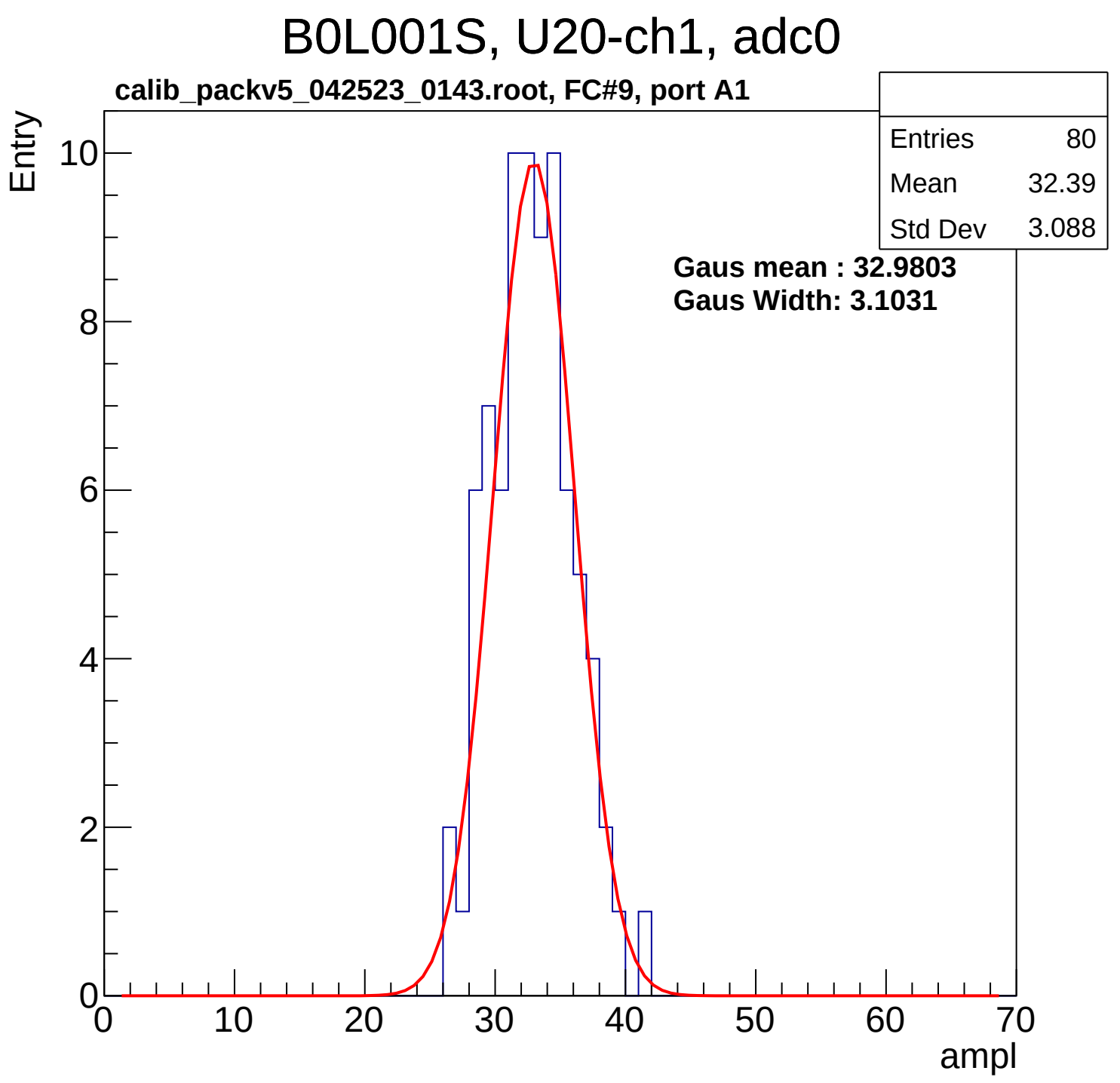
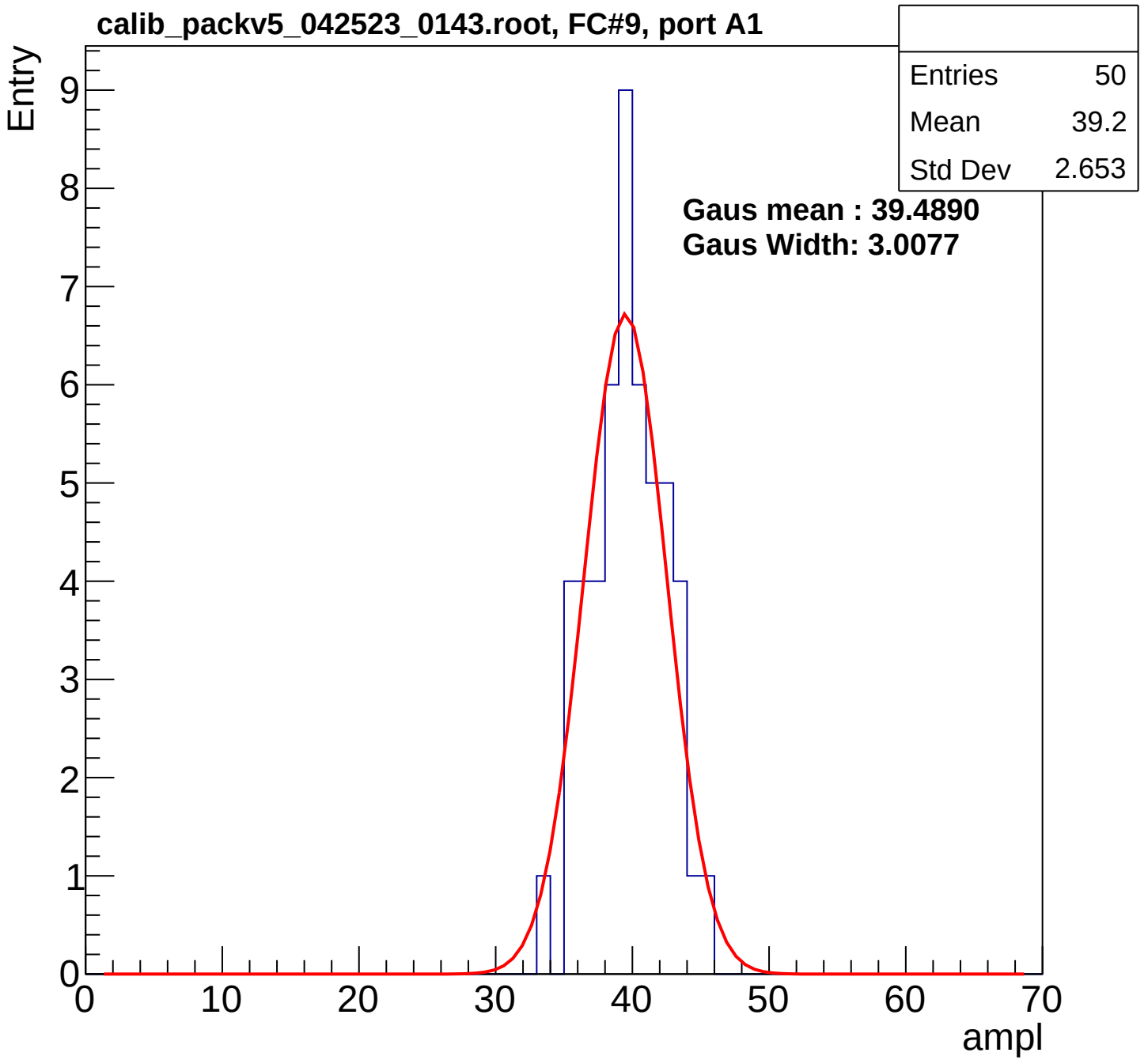

B0L001S, U20-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56
Std Dev 7.832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 61.18
Std Dev 1.696
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 32.39
Std Dev 3.088
Gaus mean : 32.9803
Gaus Width: 3.1031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 39.2
Std Dev 2.653
Gaus mean : 39.4890
Gaus Width: 3.0077
Entry
ampl
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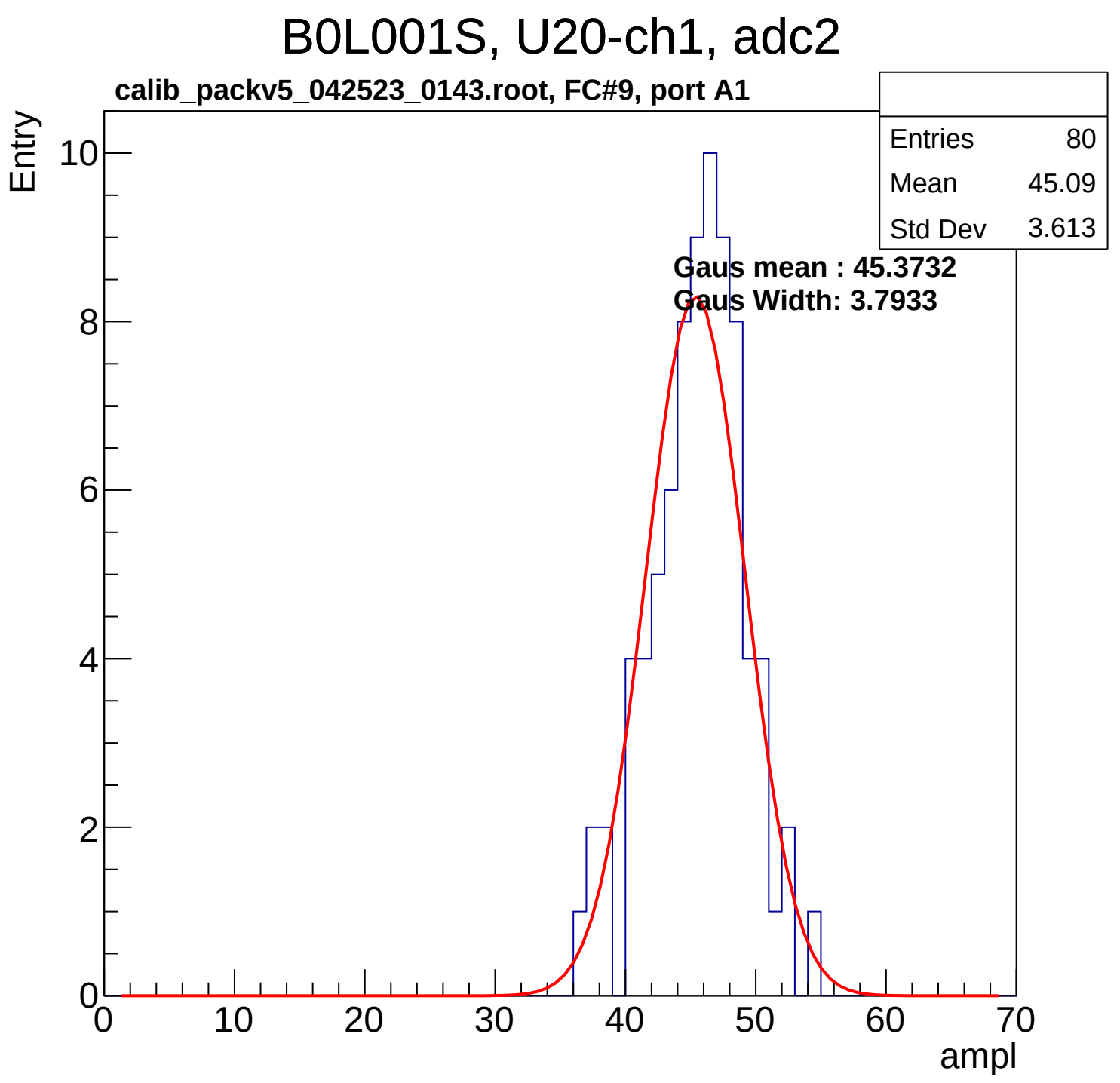
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 45.09
Std Dev 3.613
Gaus mean : 45.3732
Gaus Width: 3.7933
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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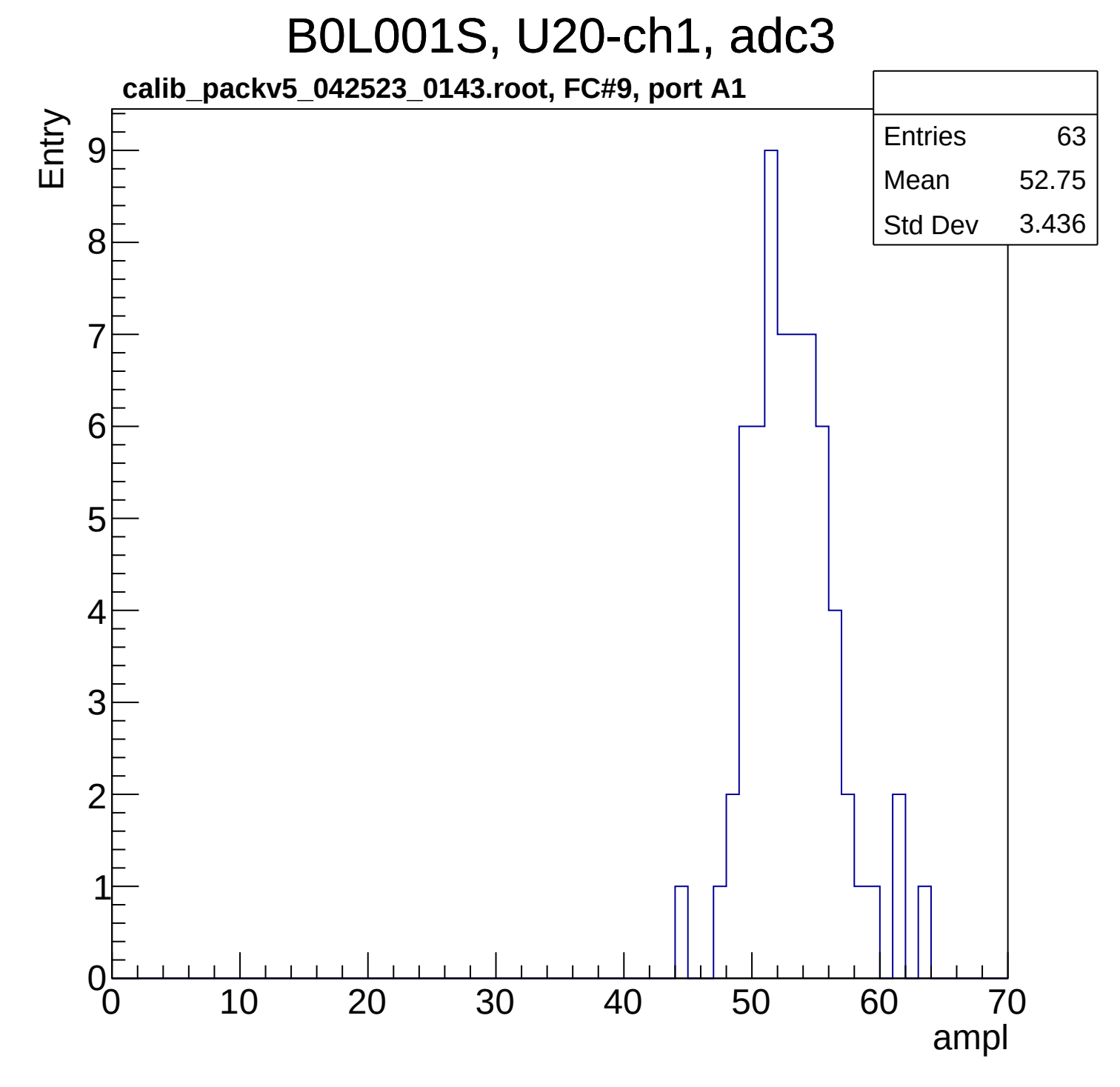
B0L001S, U20-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 52.75
Std Dev 3.436
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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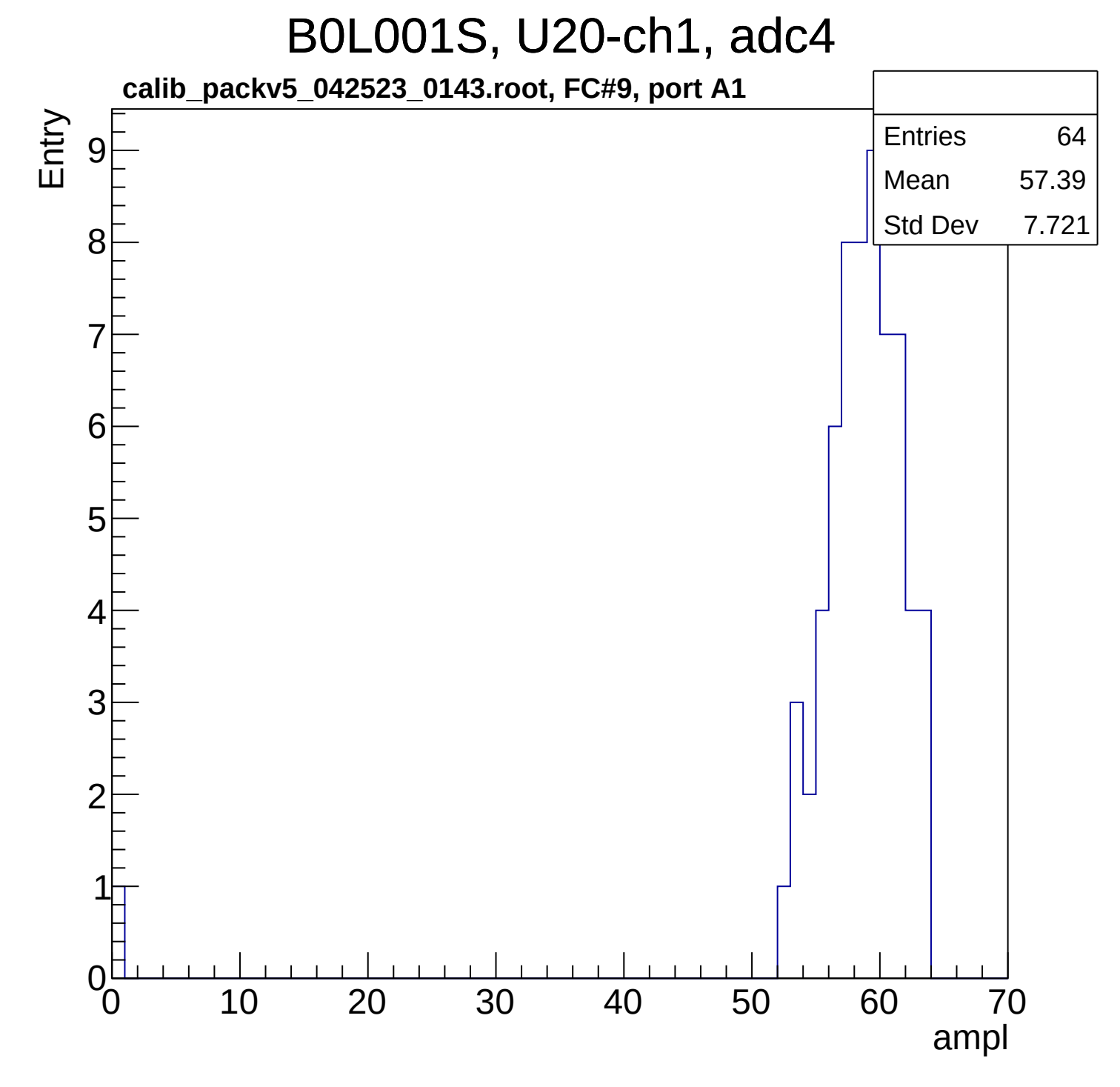
B0L001S, U20-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.39
Std Dev 7.721
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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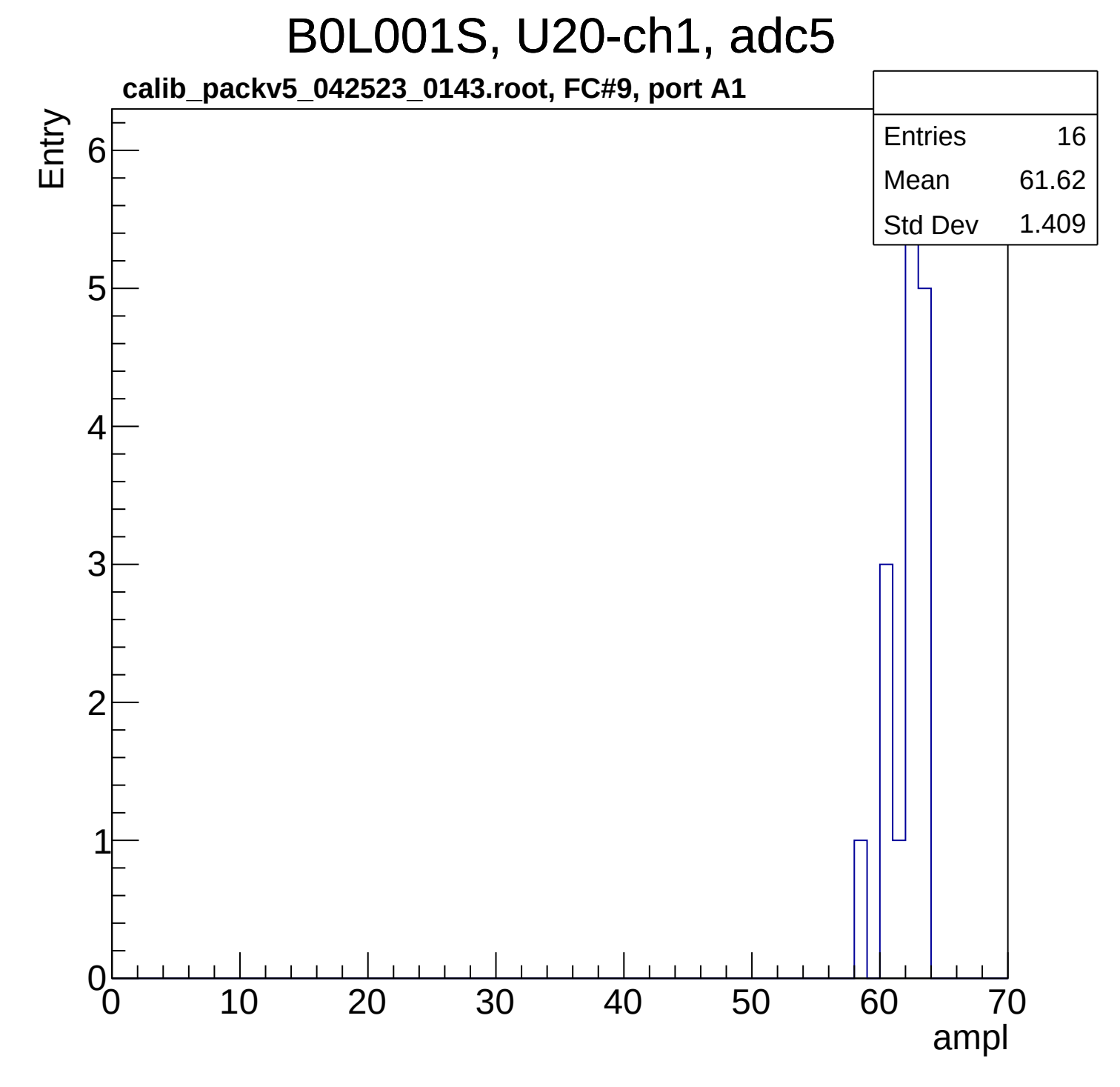
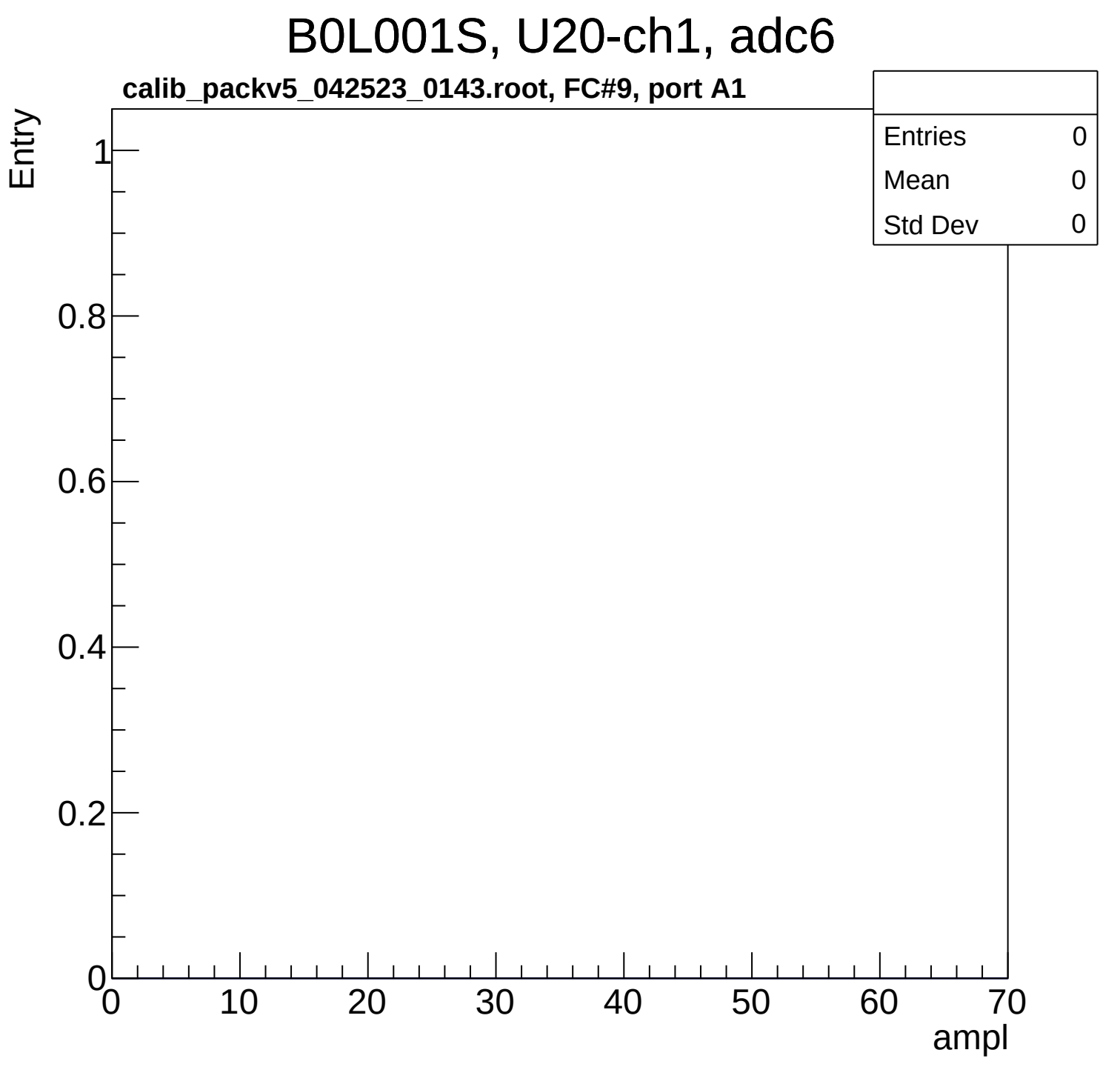
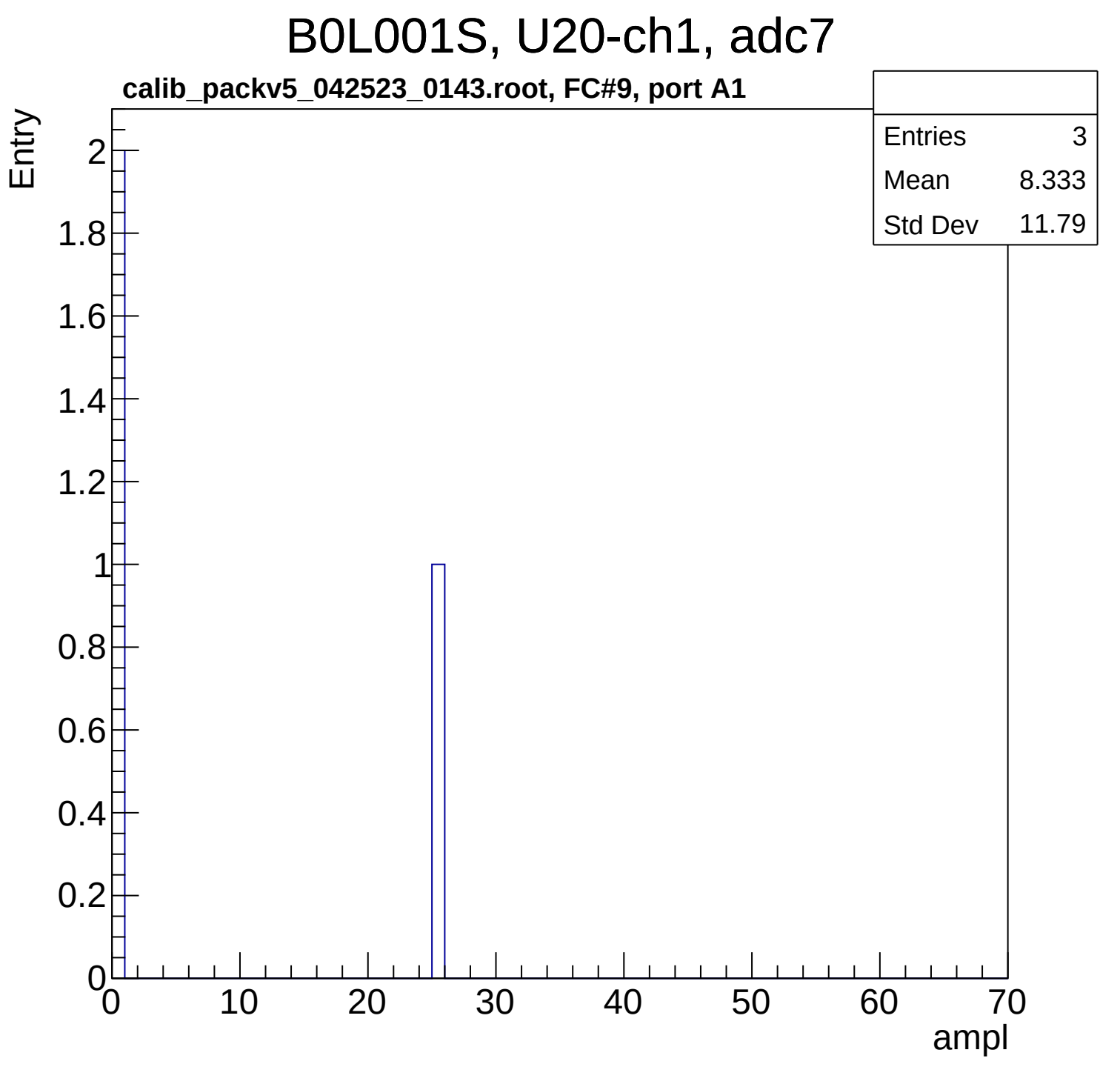

B0L001S, U20-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 61.62
Std Dev 1.409
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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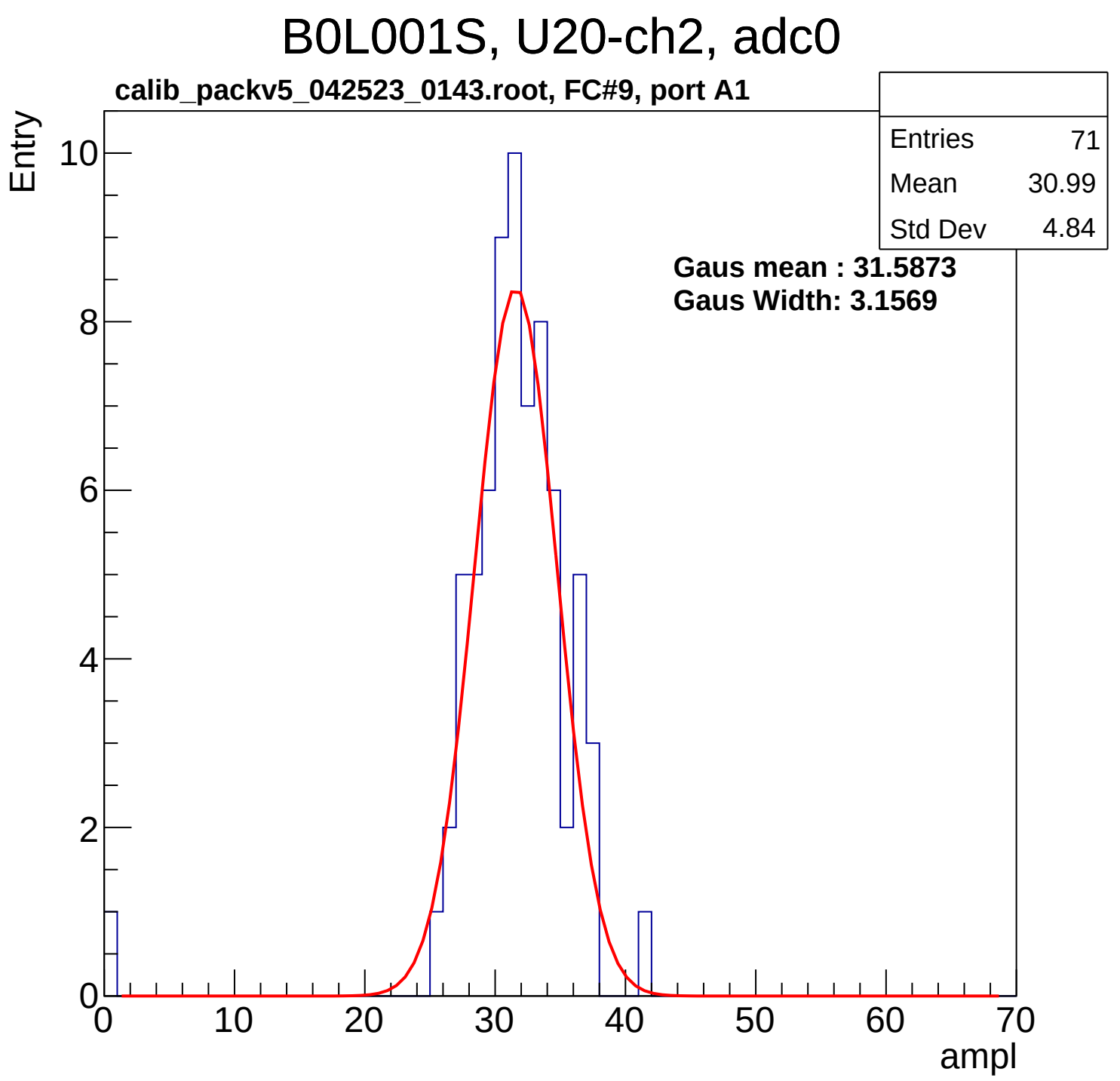
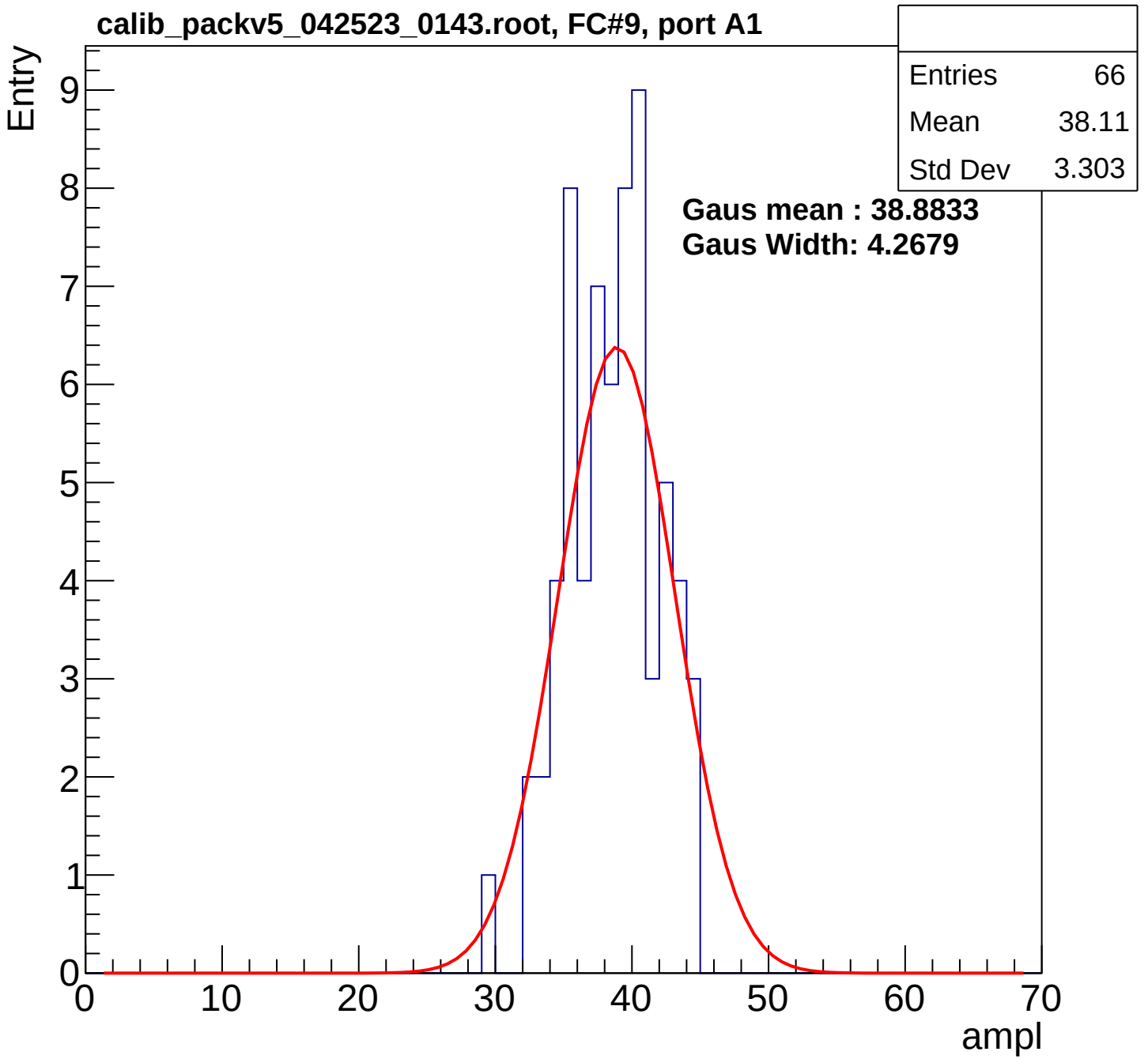

B0L001S, U20-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.99
Std Dev 4.84
Gaus mean : 31.5873
Gaus Width: 3.1569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.11
Std Dev 3.303
Gaus mean : 38.8833
Gaus Width: 4.2679
Entry
ampl
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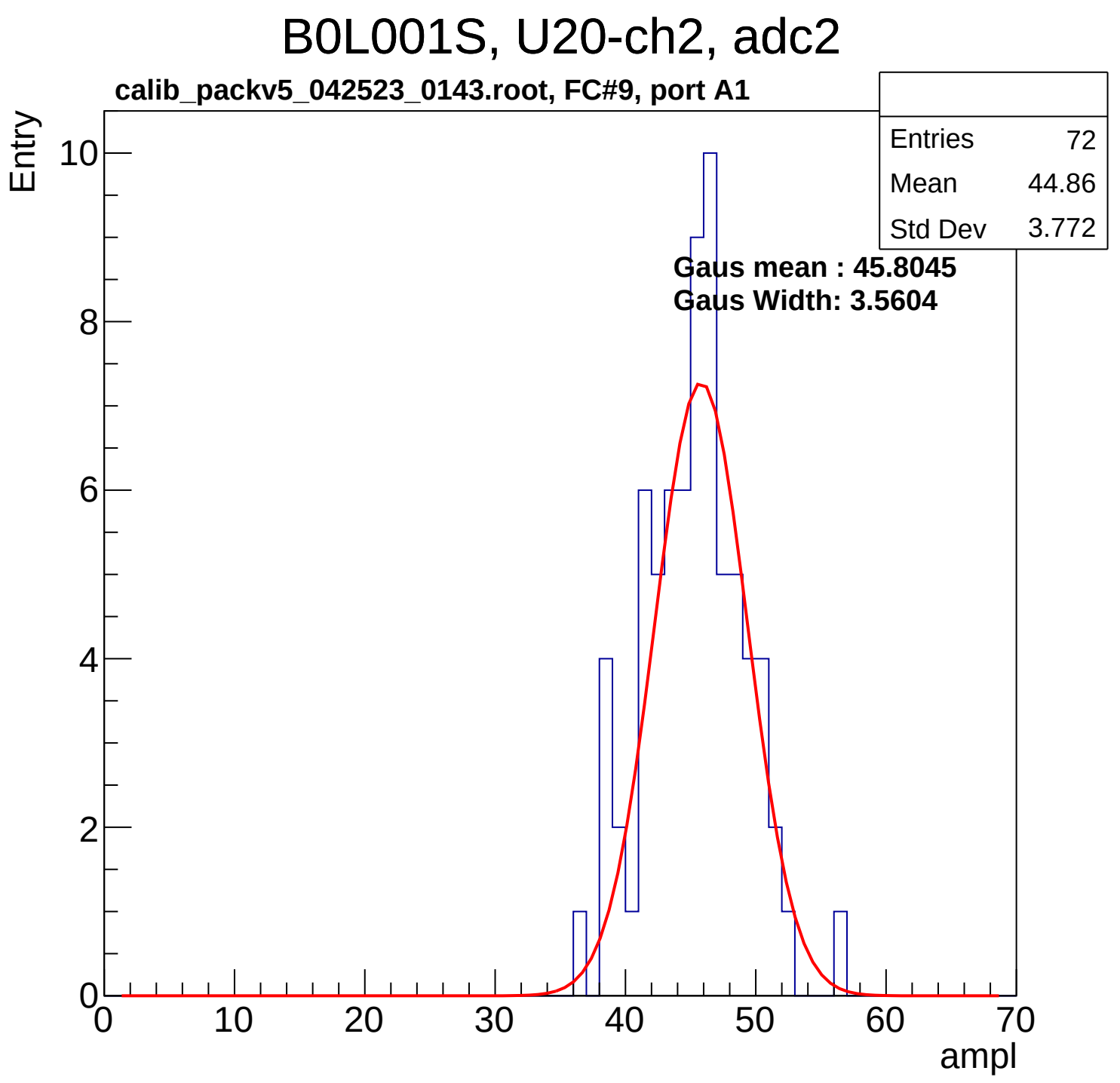
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.86
Std Dev 3.772
Gaus mean : 45.8045
Gaus Width: 3.5604
Entry
ampl
0
2
4
6
8
10
0
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40
50
60
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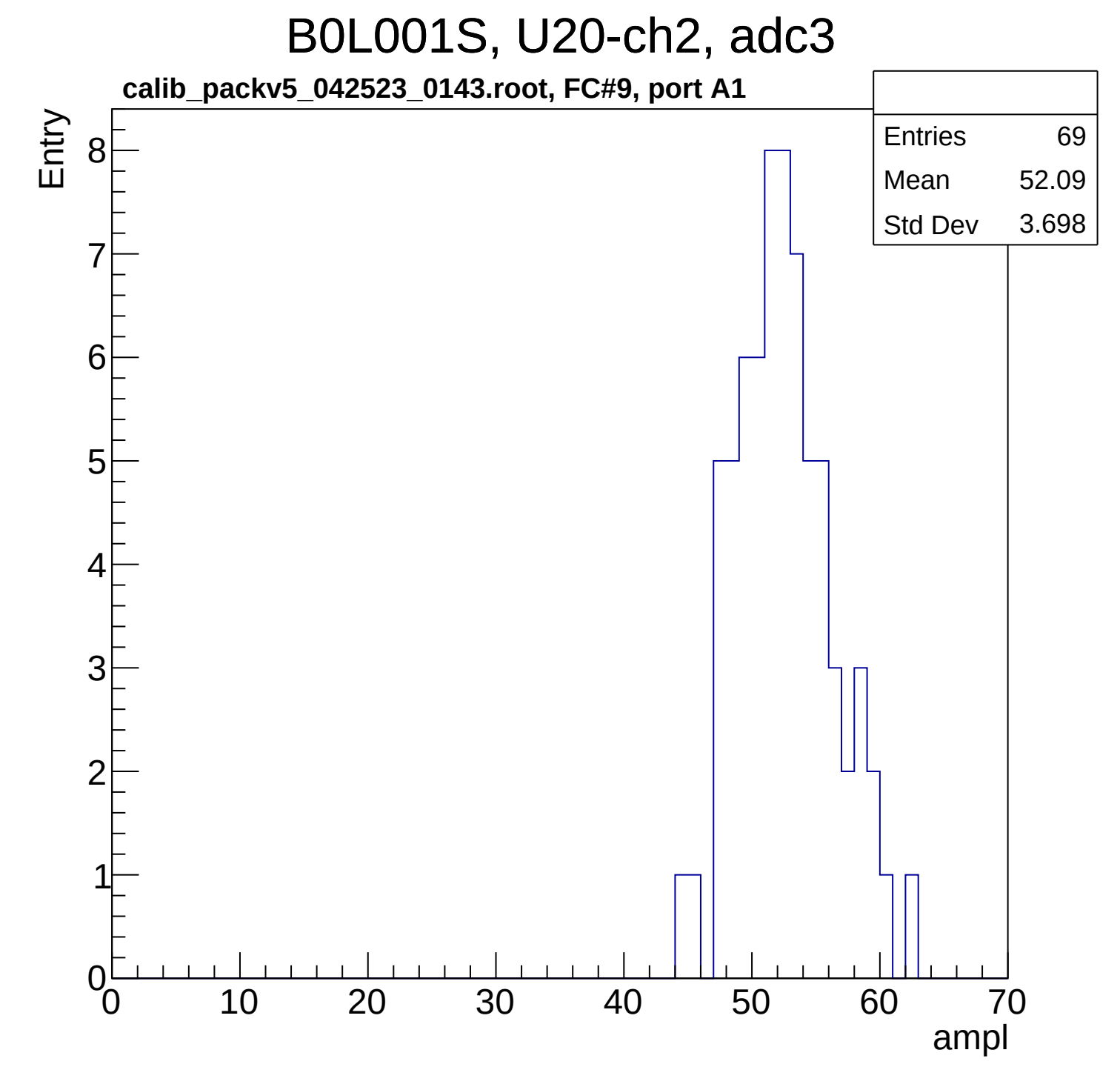
B0L001S, U20-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 52.09
Std Dev 3.698
Entry
ampl
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4
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0
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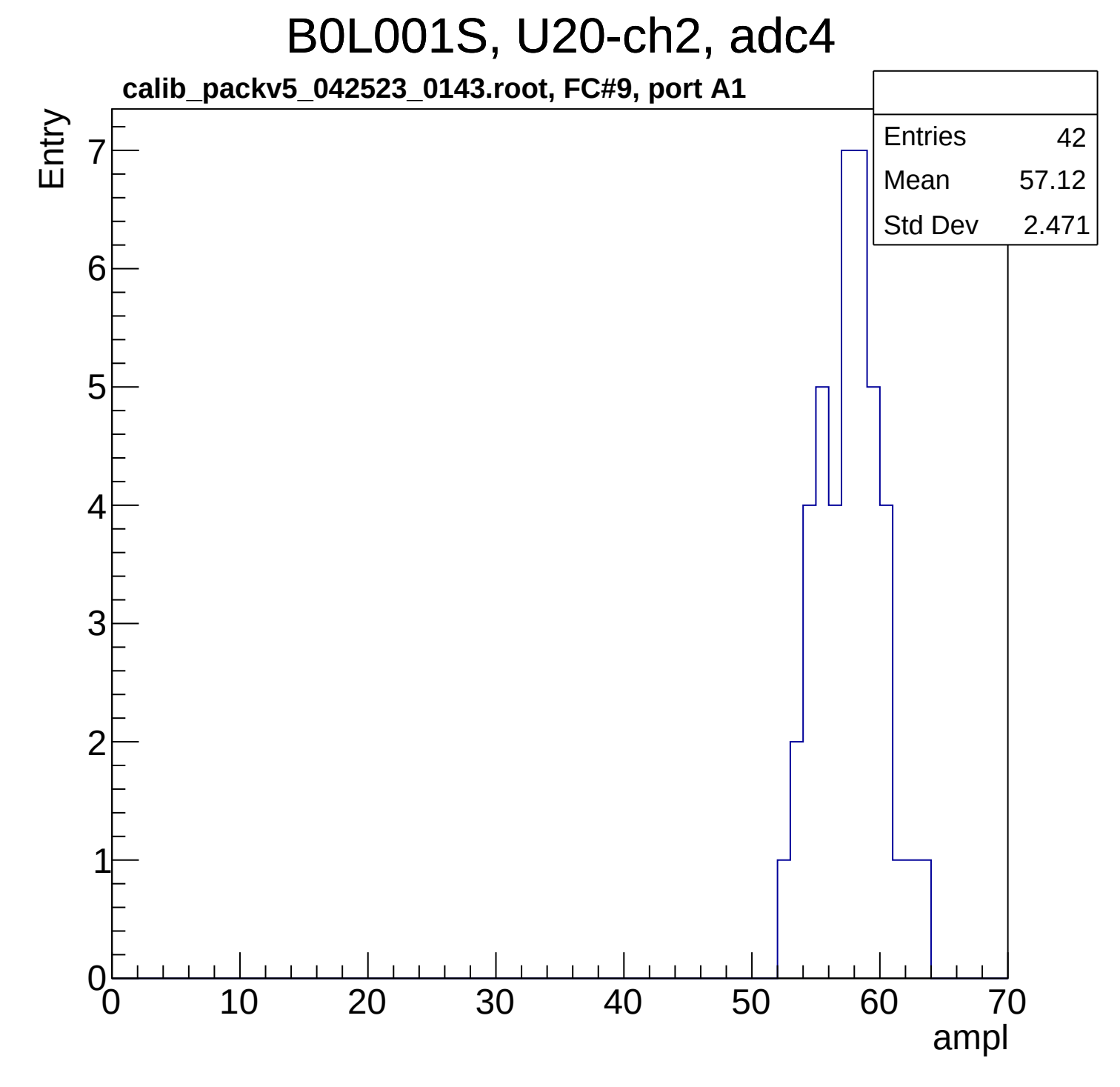
B0L001S, U20-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 57.12
Std Dev 2.471
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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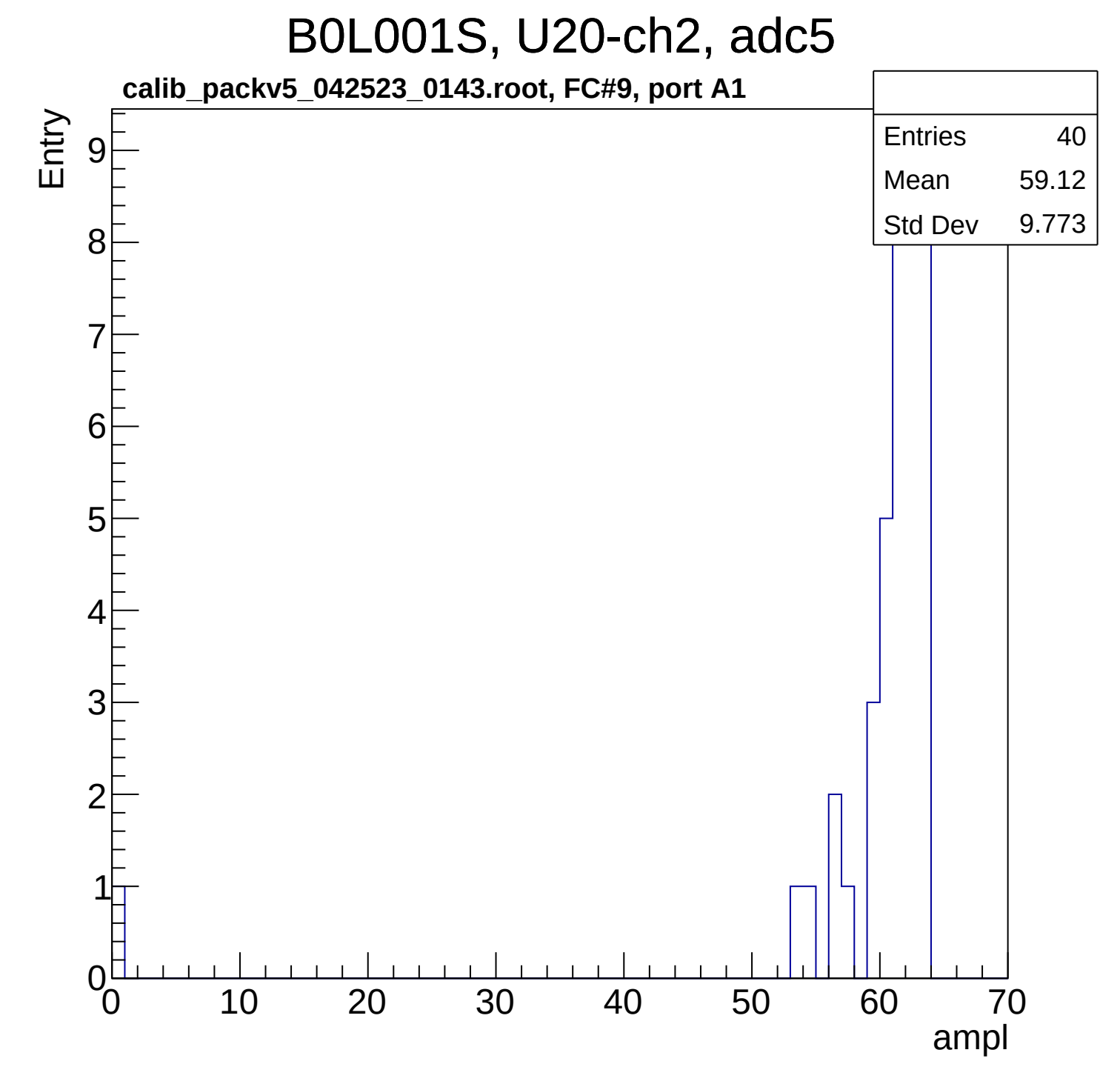
60
70


B0L001S, U20-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.12
Std Dev 9.773
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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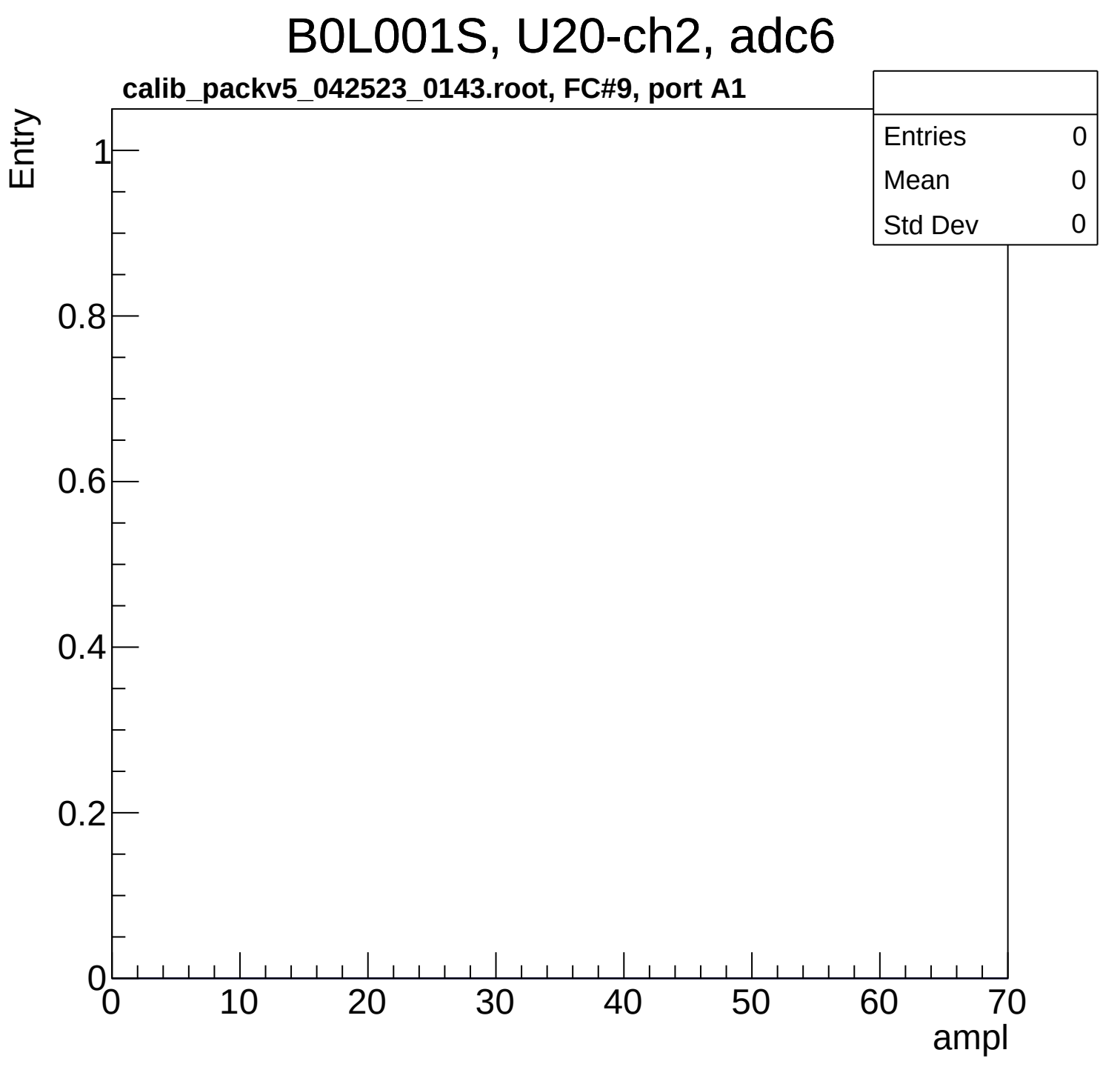
B0L001S, U20-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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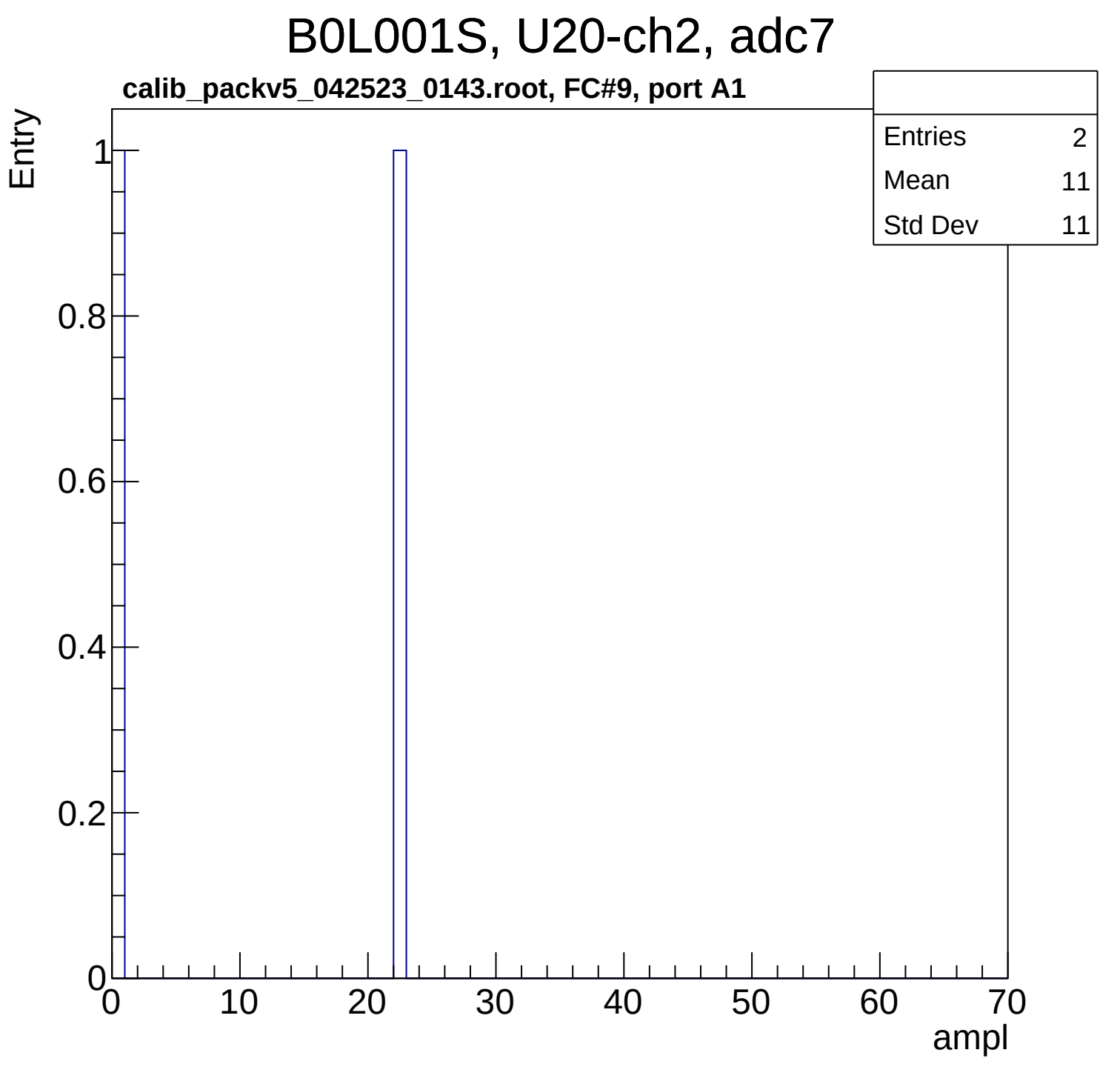
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
0
0
10
20
30
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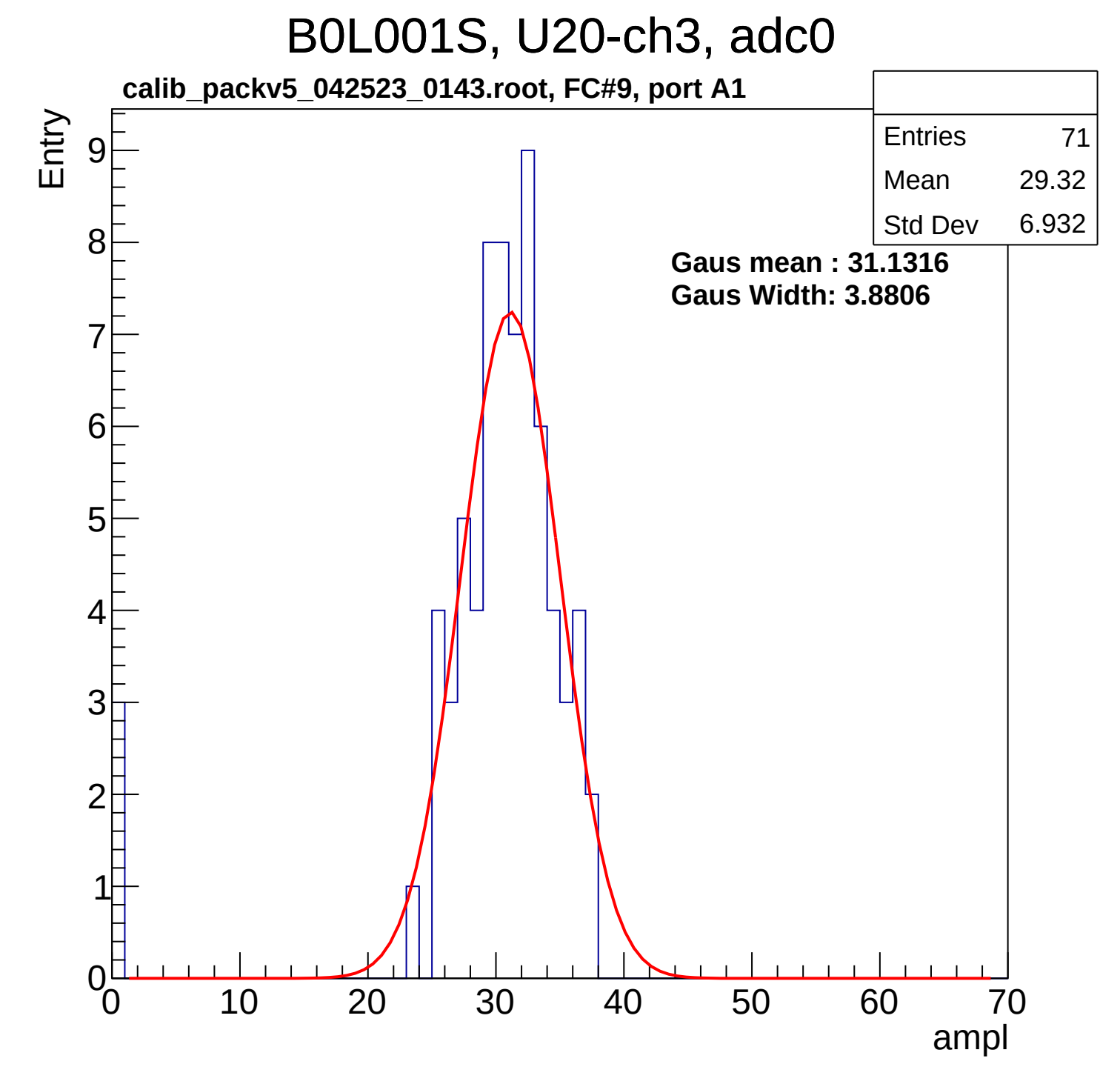
50
60
70
ampl


B0L001S, U20-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.32
Std Dev 6.932
Gaus mean : 31.1316
Gaus Width: 3.8806
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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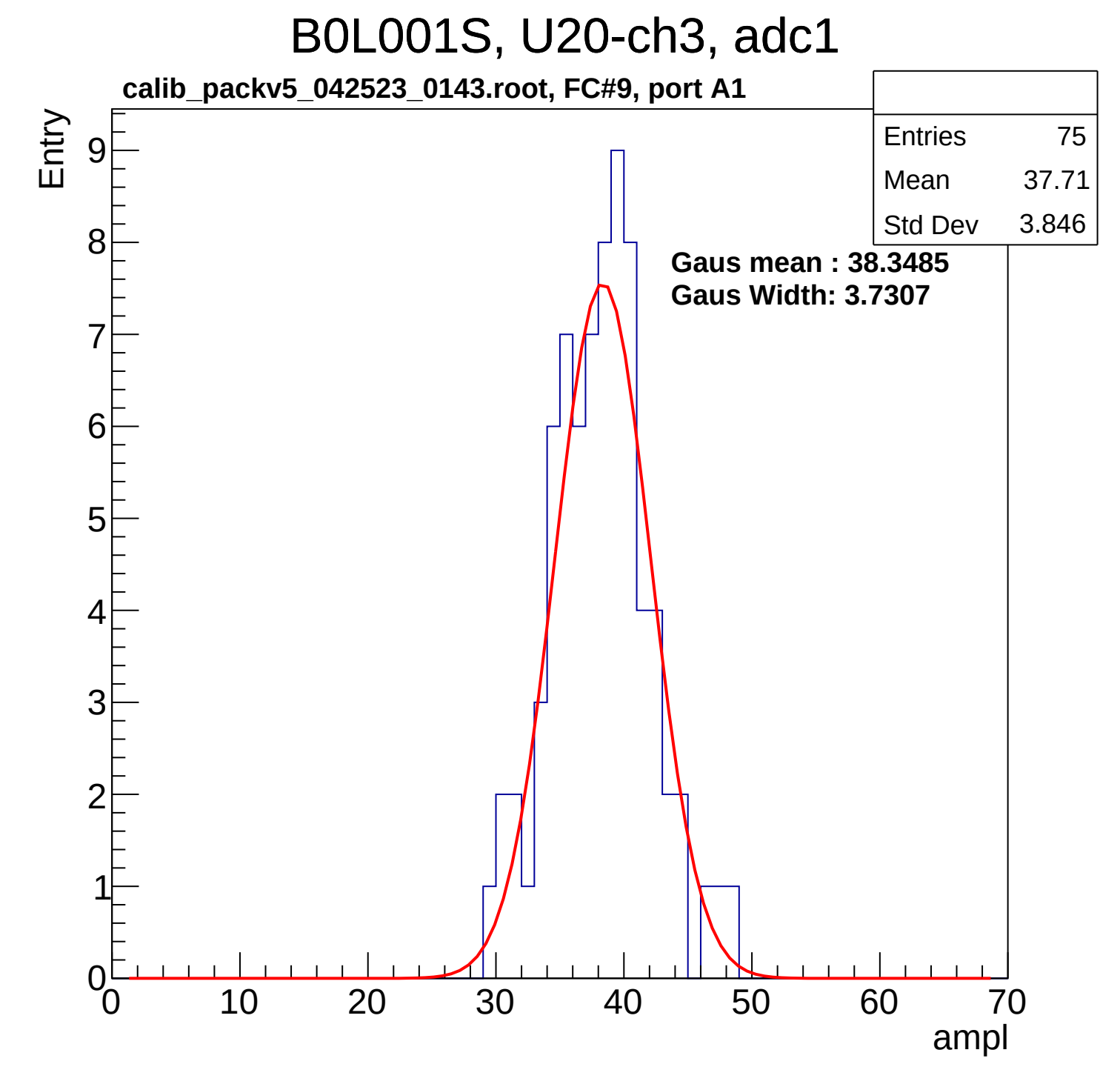
B0L001S, U20-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.71
Std Dev 3.846
Gaus mean : 38.3485
Gaus Width: 3.7307
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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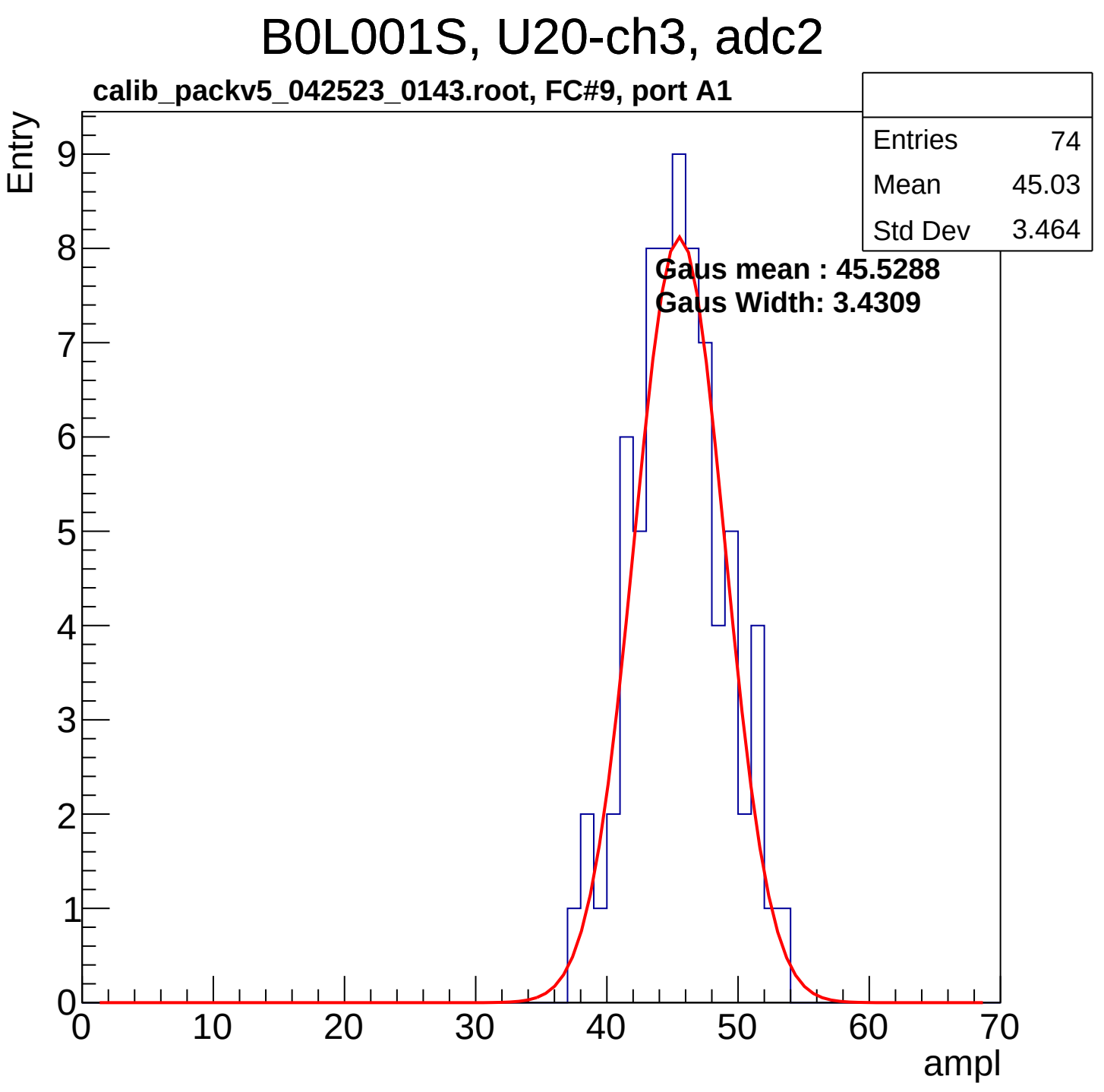
B0L001S, U20-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 45.03
Std Dev 3.464
Gaus mean : 45.5288
Gaus Width: 3.4309
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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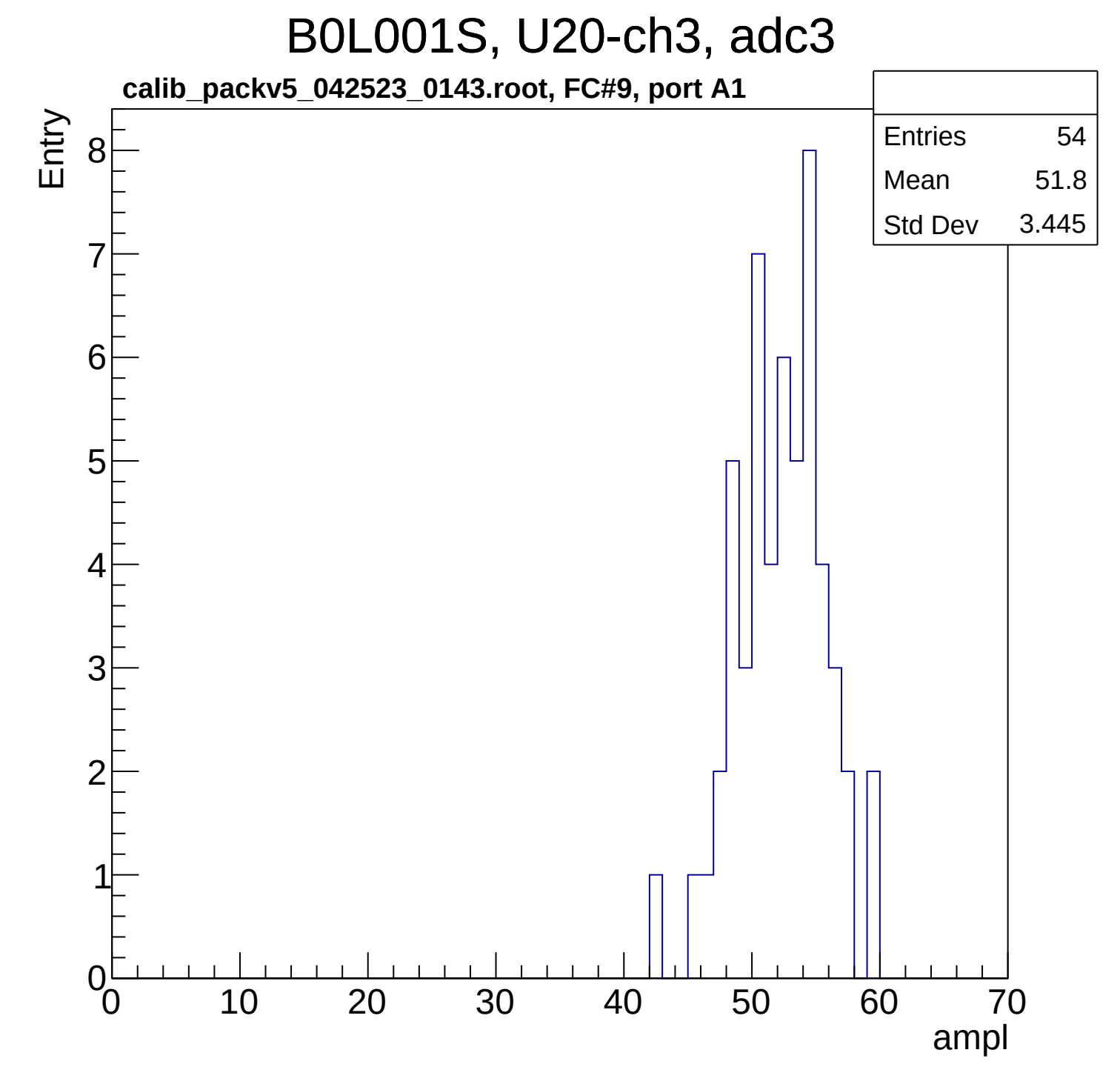
B0L001S, U20-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.8
Std Dev 3.445
Entry
ampl
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5
6
7
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20
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60
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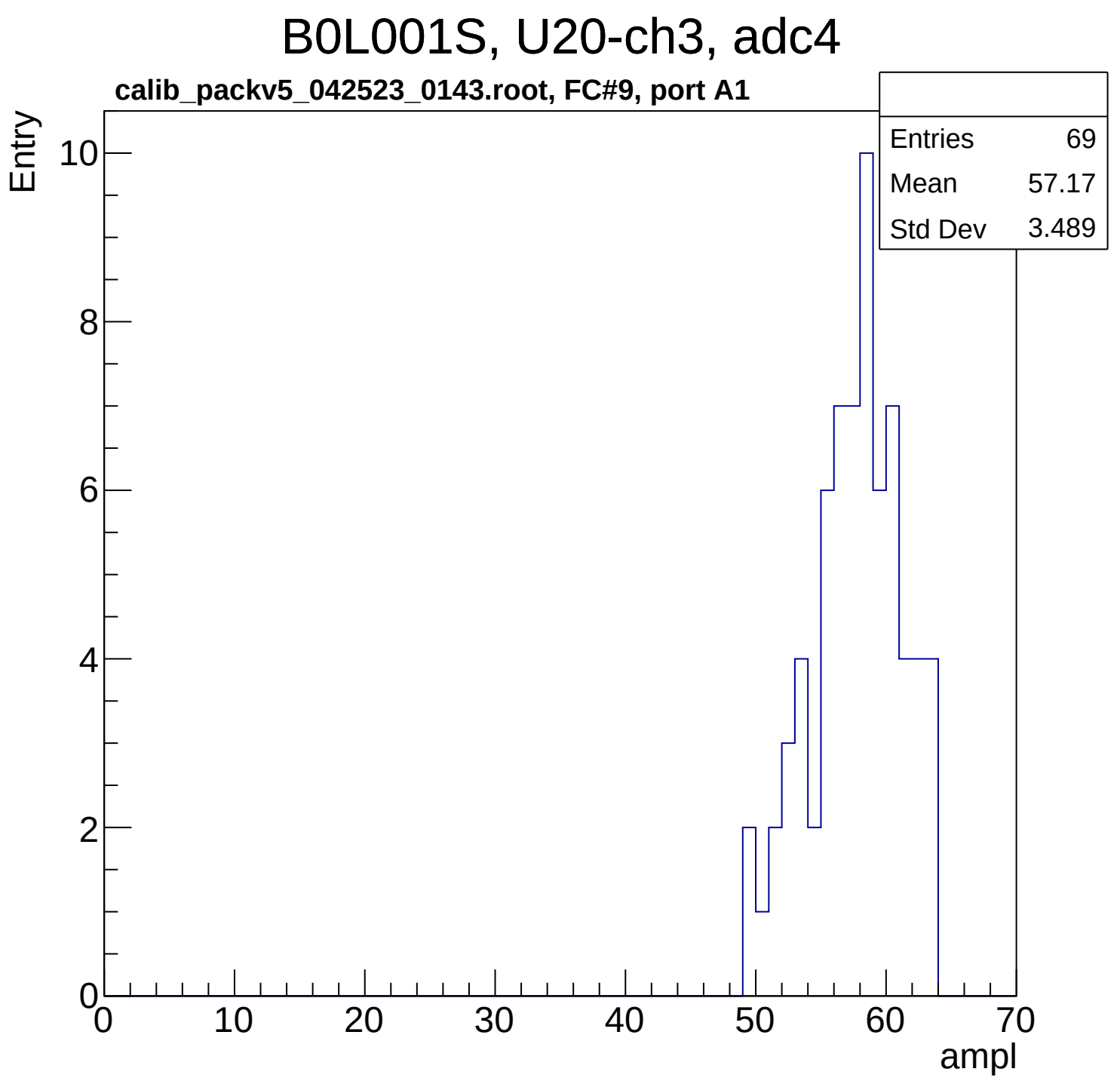
B0L001S, U20-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 57.17
Std Dev 3.489
Entry
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4
6
8
10
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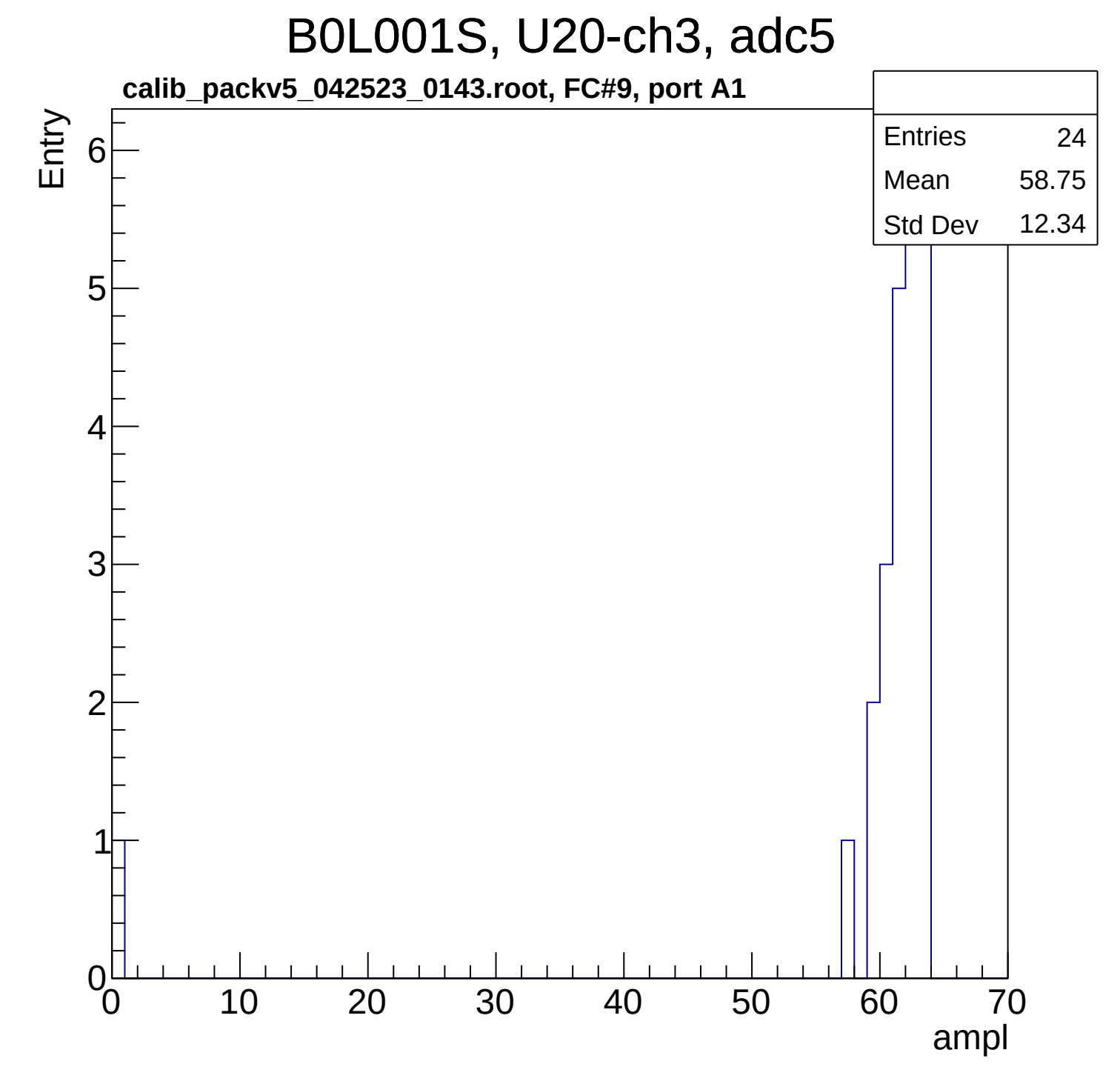
50
60
70
ampl


B0L001S, U20-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.75
Std Dev 12.34
Entry
ampl
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3
4
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6
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40
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60
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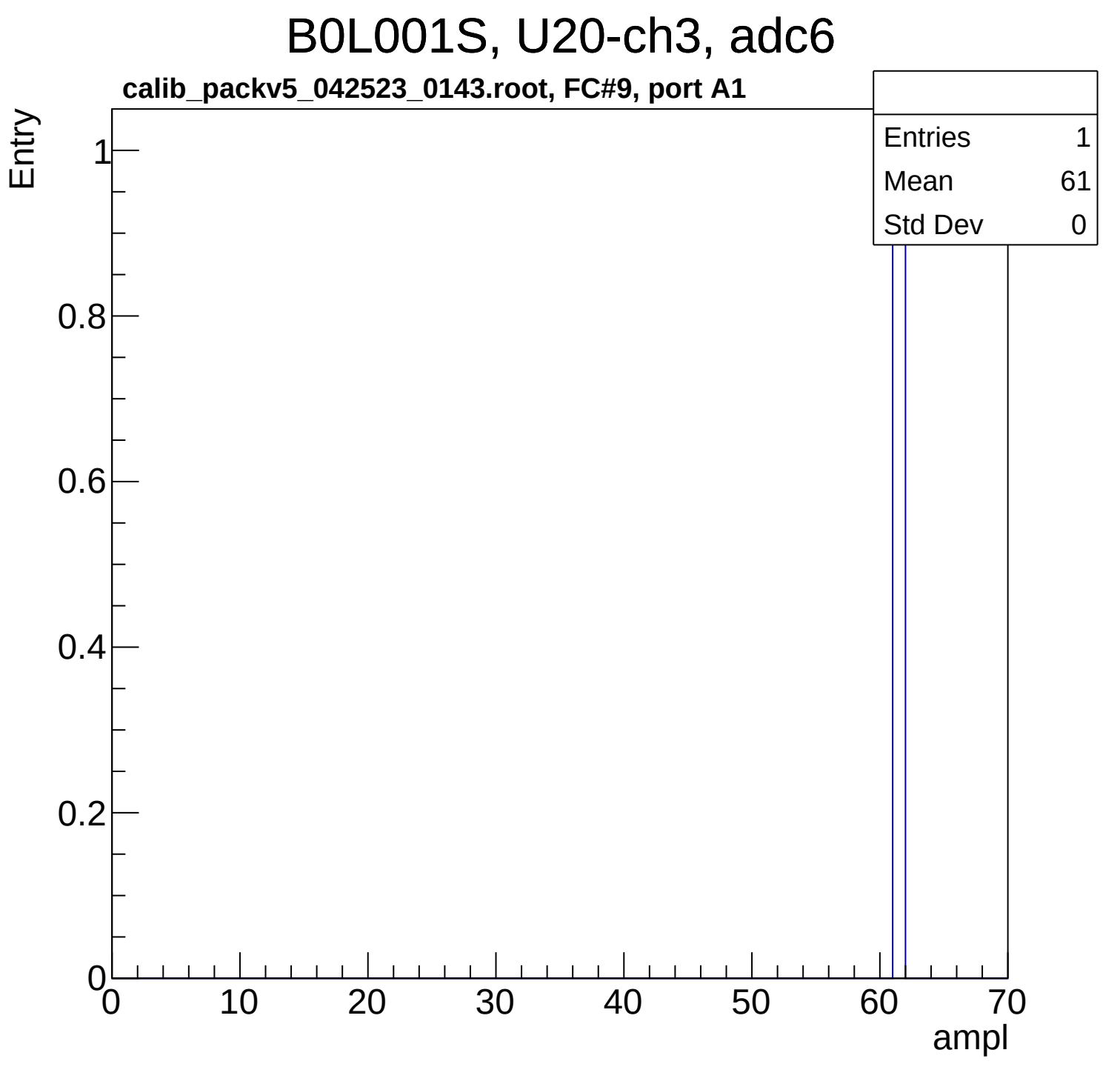
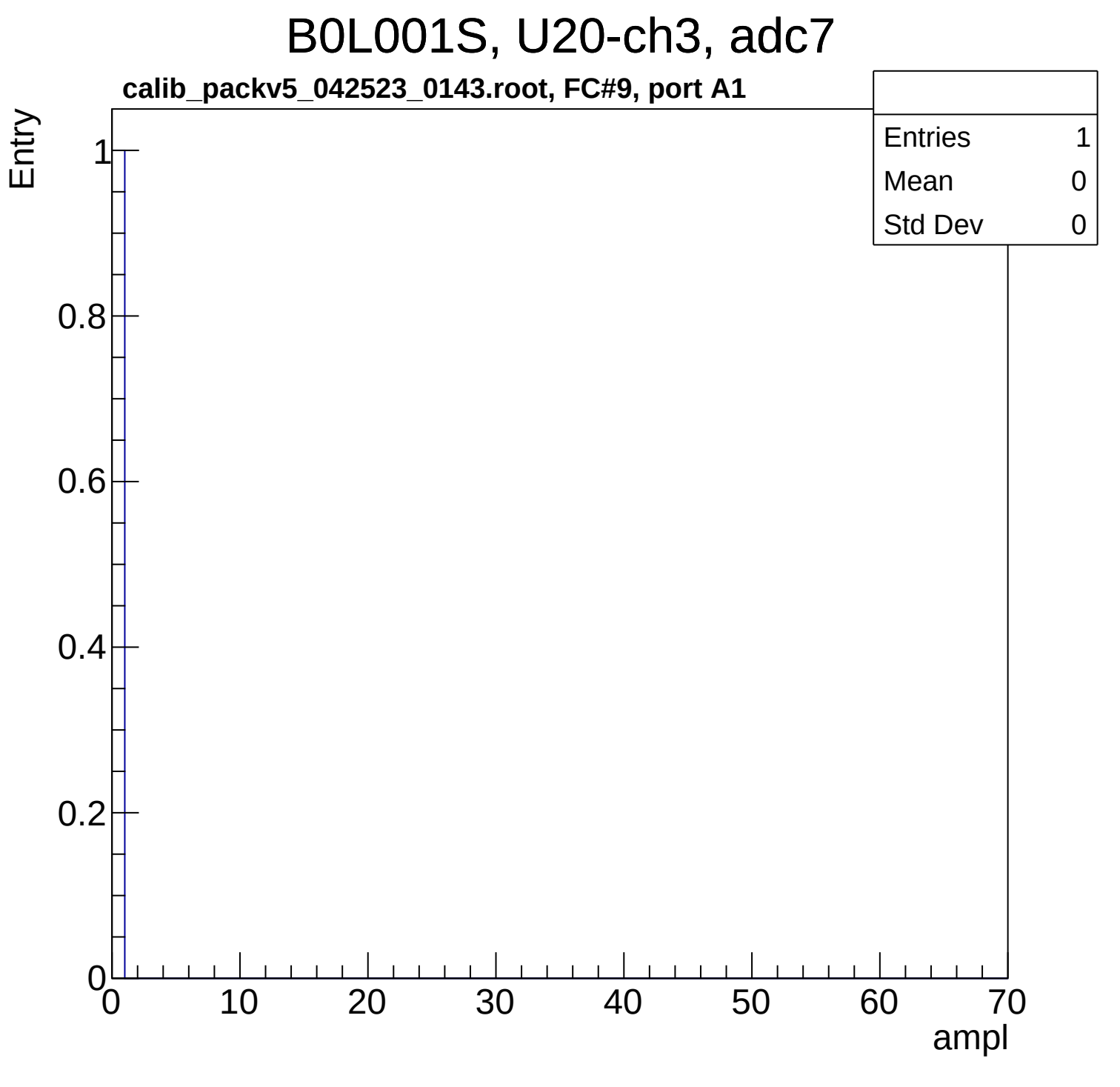

B0L001S, U20-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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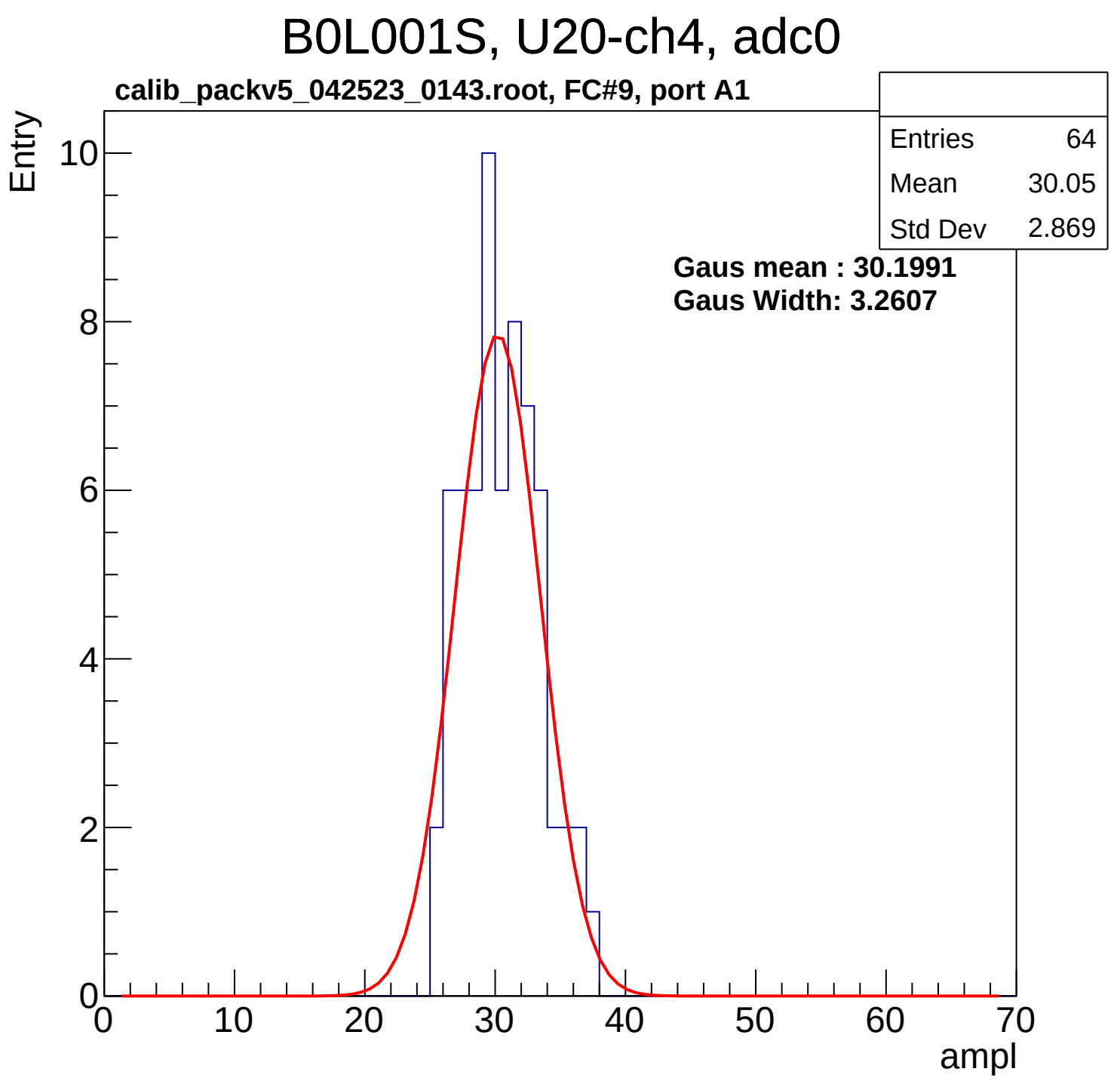
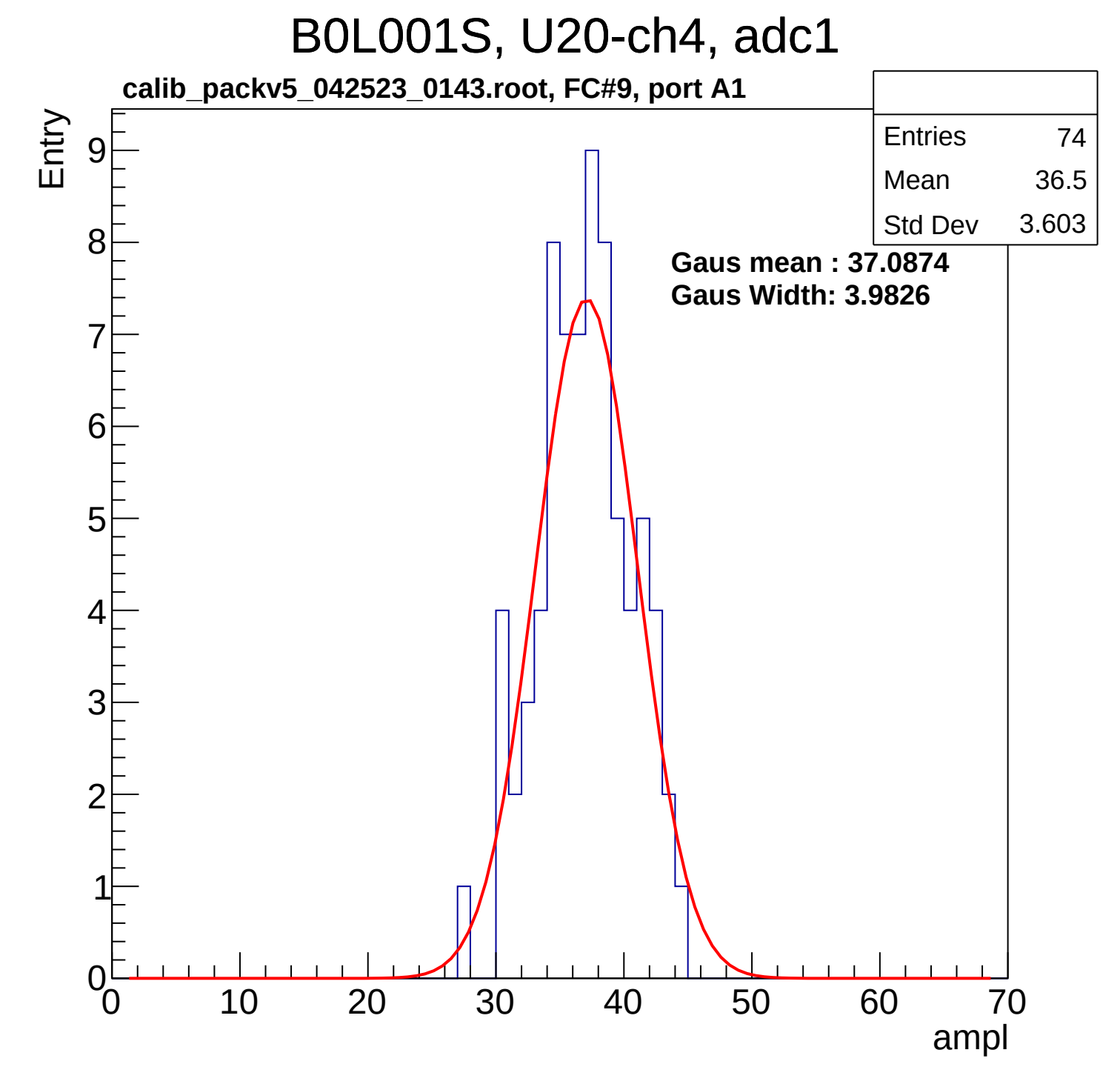
70
ampl


B0L001S, U20-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.05
Std Dev 2.869
Gaus mean : 30.1991
Gaus Width: 3.2607
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.5
Std Dev 3.603
Gaus mean : 37.0874
Gaus Width: 3.9826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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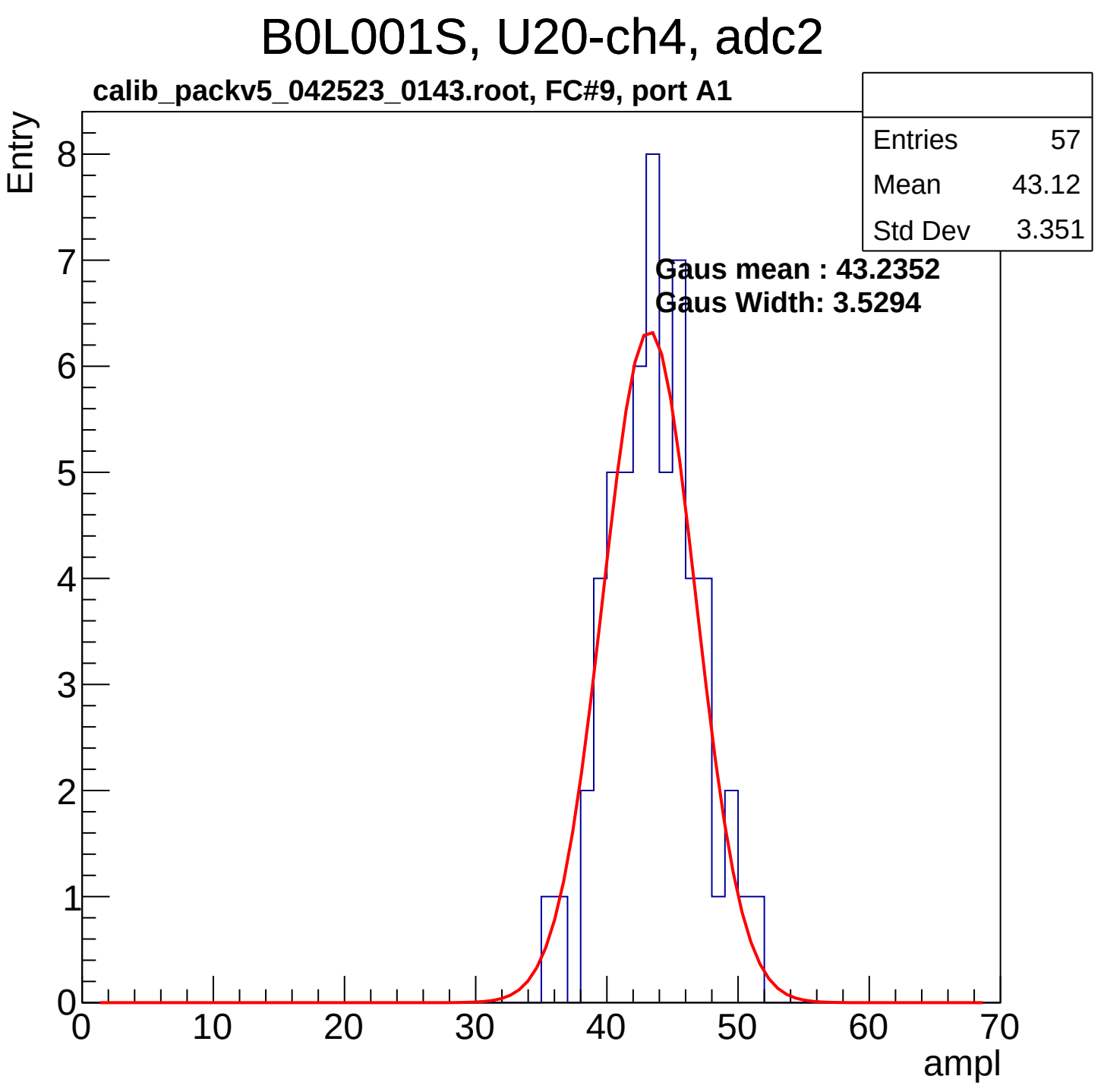
B0L001S, U20-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 43.12
Std Dev 3.351
Gaus mean : 43.2352
Gaus Width: 3.5294
Entry
ampl
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1
2
3
4
5
6
7
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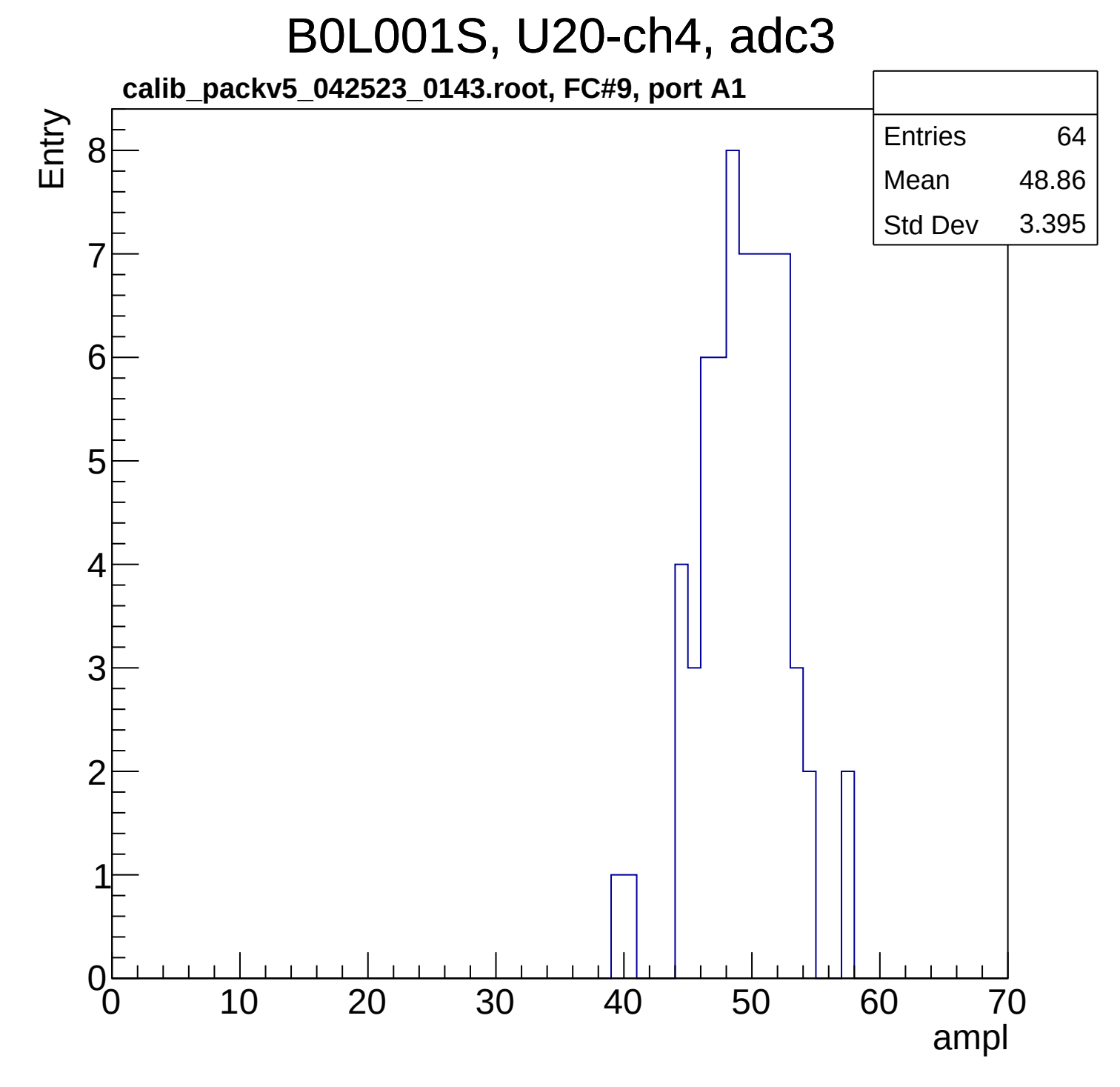
B0L001S, U20-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.86
Std Dev 3.395
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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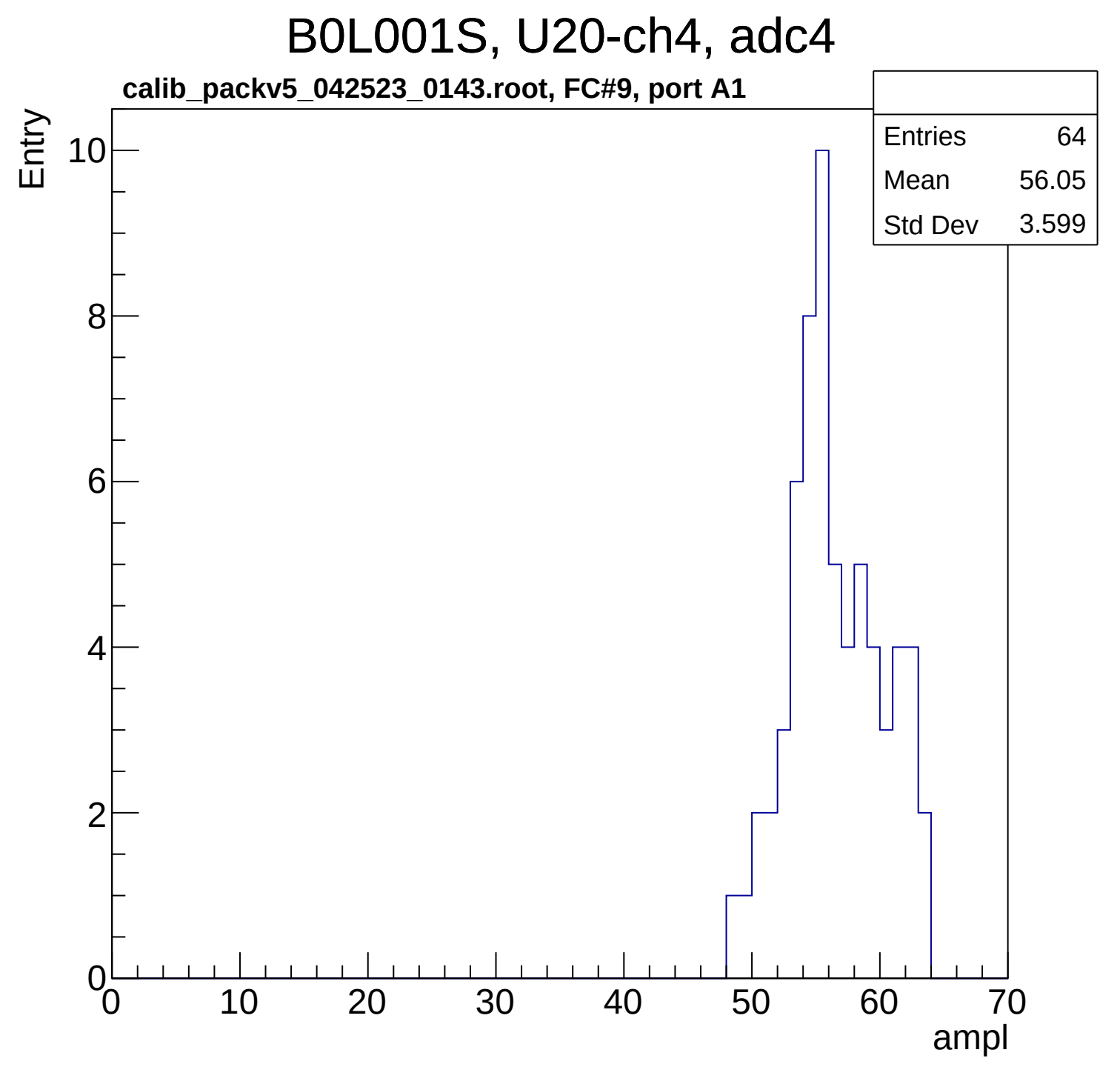
B0L001S, U20-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.05
Std Dev 3.599
Entry
ampl
0
2
4
6
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10
0
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60
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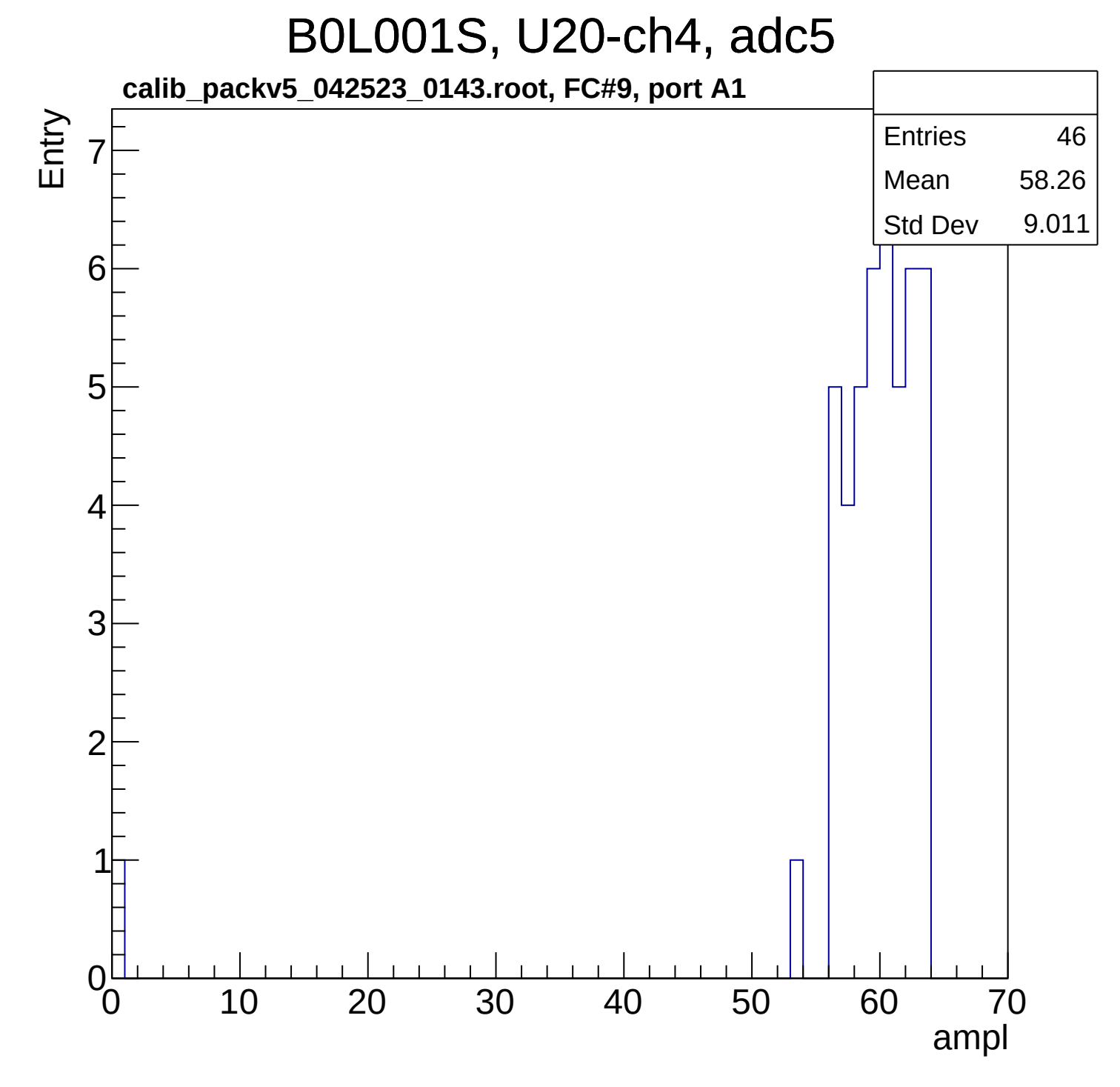
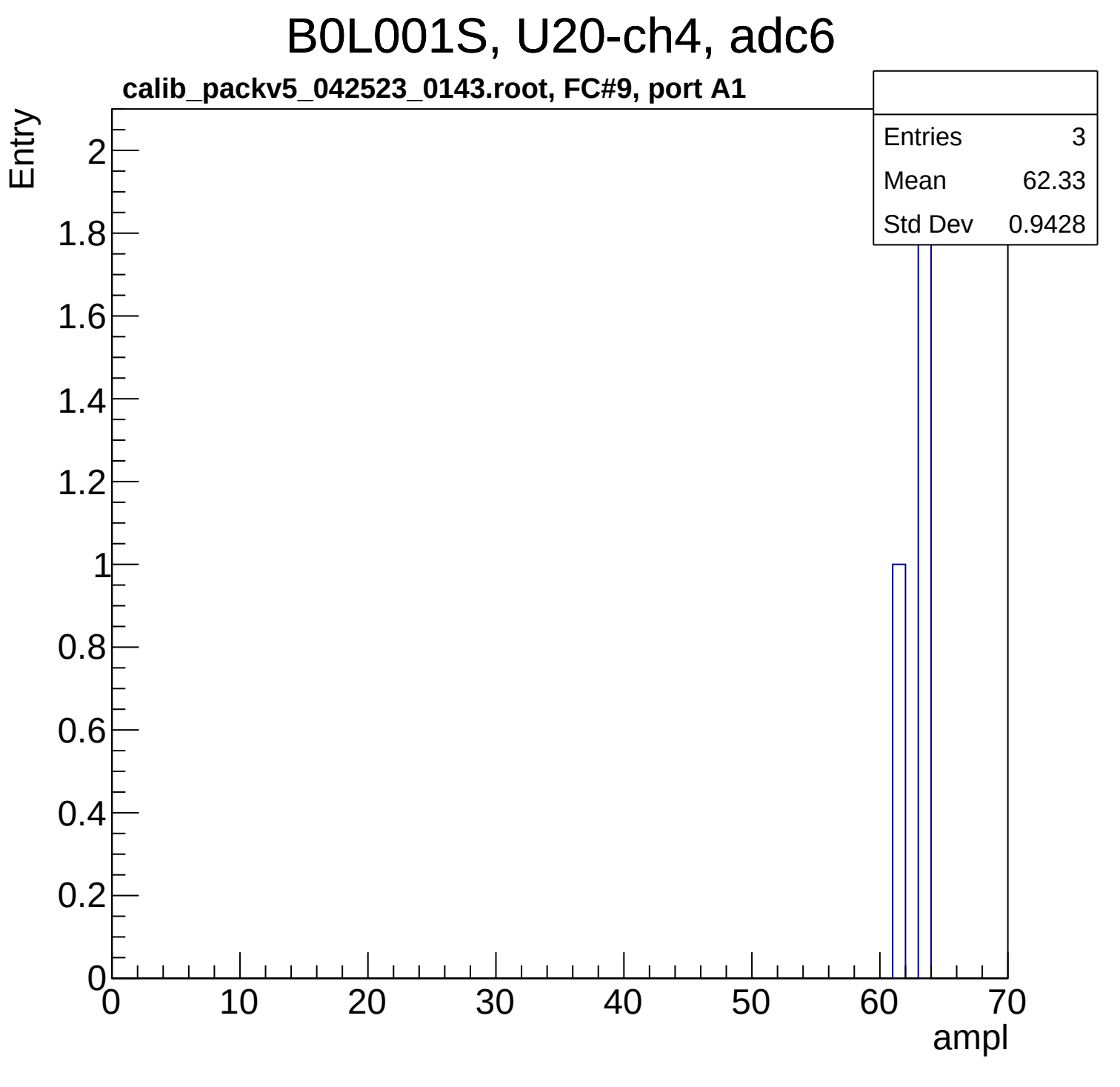

B0L001S, U20-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.26
Std Dev 9.011
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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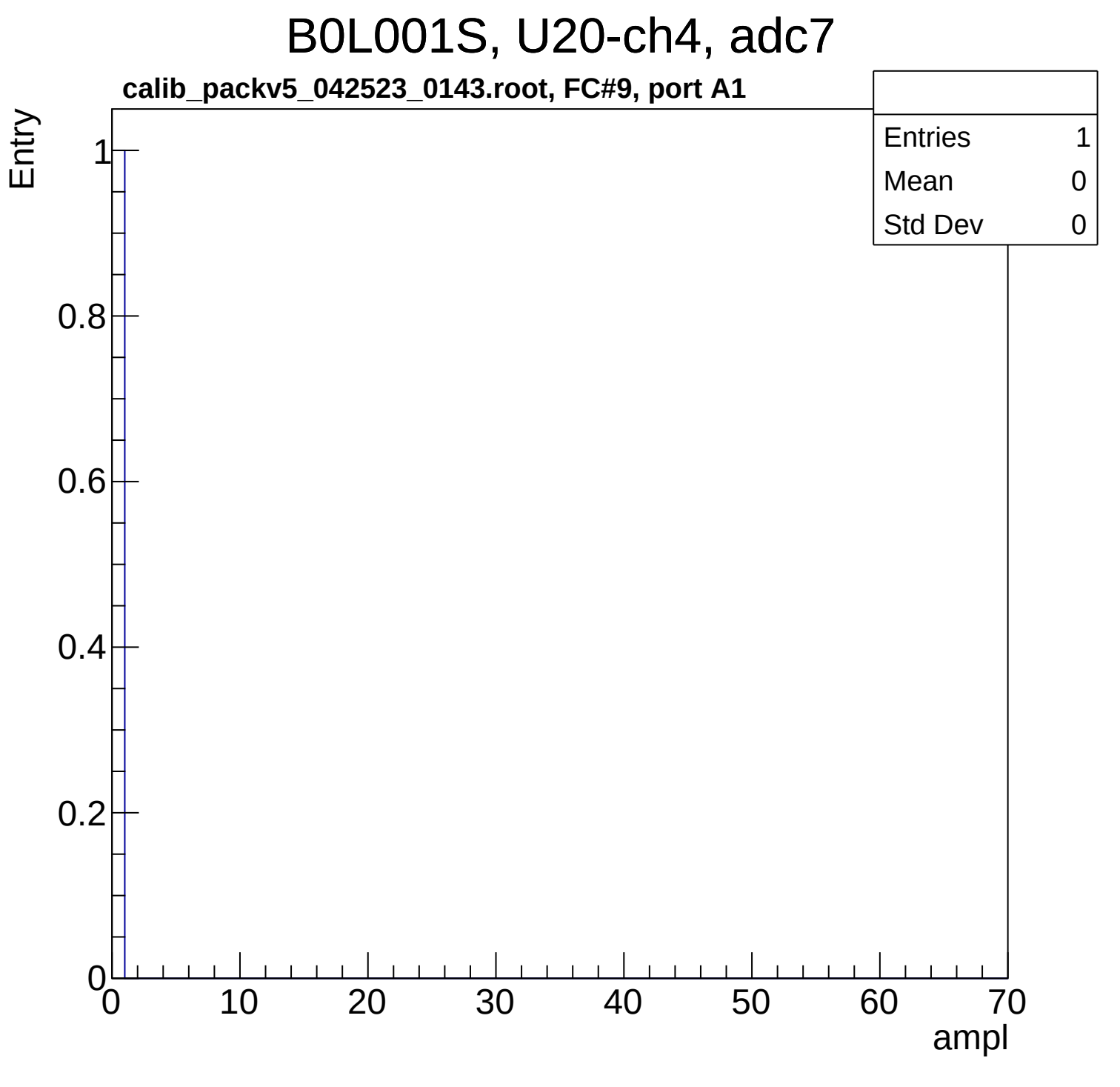
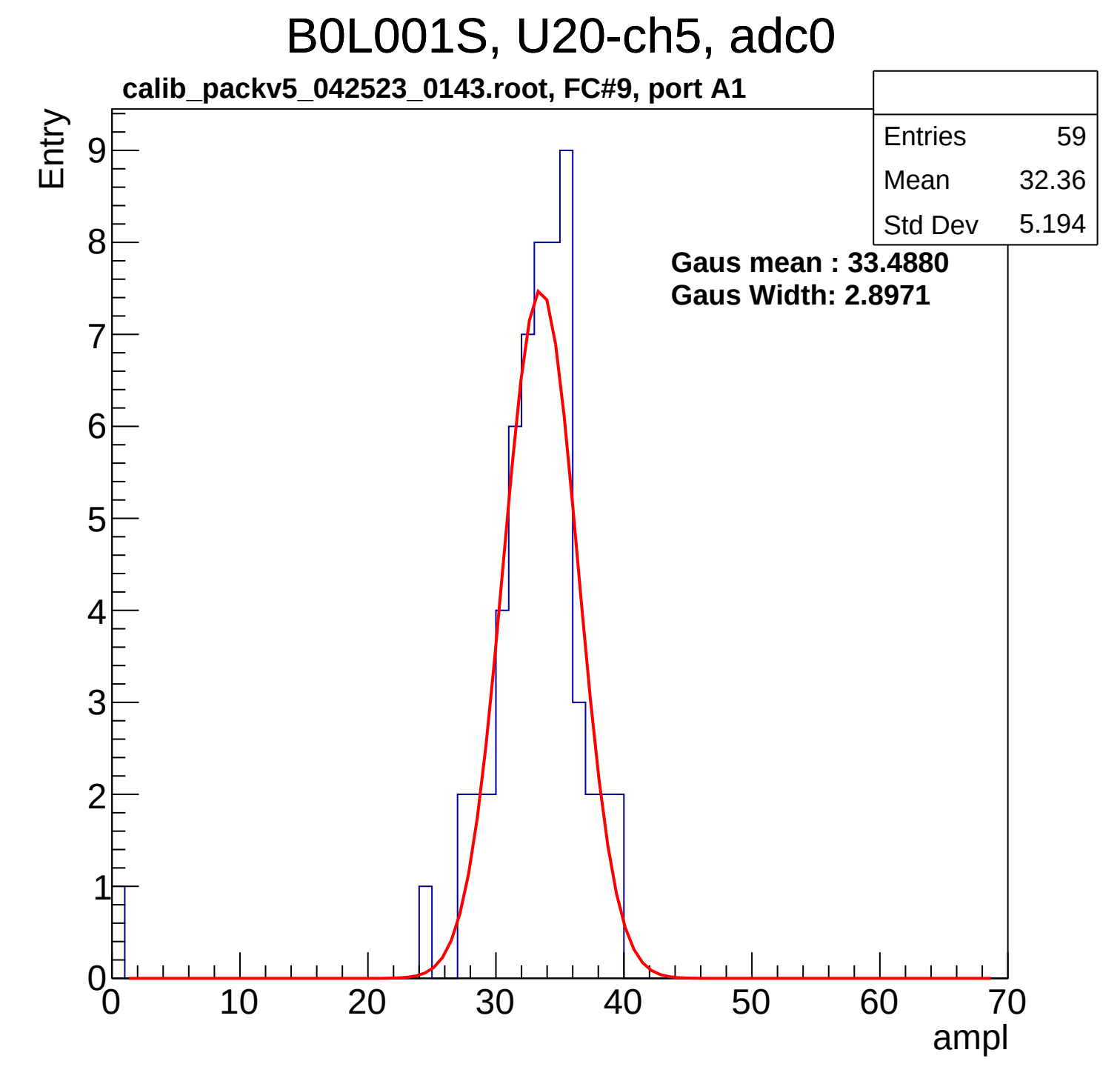
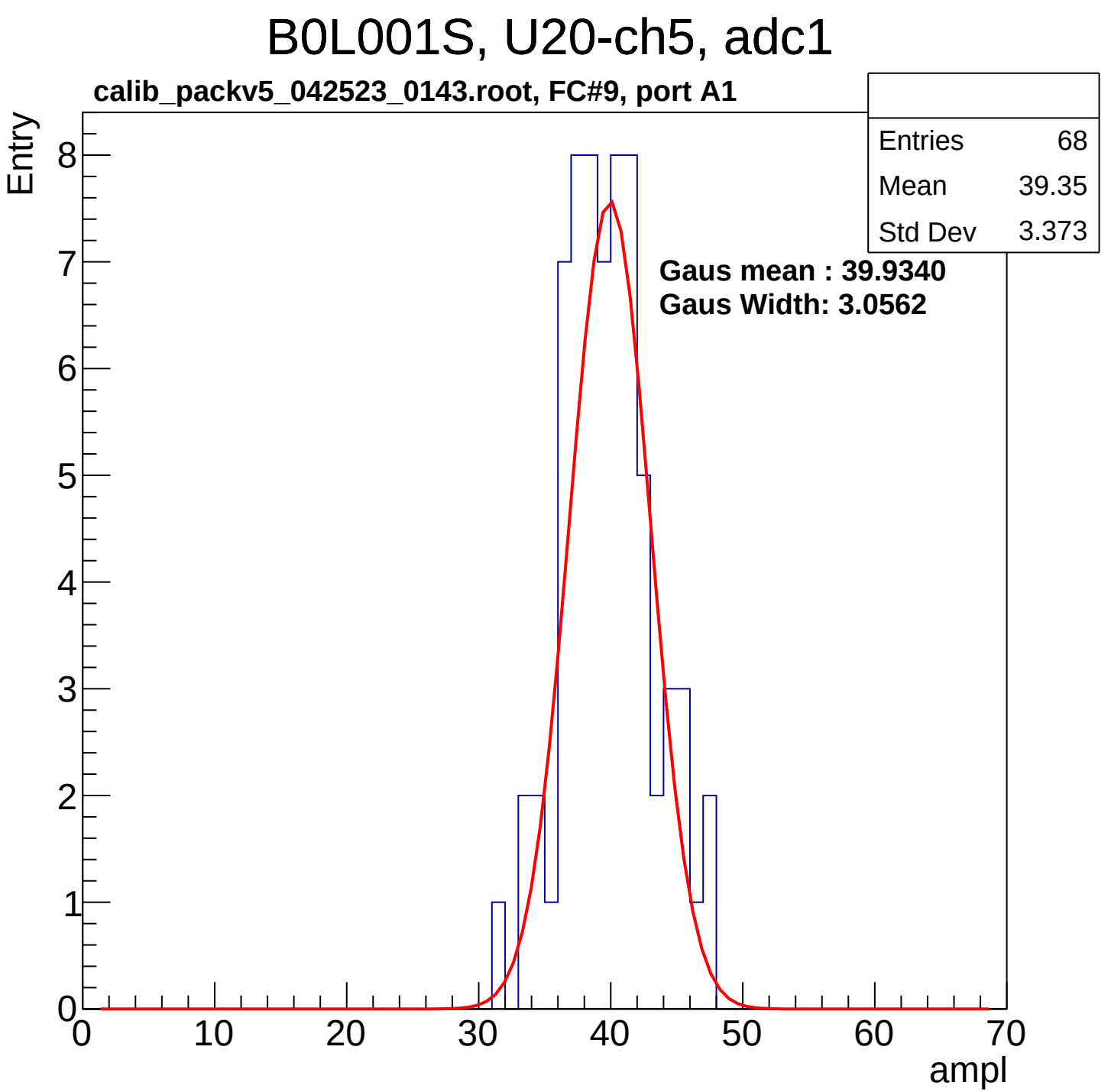
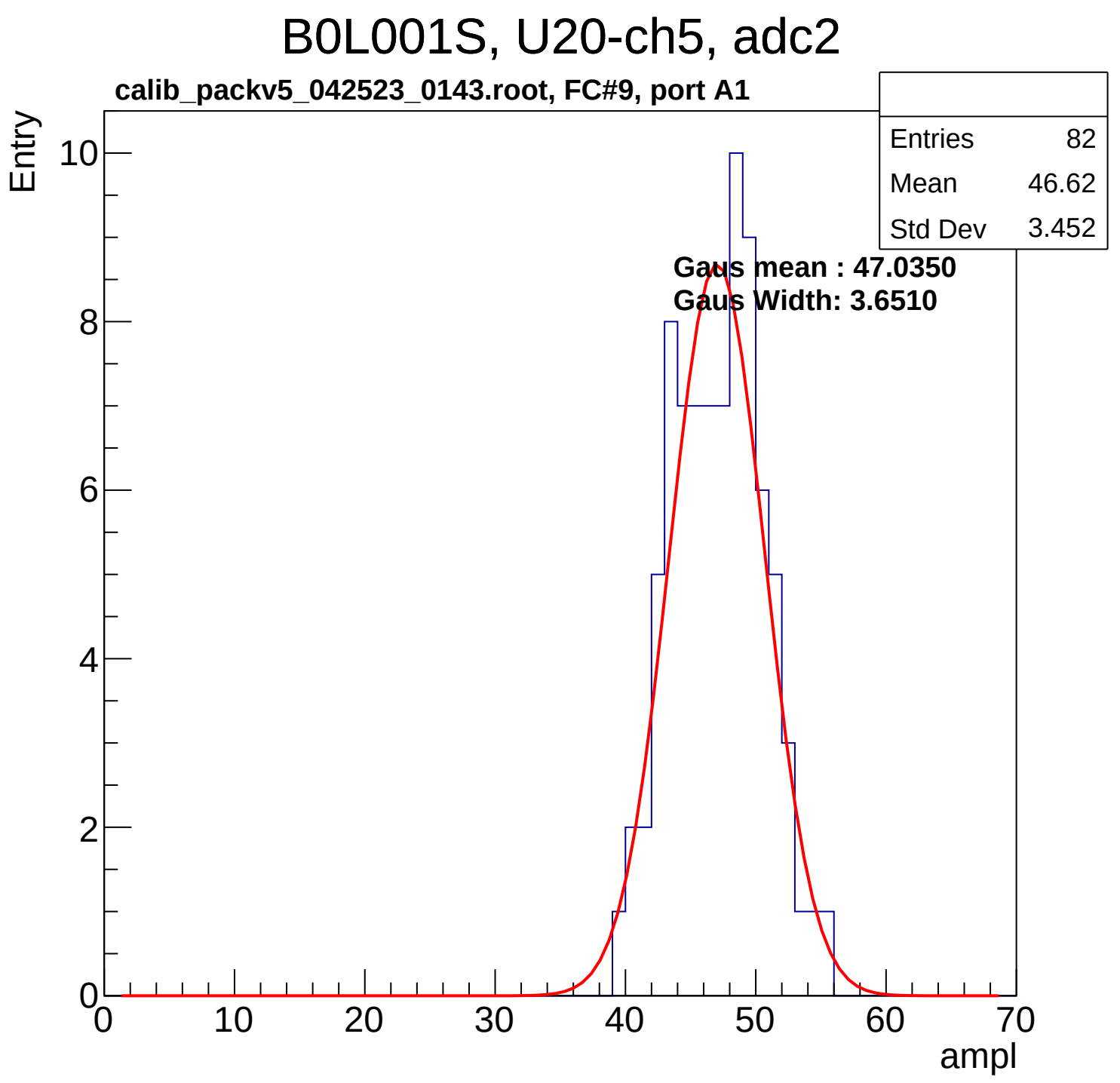
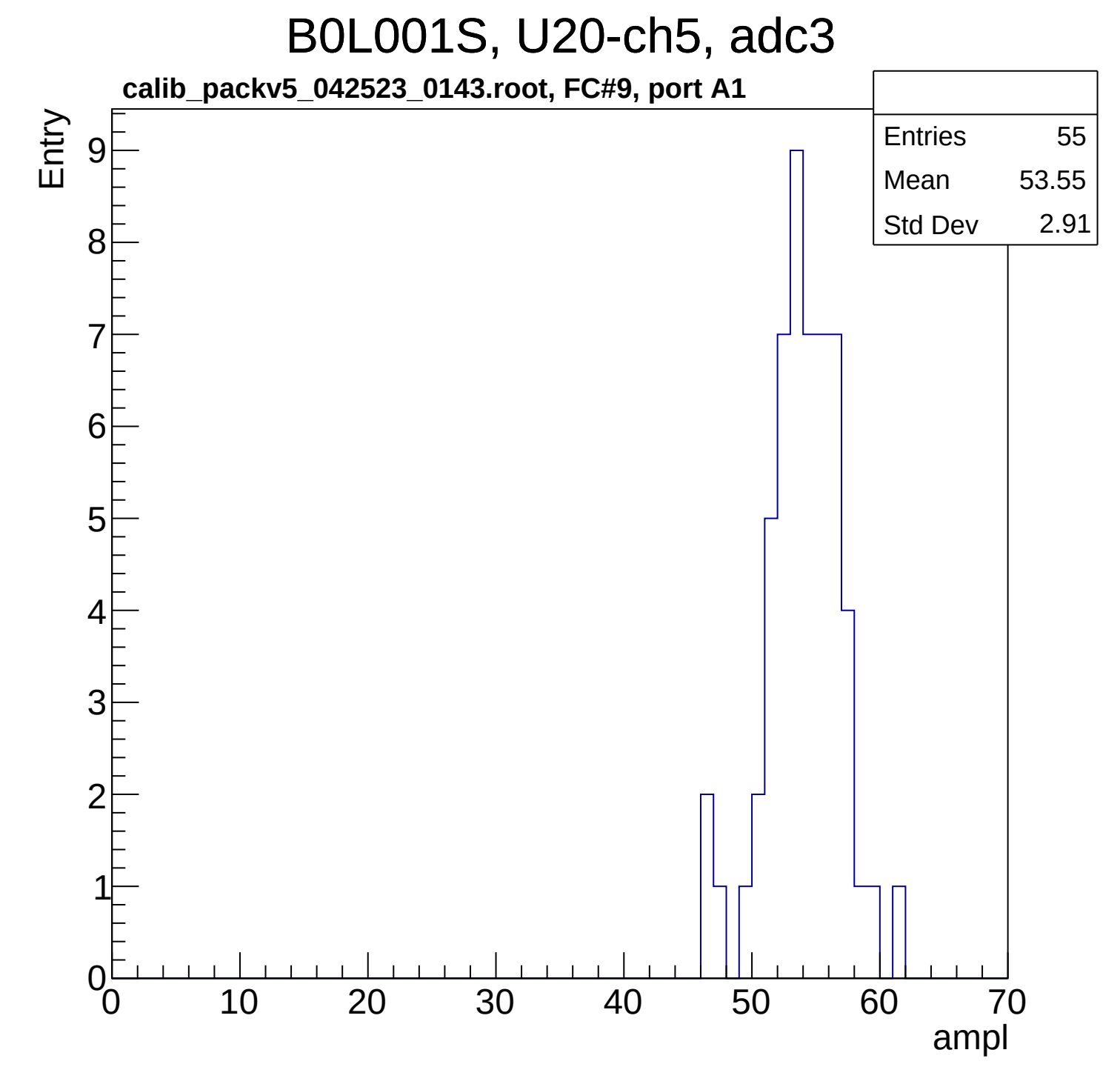

B0L001S, U20-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 32.36
Std Dev 5.194
Gaus mean : 33.4880
Gaus Width: 2.8971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 39.35
Std Dev 3.373
Gaus mean : 39.9340
Gaus Width: 3.0562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 46.62
Std Dev 3.452
Gaus mean : 47.0350
Gaus Width: 3.6510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 53.55
Std Dev 2.91
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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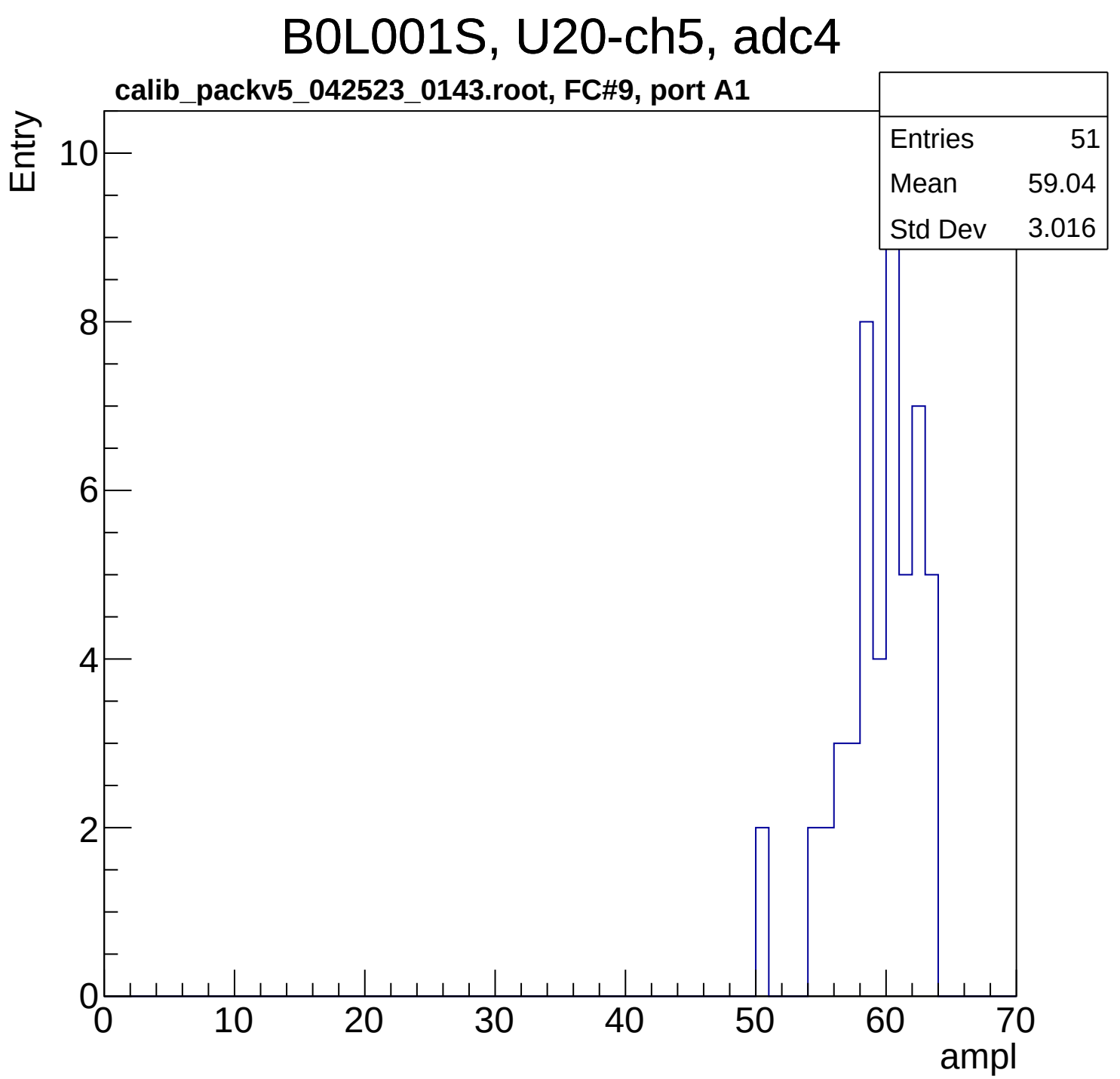
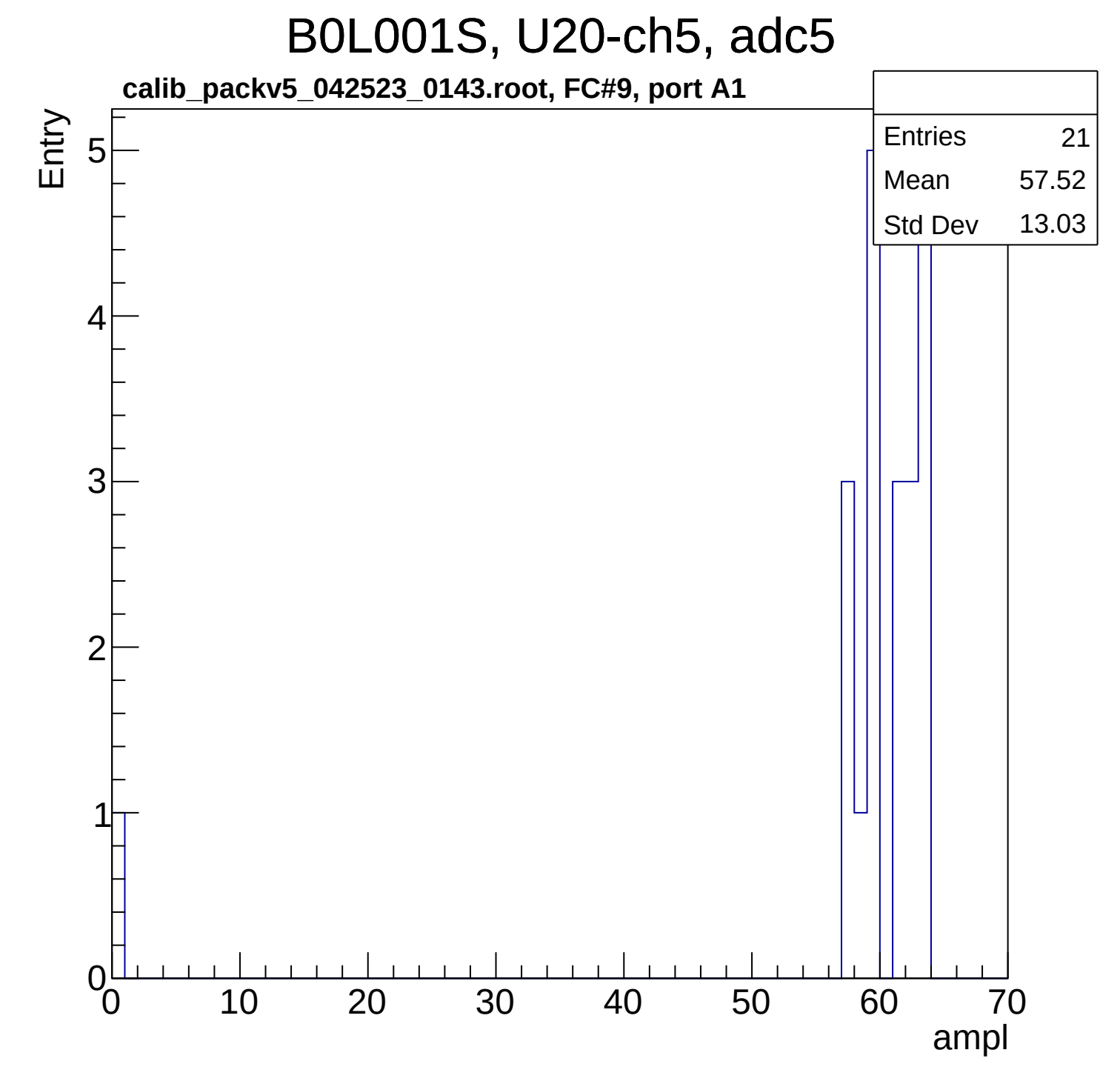
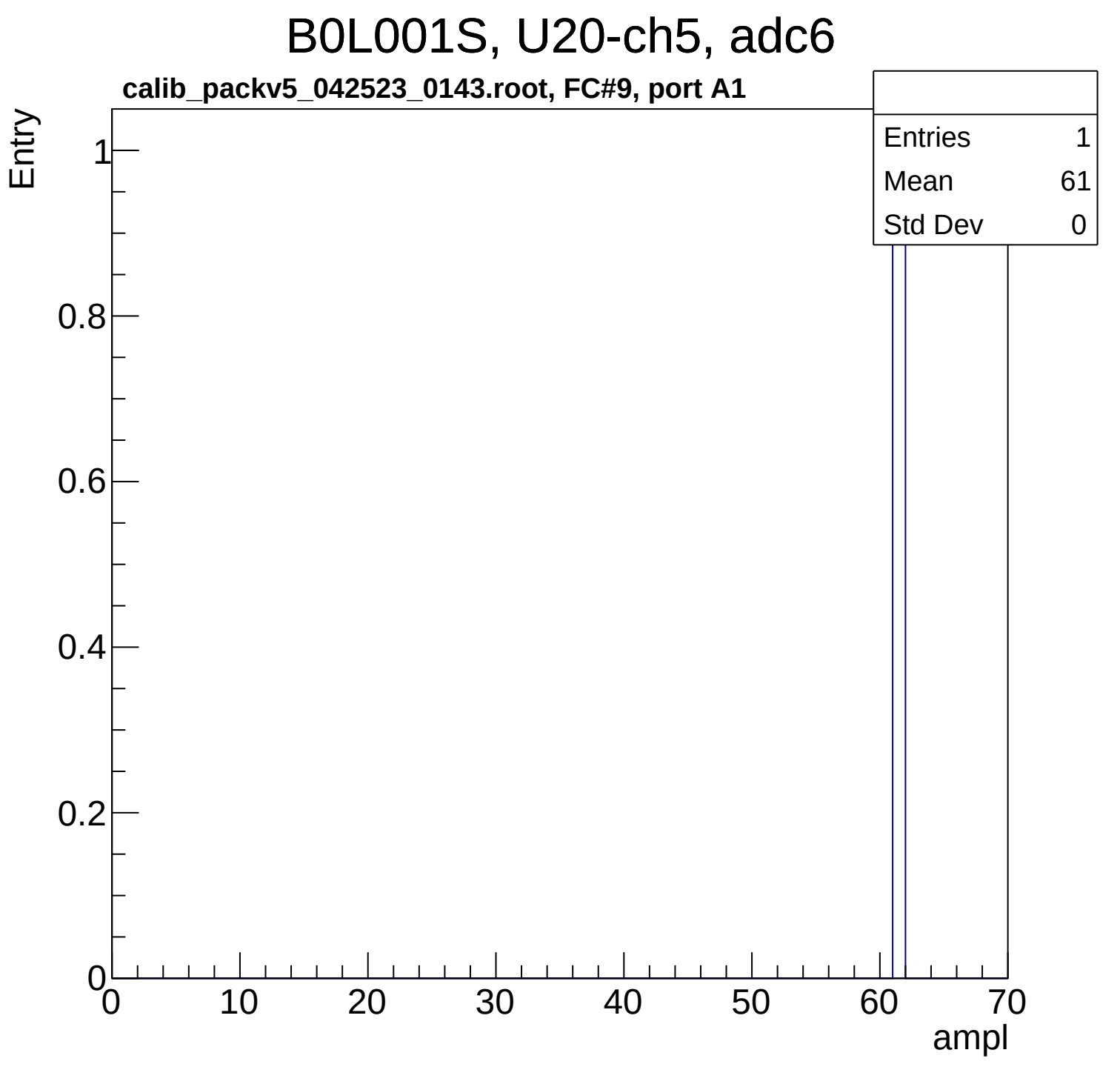
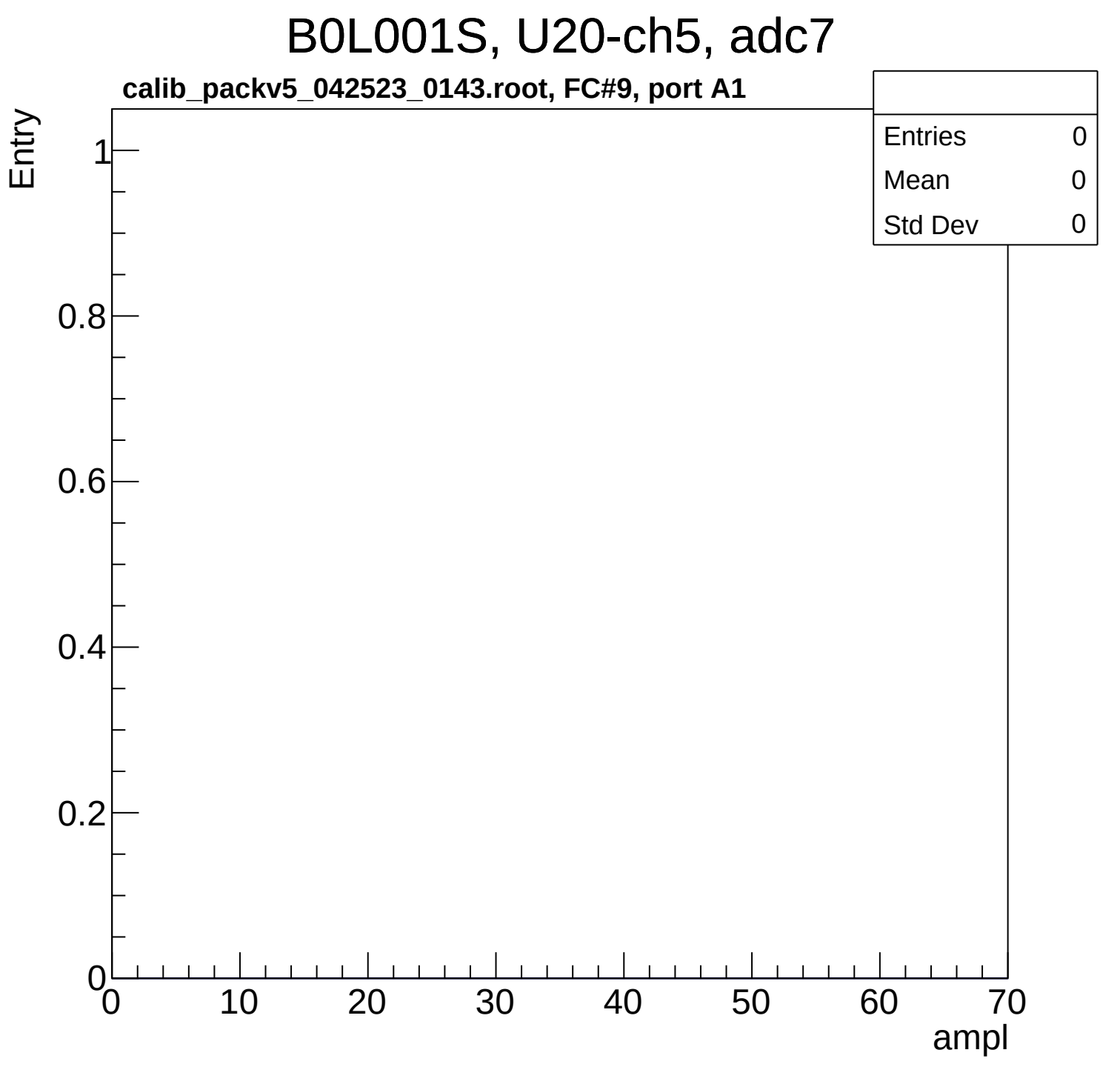

B0L001S, U20-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 59.04
Std Dev 3.016
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 57.52
Std Dev 13.03
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U20-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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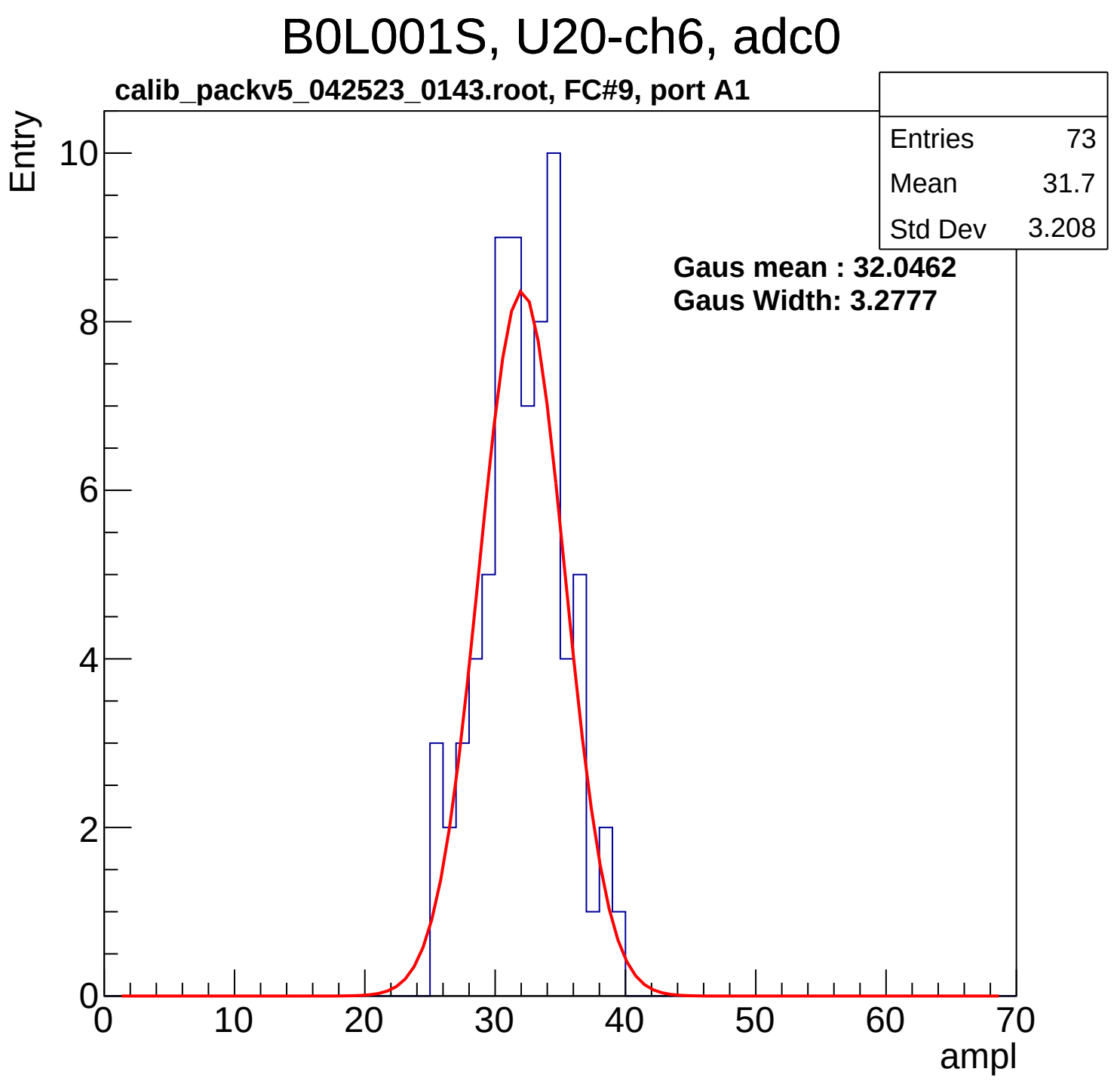
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 31.7
Std Dev 3.208
Gaus mean : 32.0462
Gaus Width: 3.2777
Entry
ampl
0
2
4
6
8
10
0
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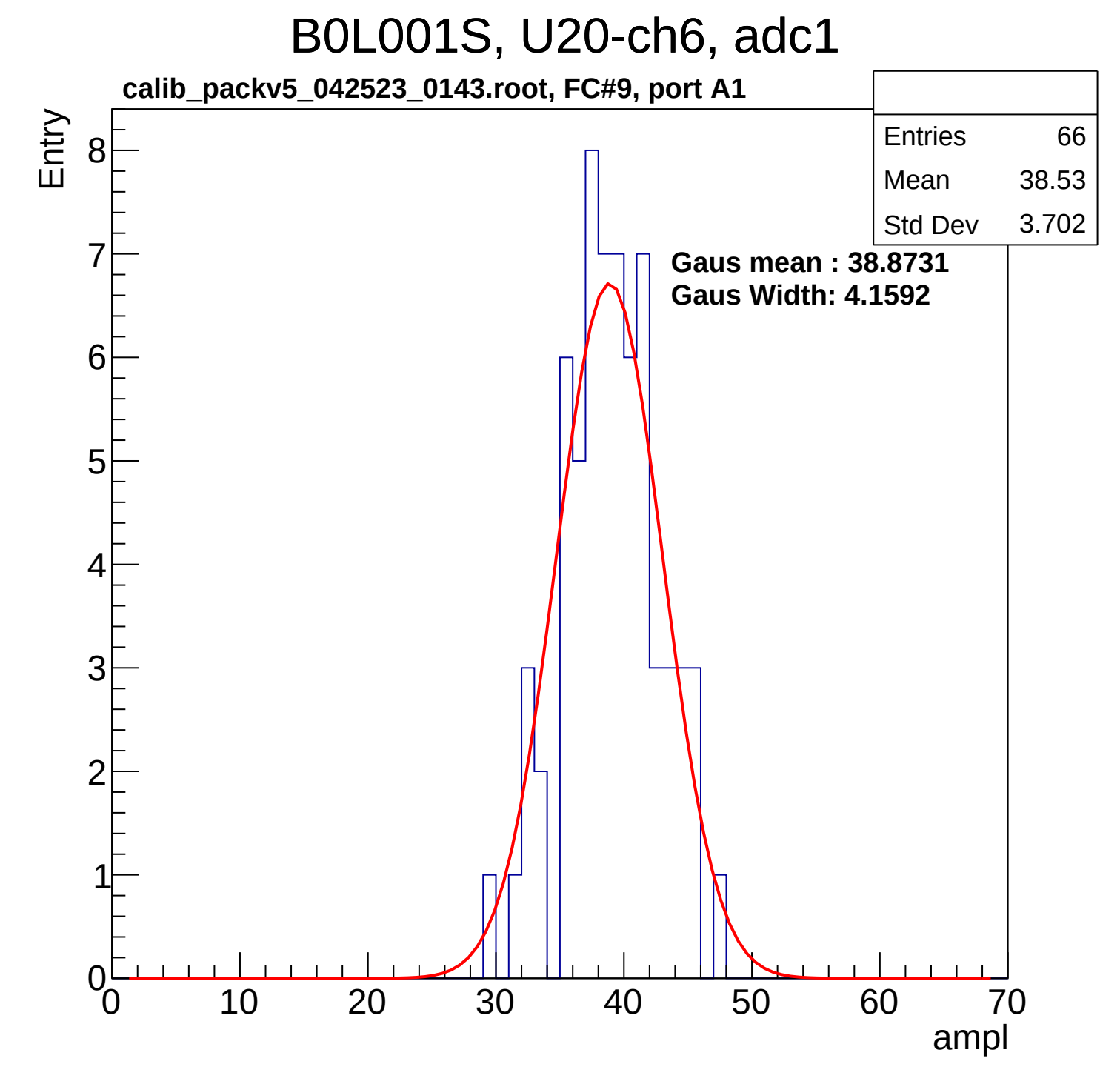
20
30
40
50
60
70


B0L001S, U20-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.53
Std Dev 3.702
Gaus mean : 38.8731
Gaus Width: 4.1592
Entry
ampl
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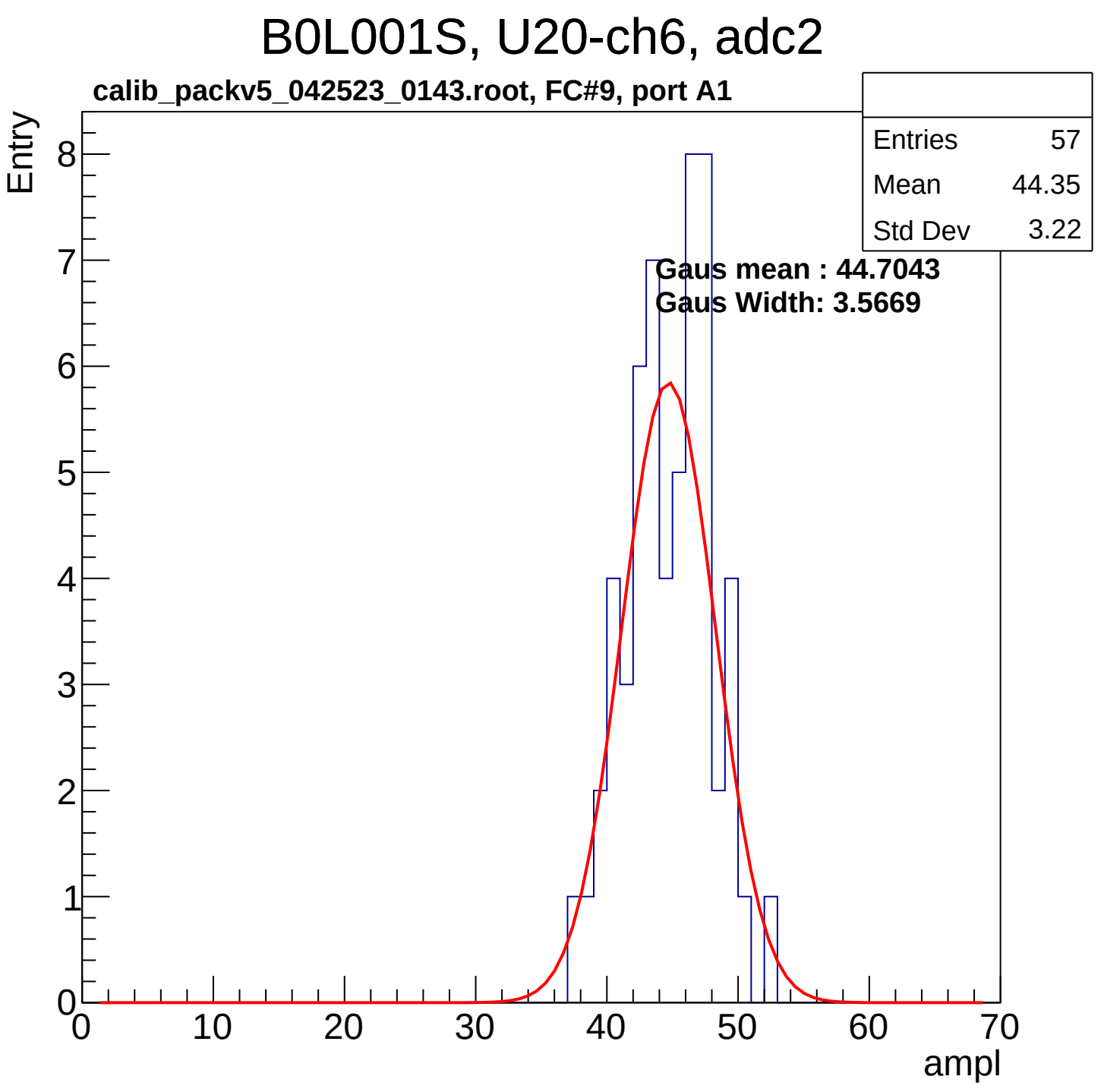
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.35
Std Dev 3.22
Gaus mean : 44.7043
Gaus Width: 3.5669
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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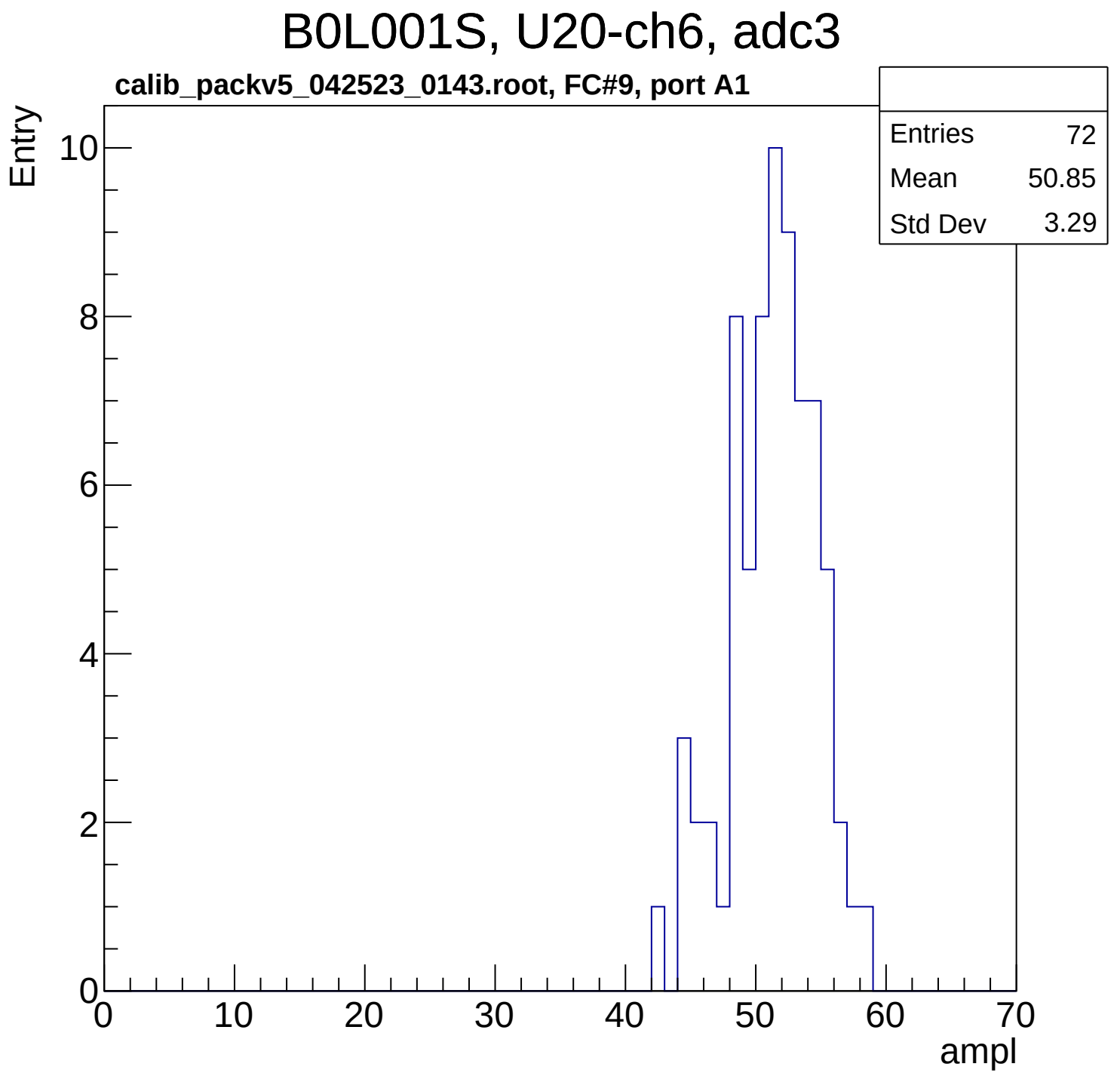
B0L001S, U20-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.85
Std Dev 3.29
Entry
ampl
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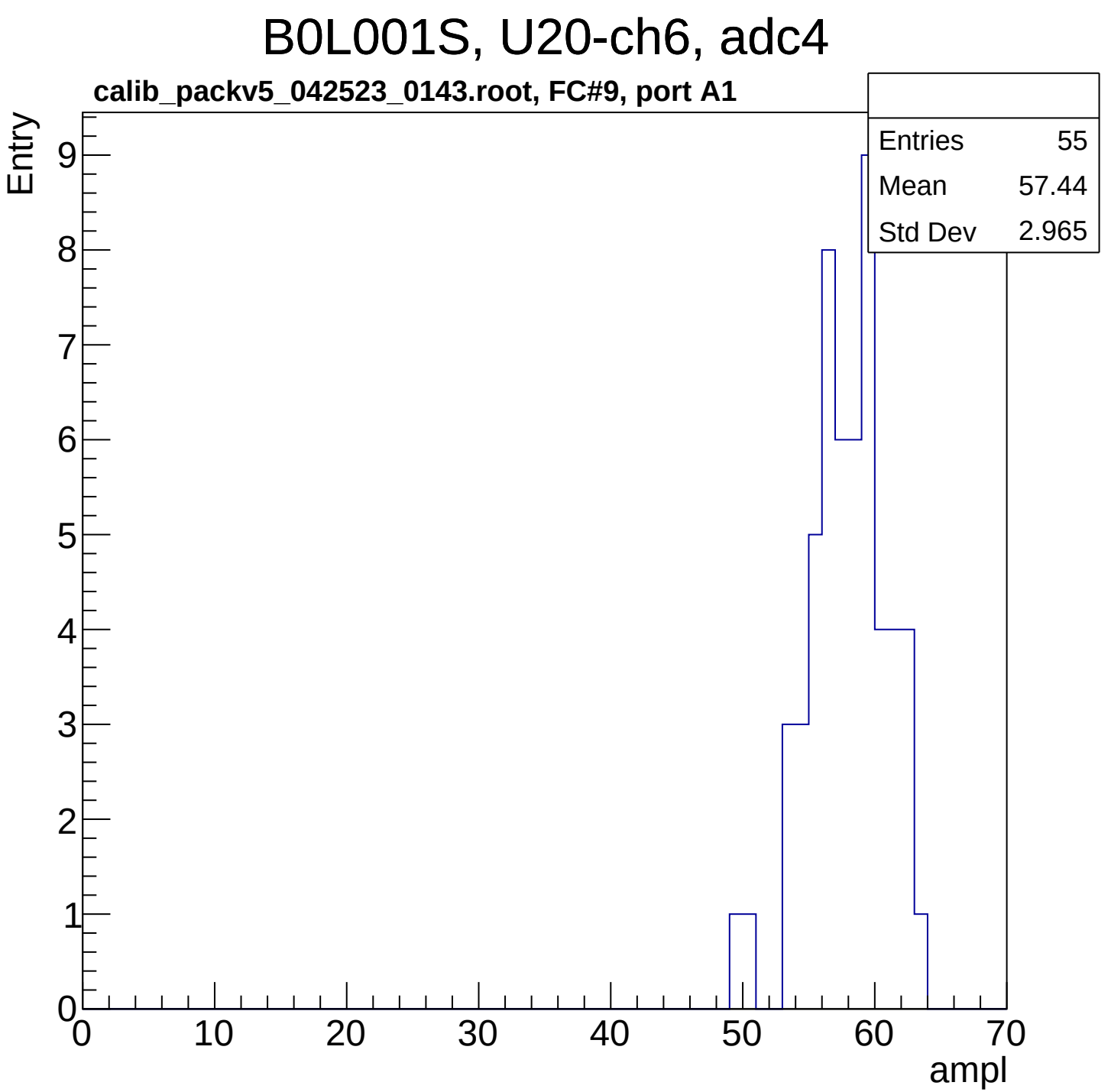
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.44
Std Dev 2.965
Entry
ampl
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3
4
5
6
7
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0
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60
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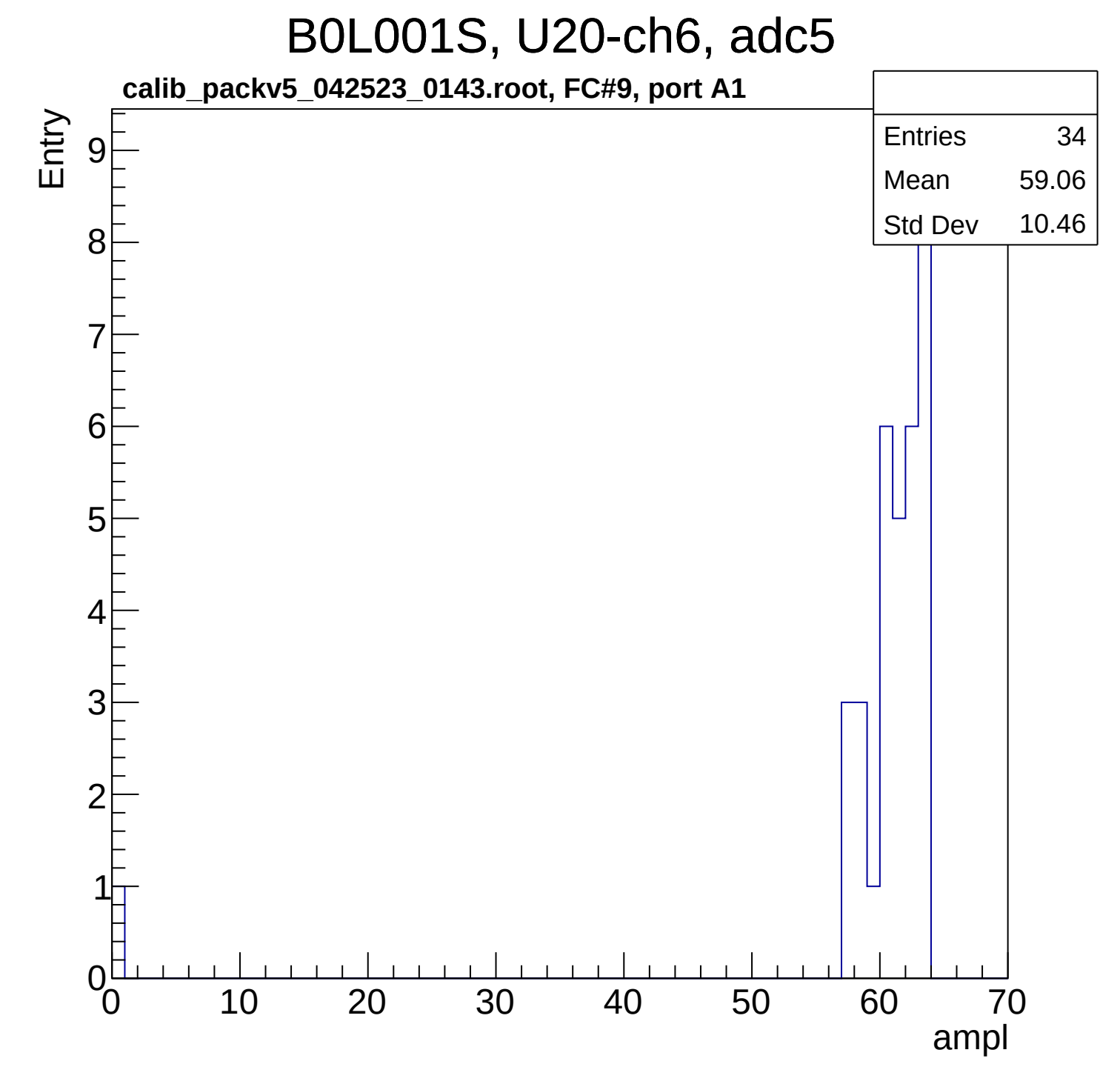
B0L001S, U20-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.06
Std Dev 10.46
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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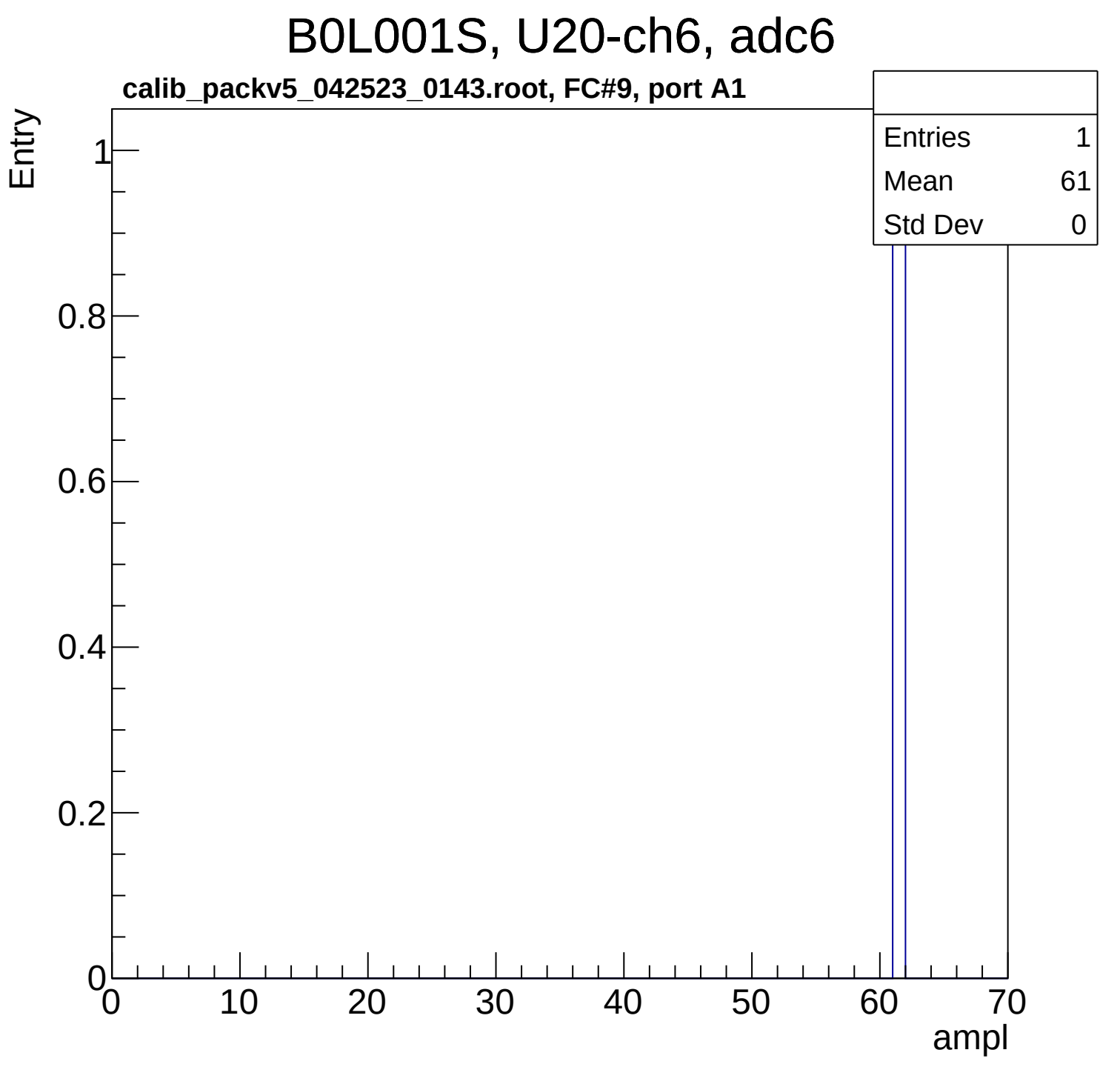
B0L001S, U20-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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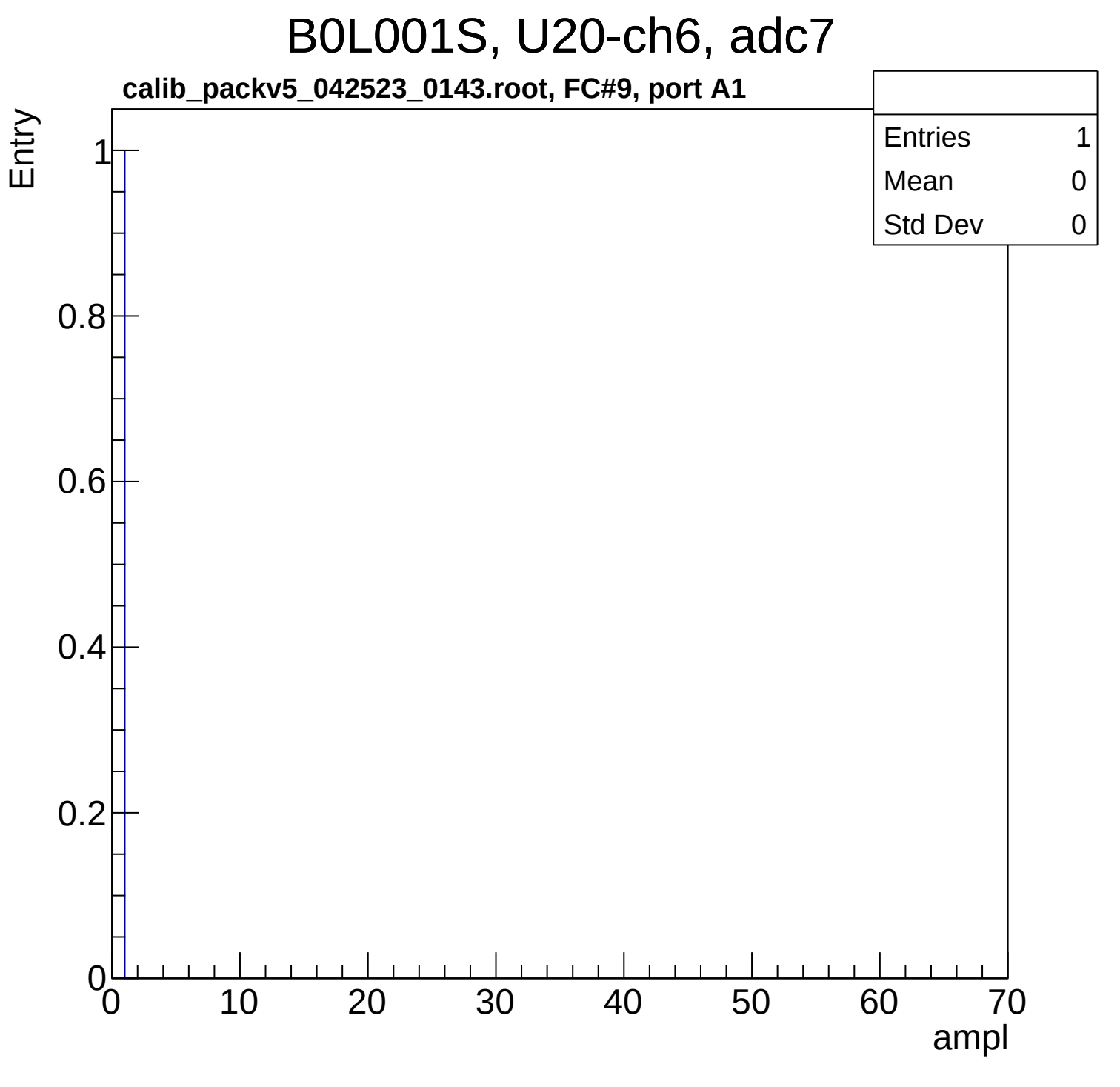
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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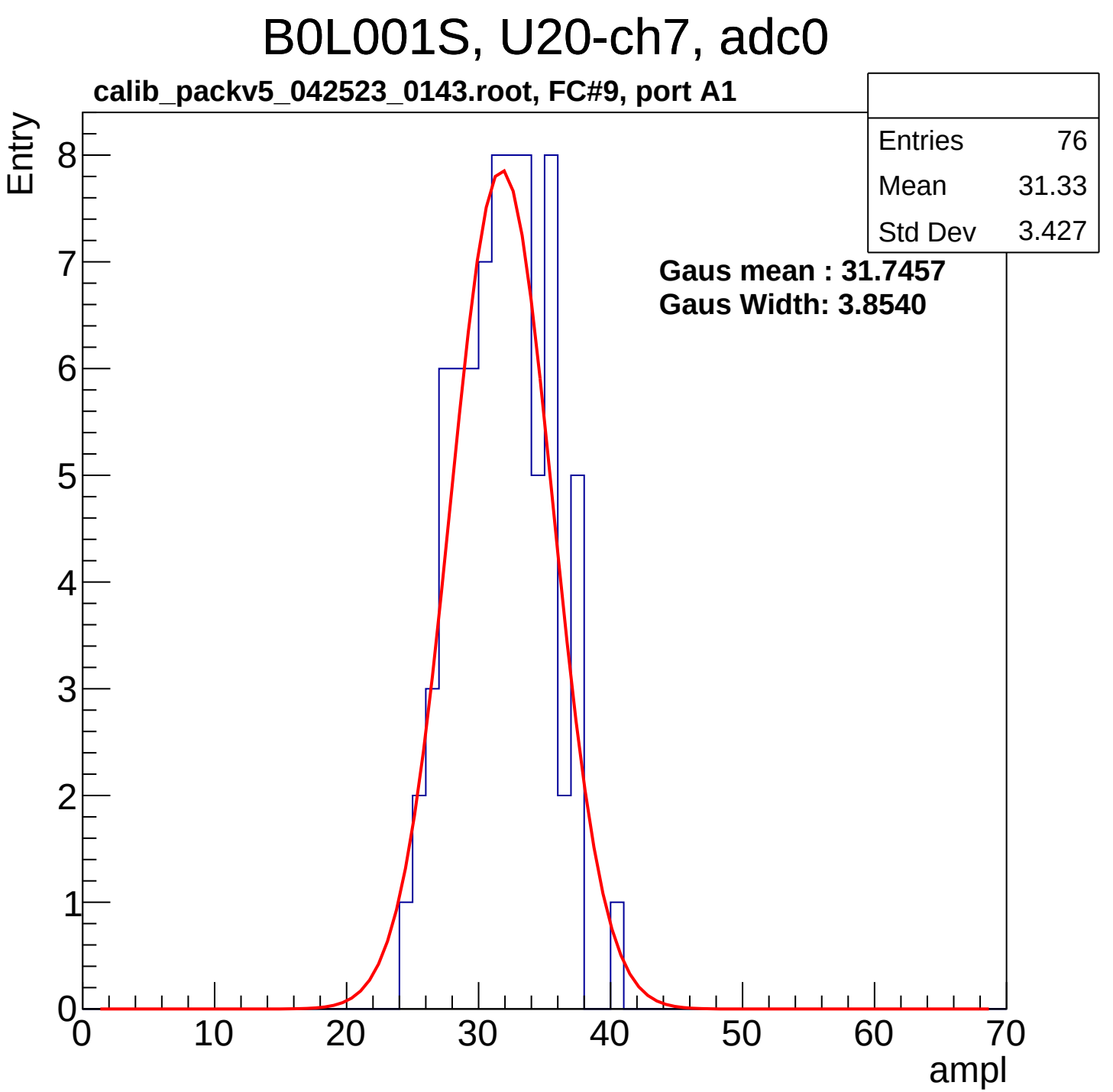
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 31.33
Std Dev 3.427
Gaus mean : 31.7457
Gaus Width: 3.8540
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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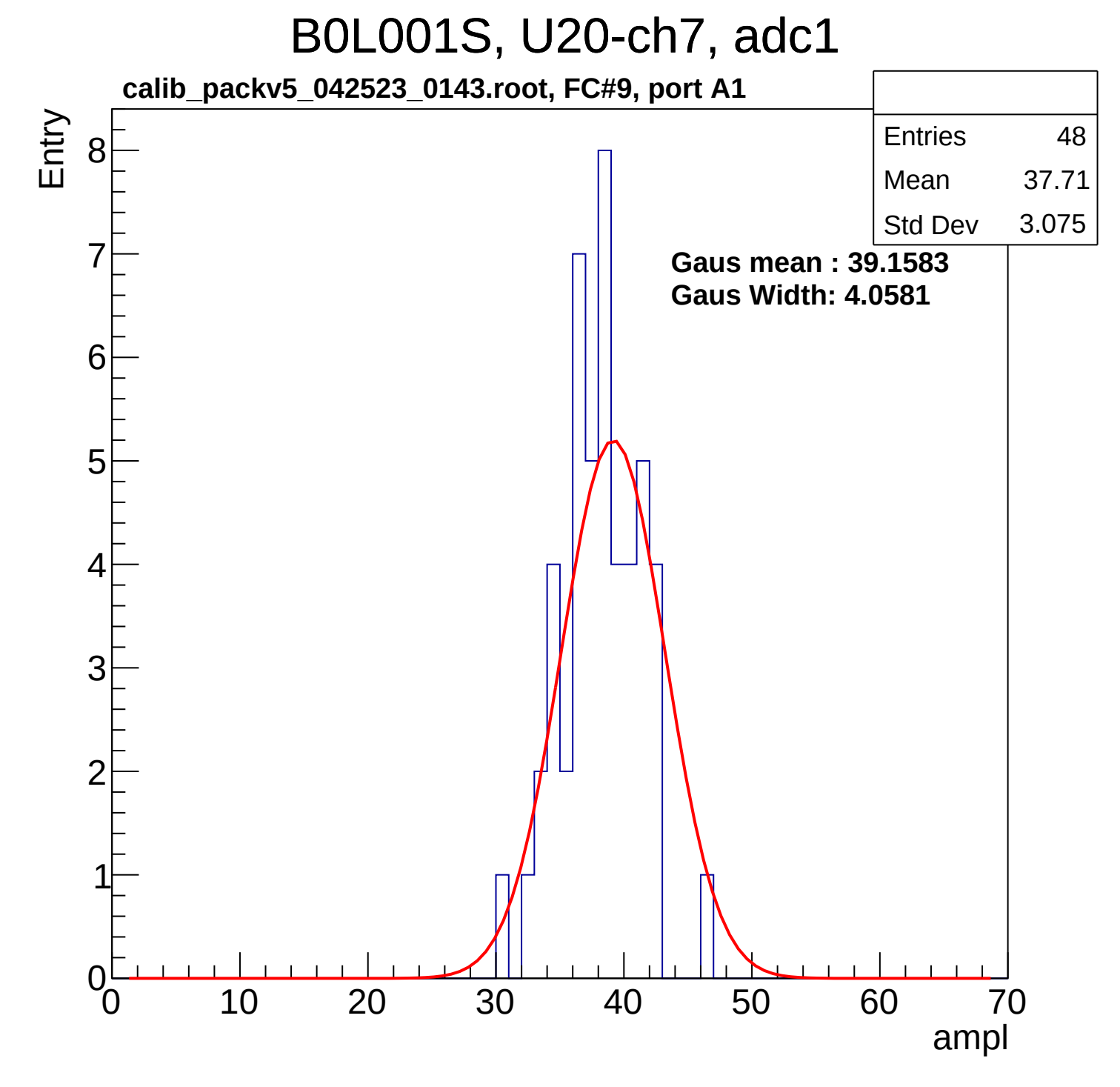
40
50
60
70


B0L001S, U20-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 37.71
Std Dev 3.075
Gaus mean : 39.1583
Gaus Width: 4.0581
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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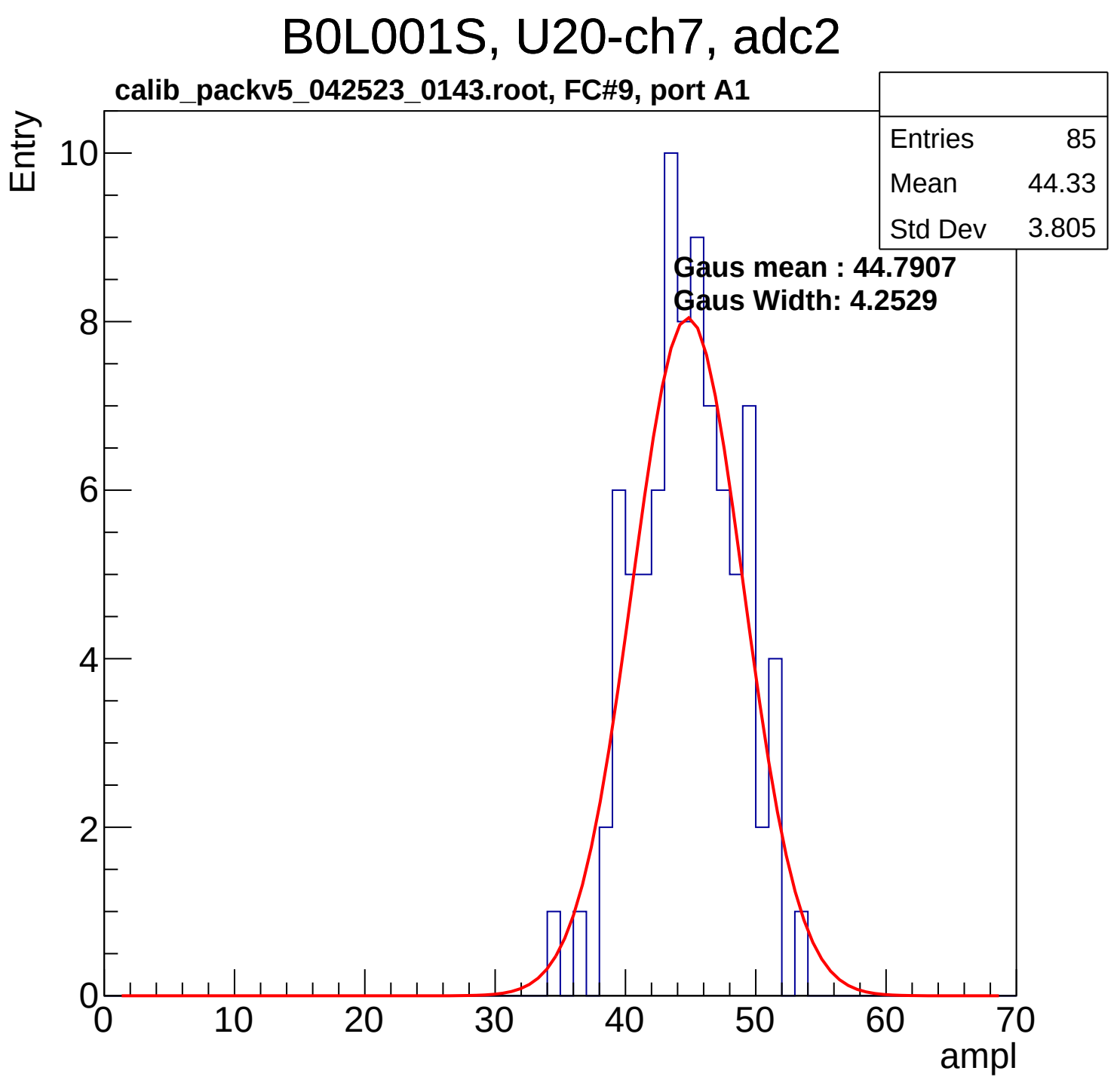
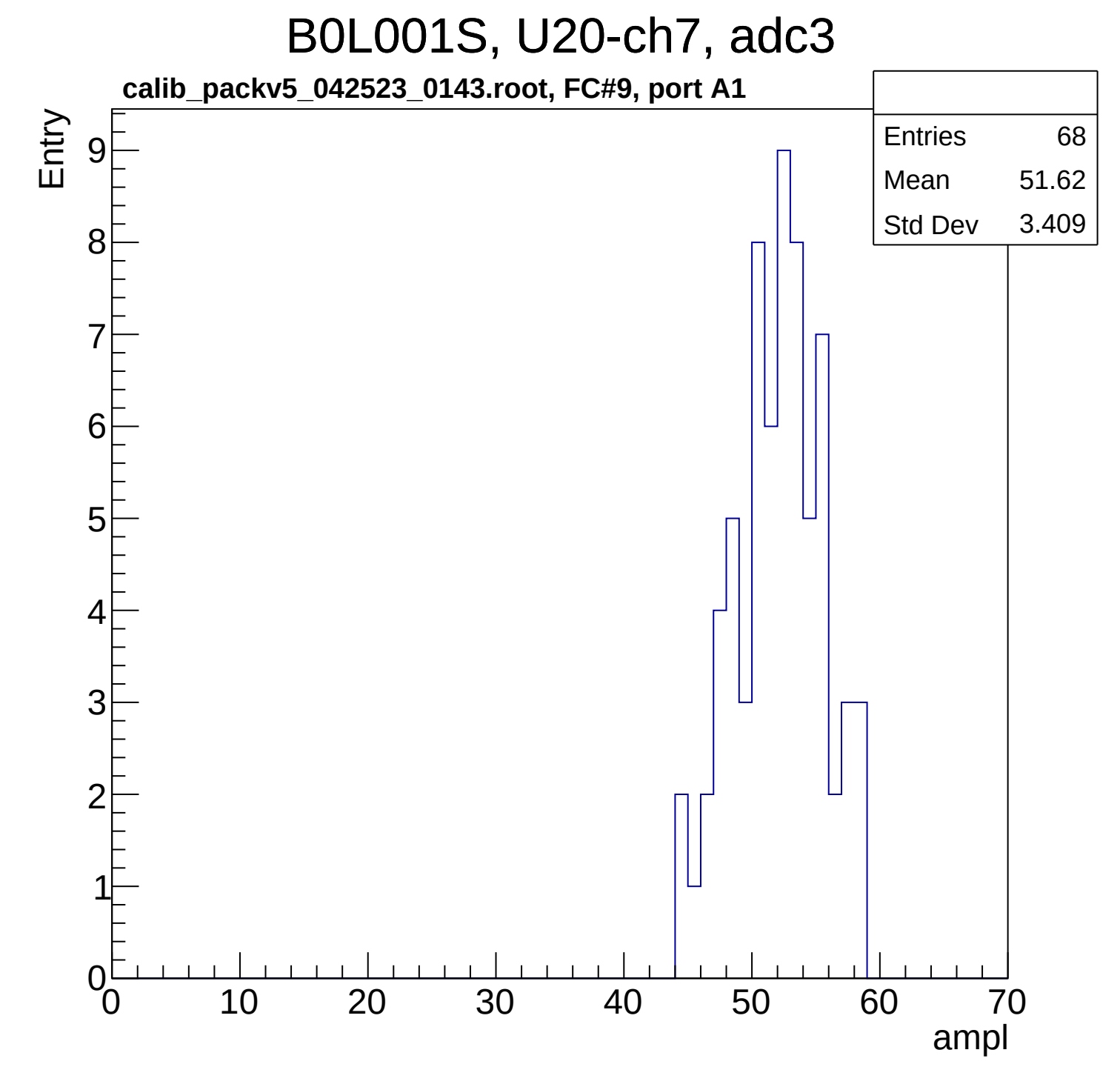

B0L001S, U20-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 44.33
Std Dev 3.805
Gaus mean : 44.7907
Gaus Width: 4.2529
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.62
Std Dev 3.409
Entry
ampl
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3
4
5
6
7
8
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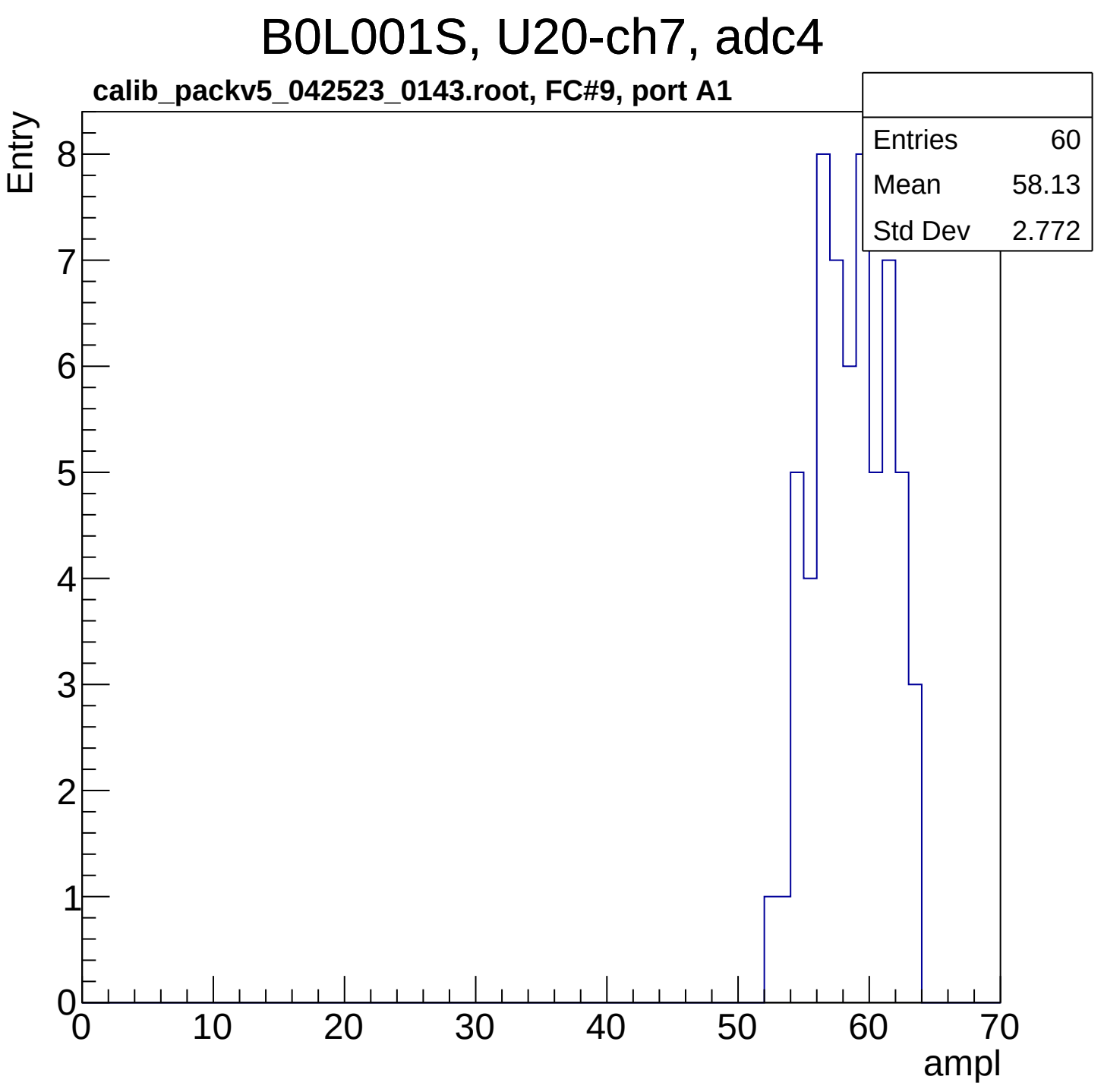
B0L001S, U20-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 58.13
Std Dev 2.772
Entry
ampl
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4
5
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7
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0
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60
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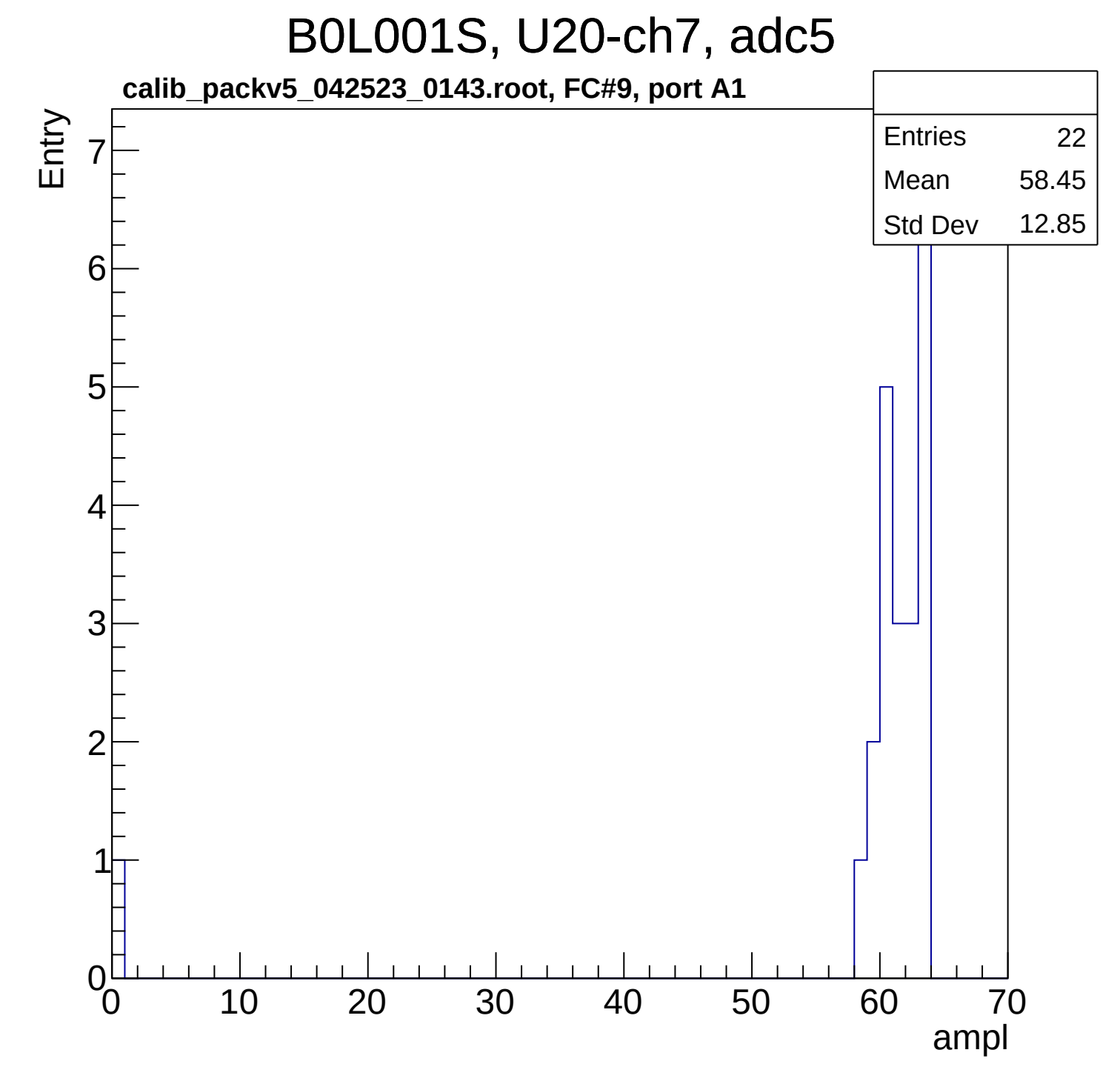
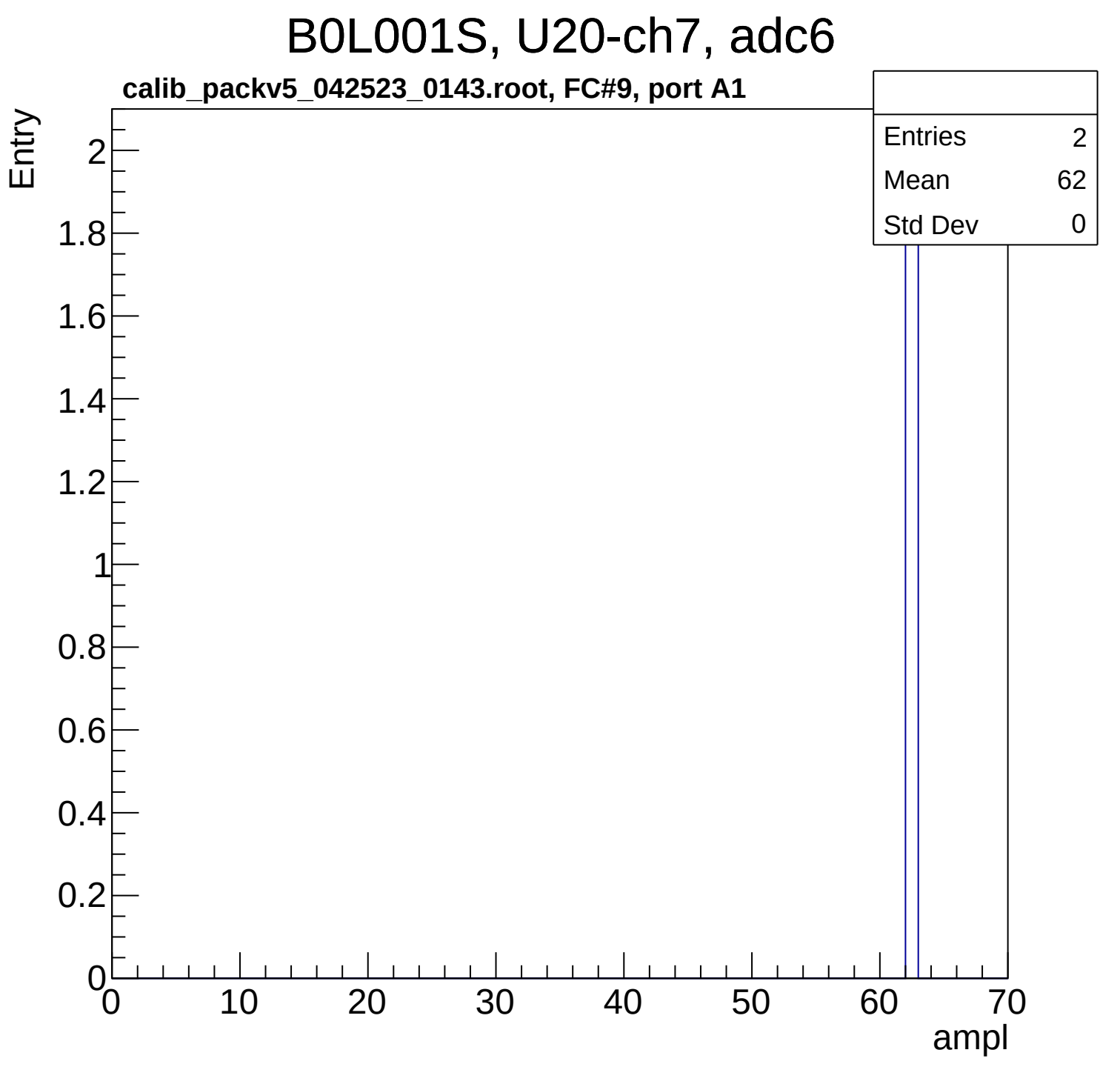

B0L001S, U20-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.45
Std Dev 12.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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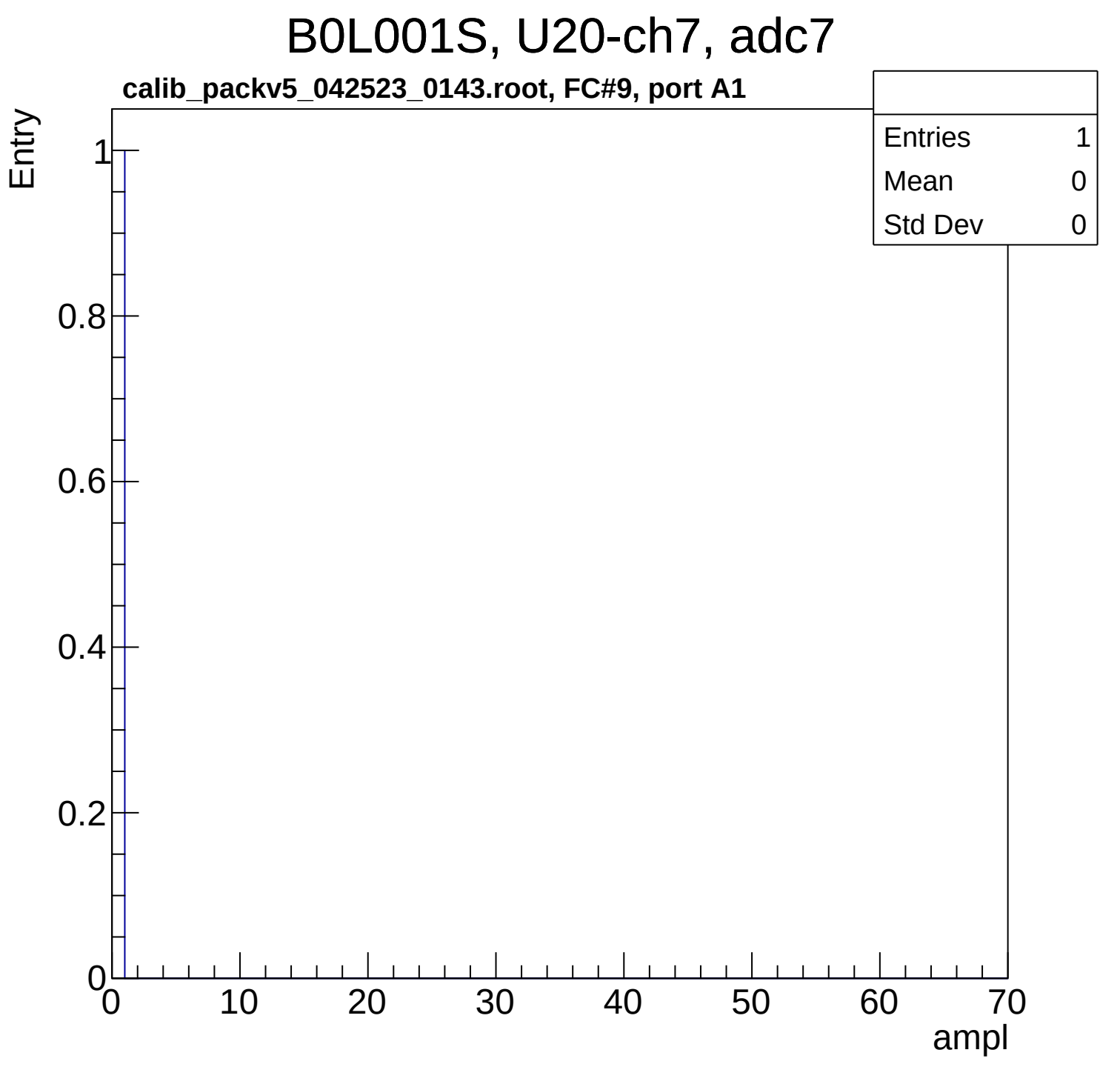
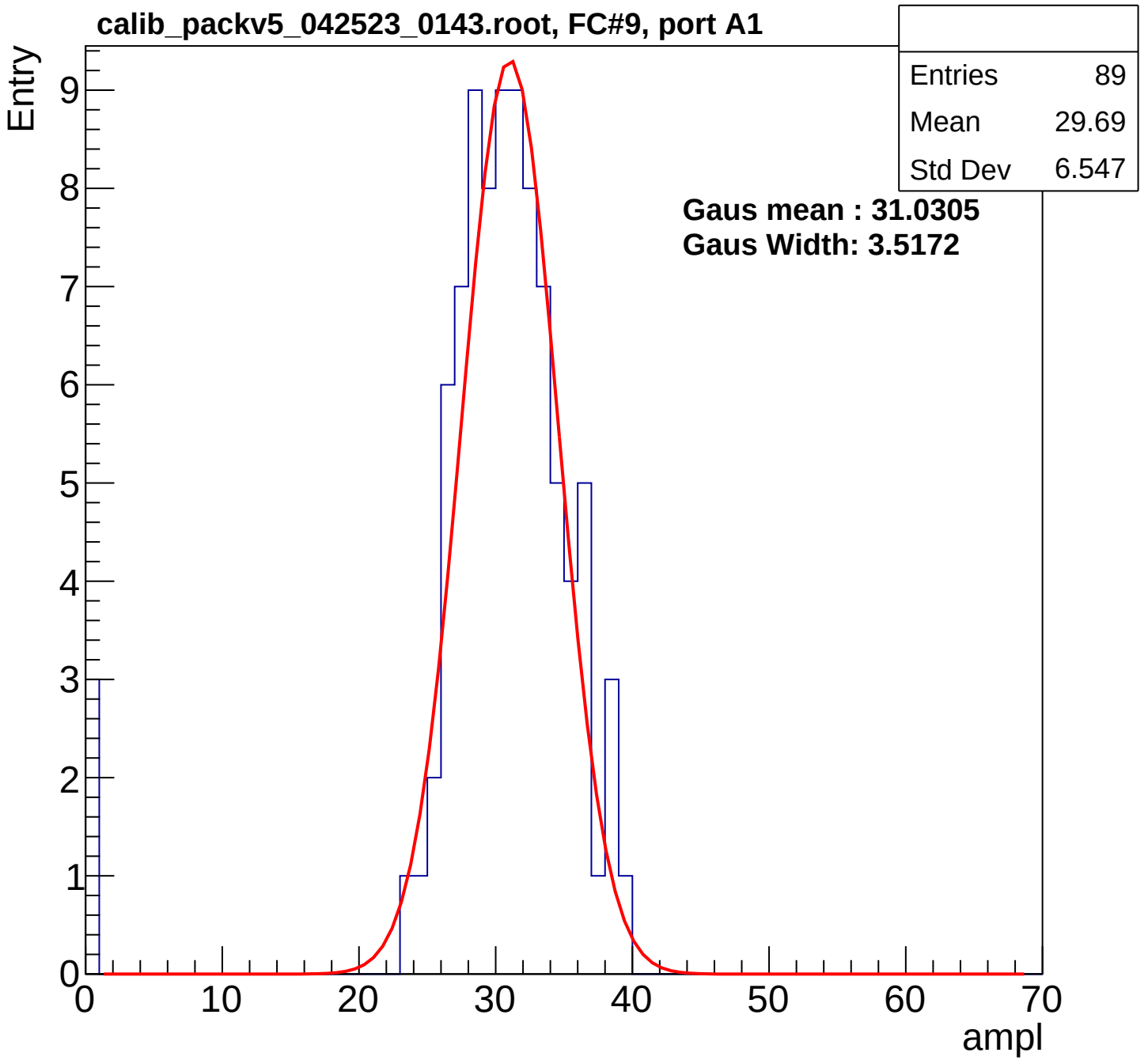

B0L001S, U20-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 29.69
Std Dev 6.547
Gaus mean : 31.0305
Gaus Width: 3.5172
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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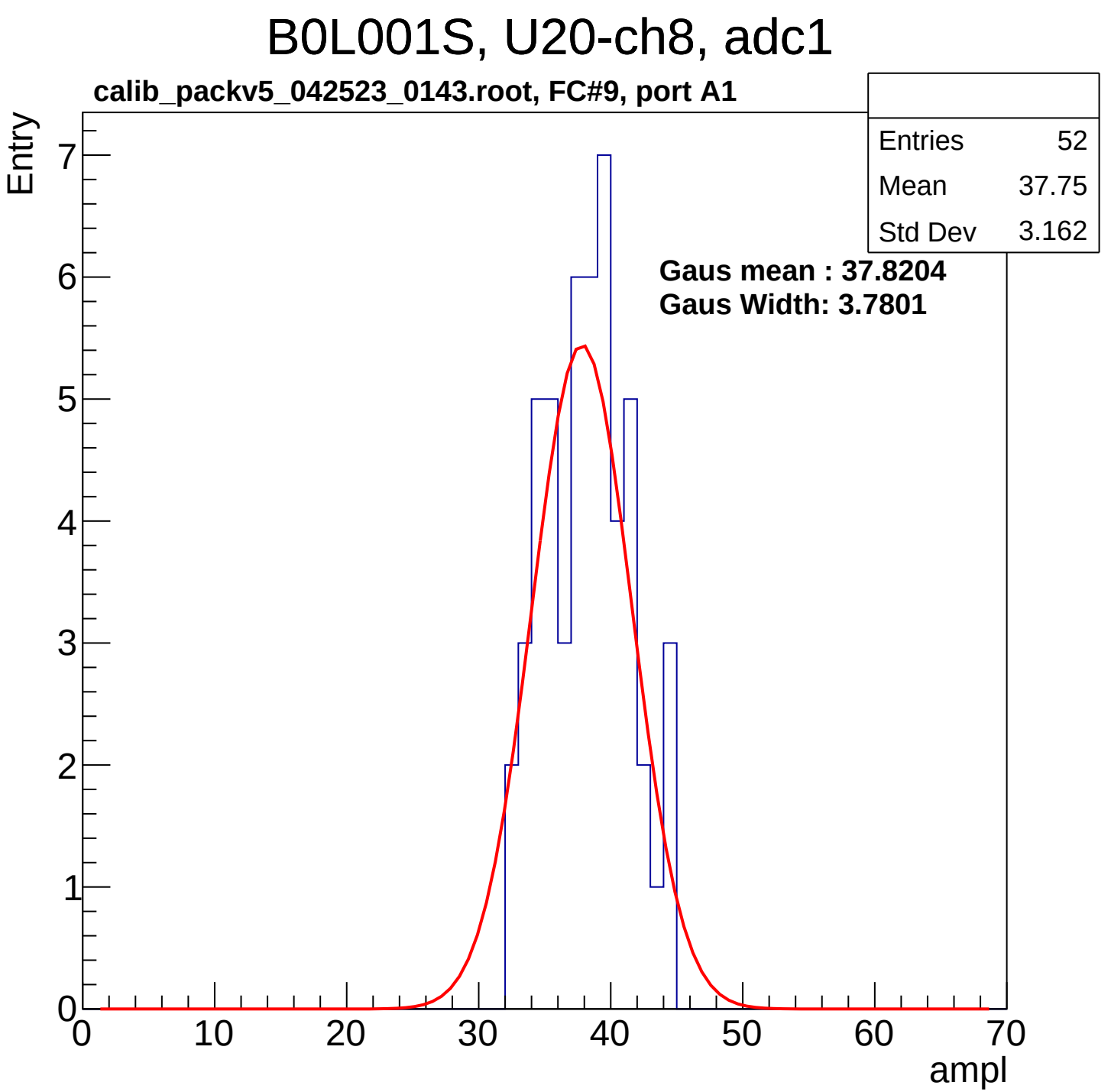
B0L001S, U20-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 37.75
Std Dev 3.162
Gaus mean : 37.8204
Gaus Width: 3.7801
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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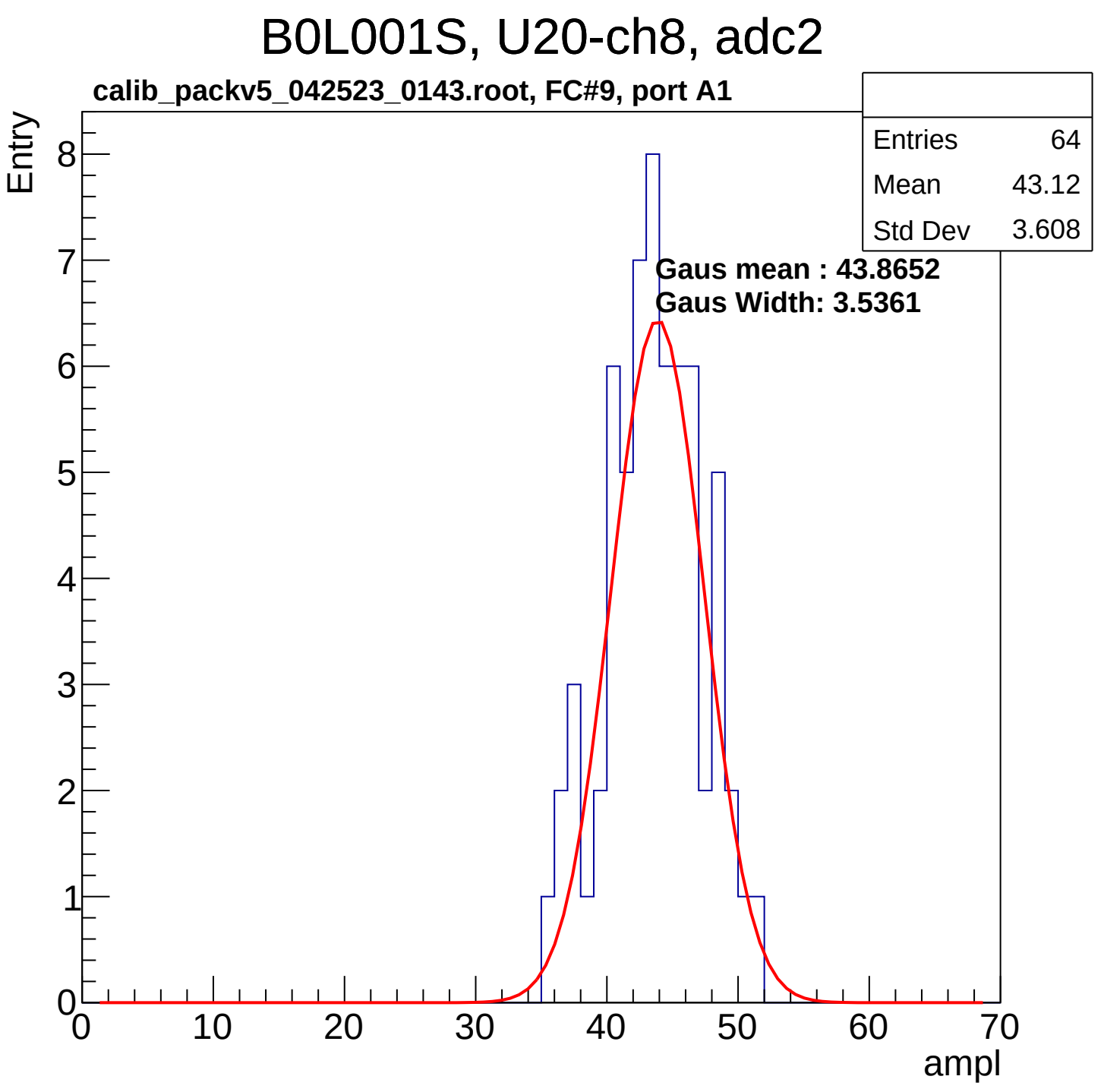
B0L001S, U20-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.12
Std Dev 3.608
Gaus mean : 43.8652
Gaus Width: 3.5361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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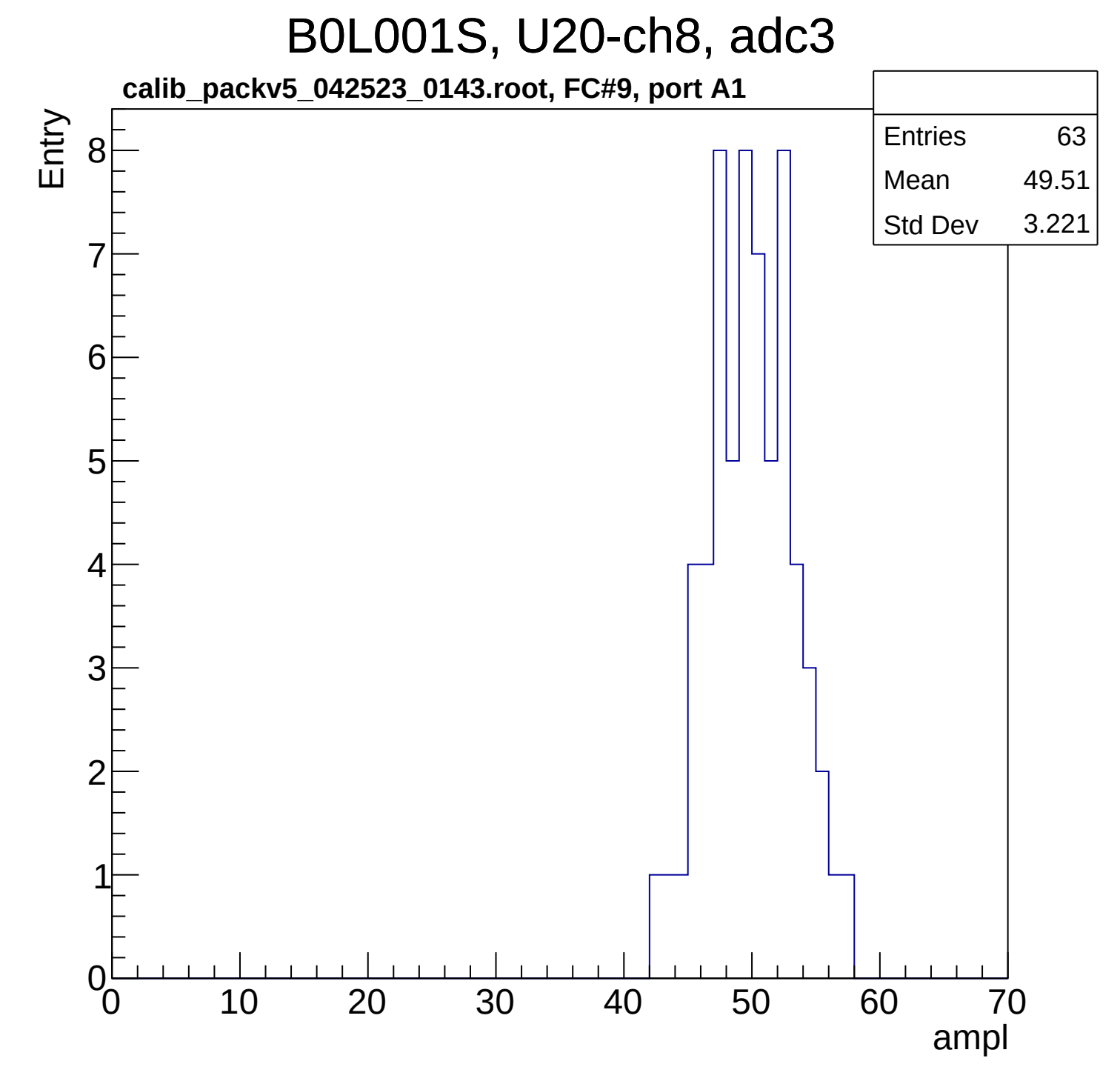
B0L001S, U20-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.51
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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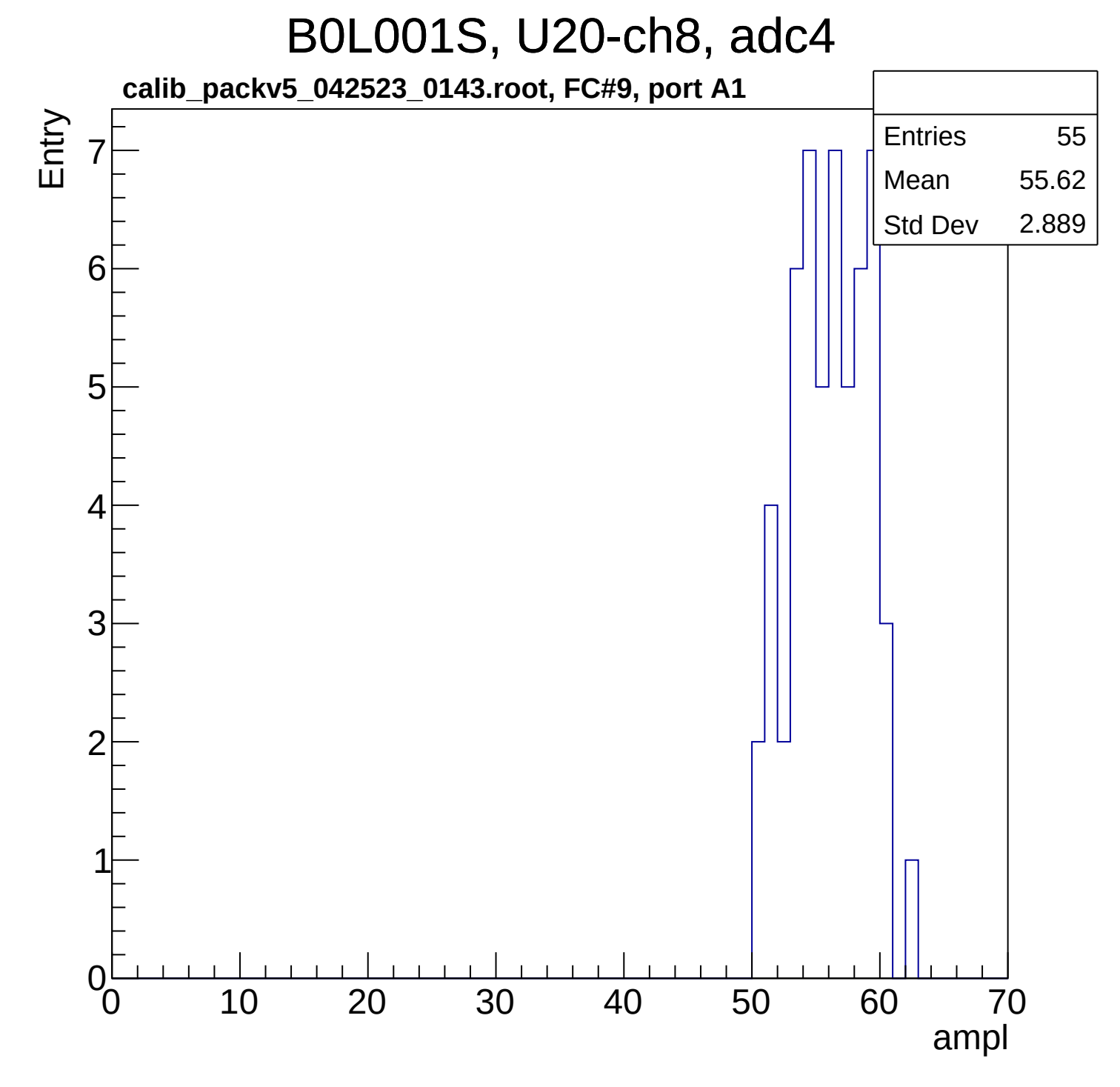
B0L001S, U20-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 55.62
Std Dev 2.889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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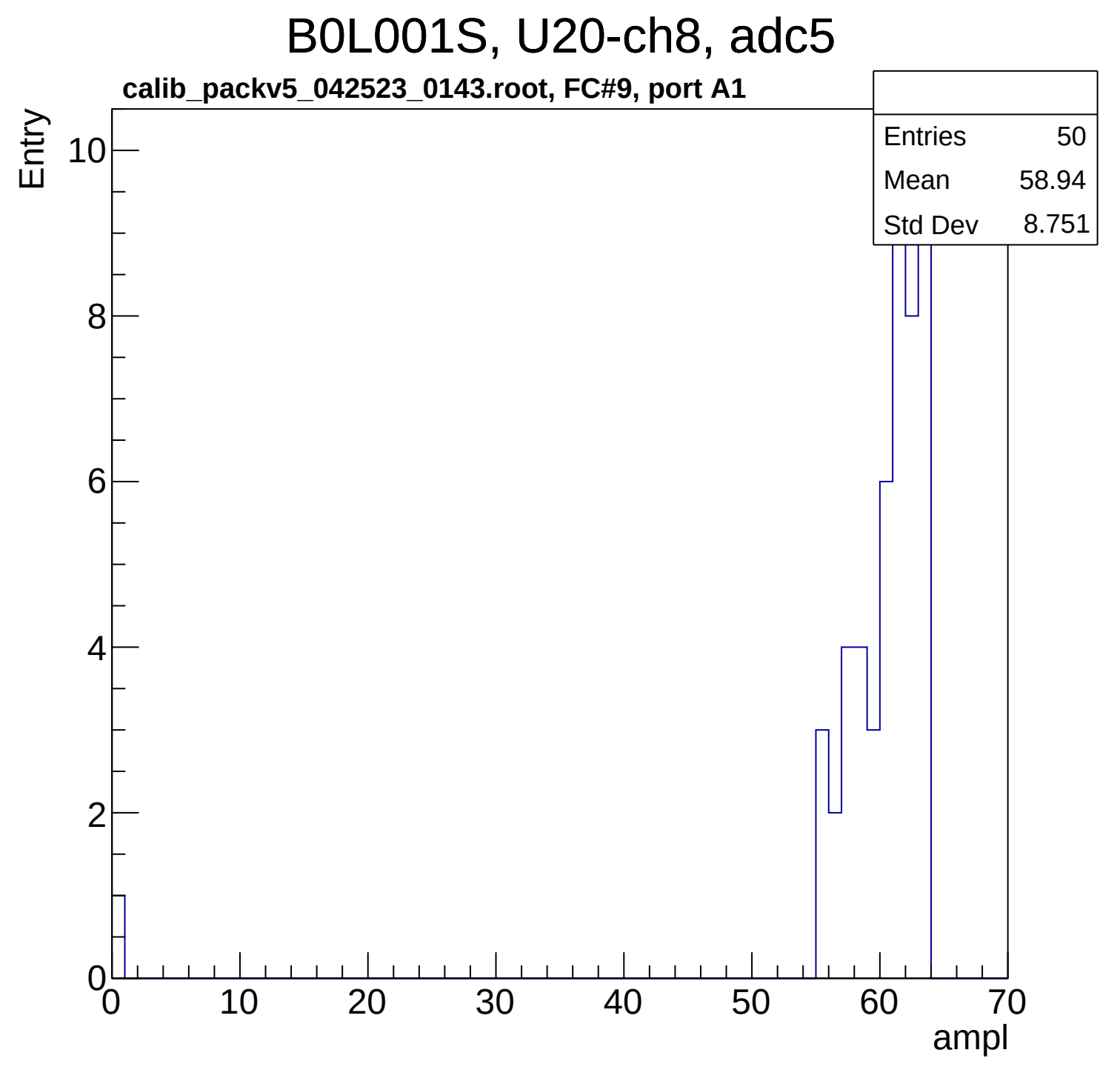
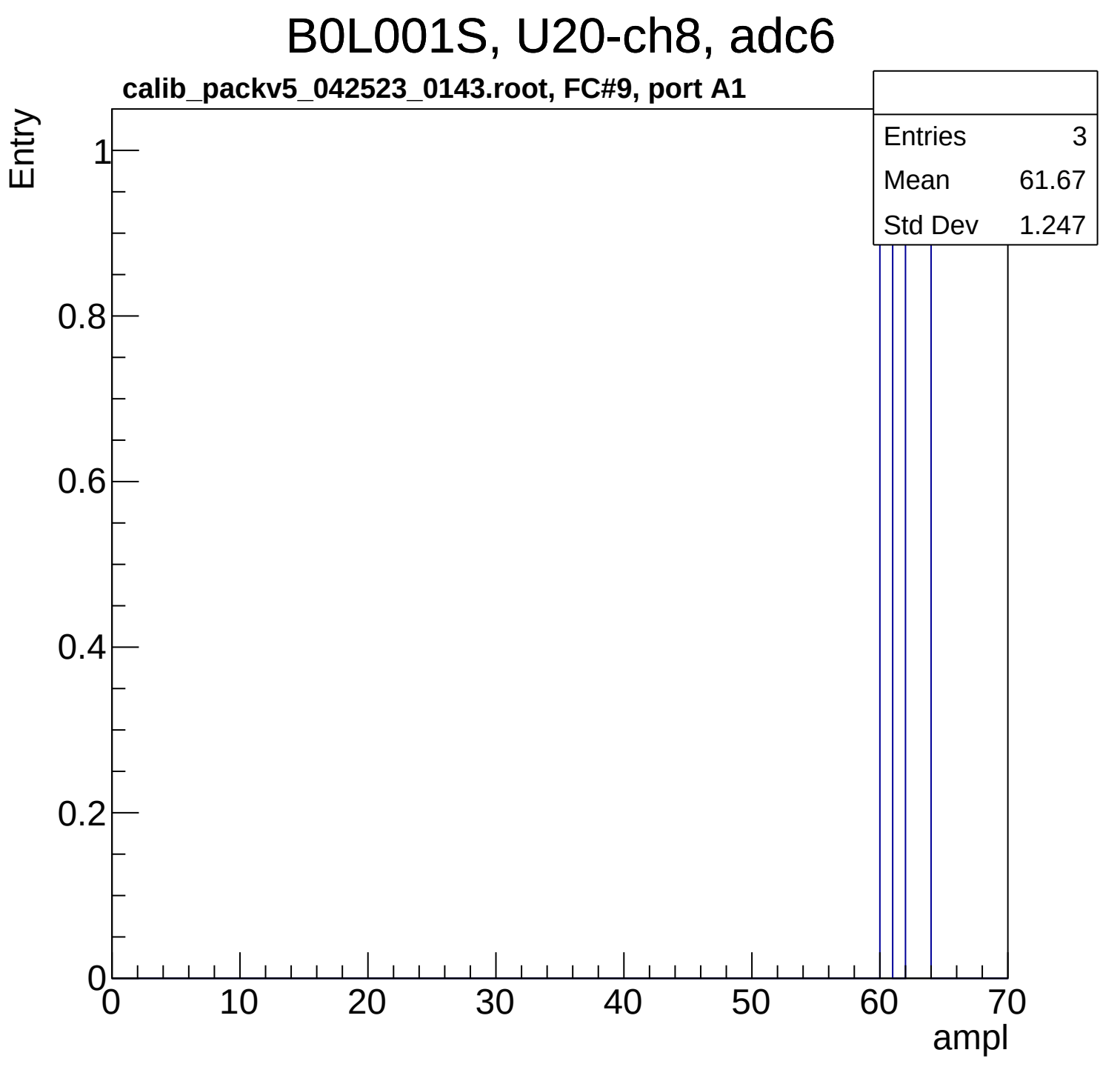
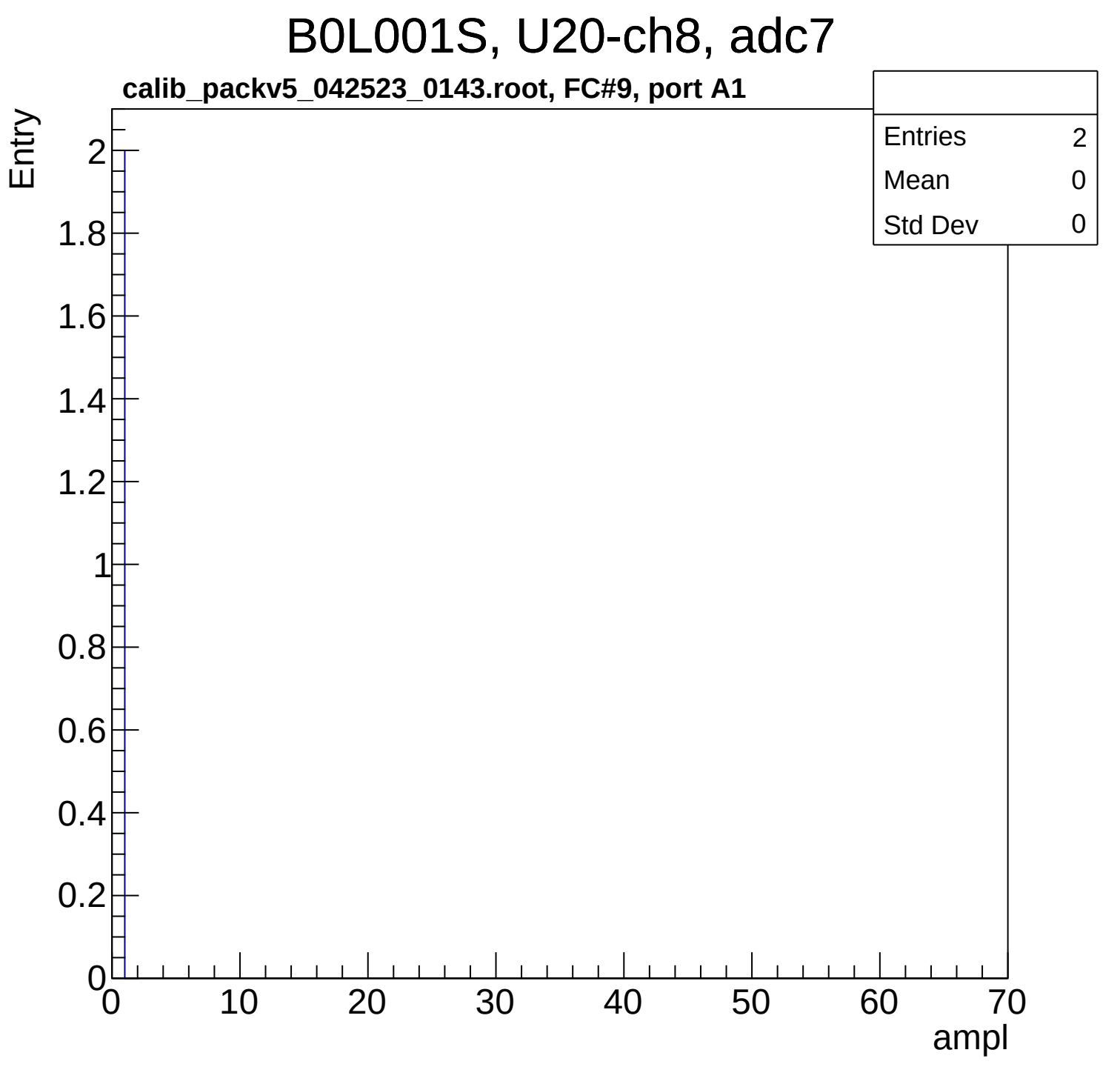
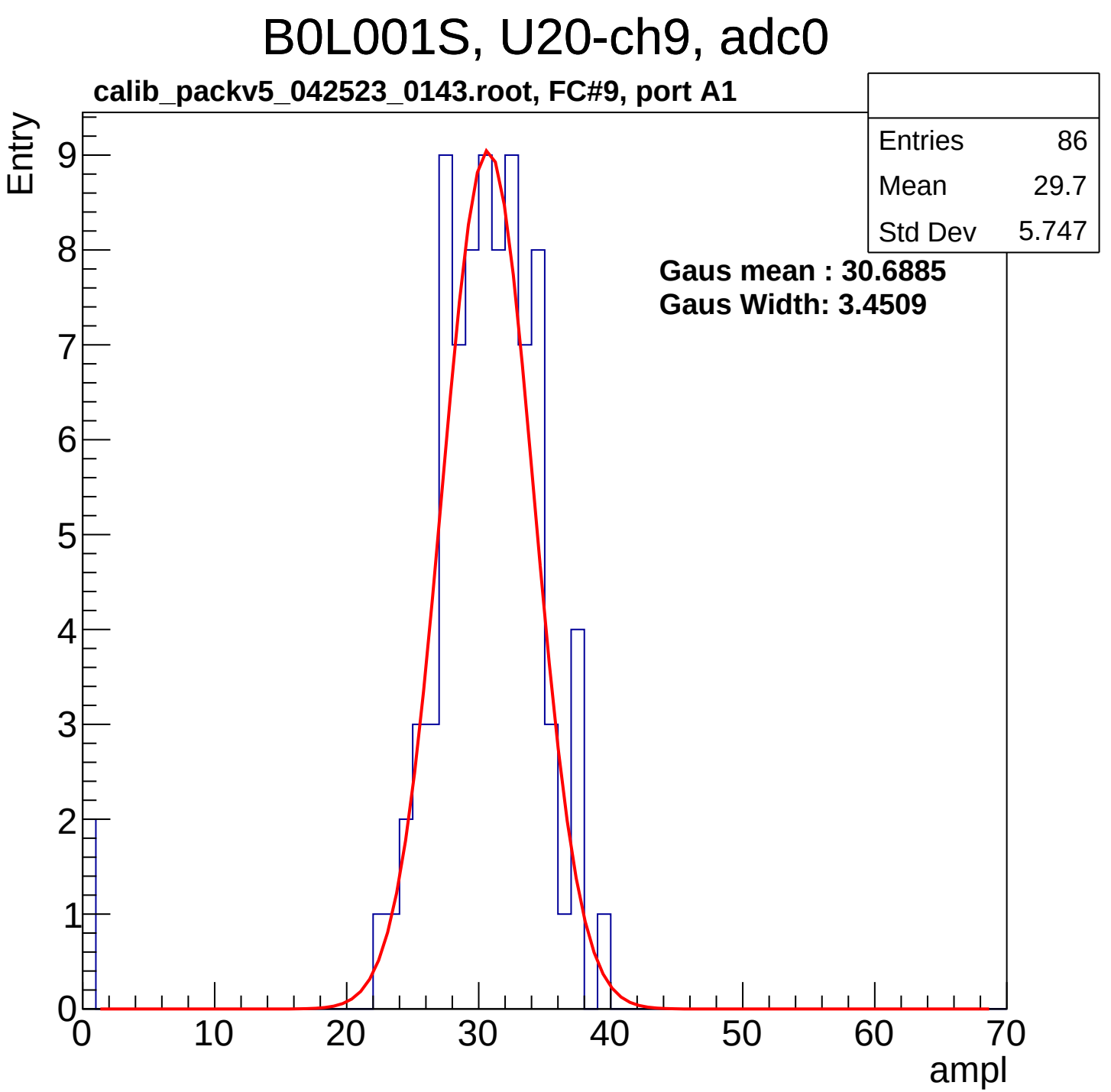
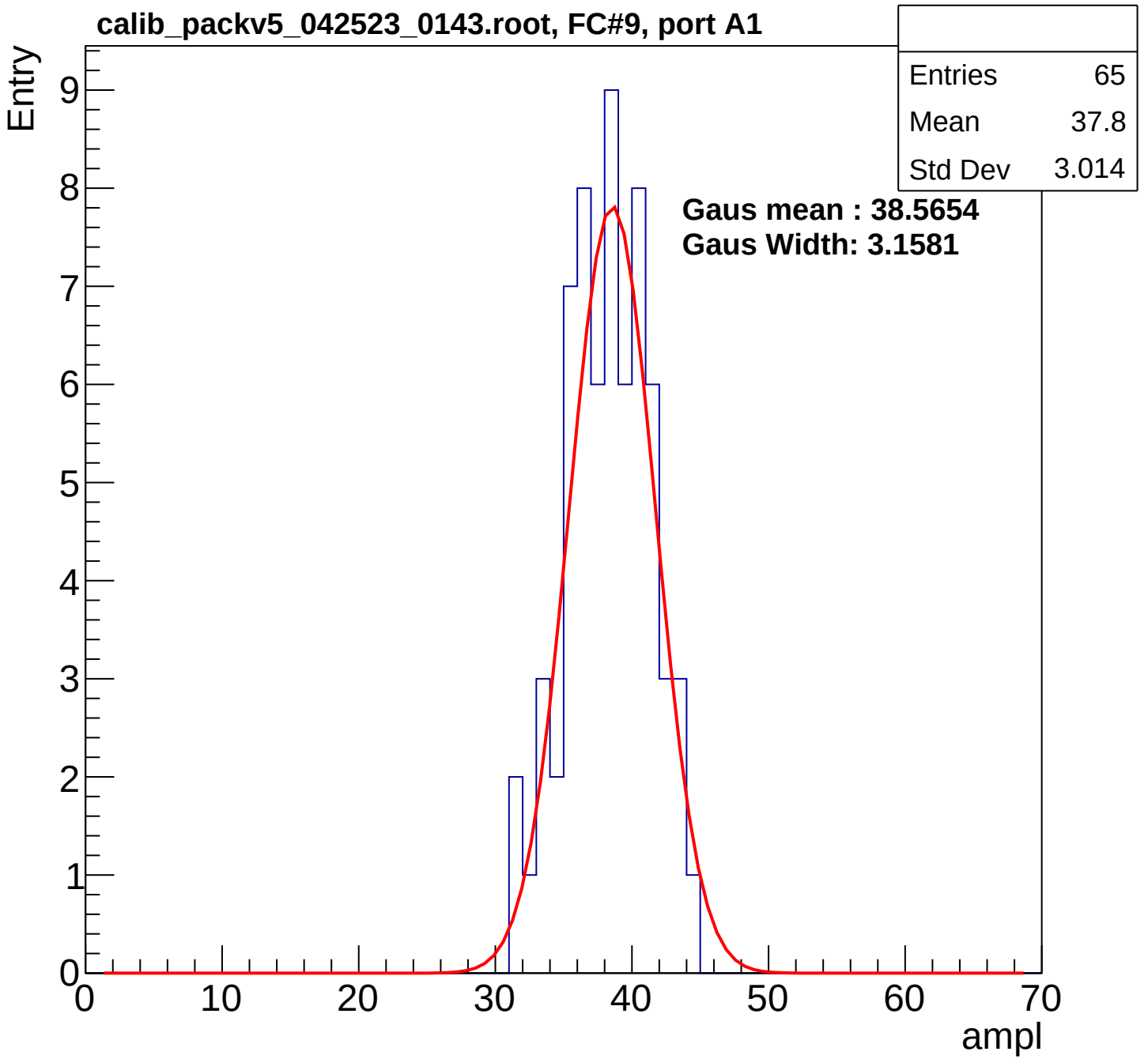

B0L001S, U20-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58.94
Std Dev 8.751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 29.7
Std Dev 5.747
Gaus mean : 30.6885
Gaus Width: 3.4509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.8
Std Dev 3.014
Gaus mean : 38.5654
Gaus Width: 3.1581
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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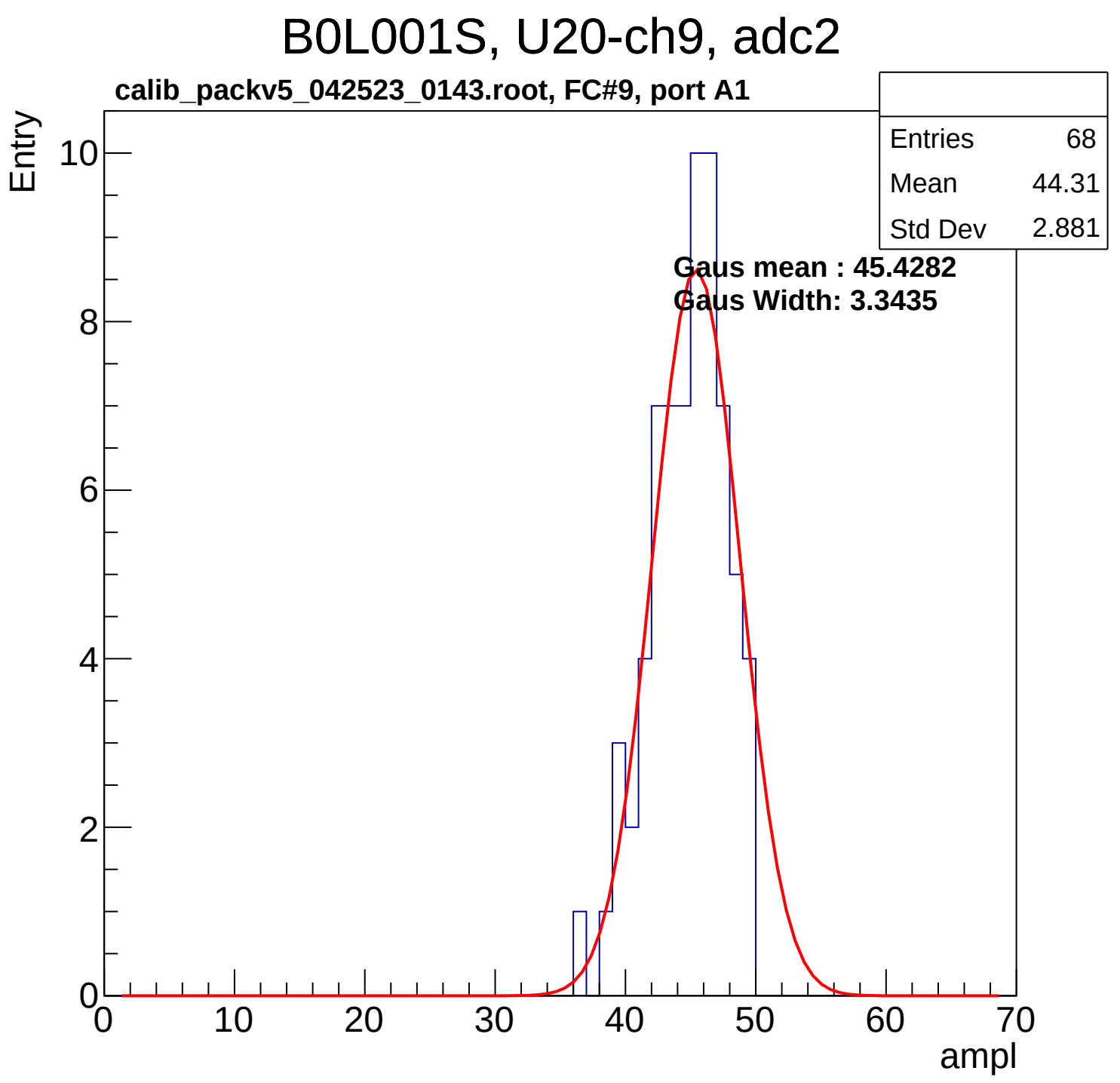
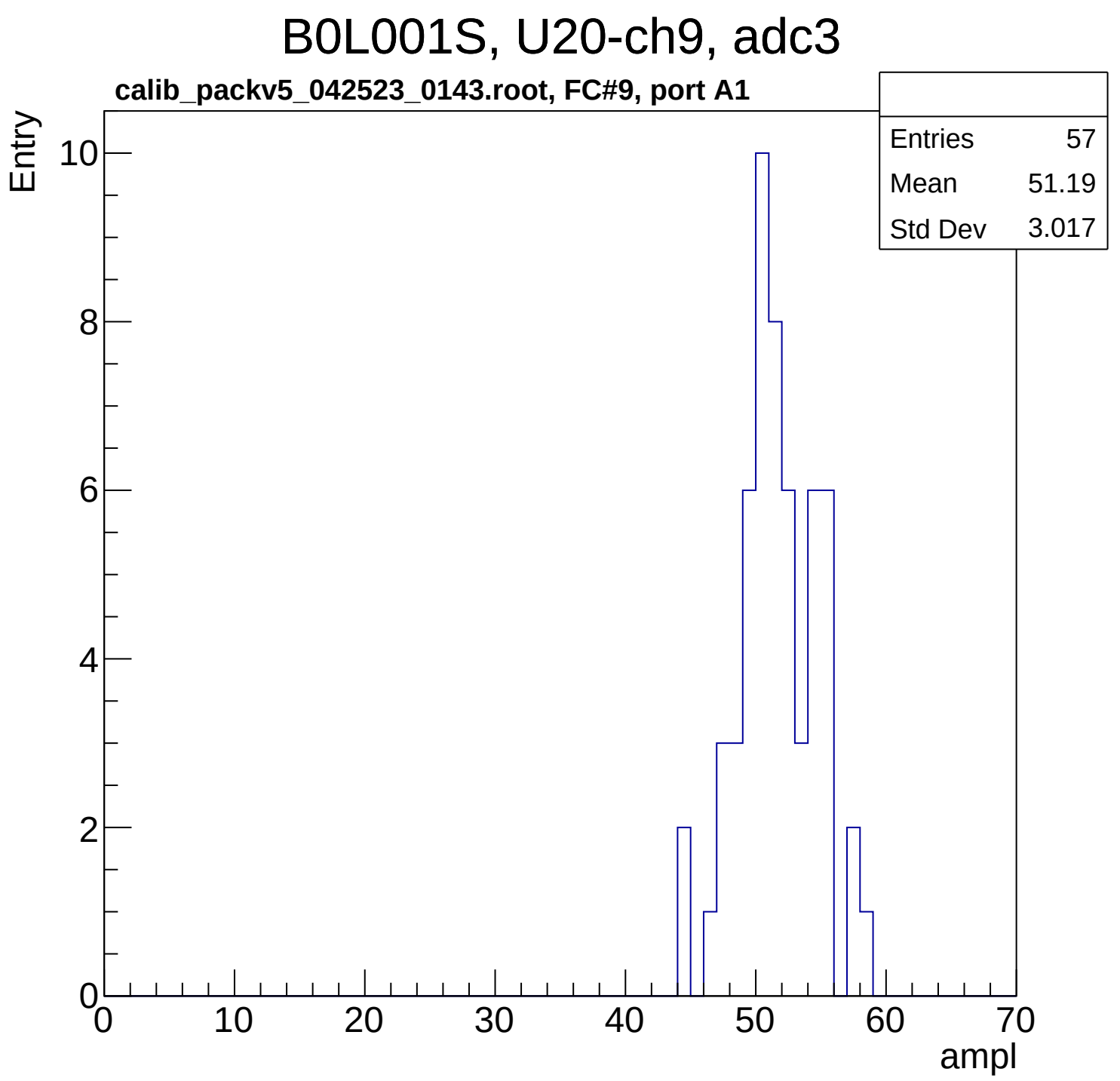
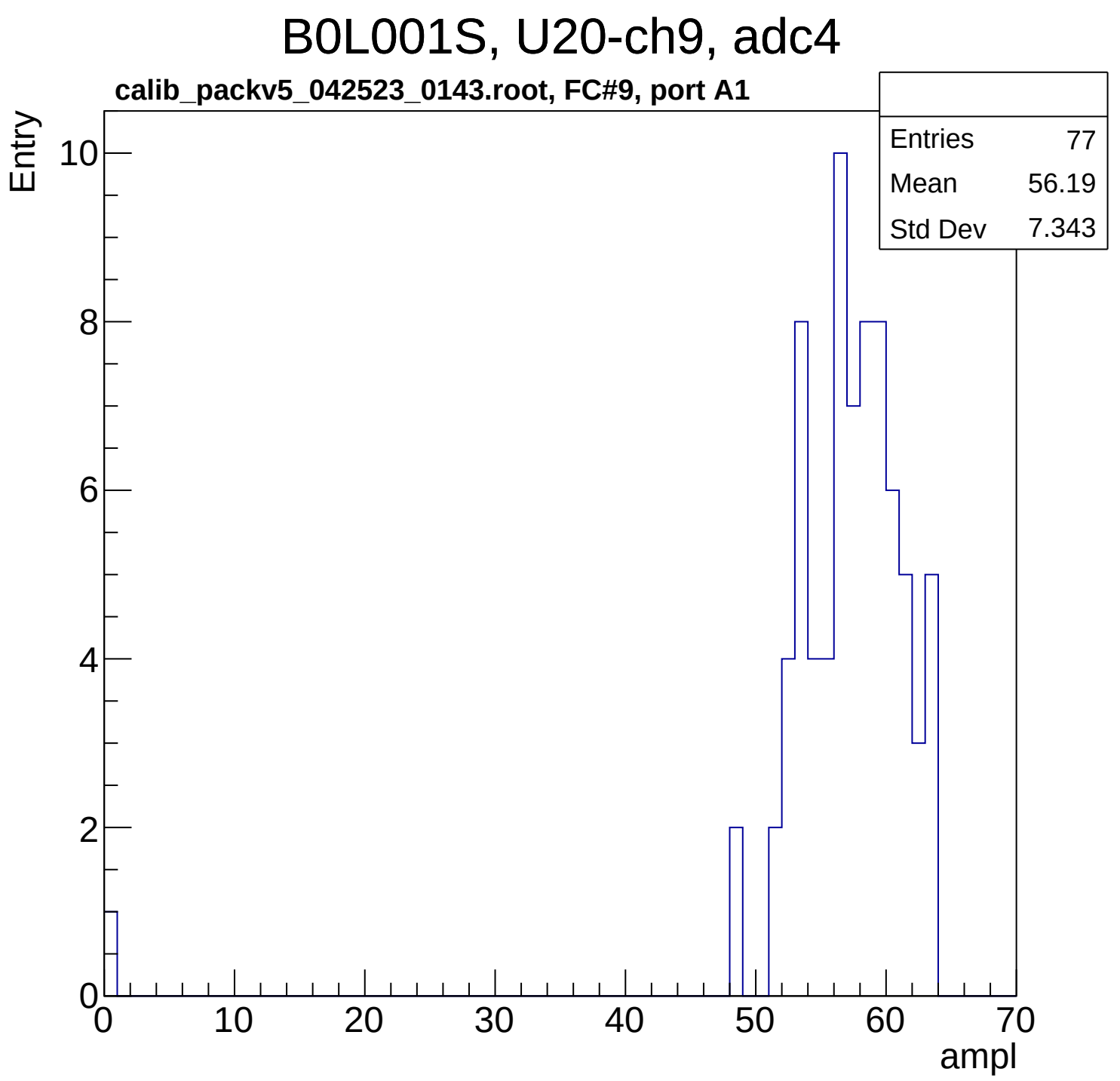
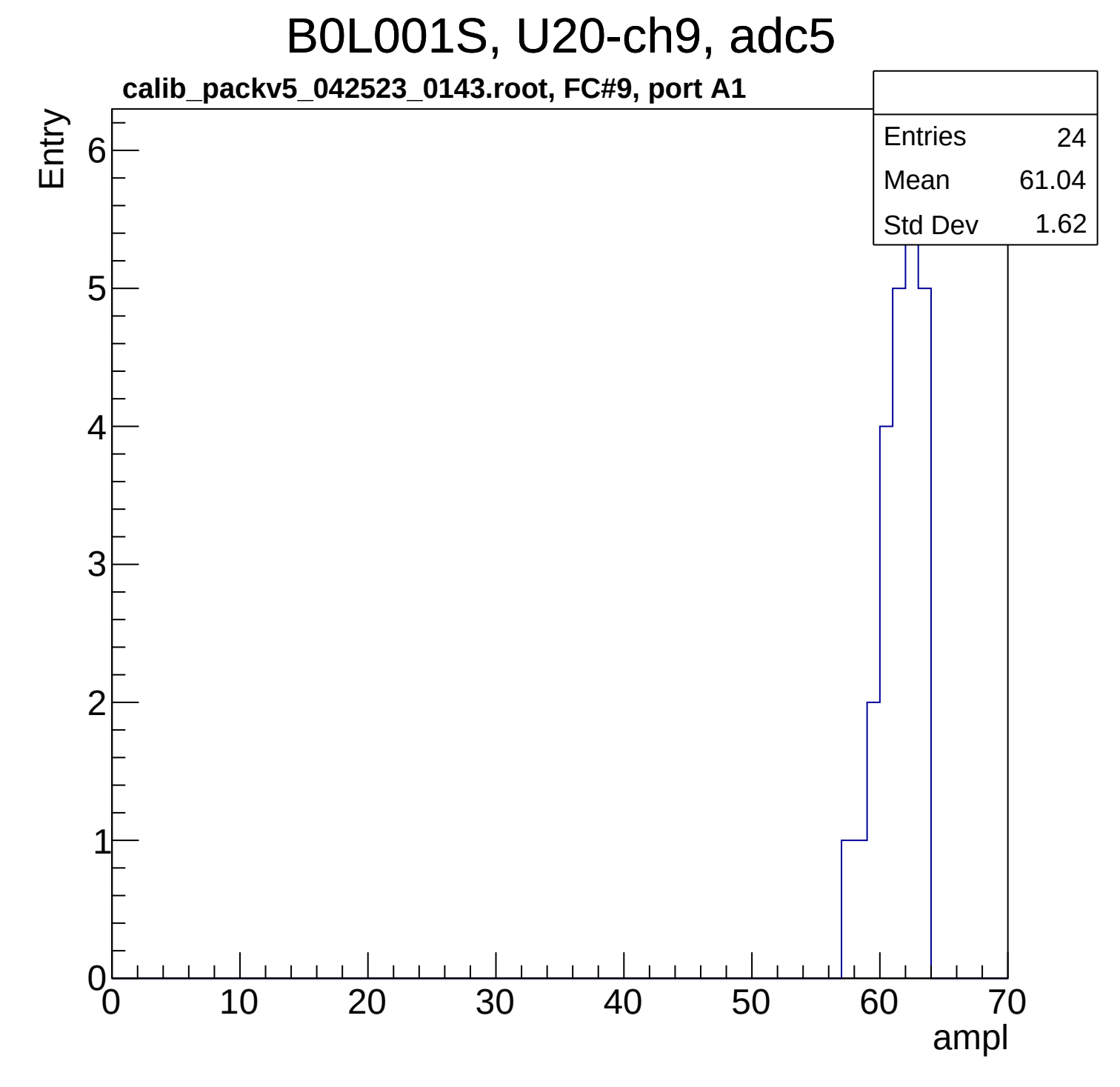
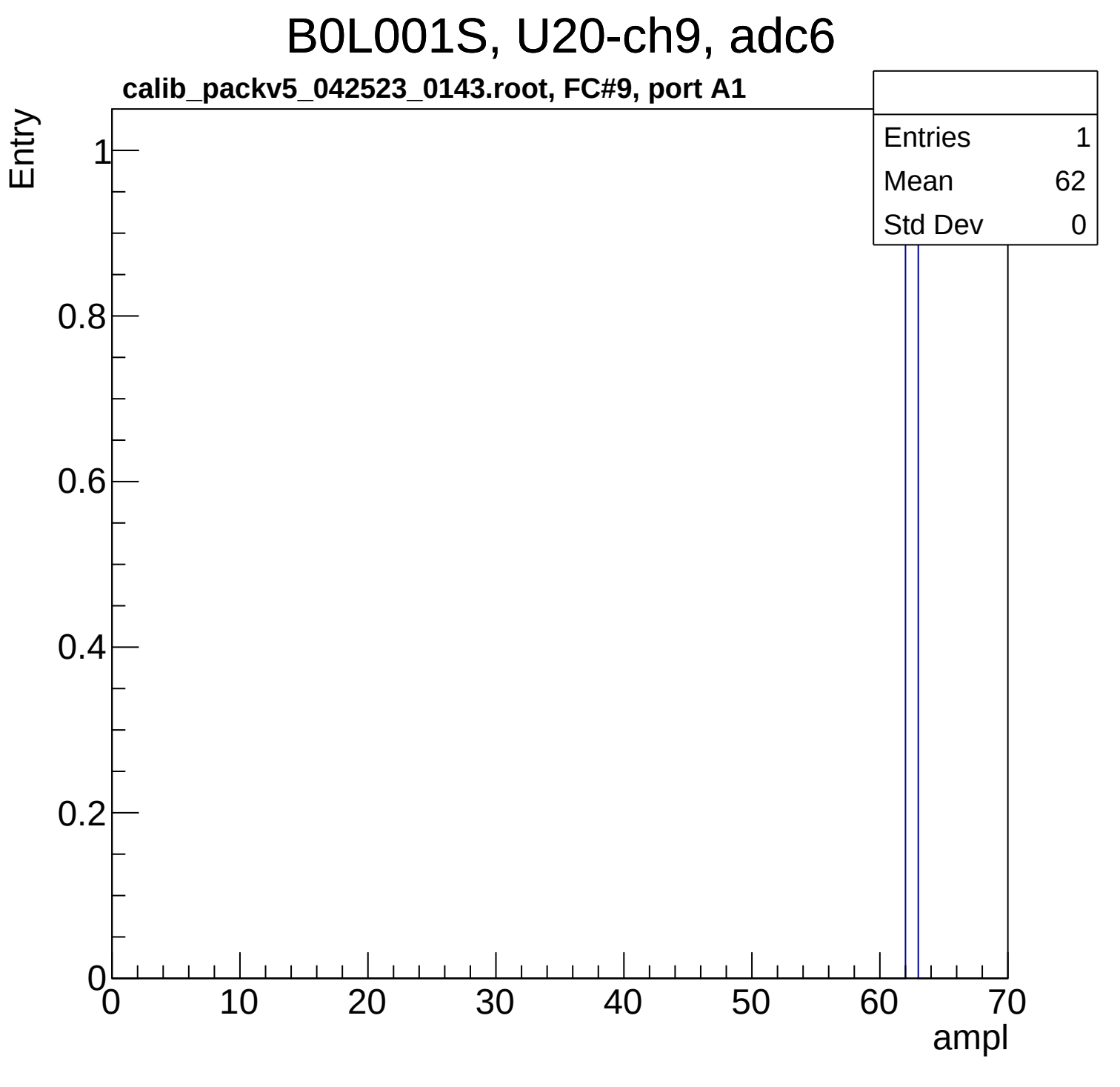
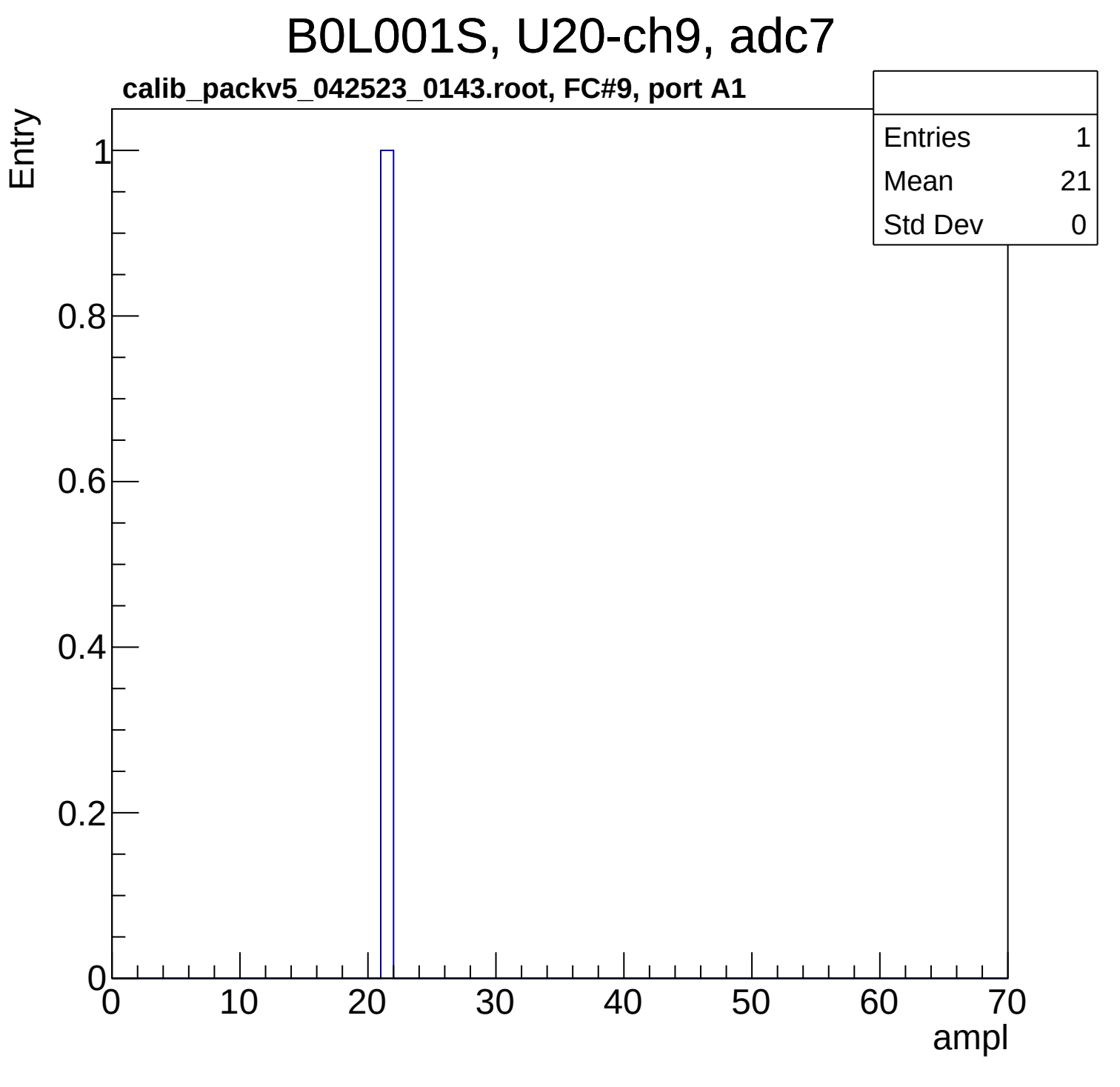

B0L001S, U20-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.31
Std Dev 2.881
Gaus mean : 45.4282
Gaus Width: 3.3435
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.19
Std Dev 3.017
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 56.19
Std Dev 7.343
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 61.04
Std Dev 1.62
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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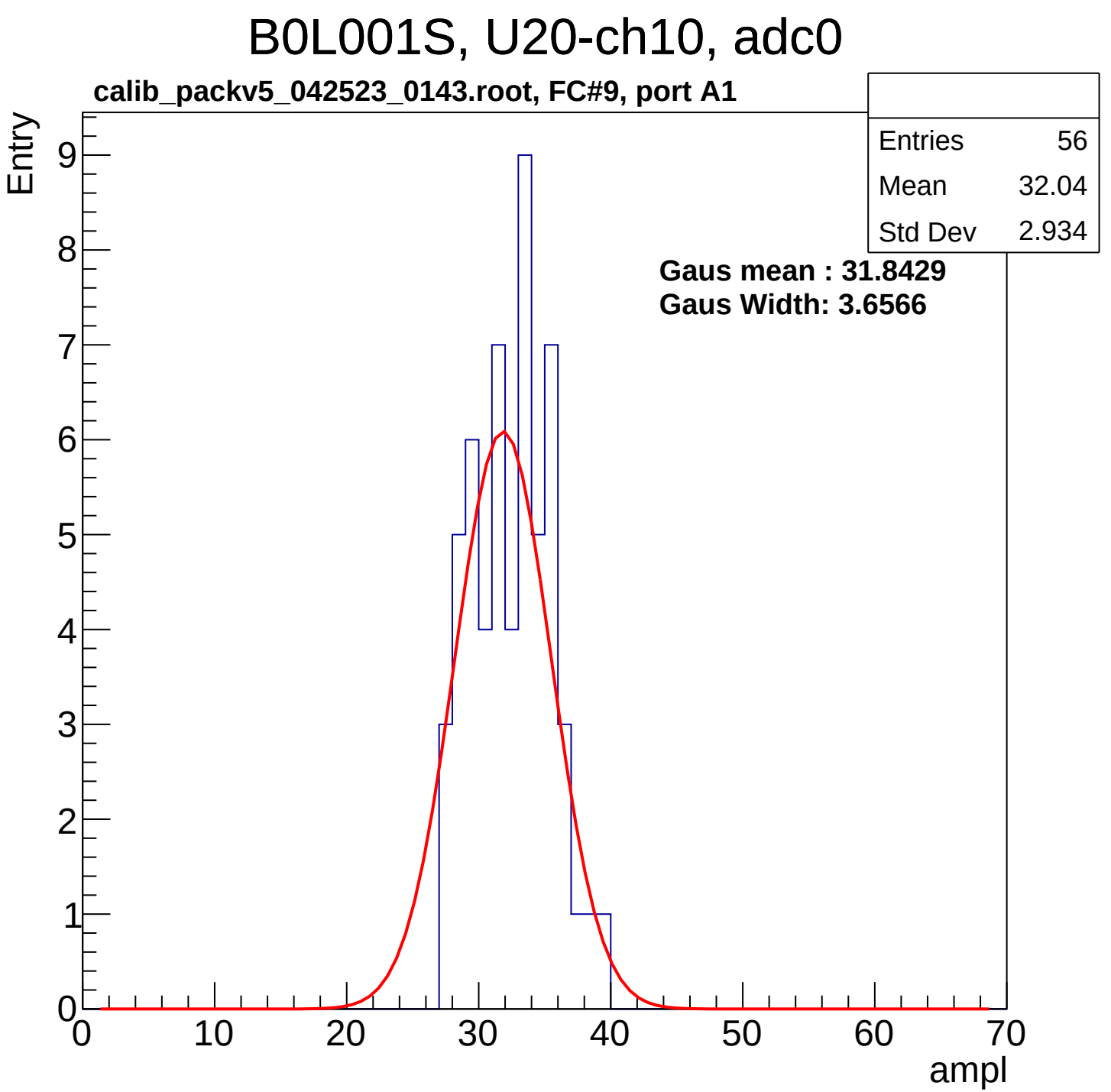
30
40
50
60
70
ampl


B0L001S, U20-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 32.04
Std Dev 2.934
Gaus mean : 31.8429
Gaus Width: 3.6566
Entry
ampl
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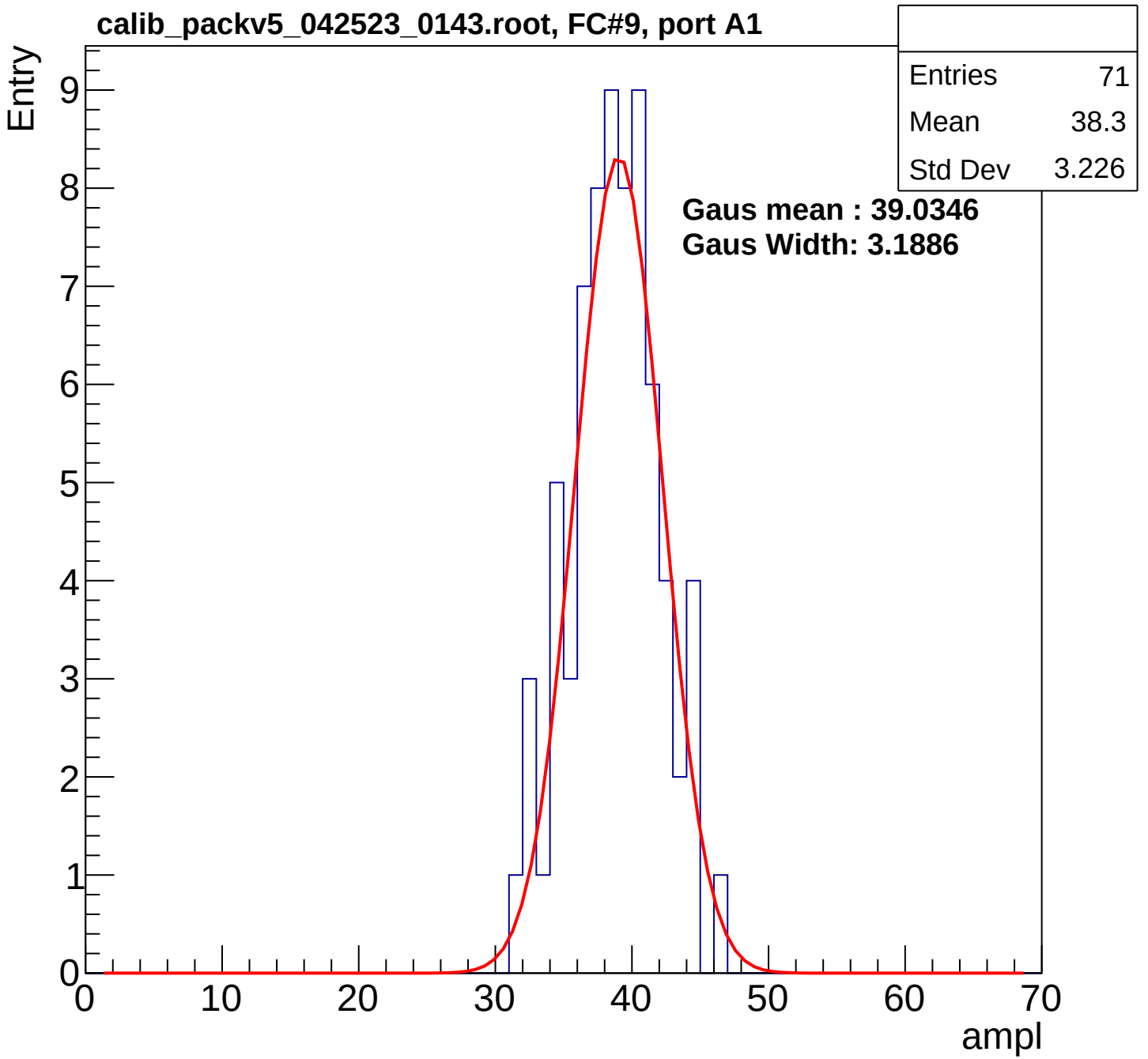
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.3
Std Dev 3.226
Gaus mean : 39.0346
Gaus Width: 3.1886
Entry
ampl
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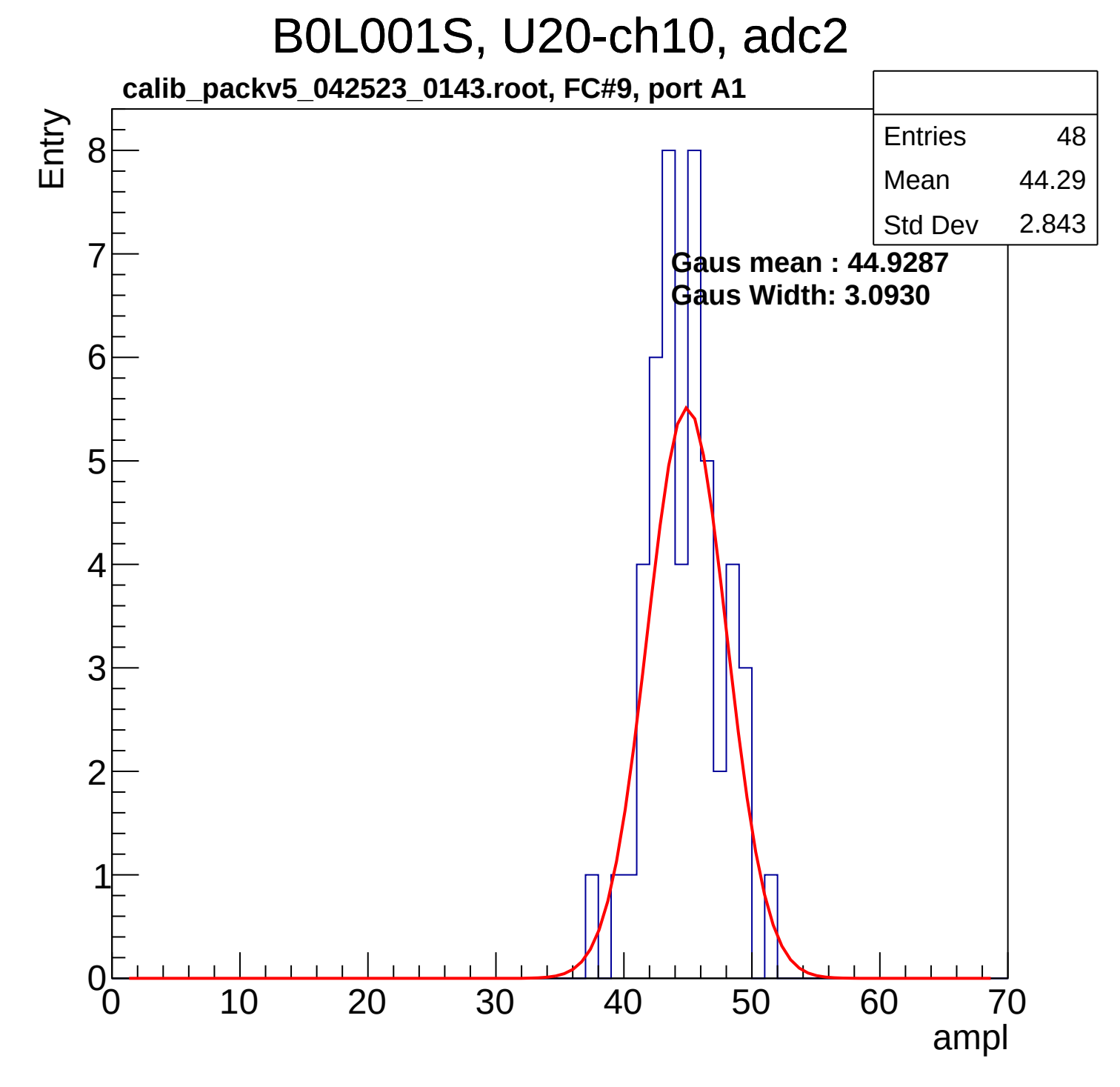
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 44.29
Std Dev 2.843
Gaus mean : 44.9287
Gaus Width: 3.0930
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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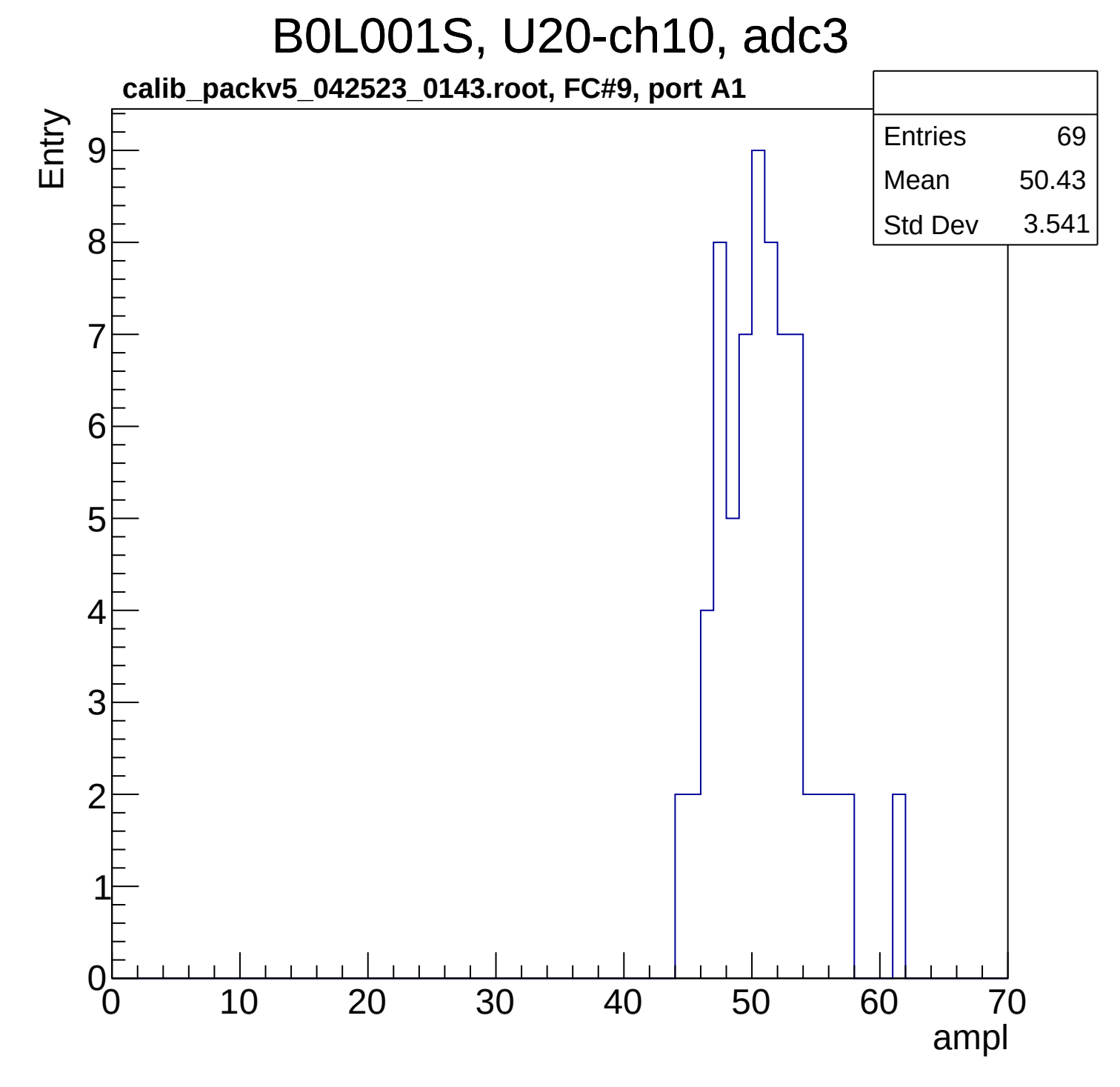
40
50
60
70


B0L001S, U20-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.43
Std Dev 3.541
Entry
ampl
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4
5
6
7
8
9
0
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40
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60
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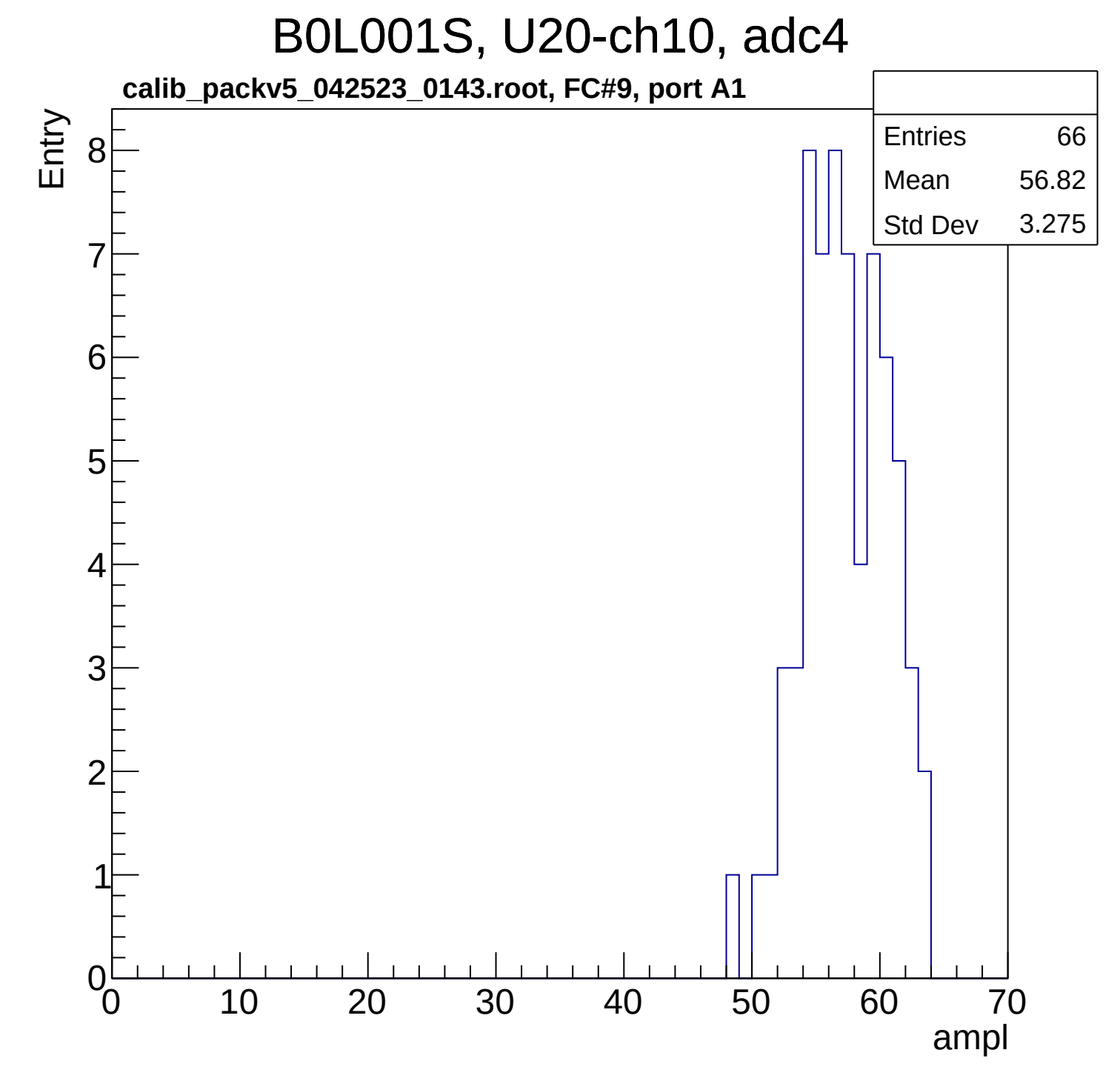
B0L001S, U20-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.82
Std Dev 3.275
Entry
ampl
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4
5
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7
8
0
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20
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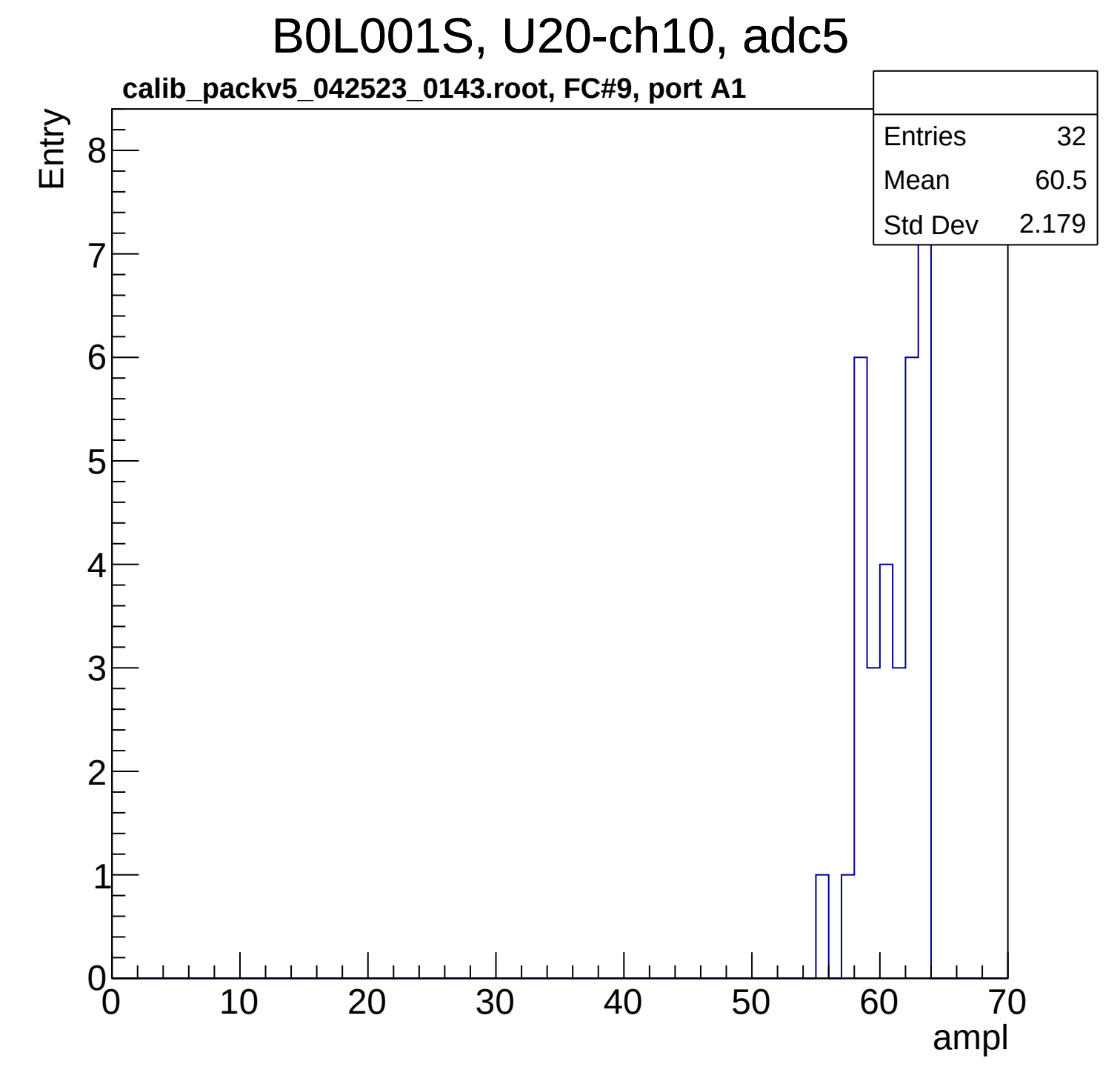
40
50
60
70


B0L001S, U20-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.5
Std Dev 2.179
Entry
ampl
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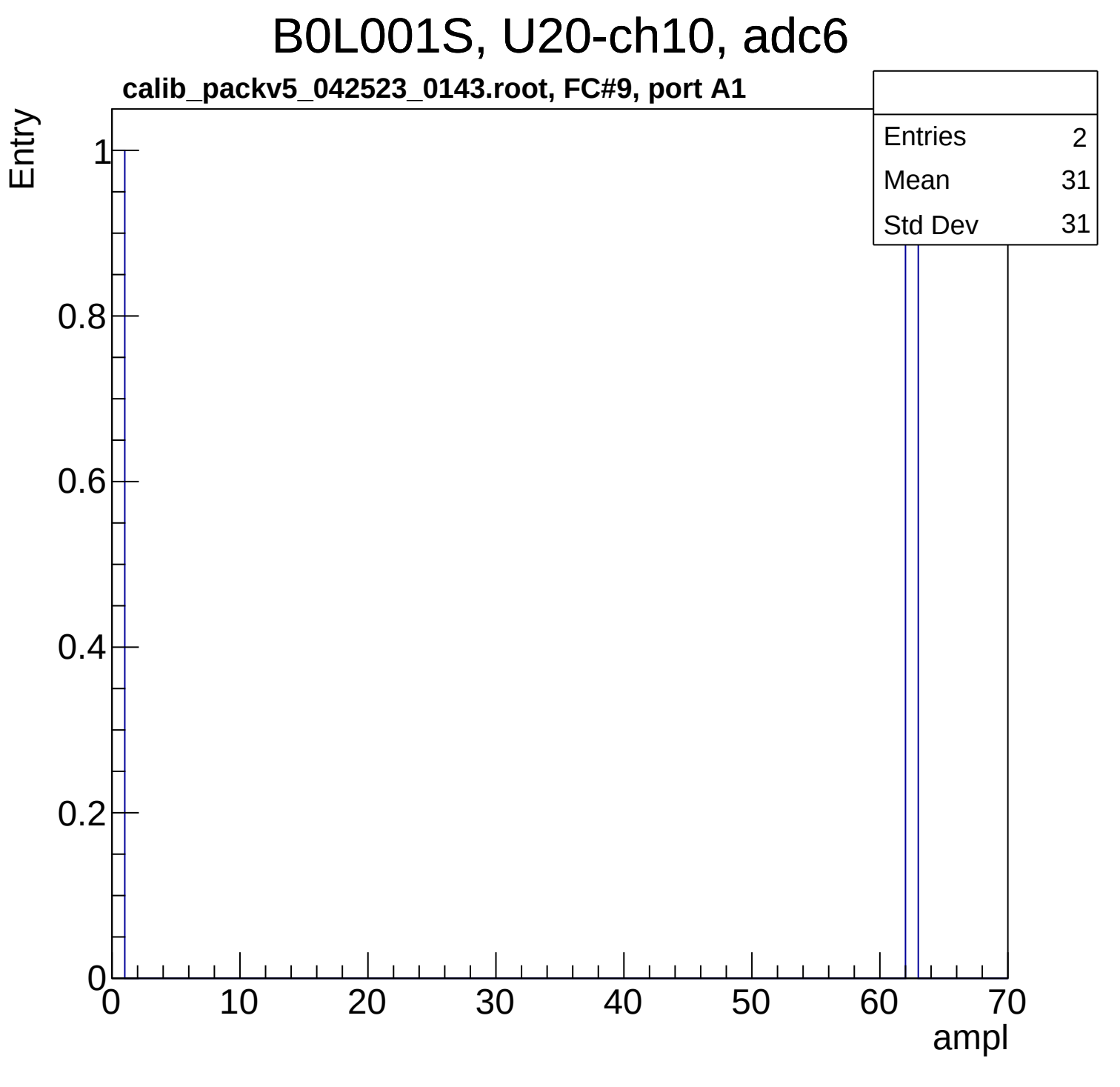
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
0.2
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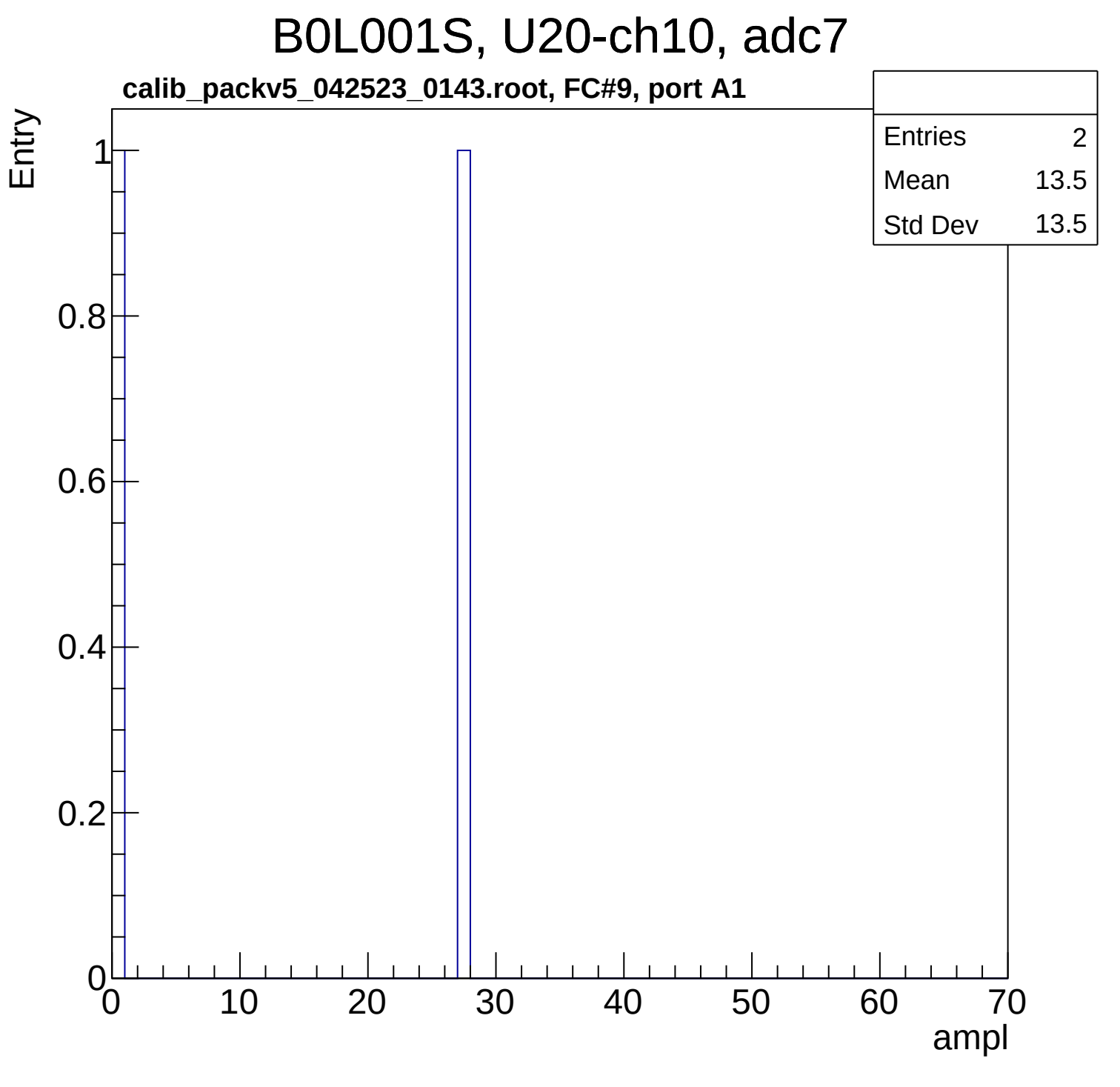
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 13.5
Std Dev 13.5
Entry
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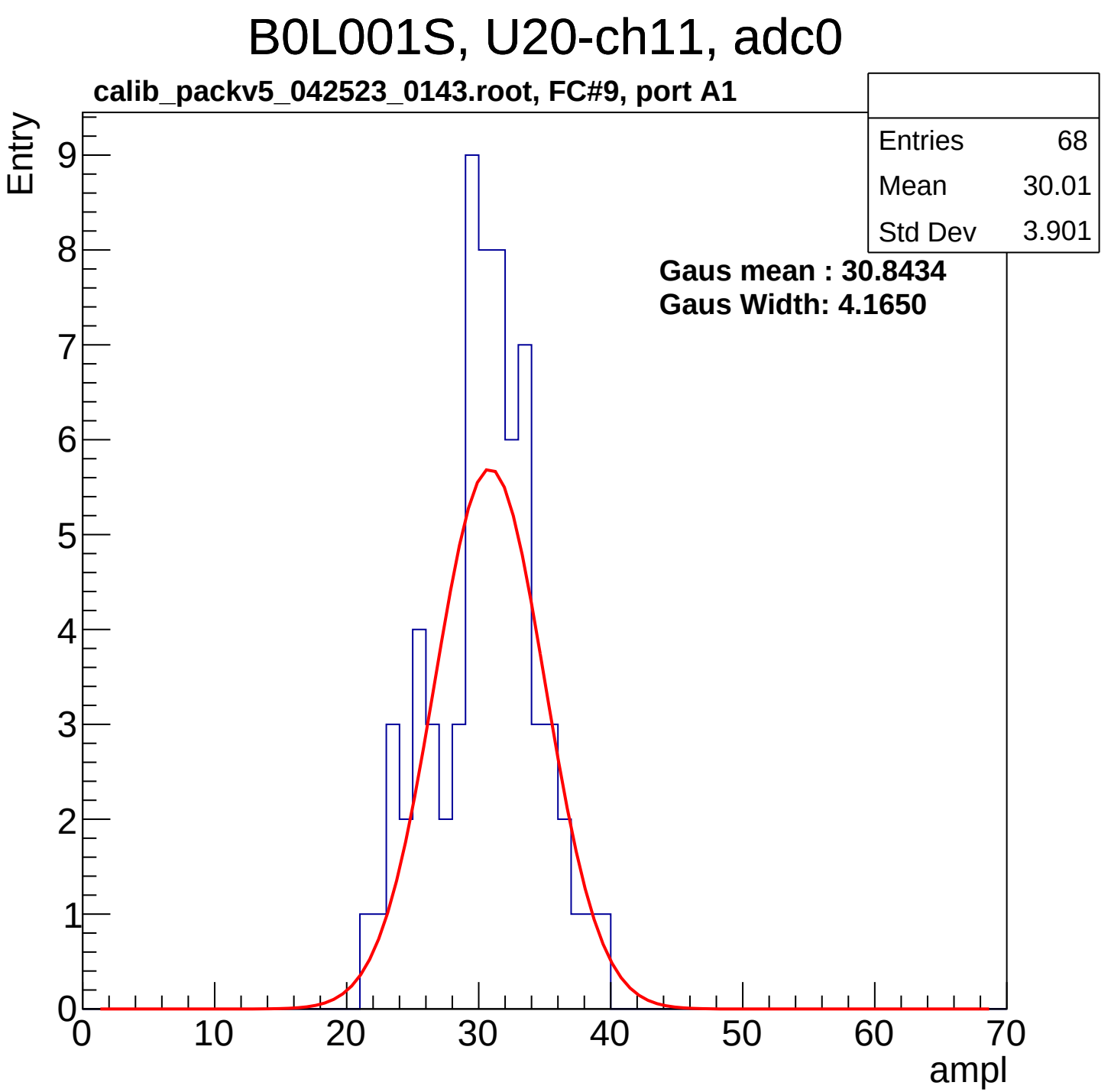
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.01
Std Dev 3.901
Gaus mean : 30.8434
Gaus Width: 4.1650
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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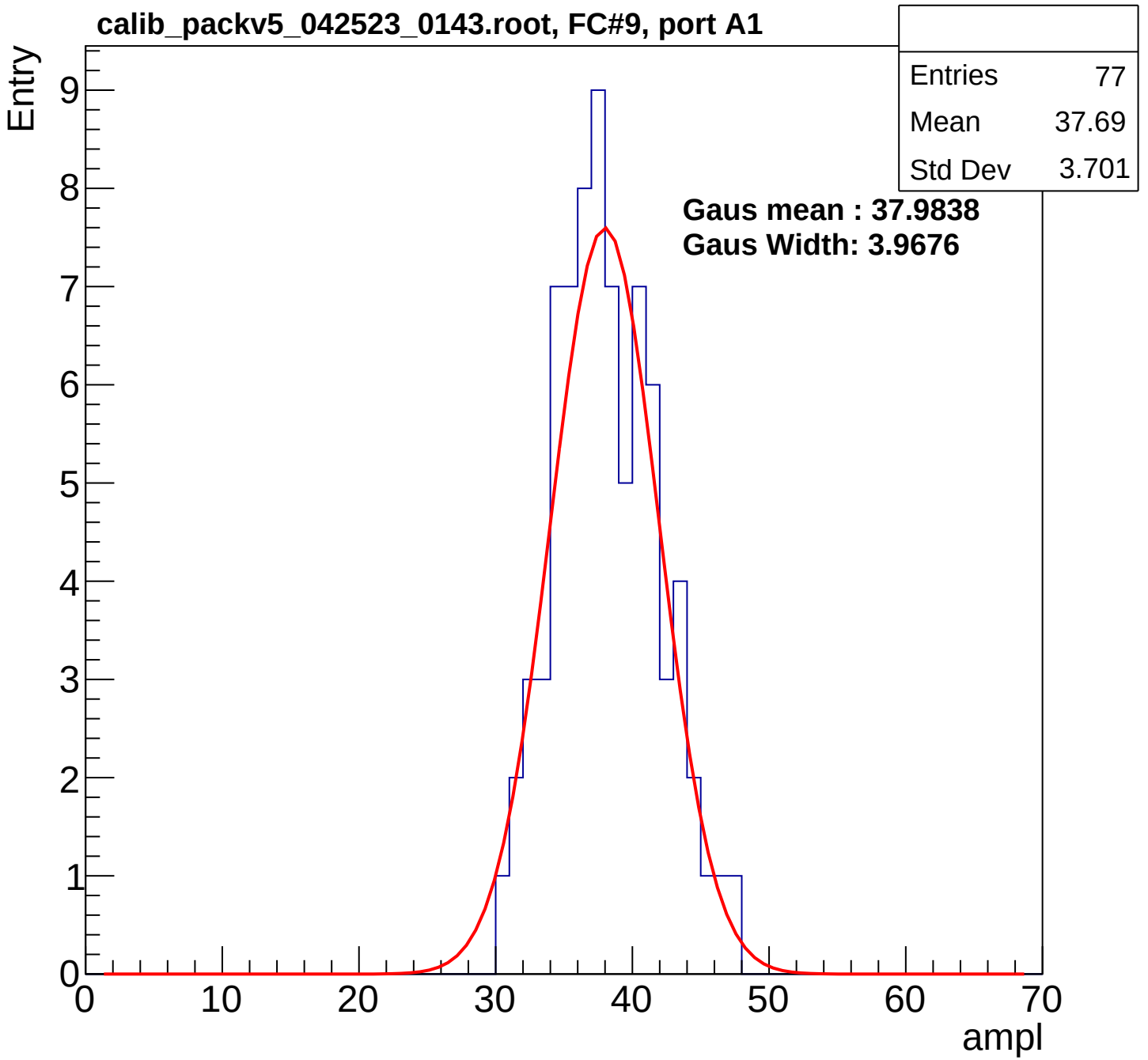
B0L001S, U20-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.69
Std Dev 3.701
Gaus mean : 37.9838
Gaus Width: 3.9676
Entry
ampl
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2
3
4
5
6
7
8
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0
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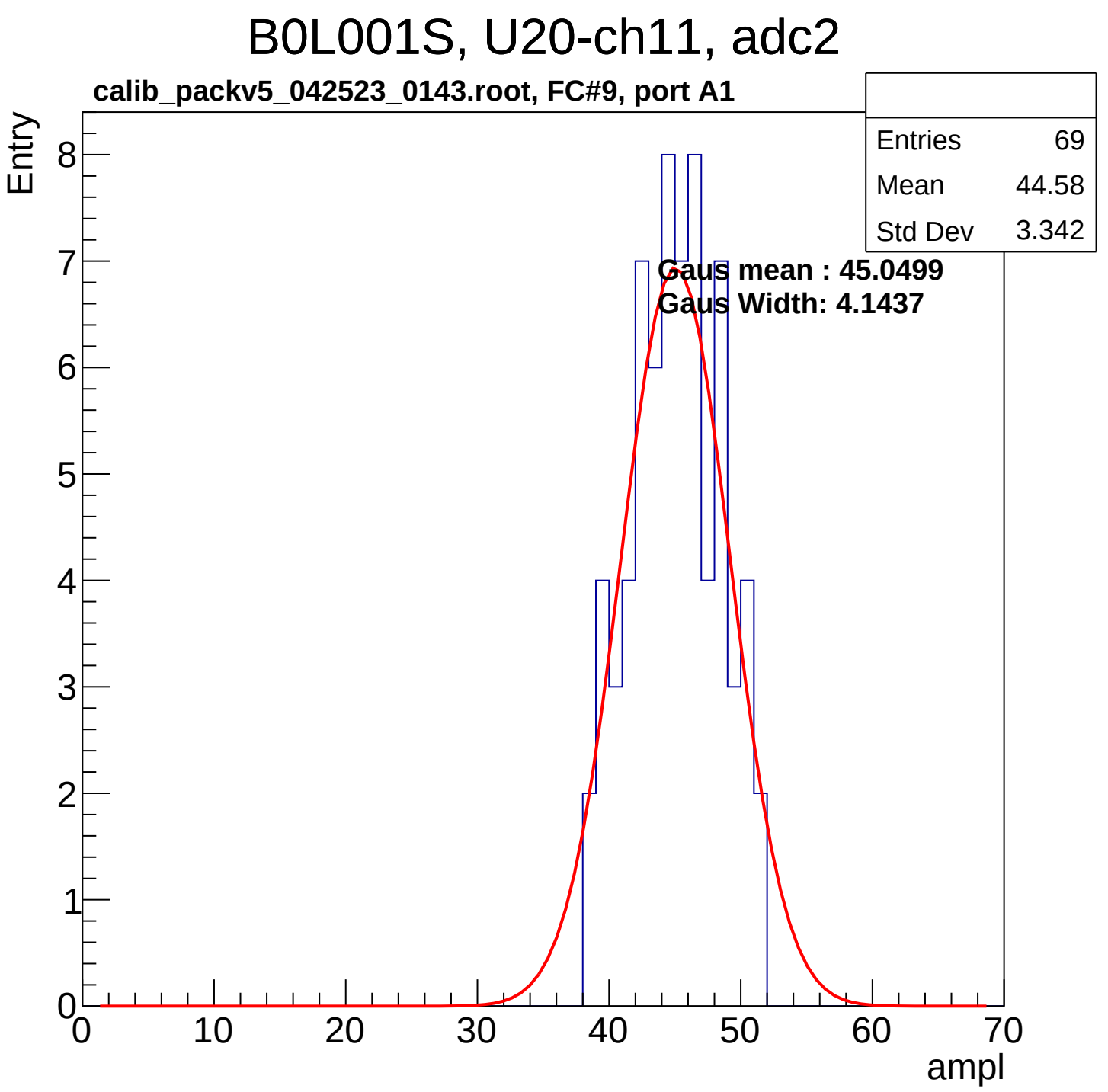
B0L001S, U20-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.58
Std Dev 3.342
Gaus mean : 45.0499
Gaus Width: 4.1437
Entry
ampl
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1
2
3
4
5
6
7
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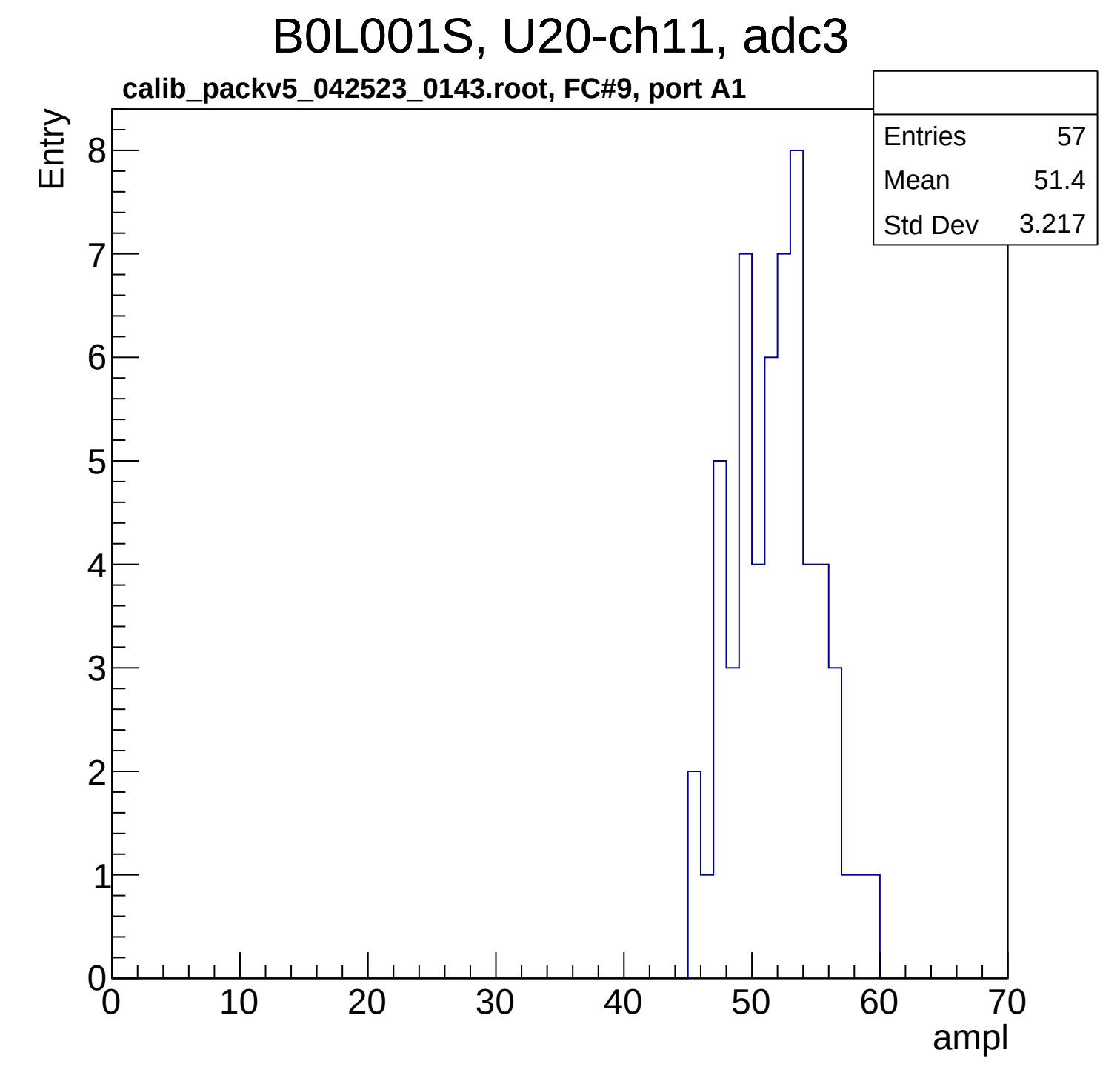
B0L001S, U20-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.4
Std Dev 3.217
Entry
ampl
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2
3
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6
7
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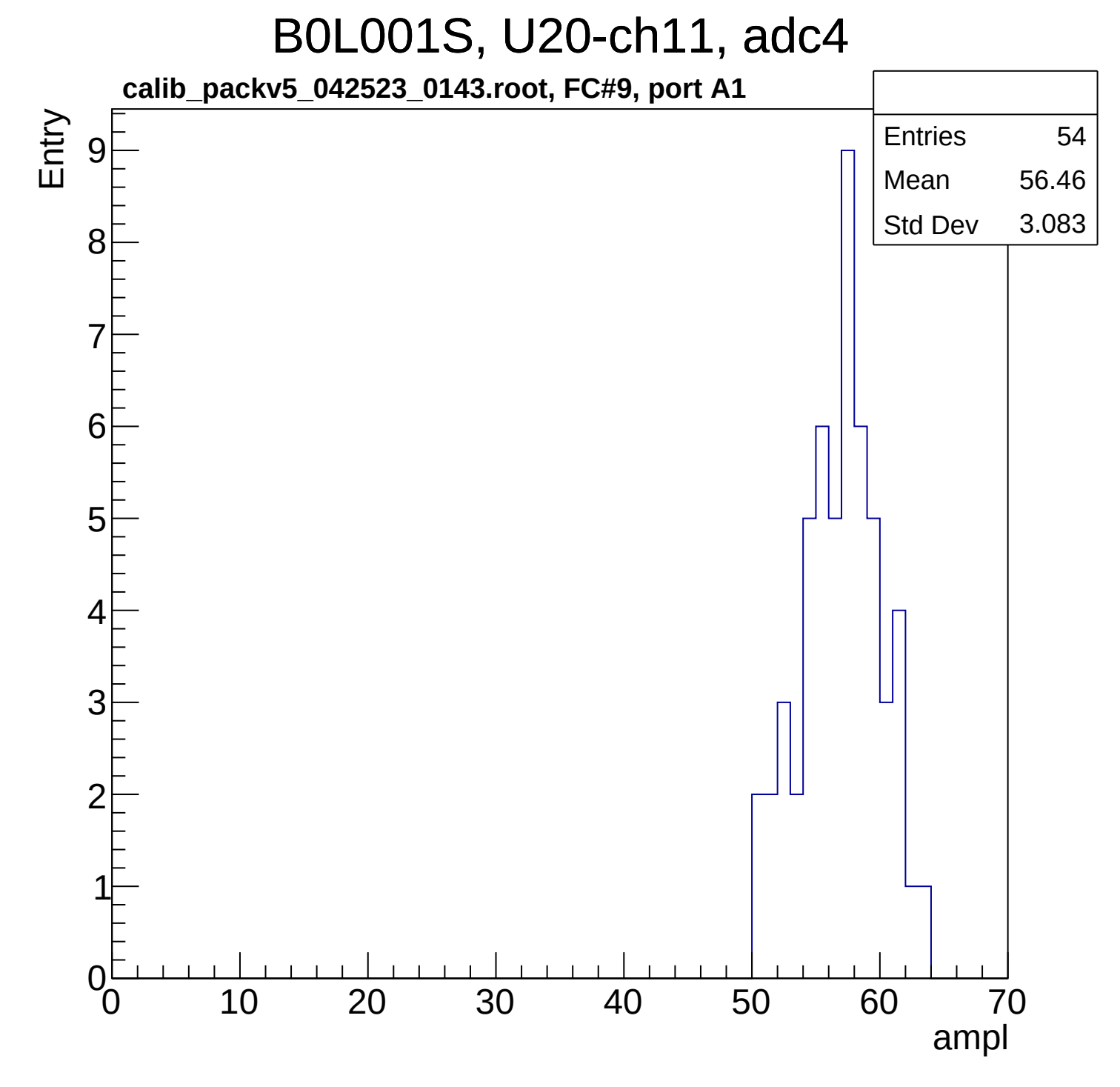
B0L001S, U20-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.46
Std Dev 3.083
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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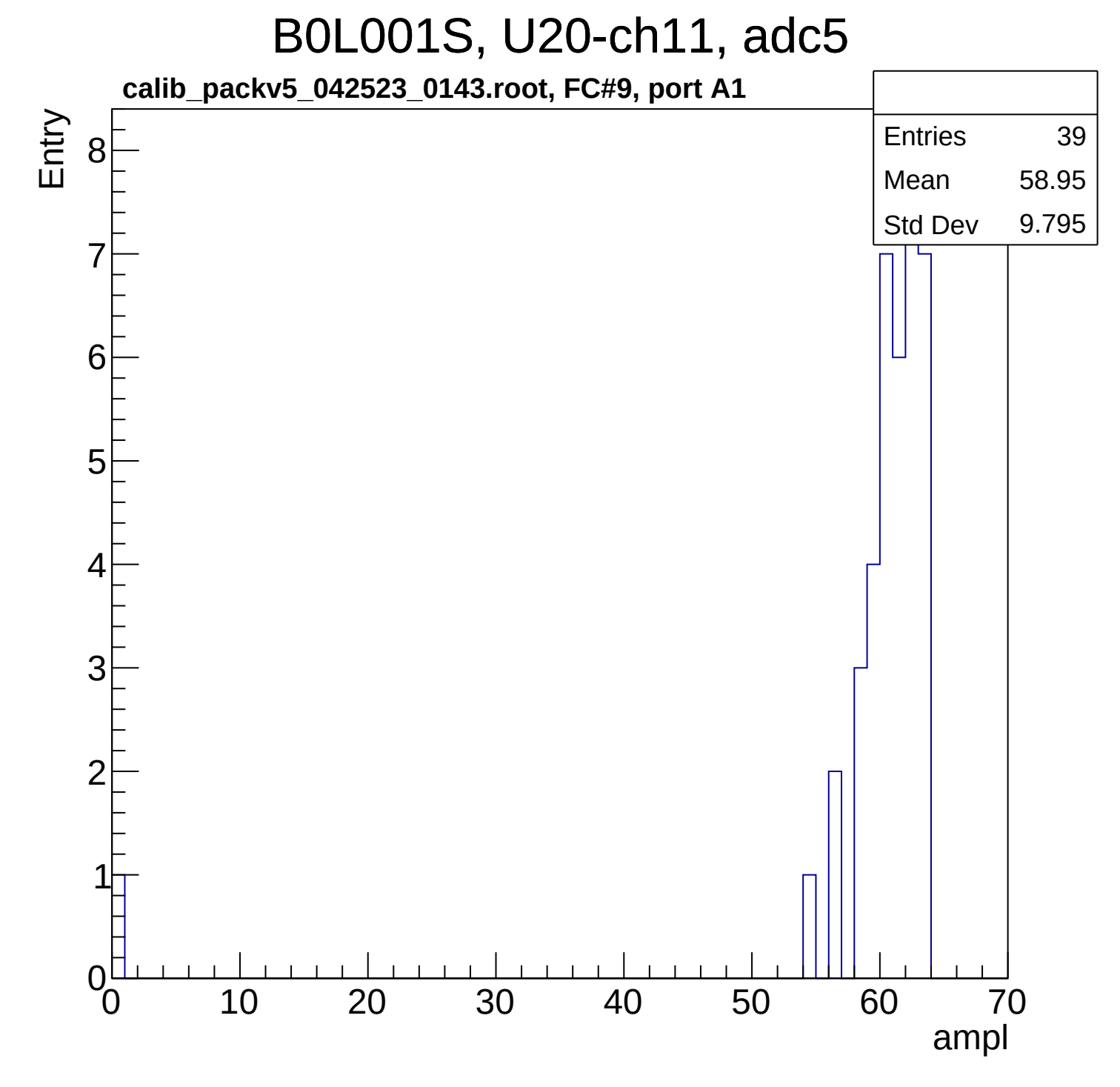
B0L001S, U20-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.95
Std Dev 9.795
Entry
ampl
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2
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4
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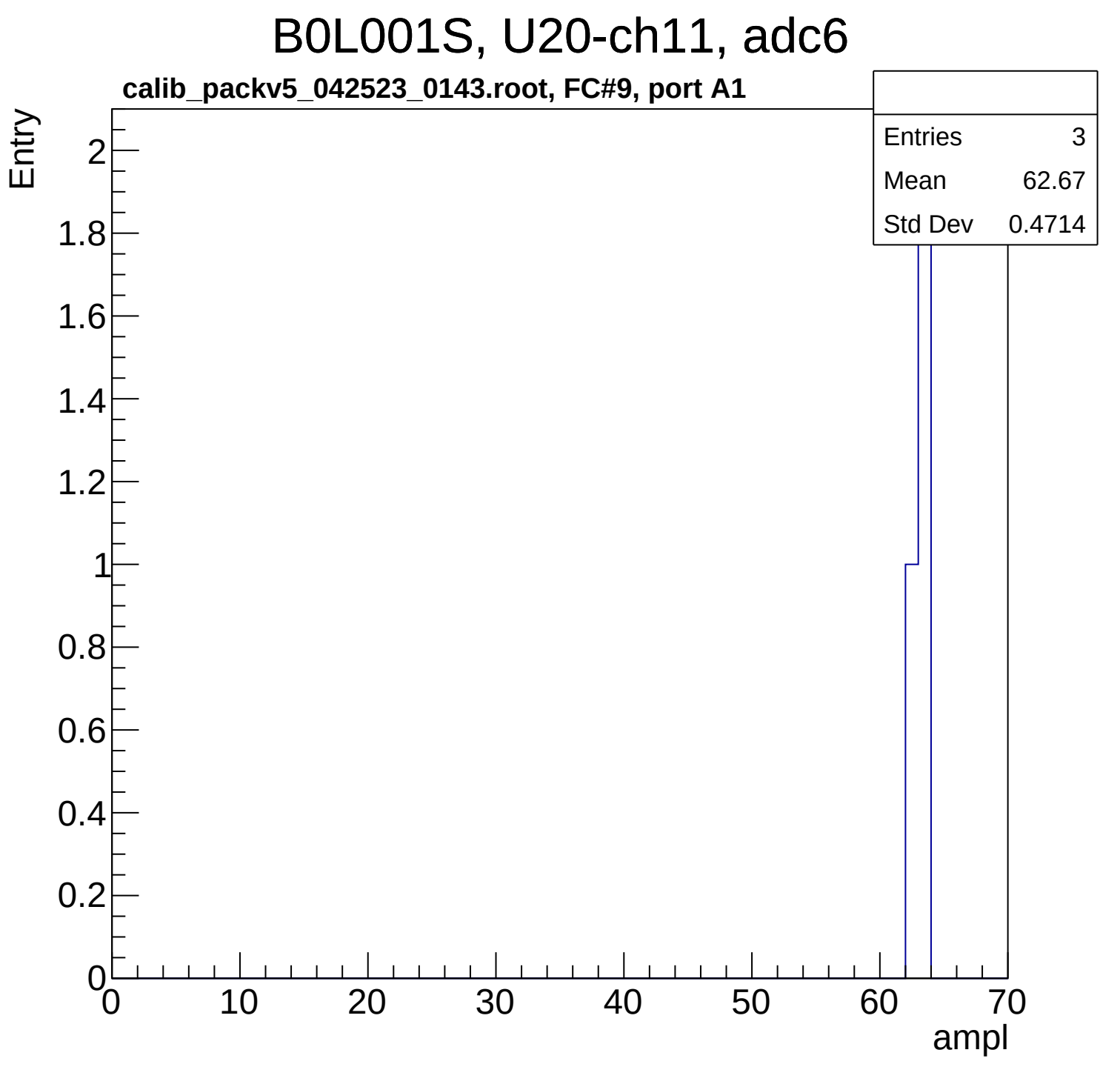
B0L001S, U20-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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0
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40
50
60
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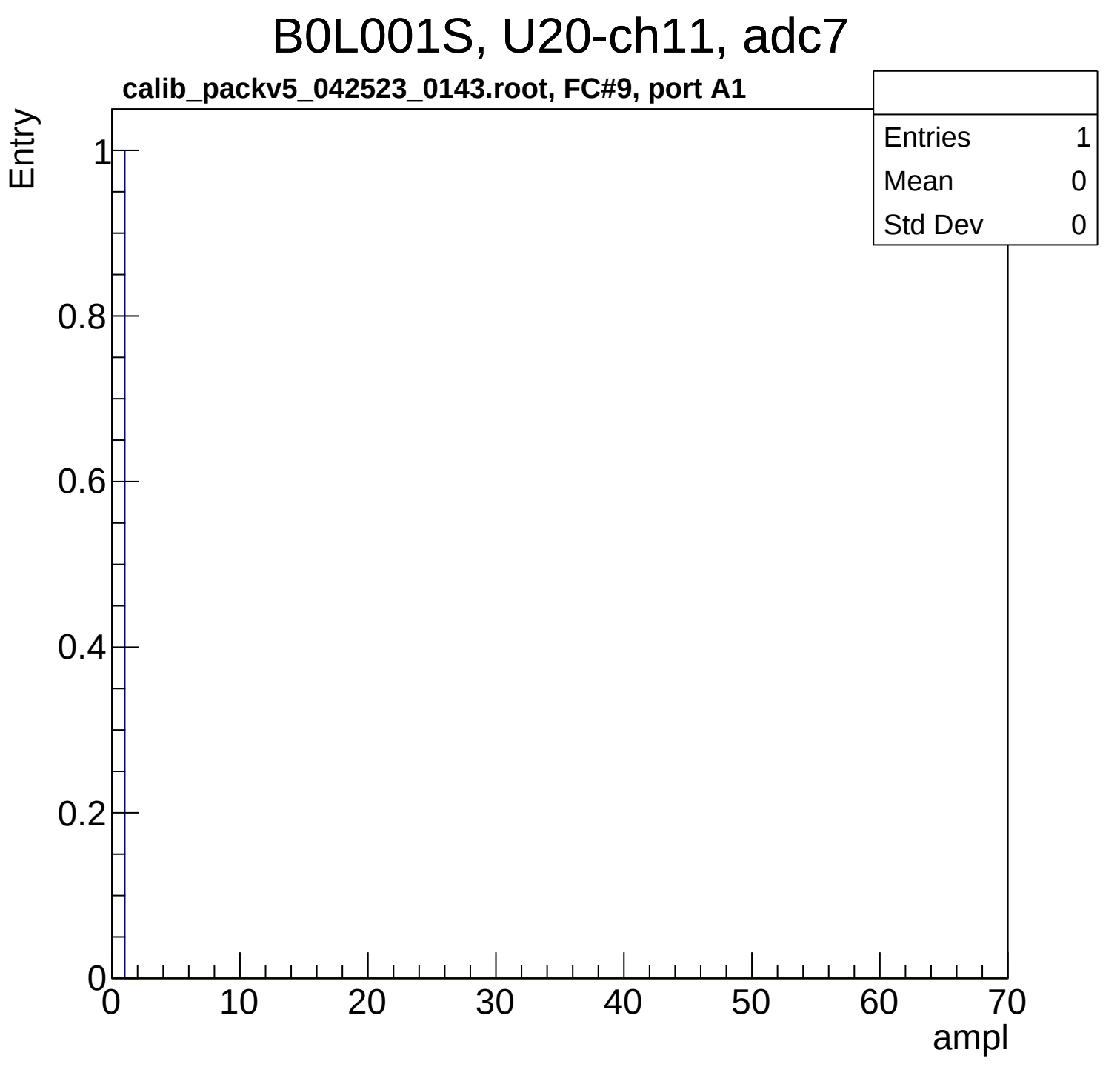
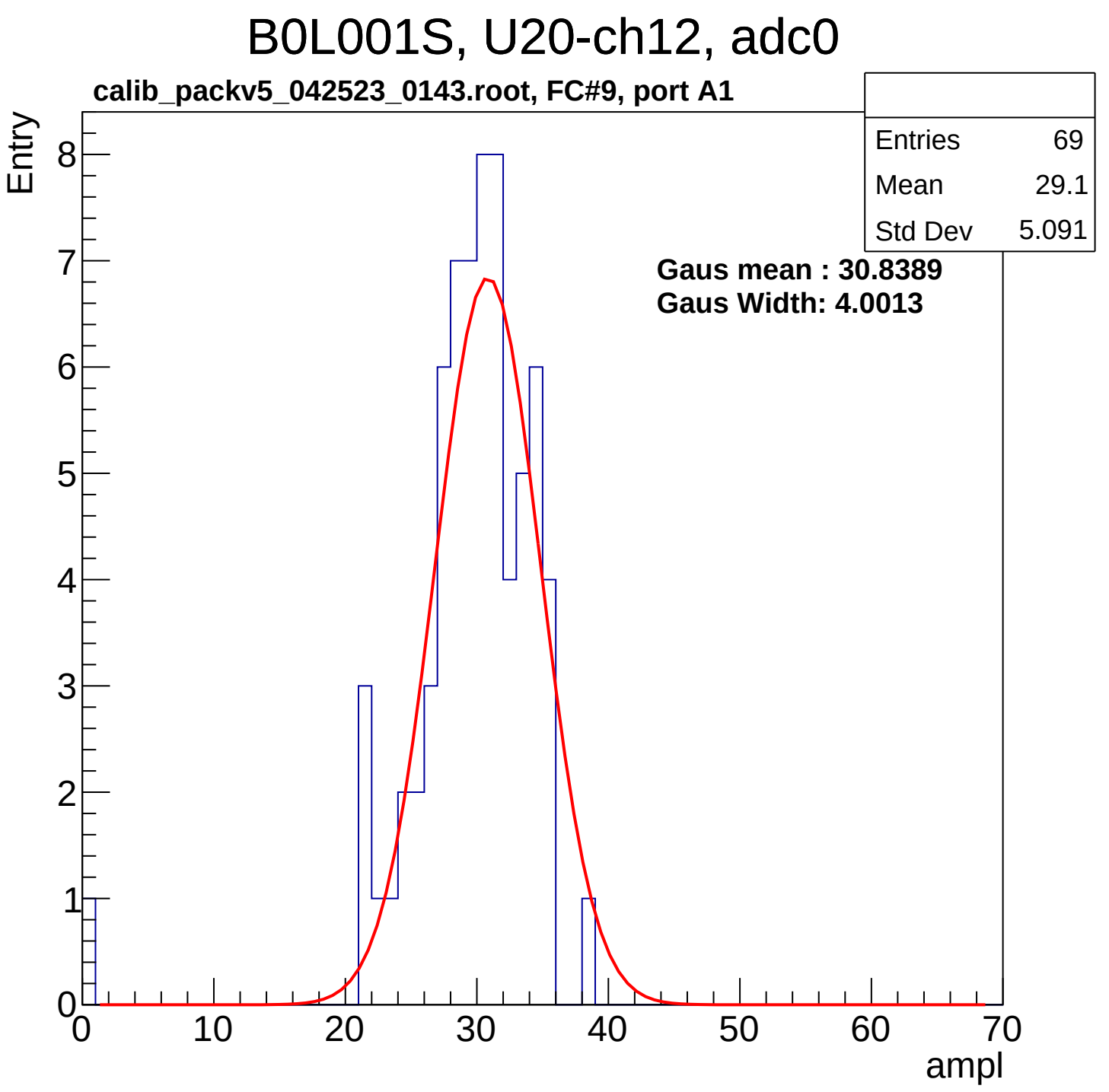

B0L001S, U20-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.1
Std Dev 5.091
Gaus mean : 30.8389
Gaus Width: 4.0013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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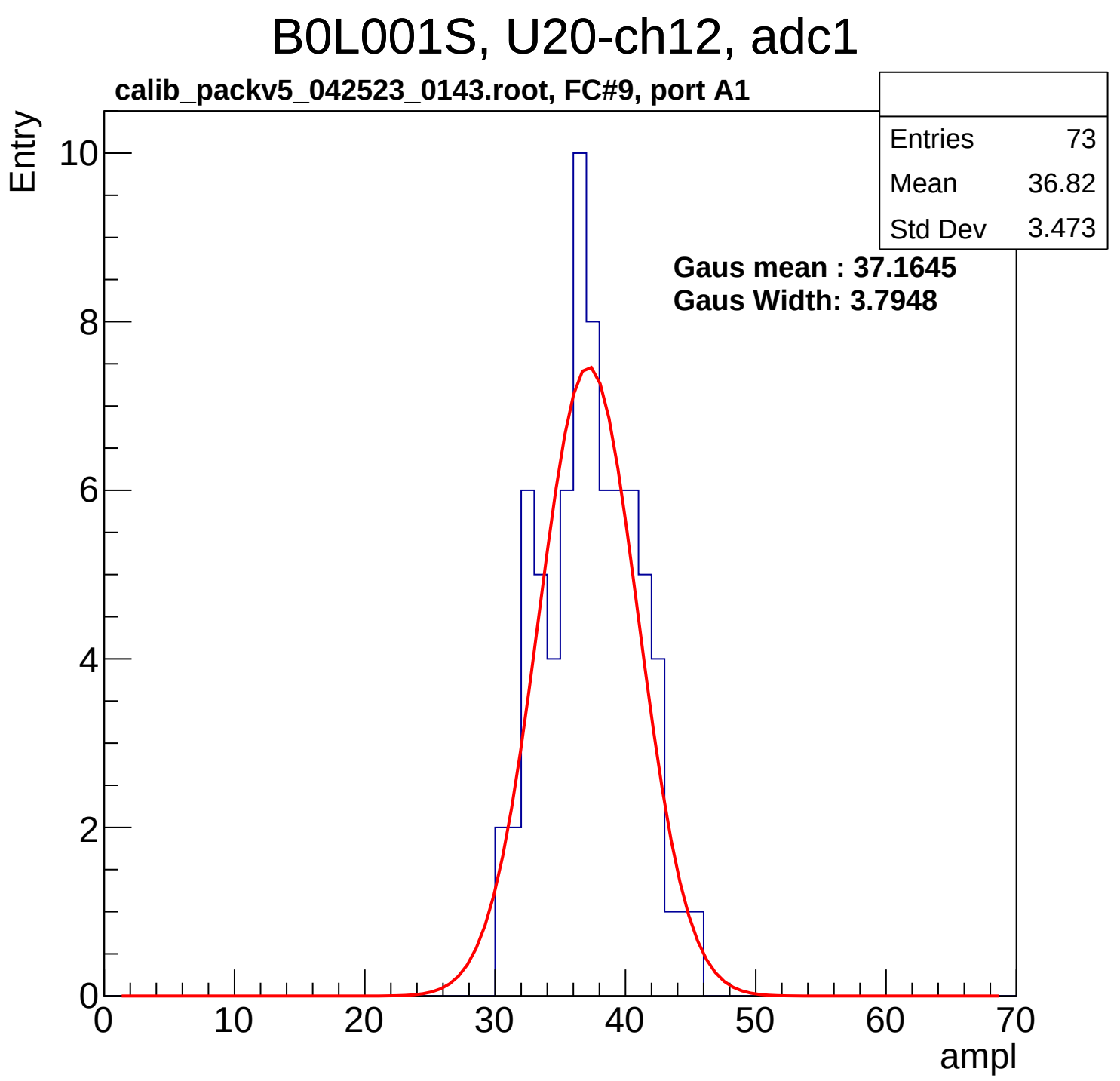
B0L001S, U20-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.82
Std Dev 3.473
Gaus mean : 37.1645
Gaus Width: 3.7948
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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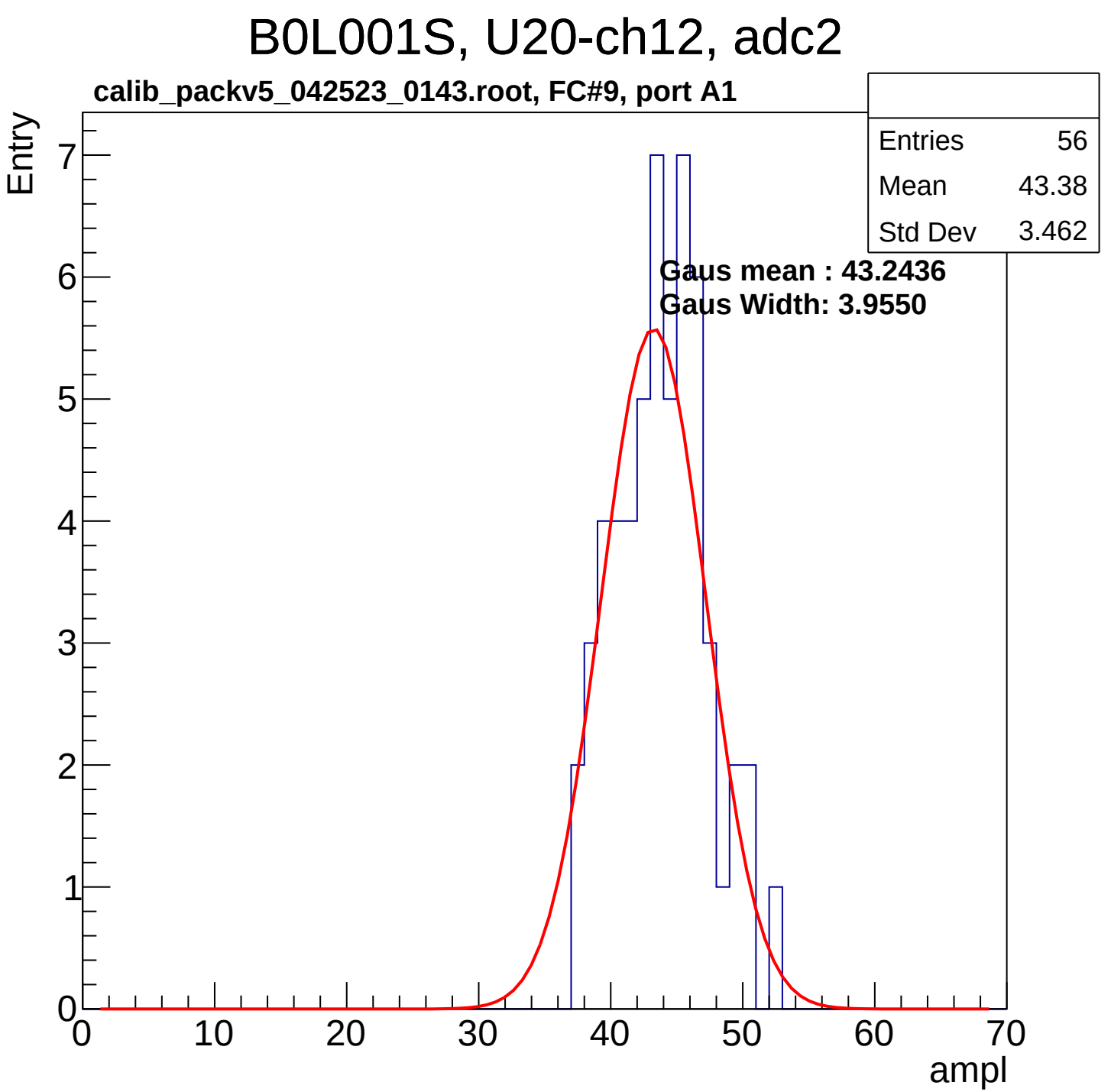
B0L001S, U20-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.38
Std Dev 3.462
Gaus mean : 43.2436
Gaus Width: 3.9550
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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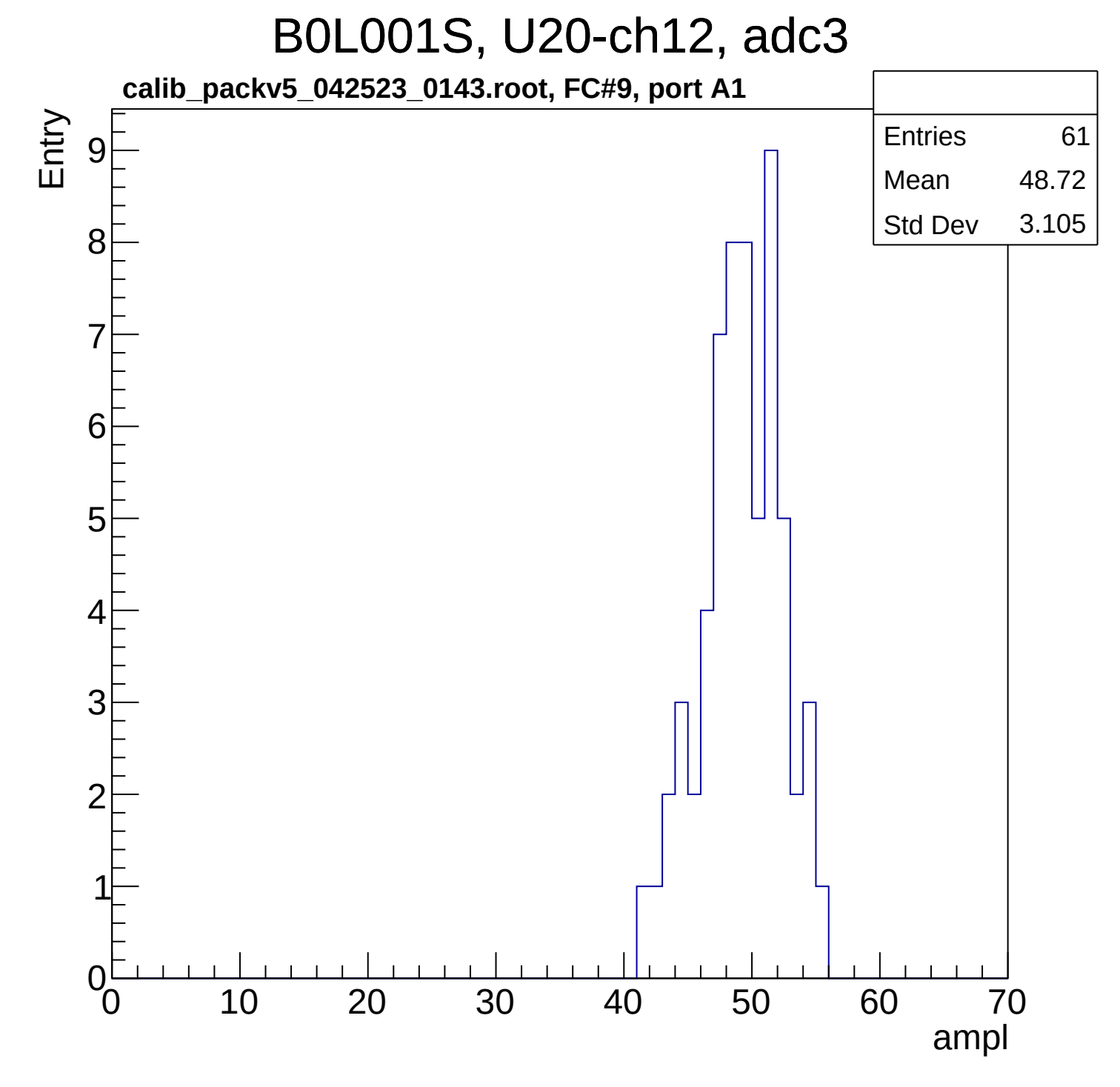
B0L001S, U20-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 48.72
Std Dev 3.105
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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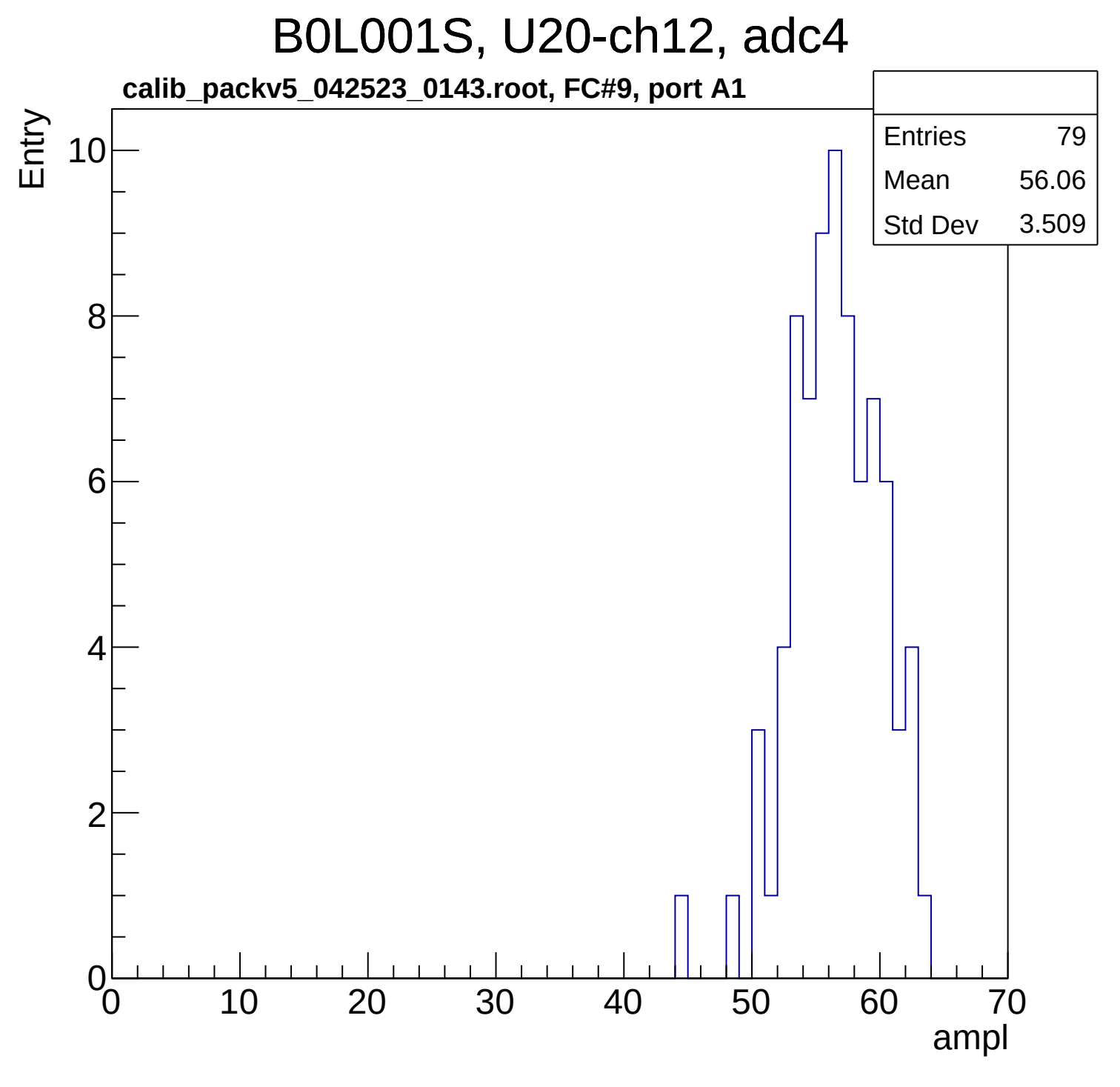
B0L001S, U20-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 56.06
Std Dev 3.509
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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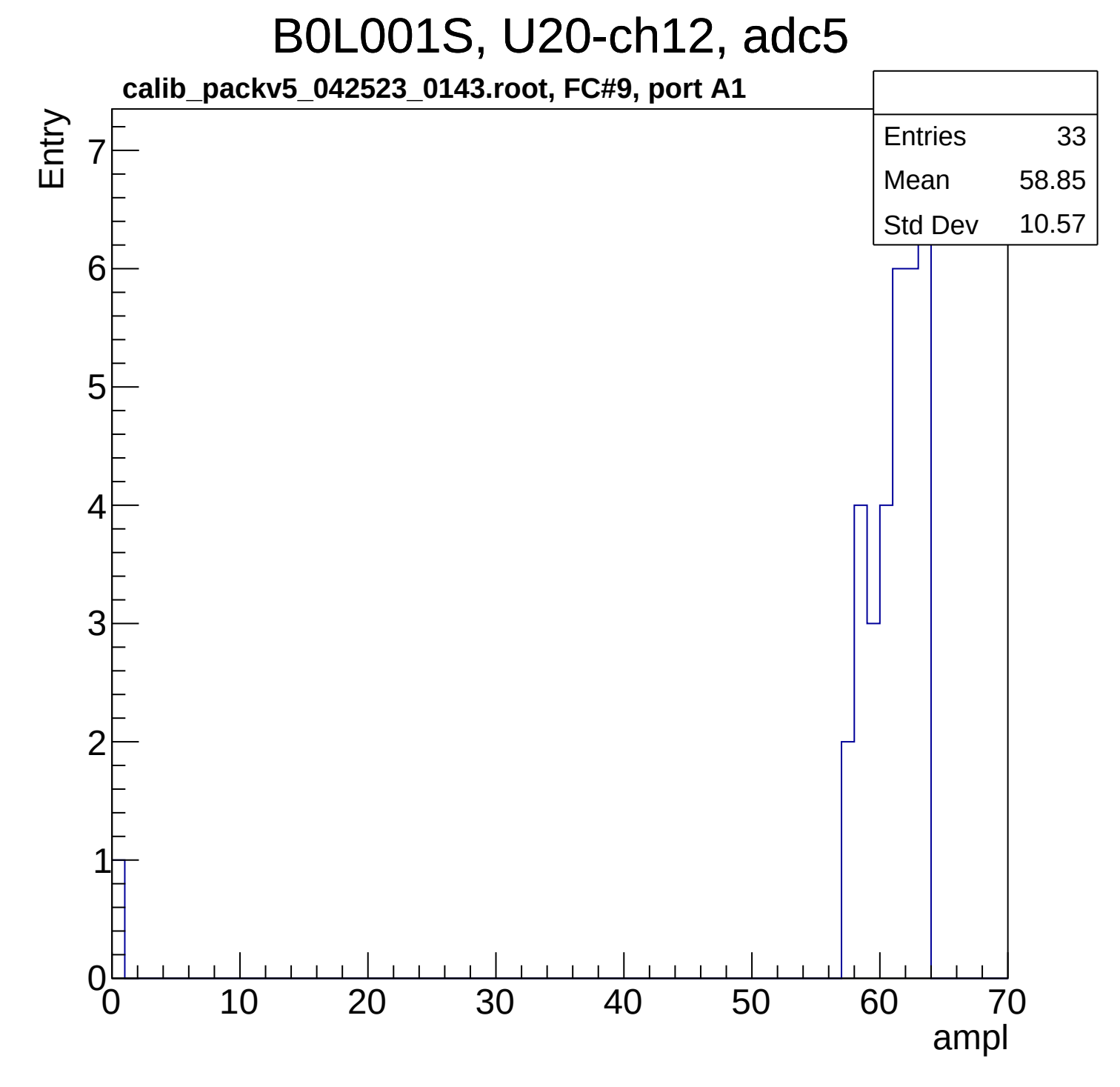
B0L001S, U20-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.85
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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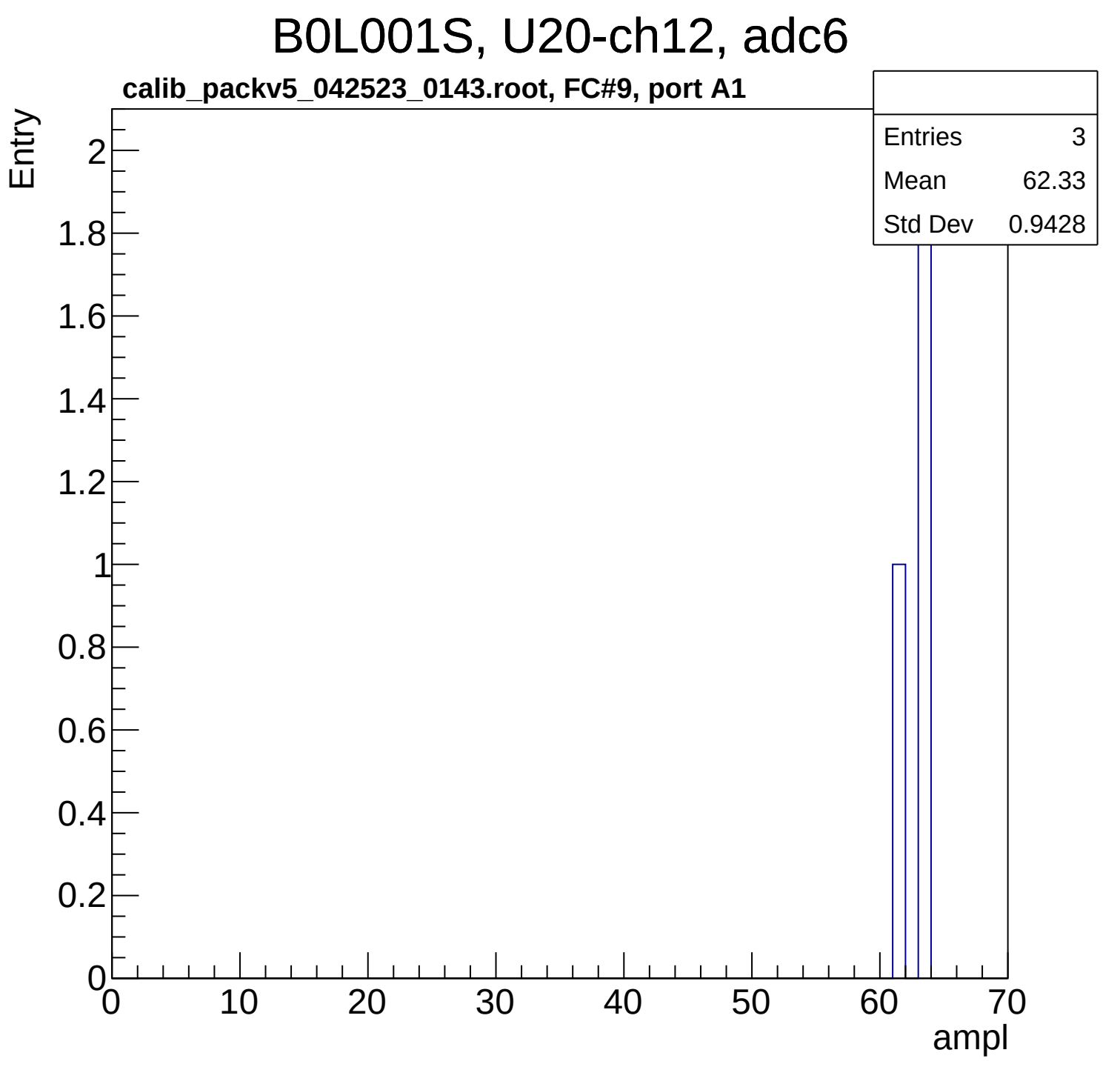
B0L001S, U20-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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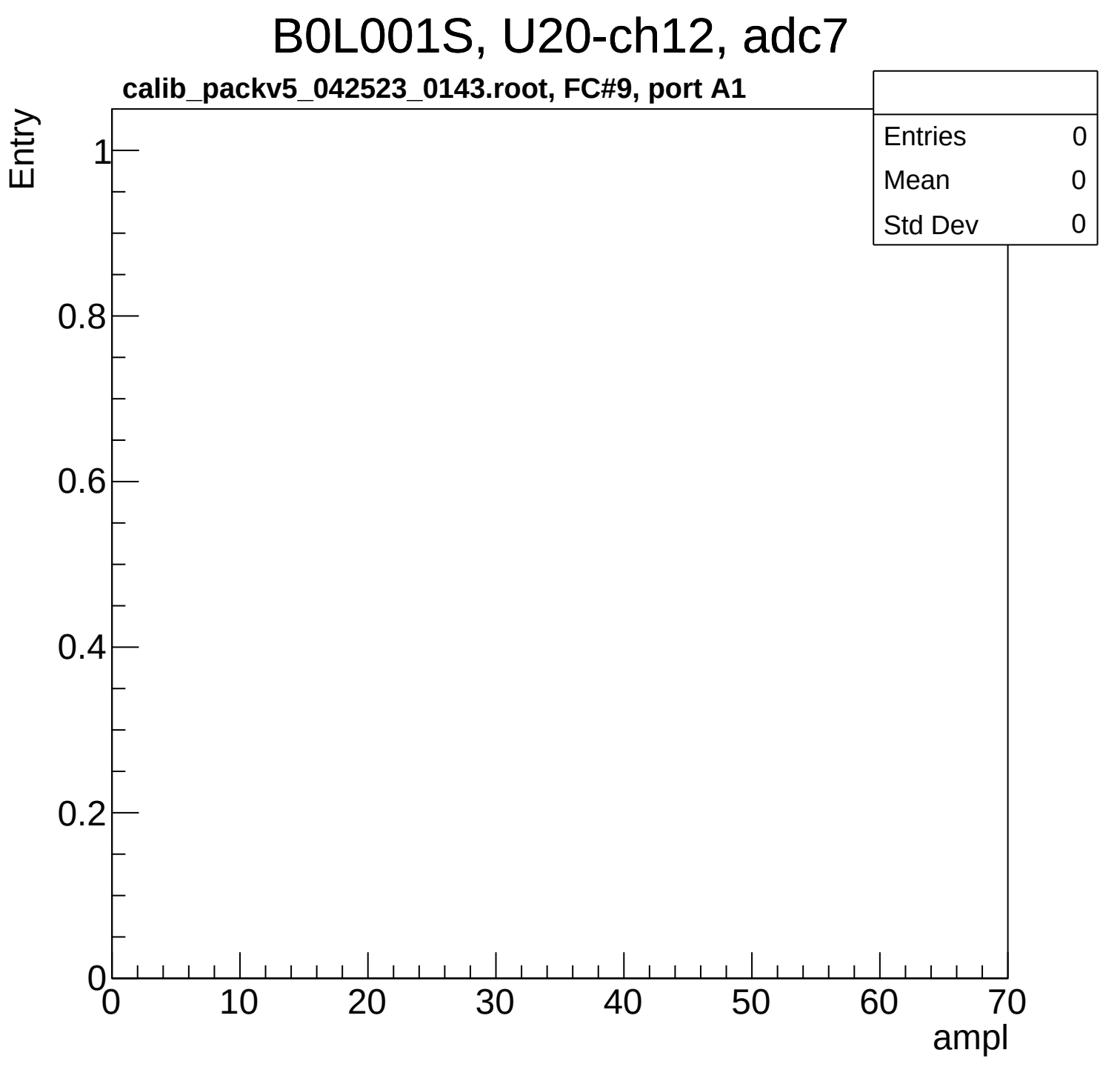
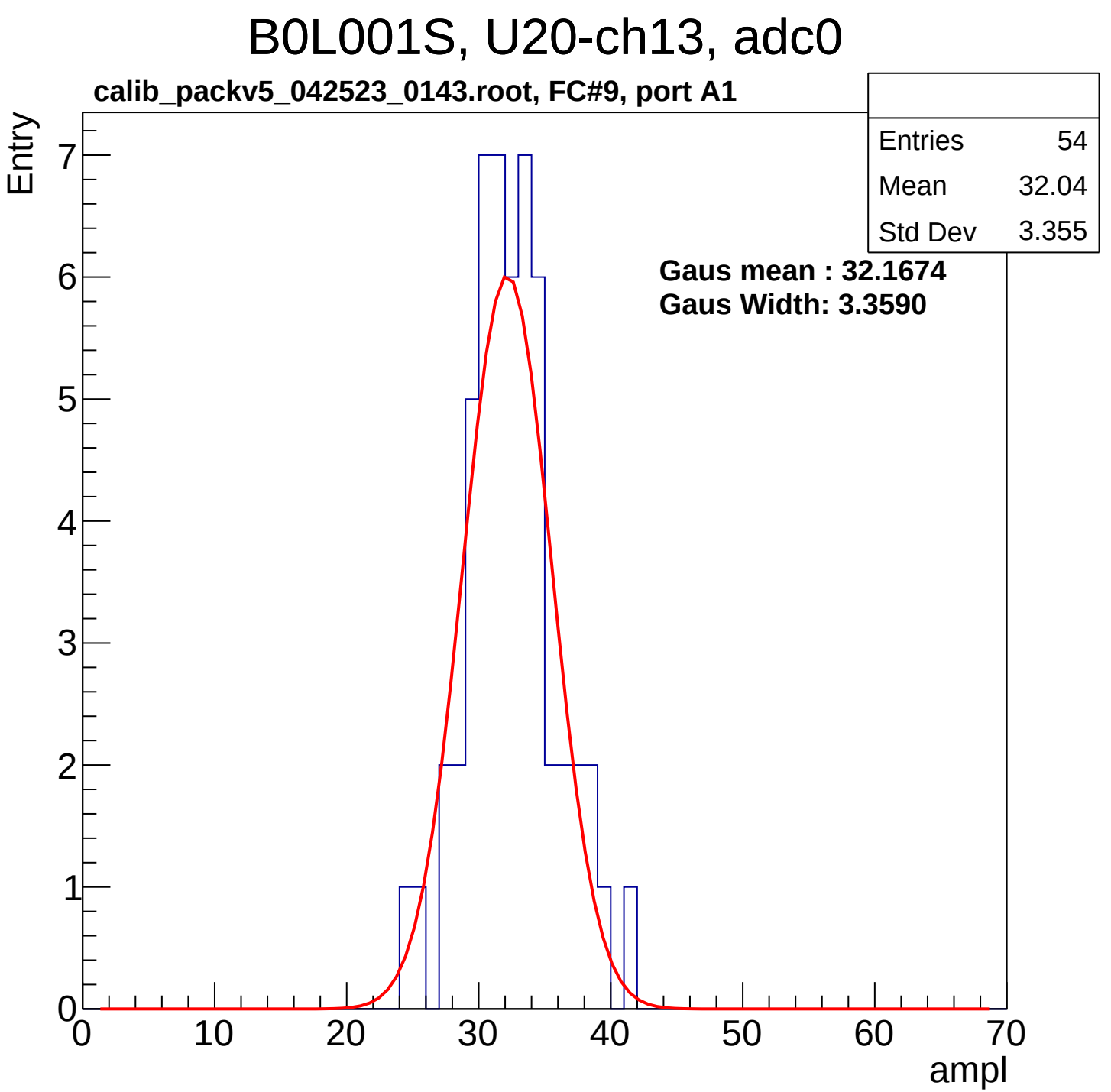

B0L001S, U20-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 32.04
Std Dev 3.355
Gaus mean : 32.1674
Gaus Width: 3.3590
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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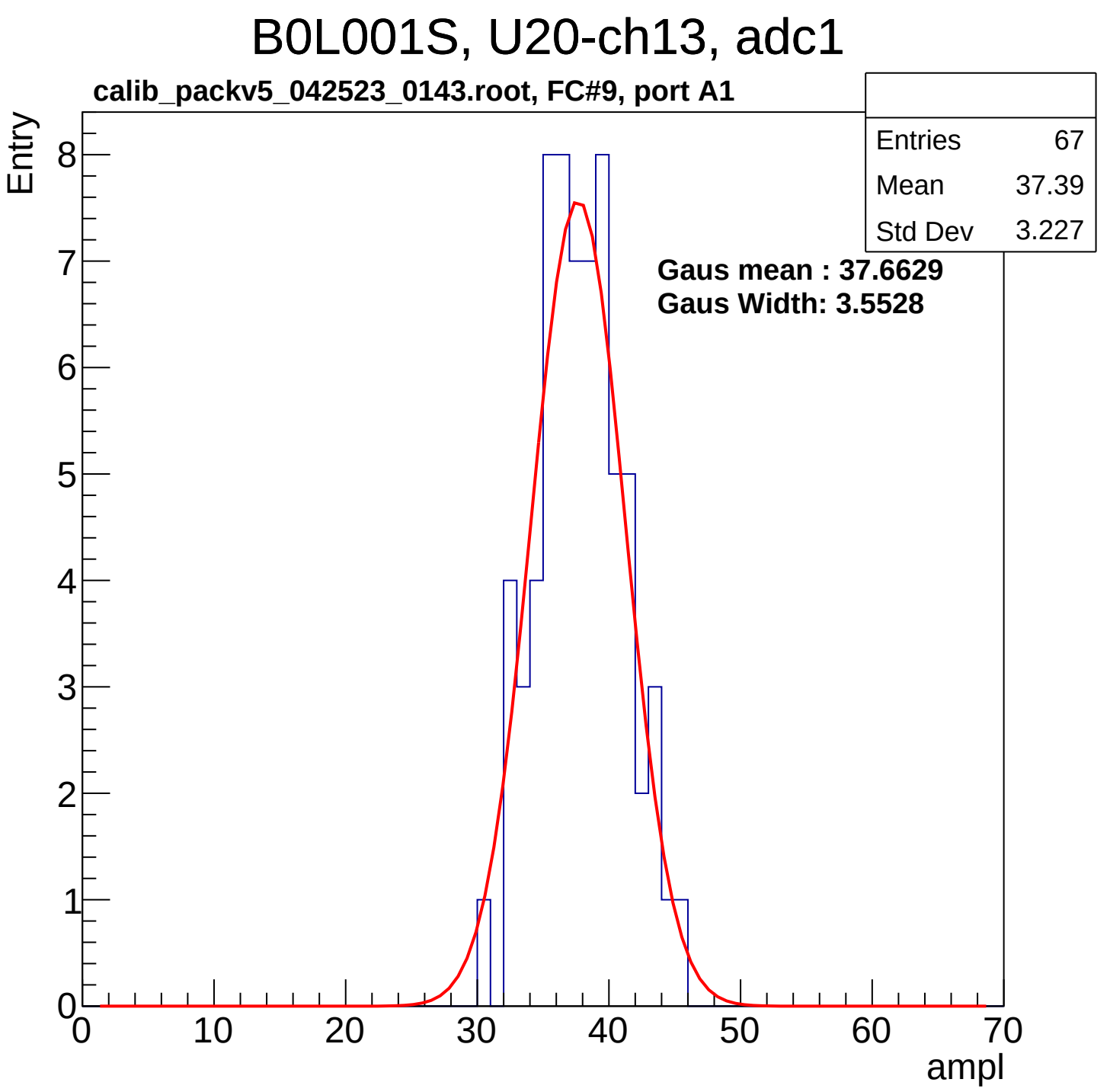
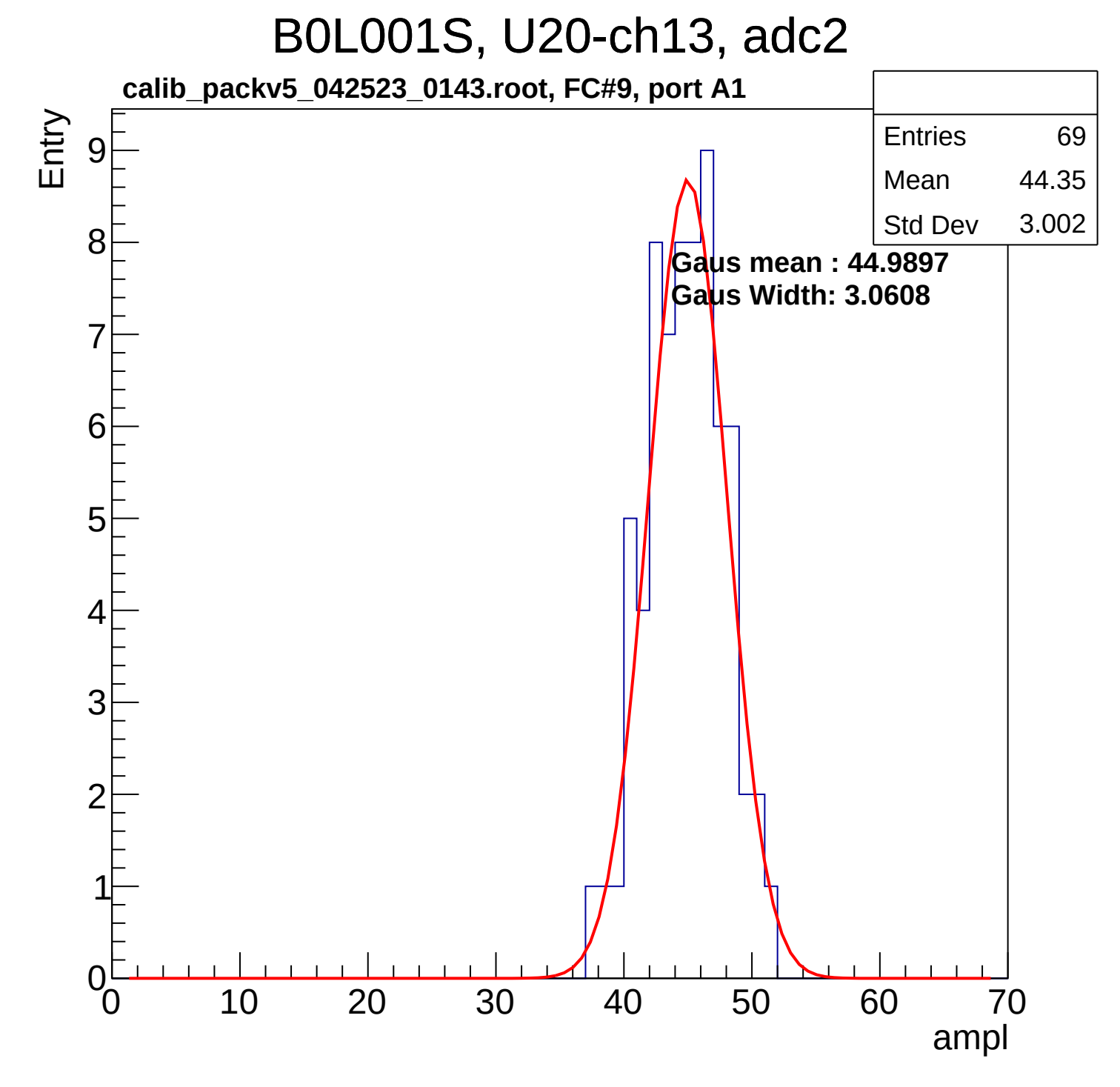

B0L001S, U20-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.39
Std Dev 3.227
Gaus mean : 37.6629
Gaus Width: 3.5528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.35
Std Dev 3.002
Gaus mean : 44.9897
Gaus Width: 3.0608
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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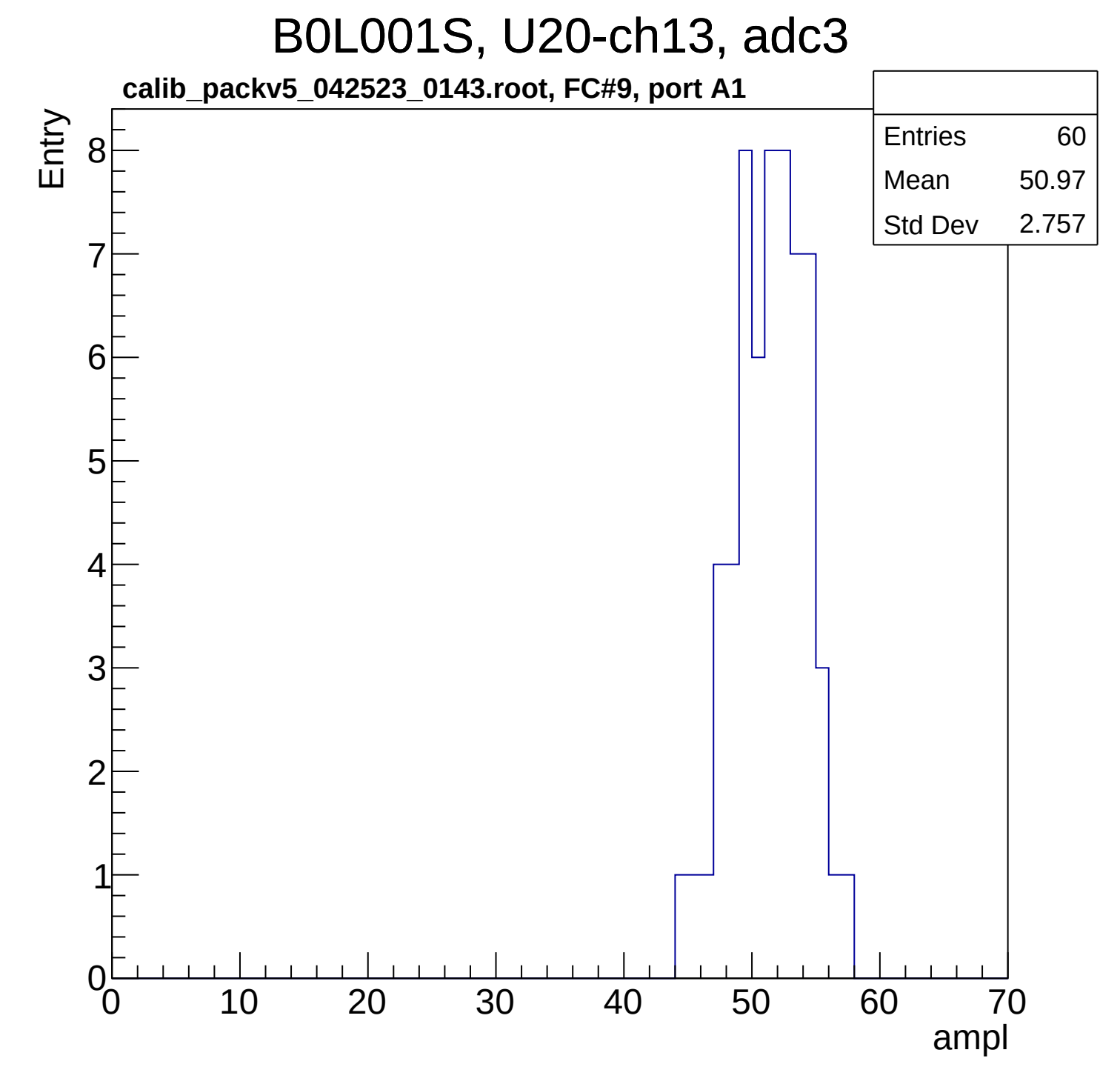
B0L001S, U20-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.97
Std Dev 2.757
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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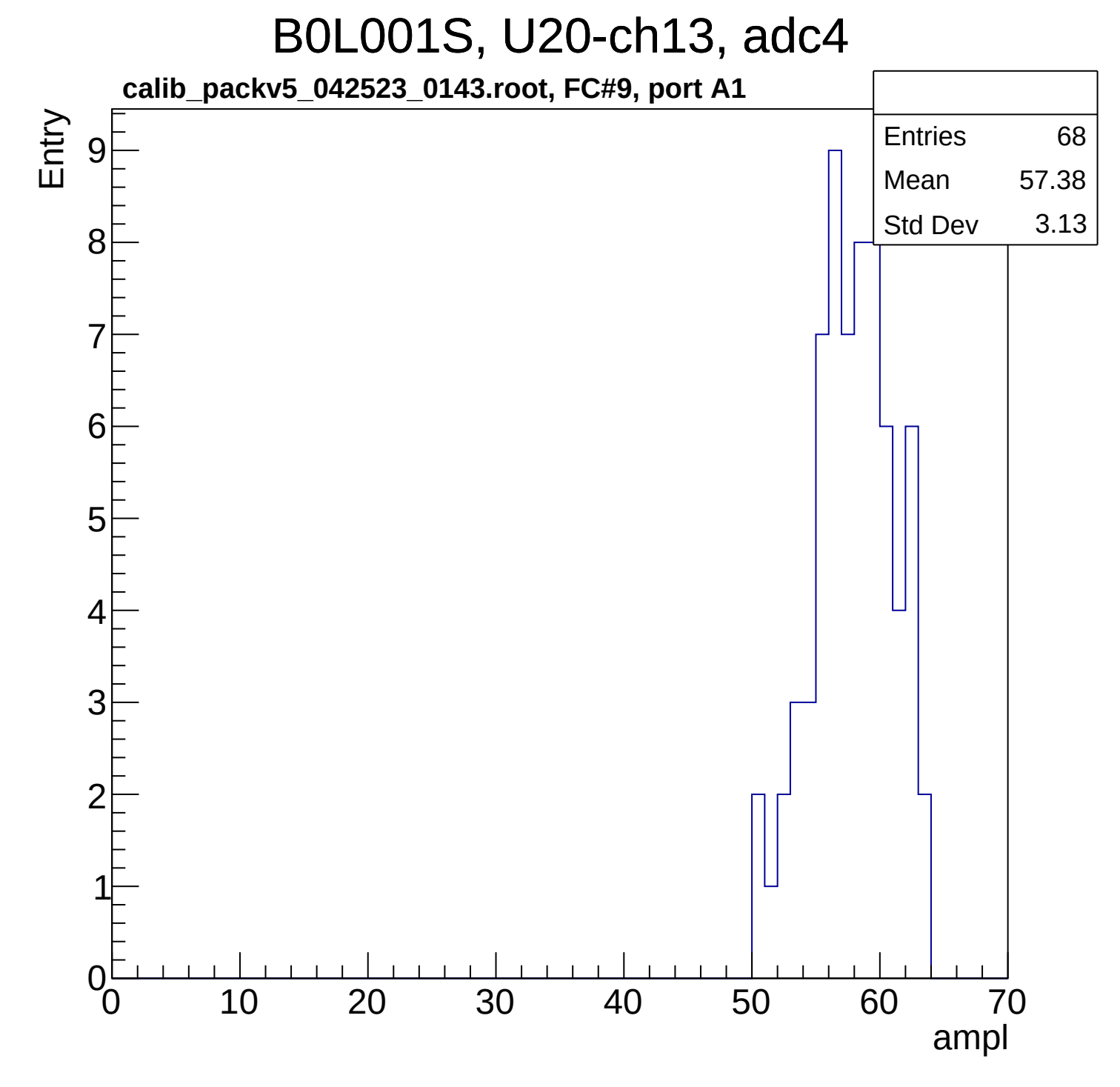
B0L001S, U20-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.38
Std Dev 3.13
Entry
ampl
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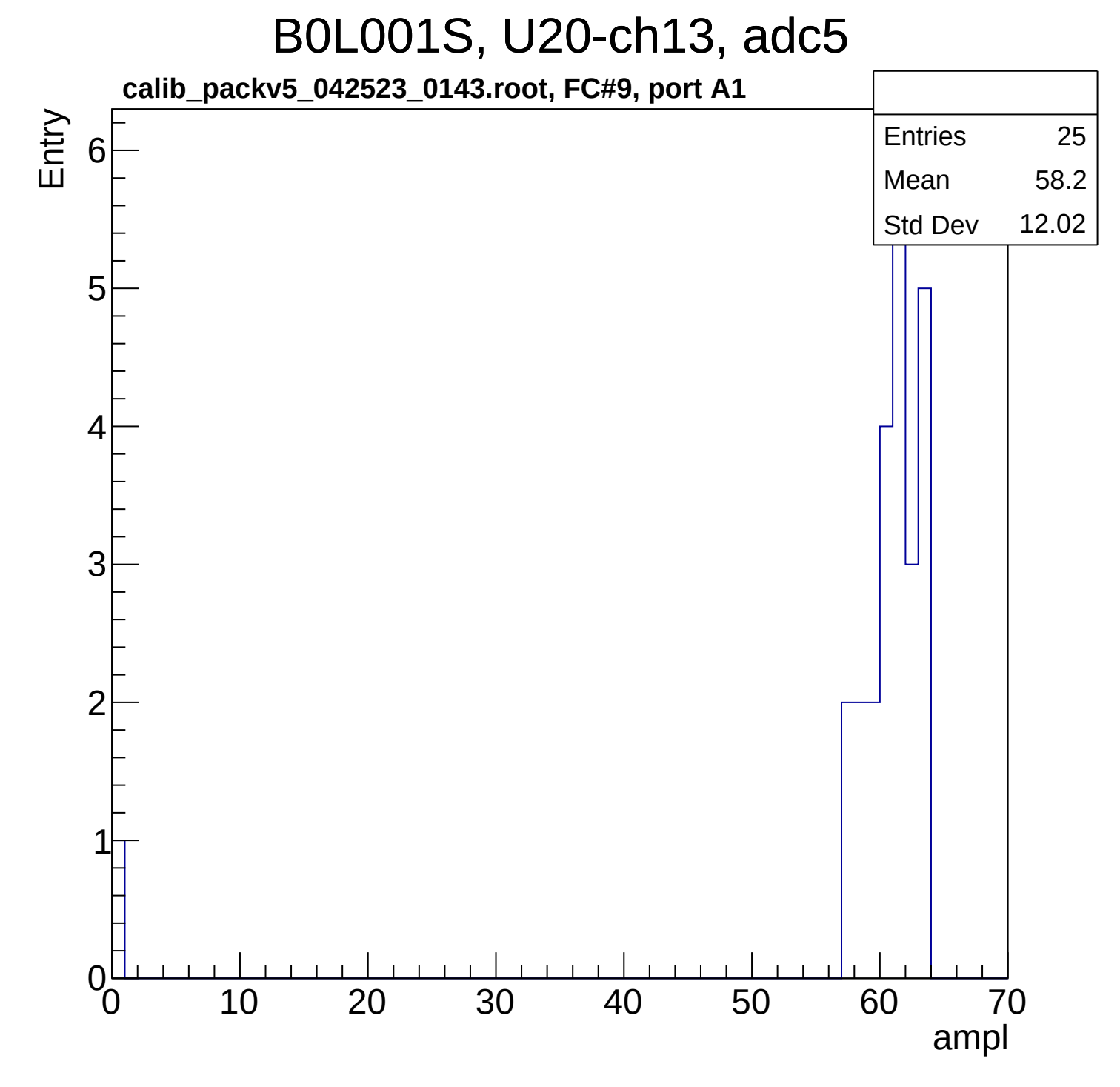
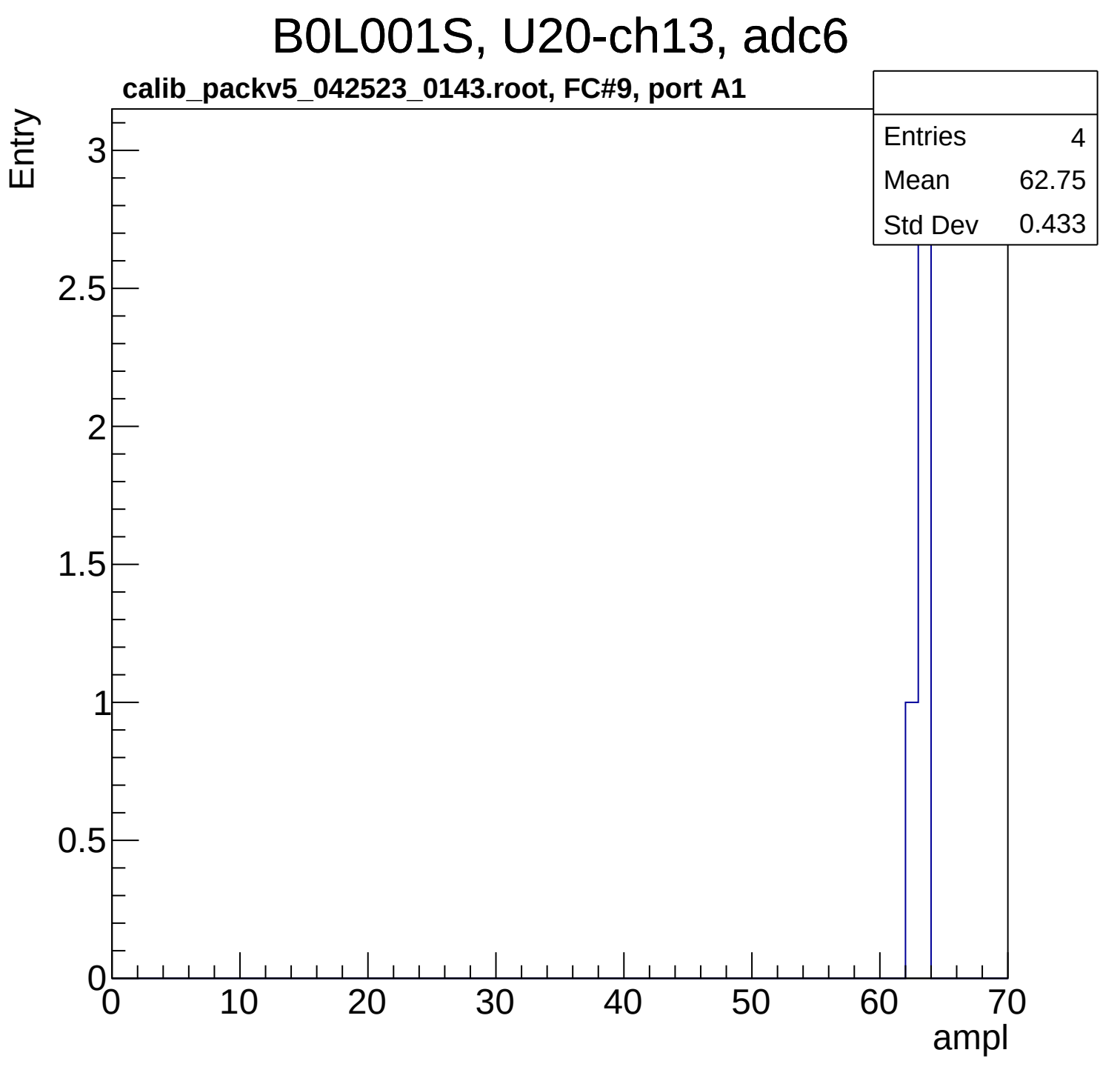
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.2
Std Dev 12.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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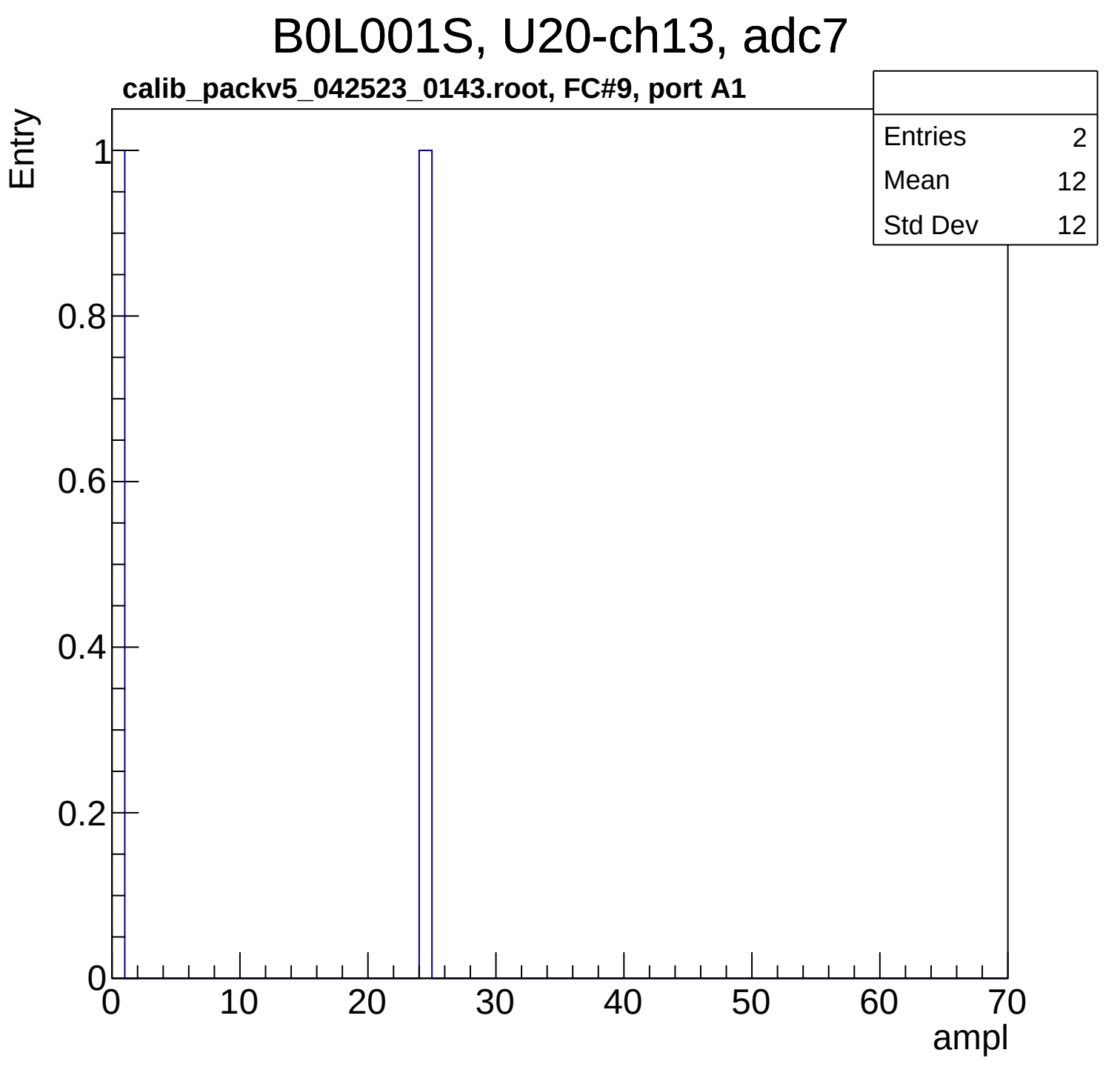
40
50
60
70


B0L001S, U20-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
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0
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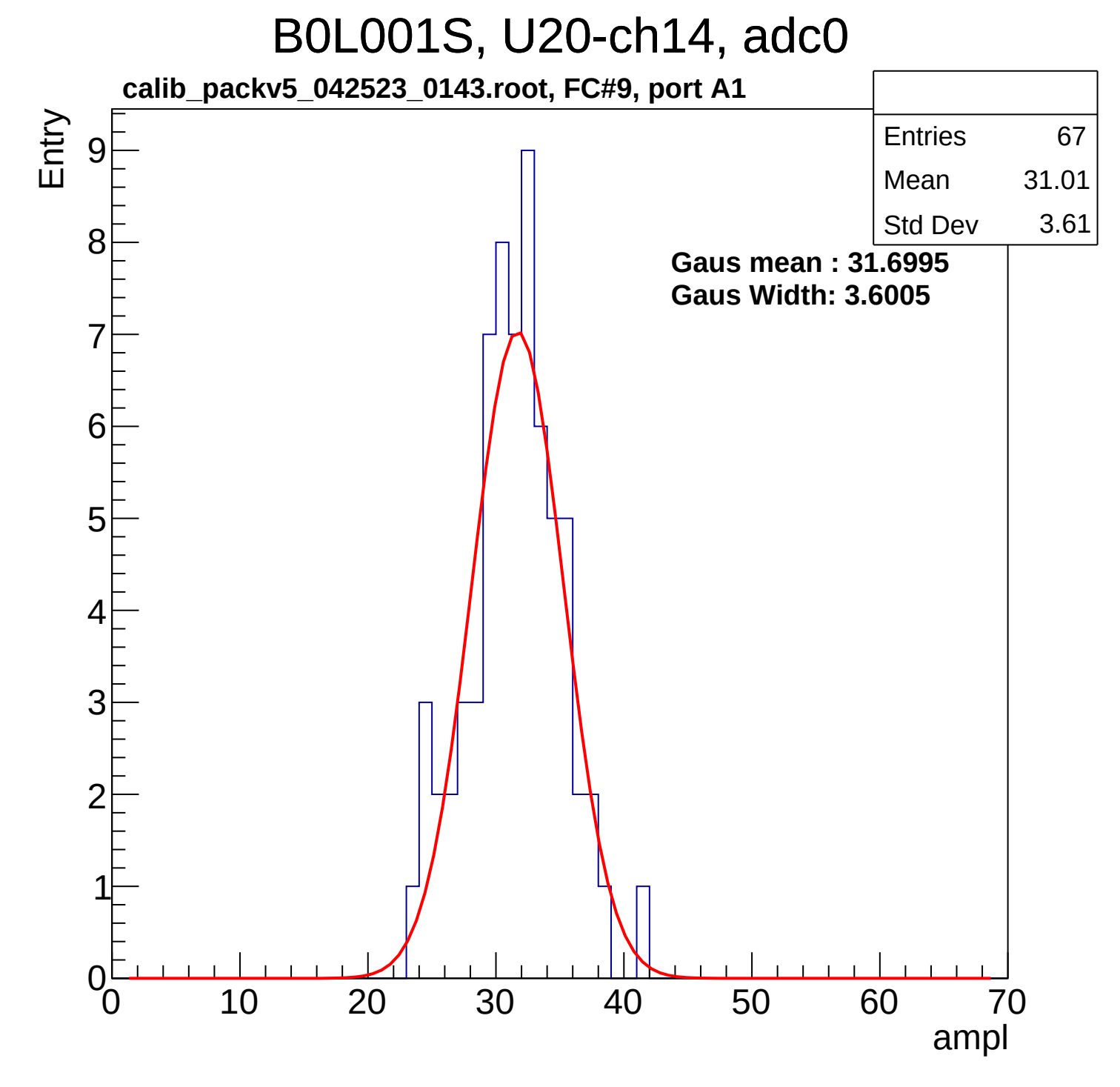
60
70
ampl


B0L001S, U20-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.01
Std Dev 3.61
Gaus mean : 31.6995
Gaus Width: 3.6005
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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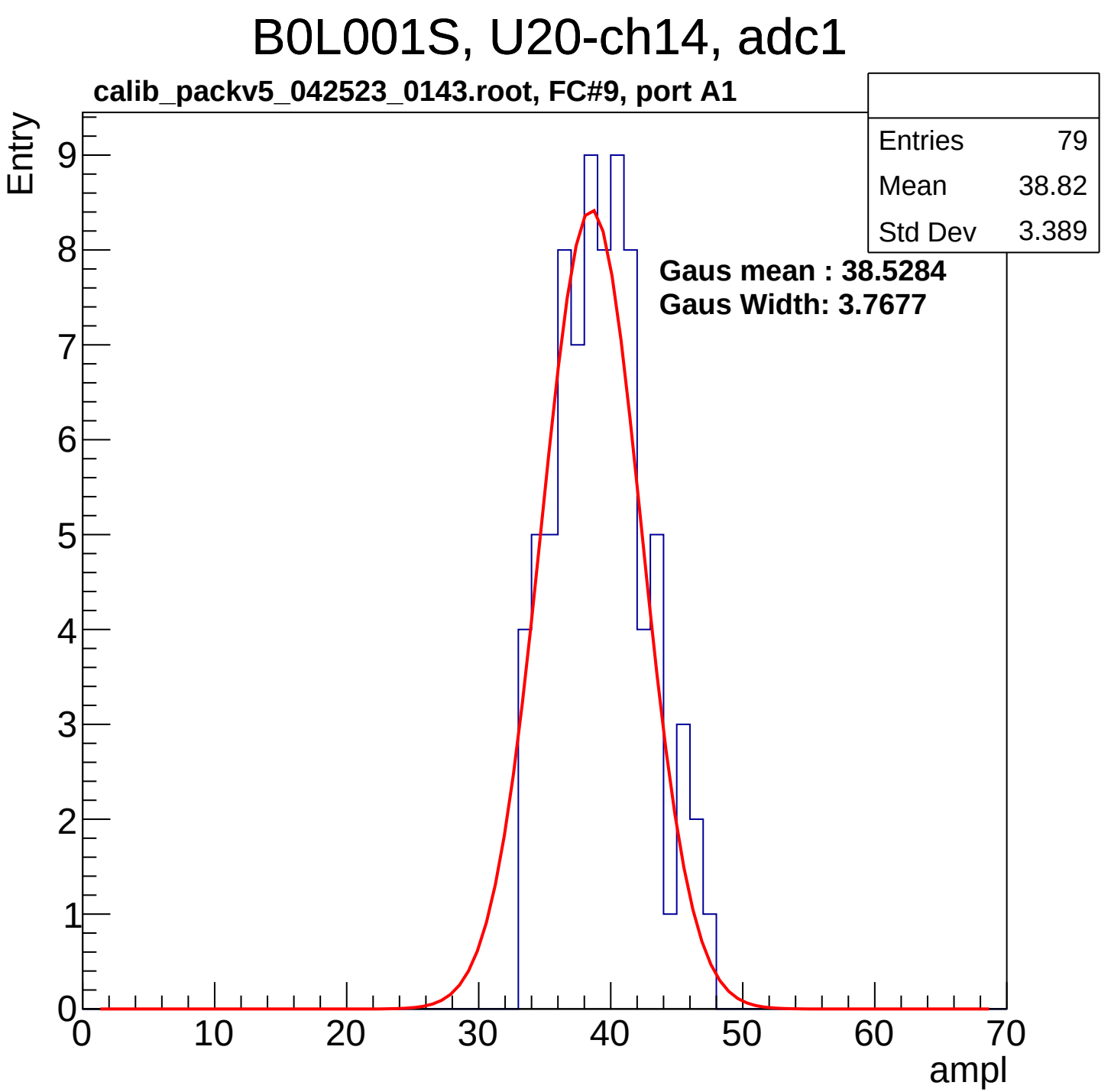
B0L001S, U20-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 38.82
Std Dev 3.389
Gaus mean : 38.5284
Gaus Width: 3.7677
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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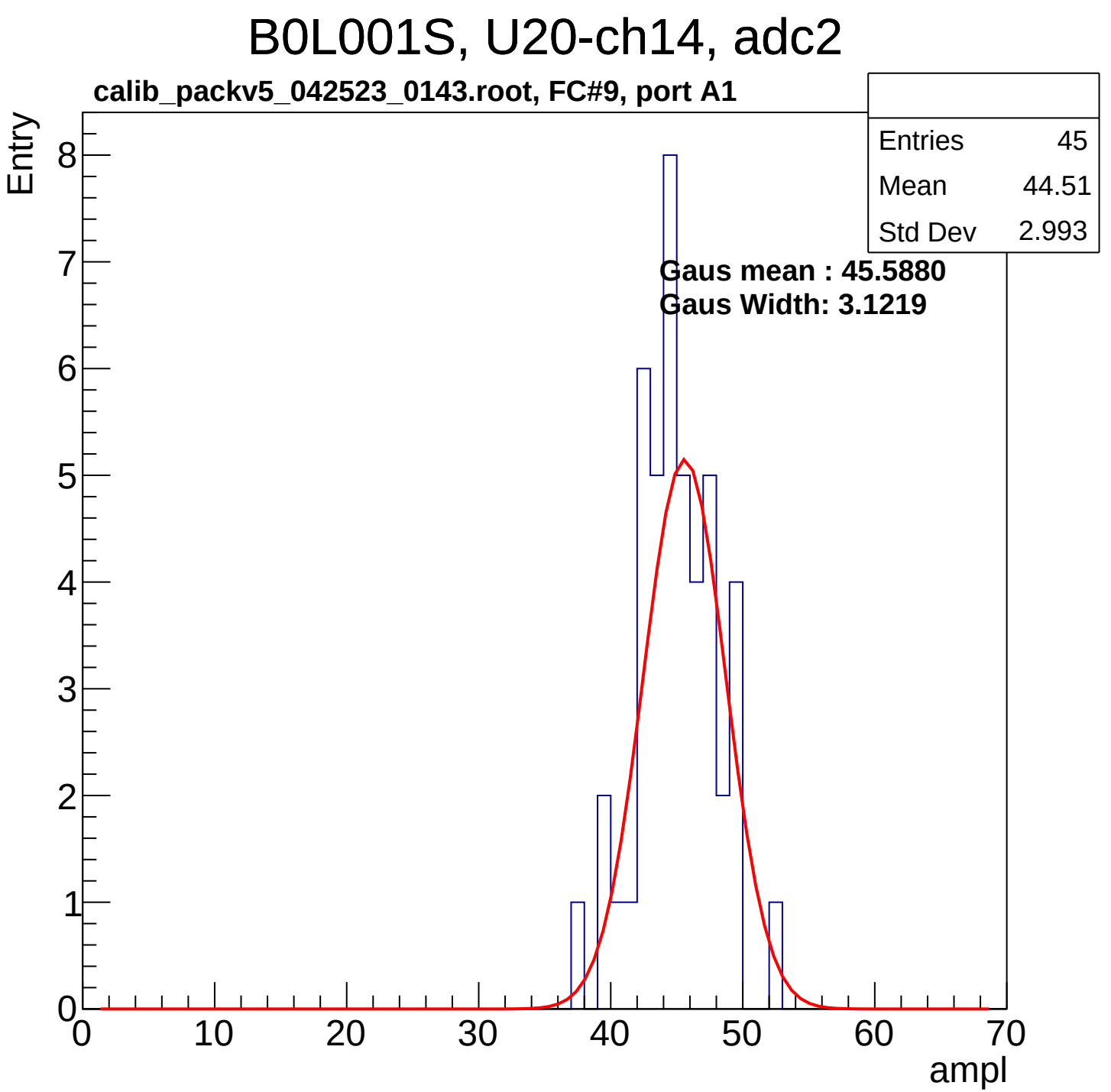
B0L001S, U20-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 44.51
Std Dev 2.993
Gaus mean : 45.5880
Gaus Width: 3.1219
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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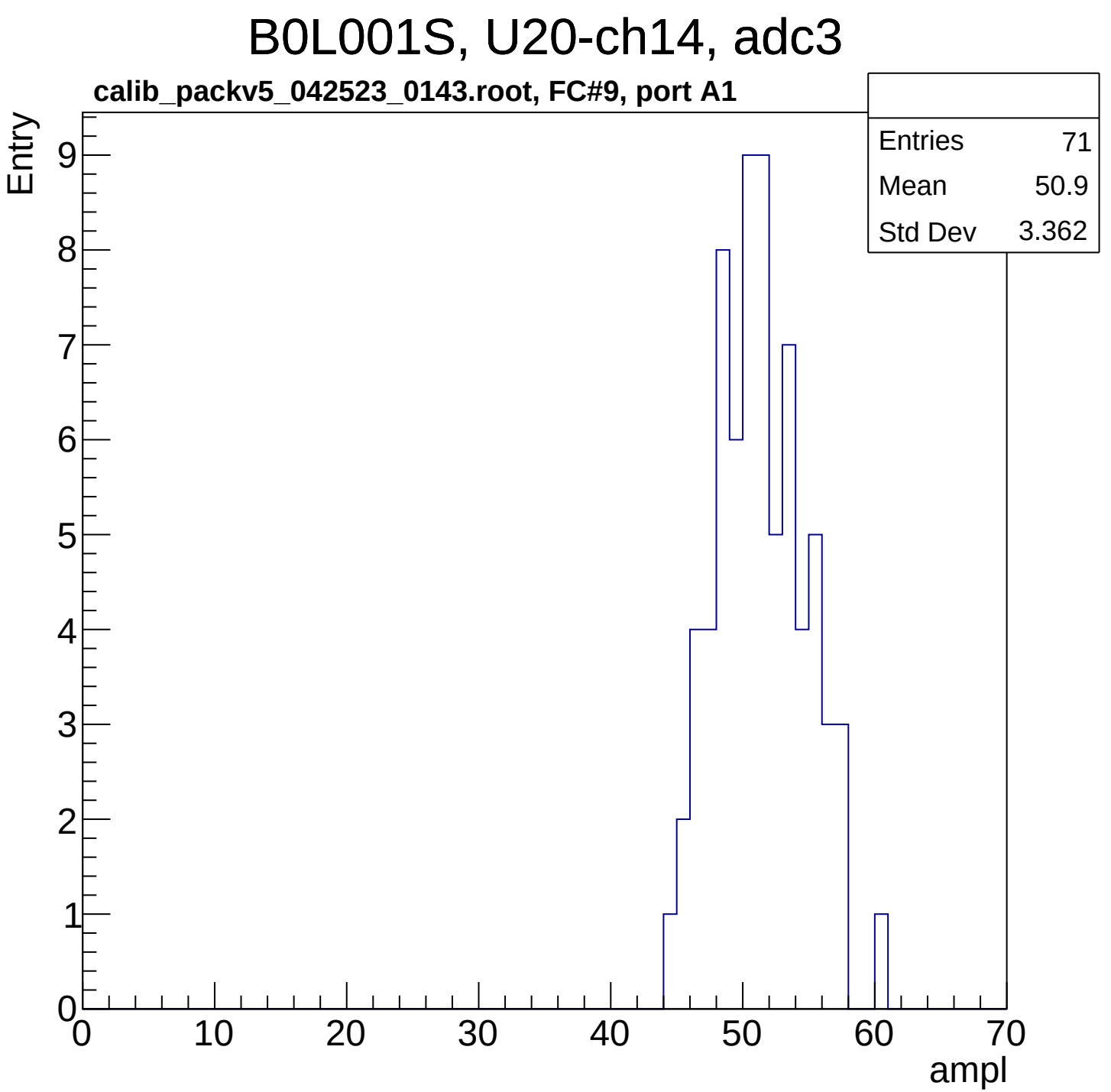
B0L001S, U20-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.9
Std Dev 3.362
Entry
ampl
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6
7
8
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0
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60
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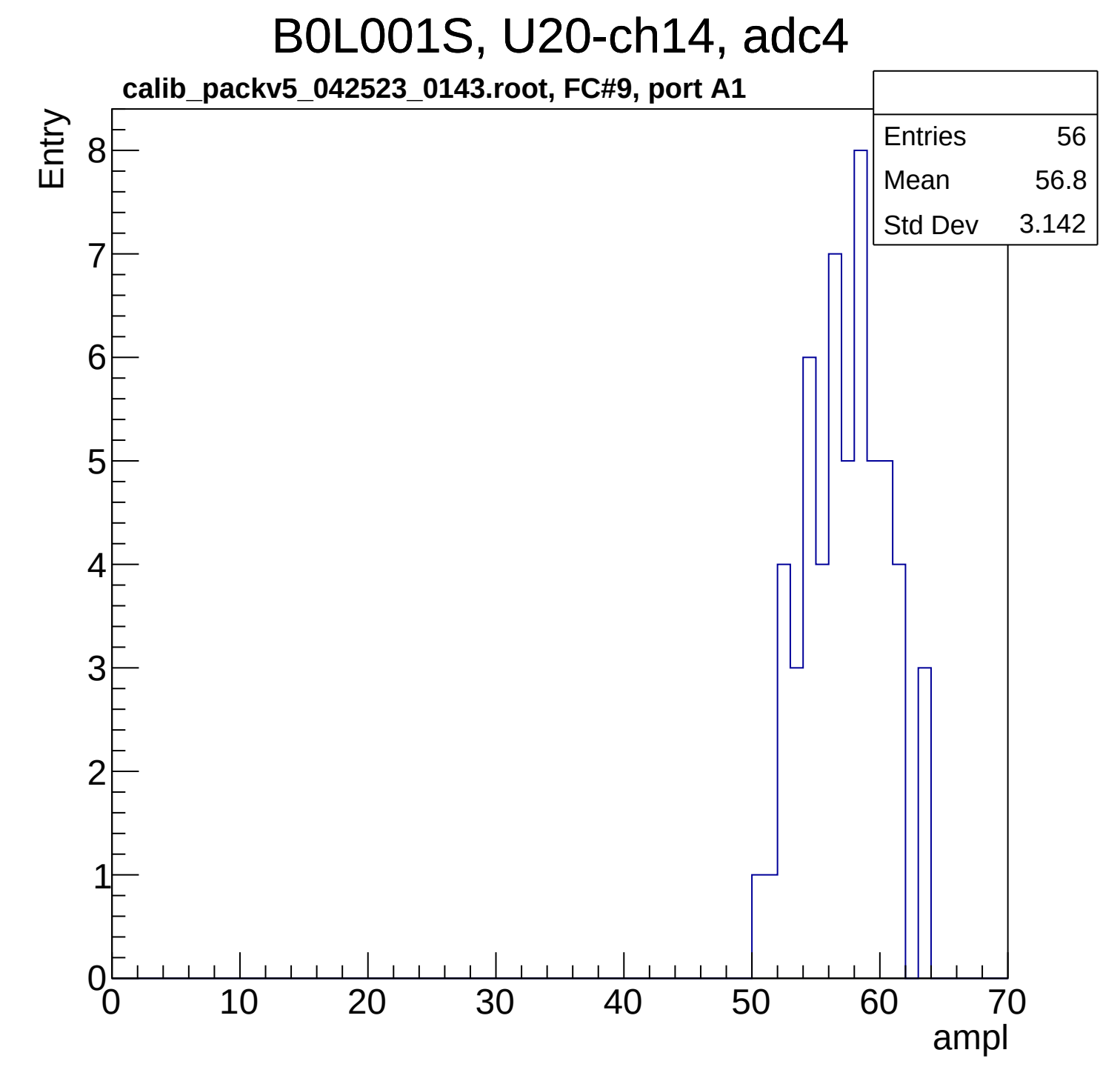
B0L001S, U20-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.8
Std Dev 3.142
Entry
ampl
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0
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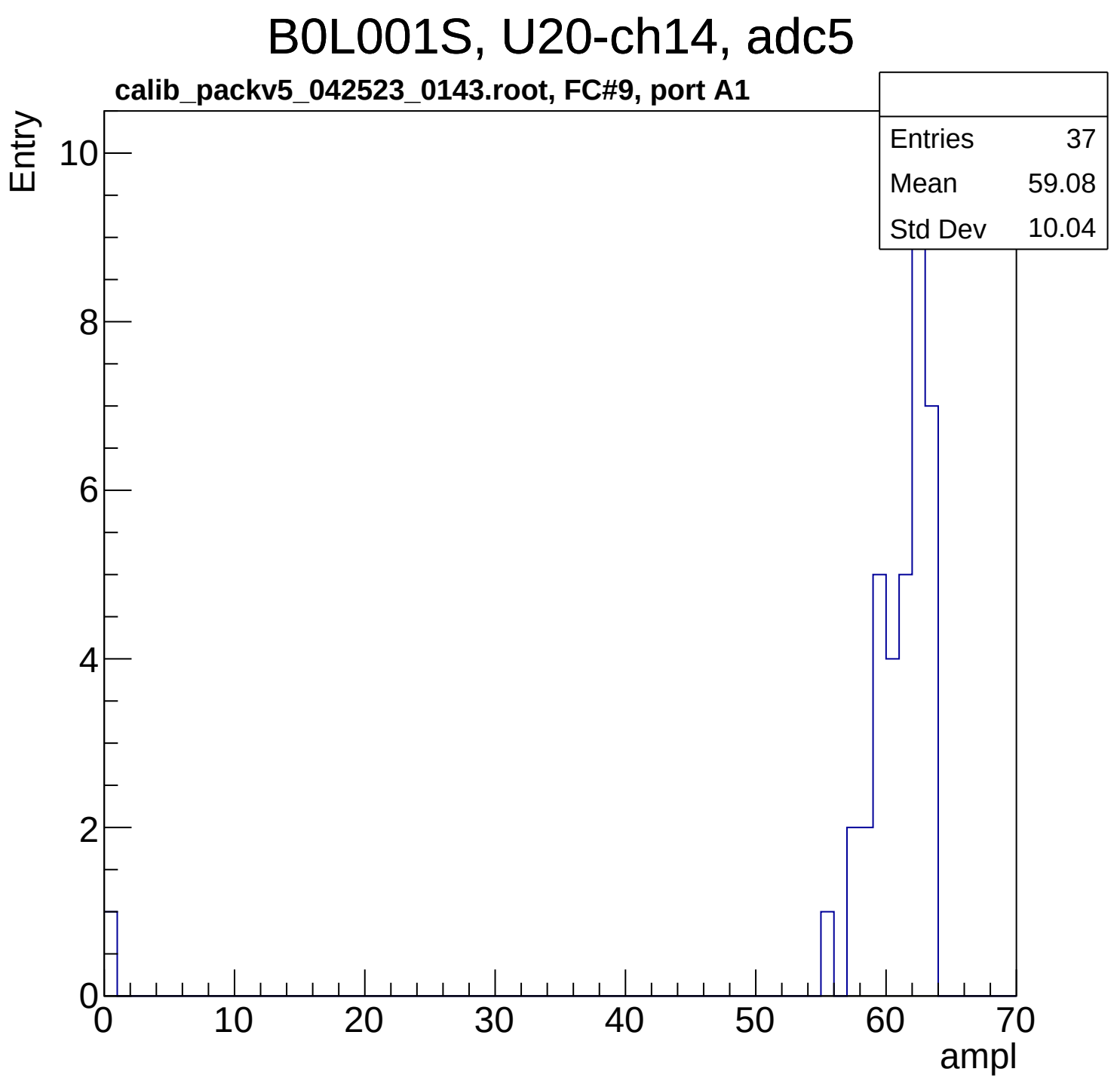
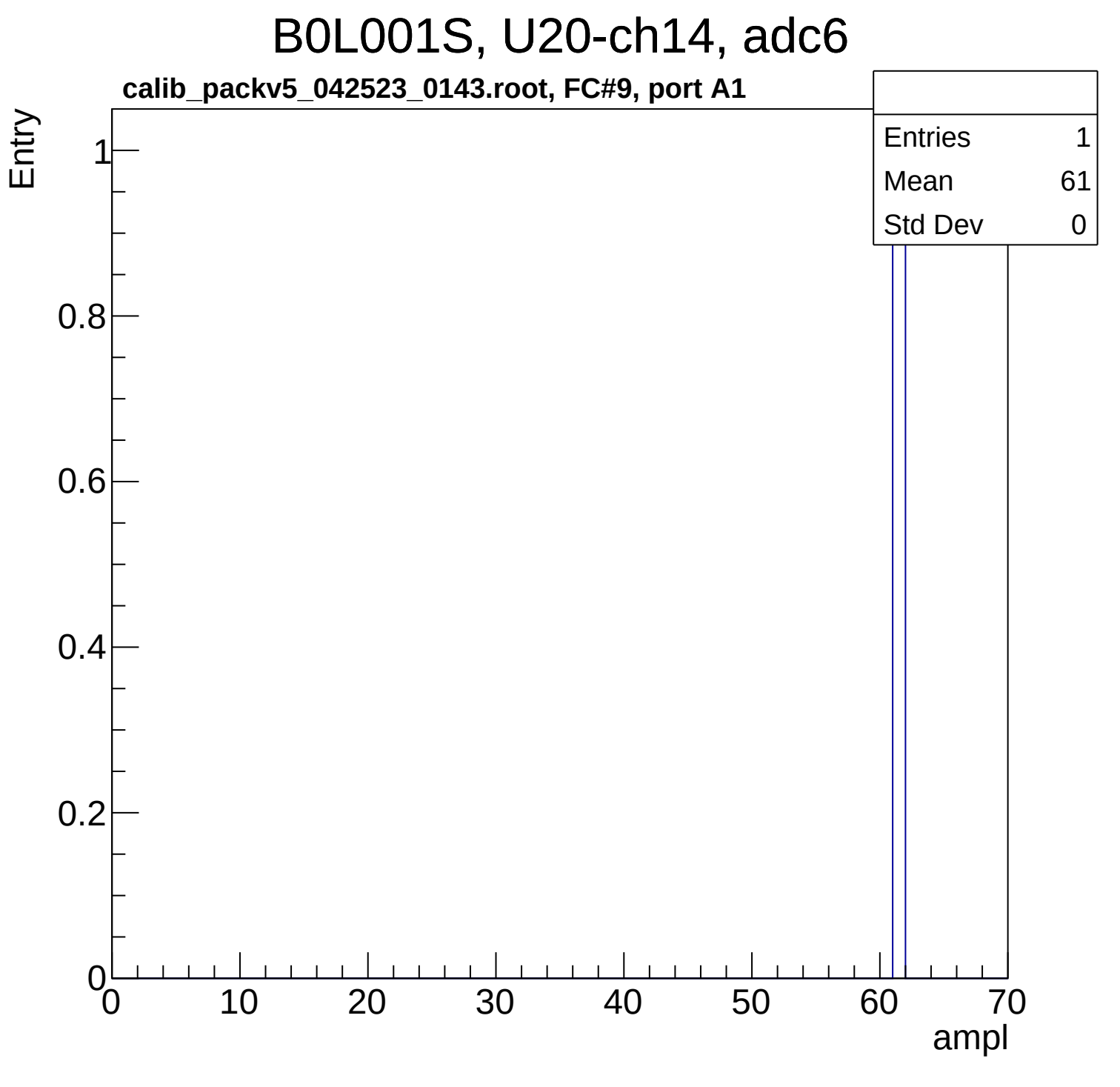

B0L001S, U20-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.08
Std Dev 10.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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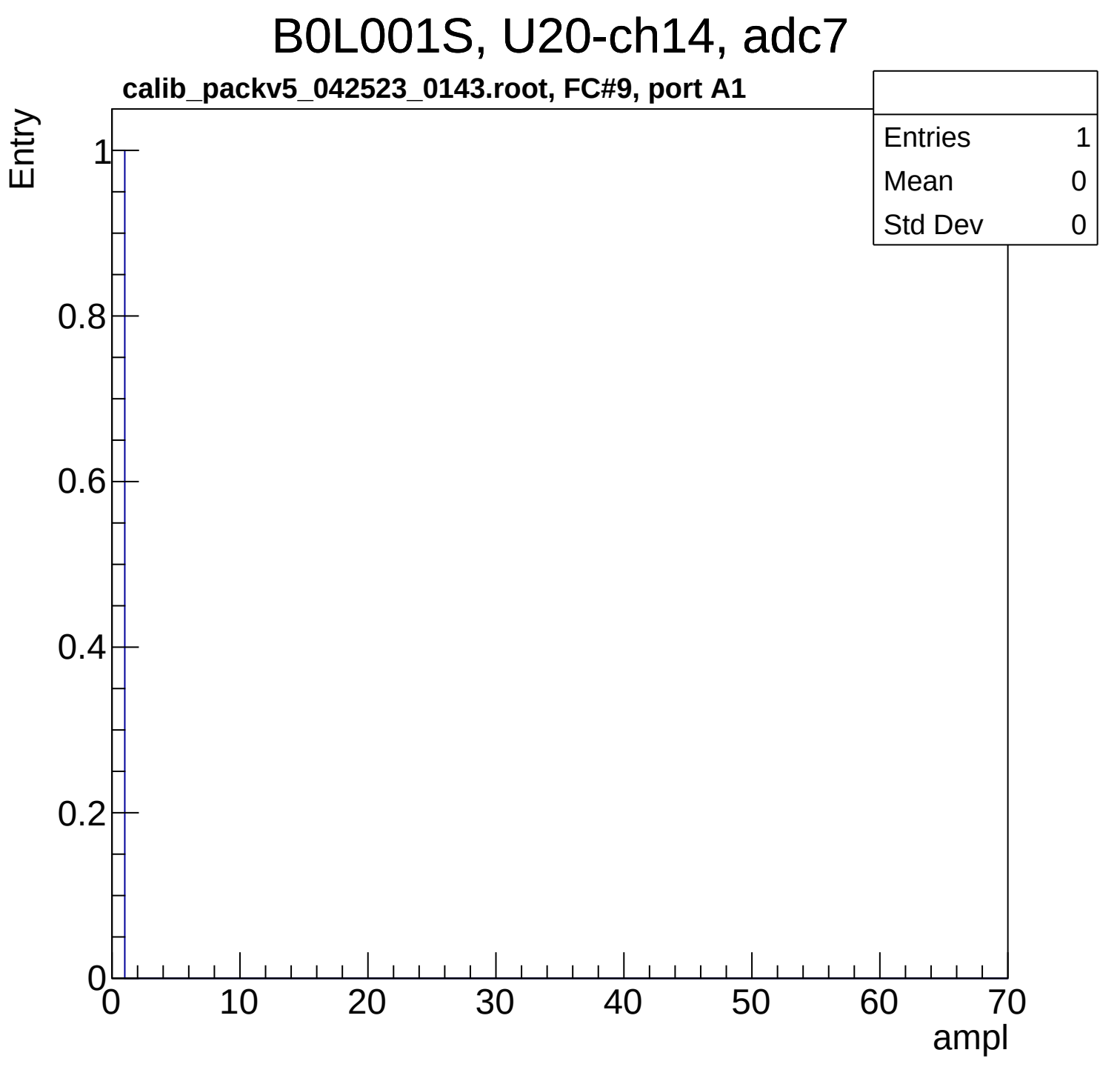
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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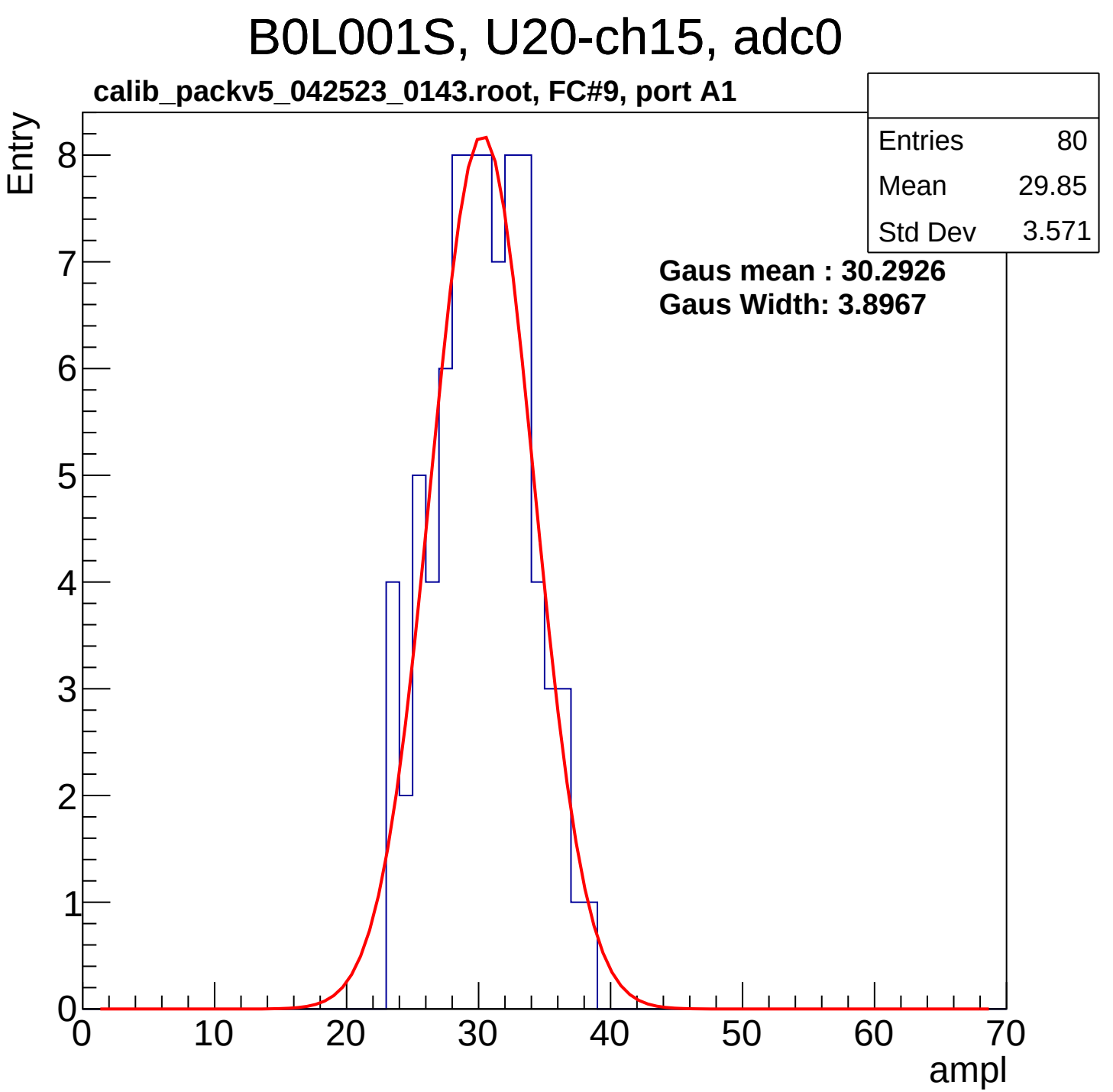
50
60
70
ampl


B0L001S, U20-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.85
Std Dev 3.571
Gaus mean : 30.2926
Gaus Width: 3.8967
Entry
ampl
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2
3
4
5
6
7
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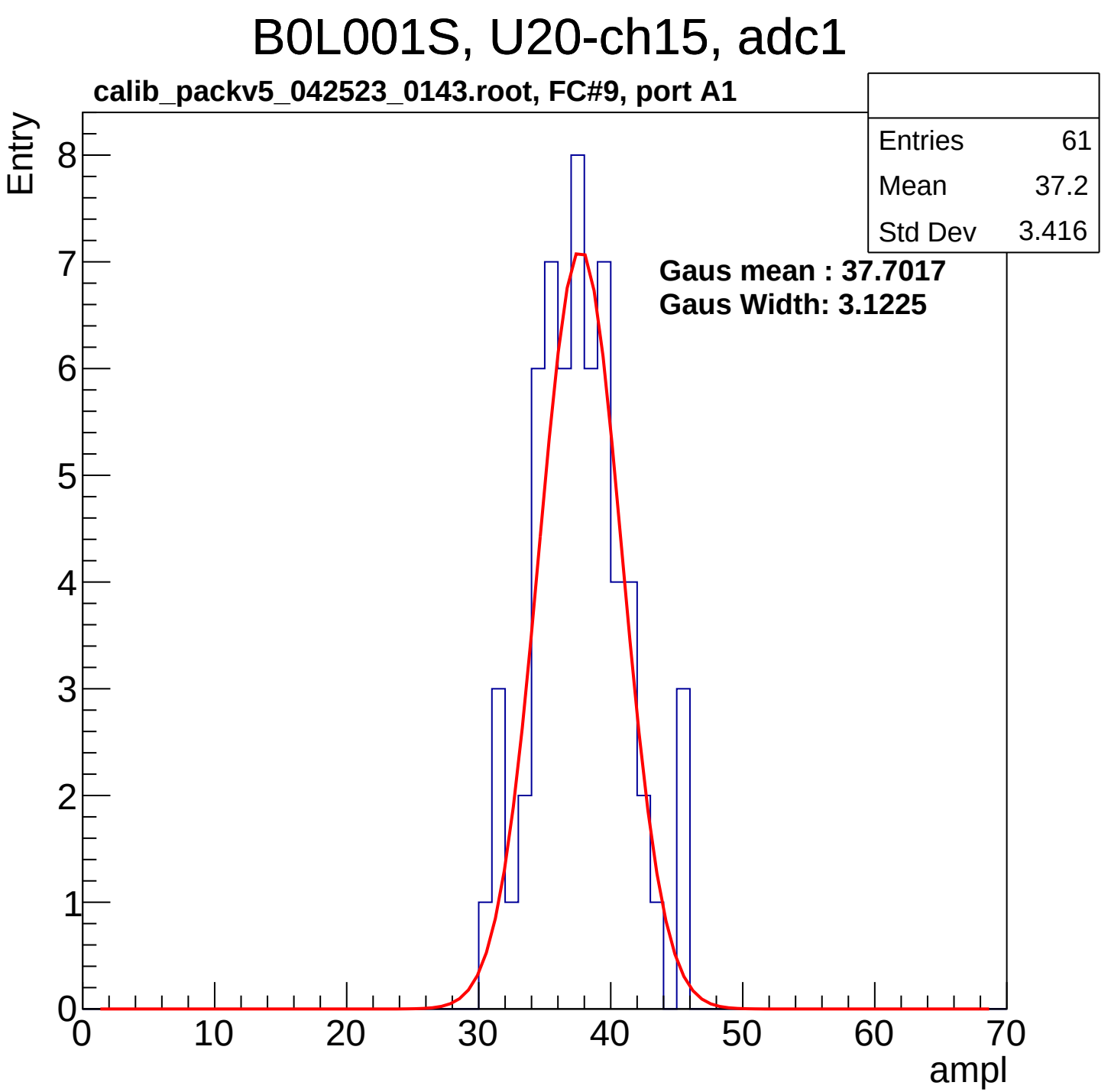
B0L001S, U20-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.2
Std Dev 3.416
Gaus mean : 37.7017
Gaus Width: 3.1225
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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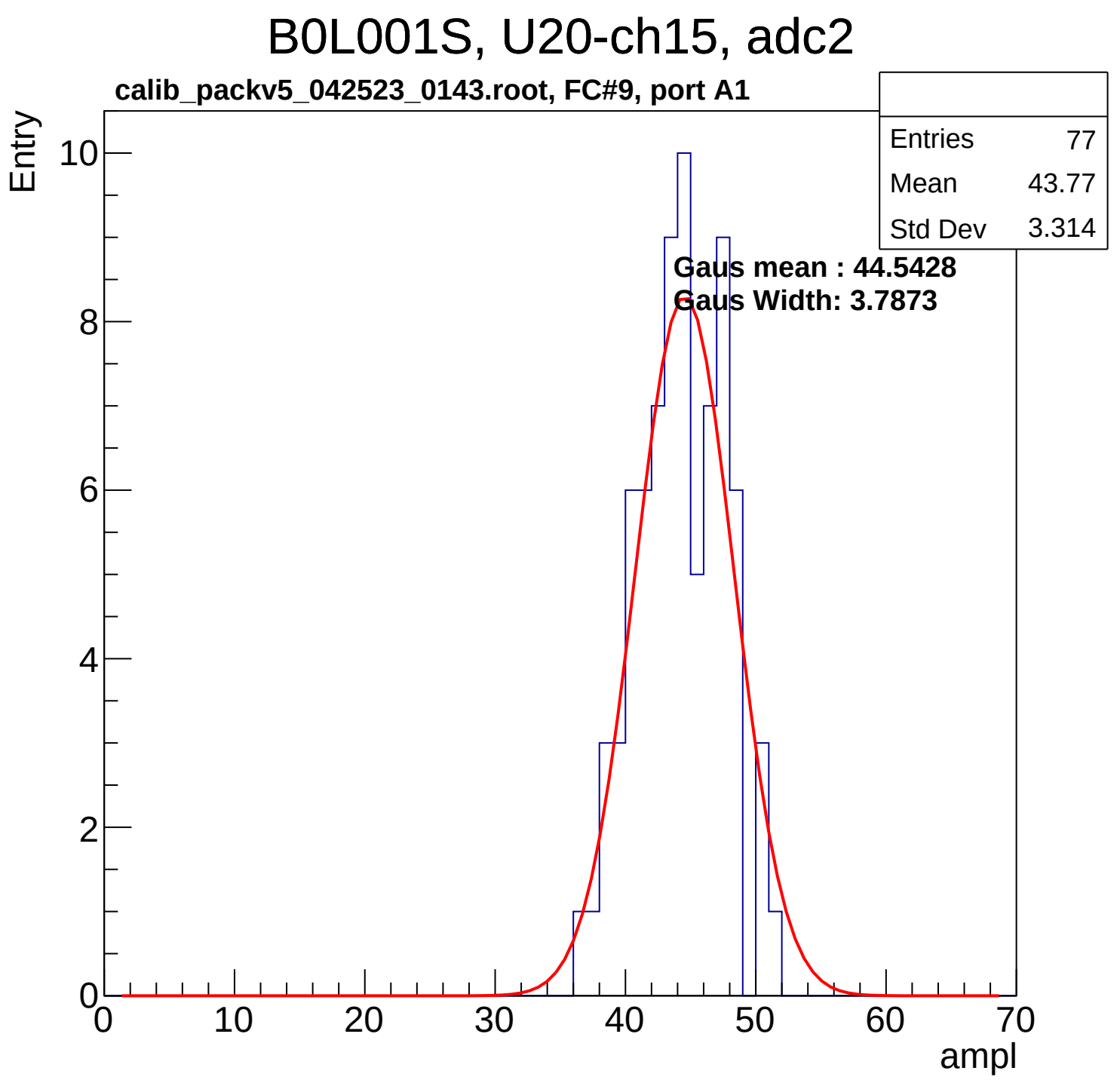
B0L001S, U20-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 43.77
Std Dev 3.314
Gaus mean : 44.5428
Gaus Width: 3.7873
Entry
ampl
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4
6
8
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40
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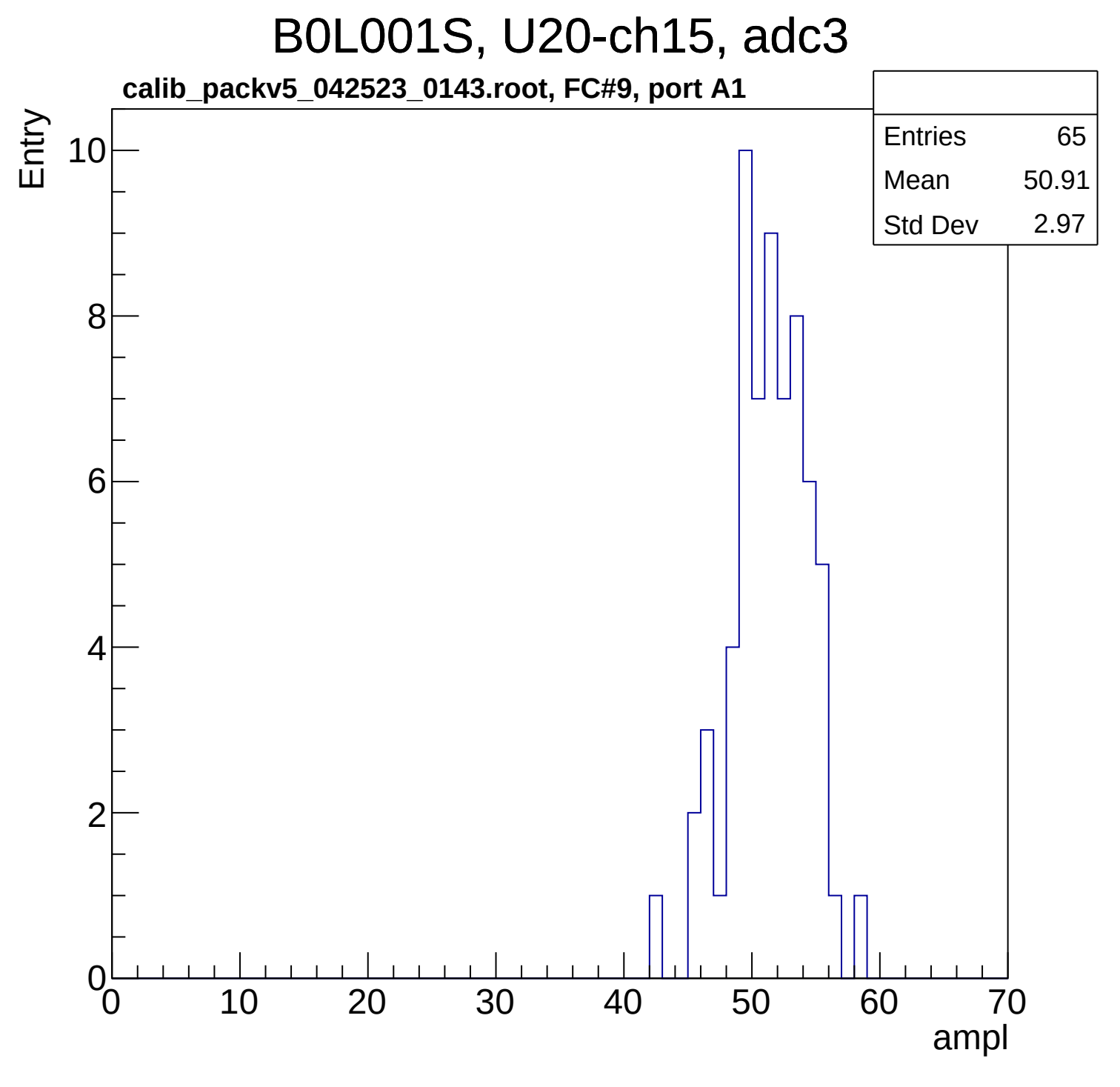
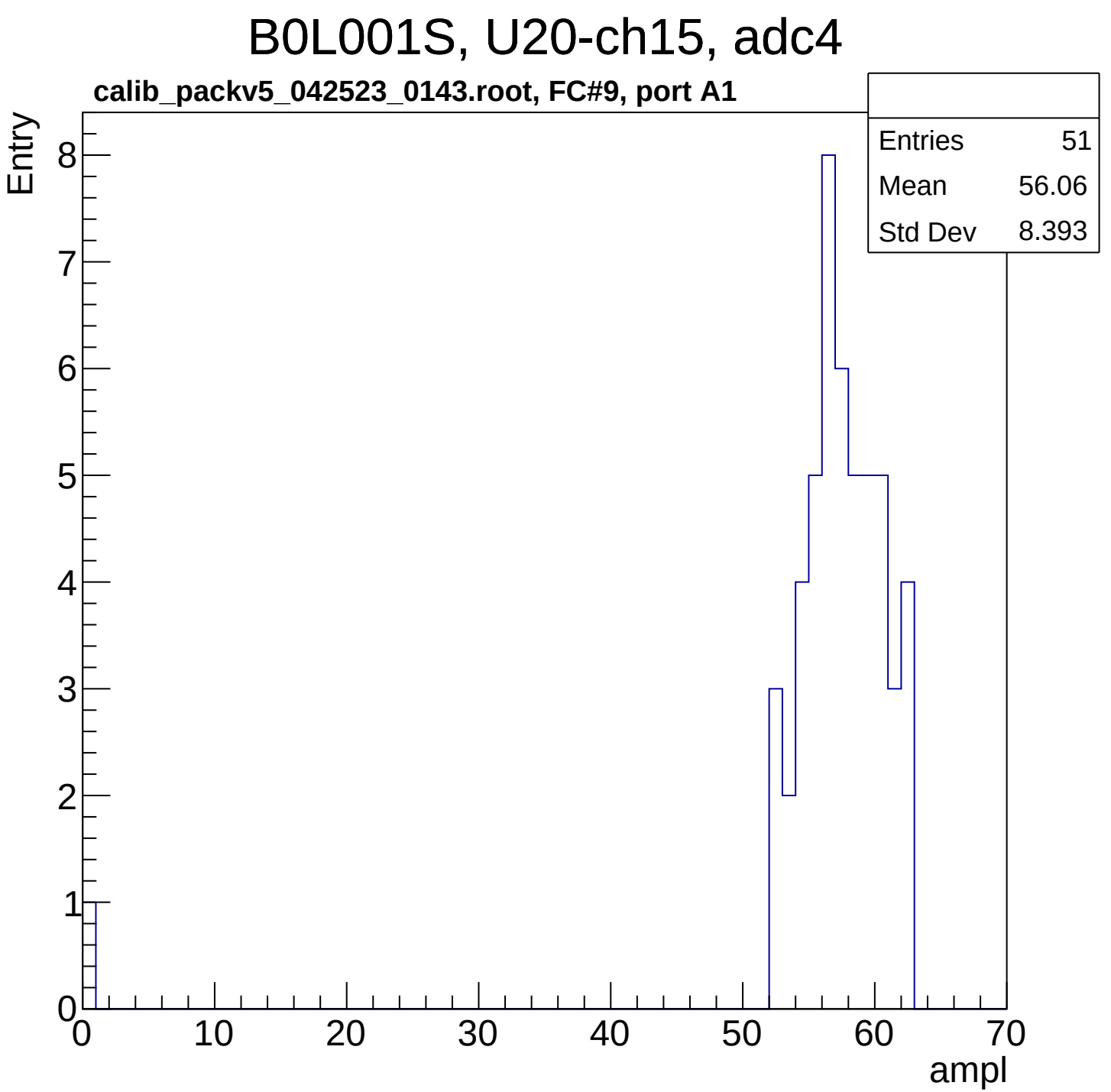

B0L001S, U20-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.91
Std Dev 2.97
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.06
Std Dev 8.393
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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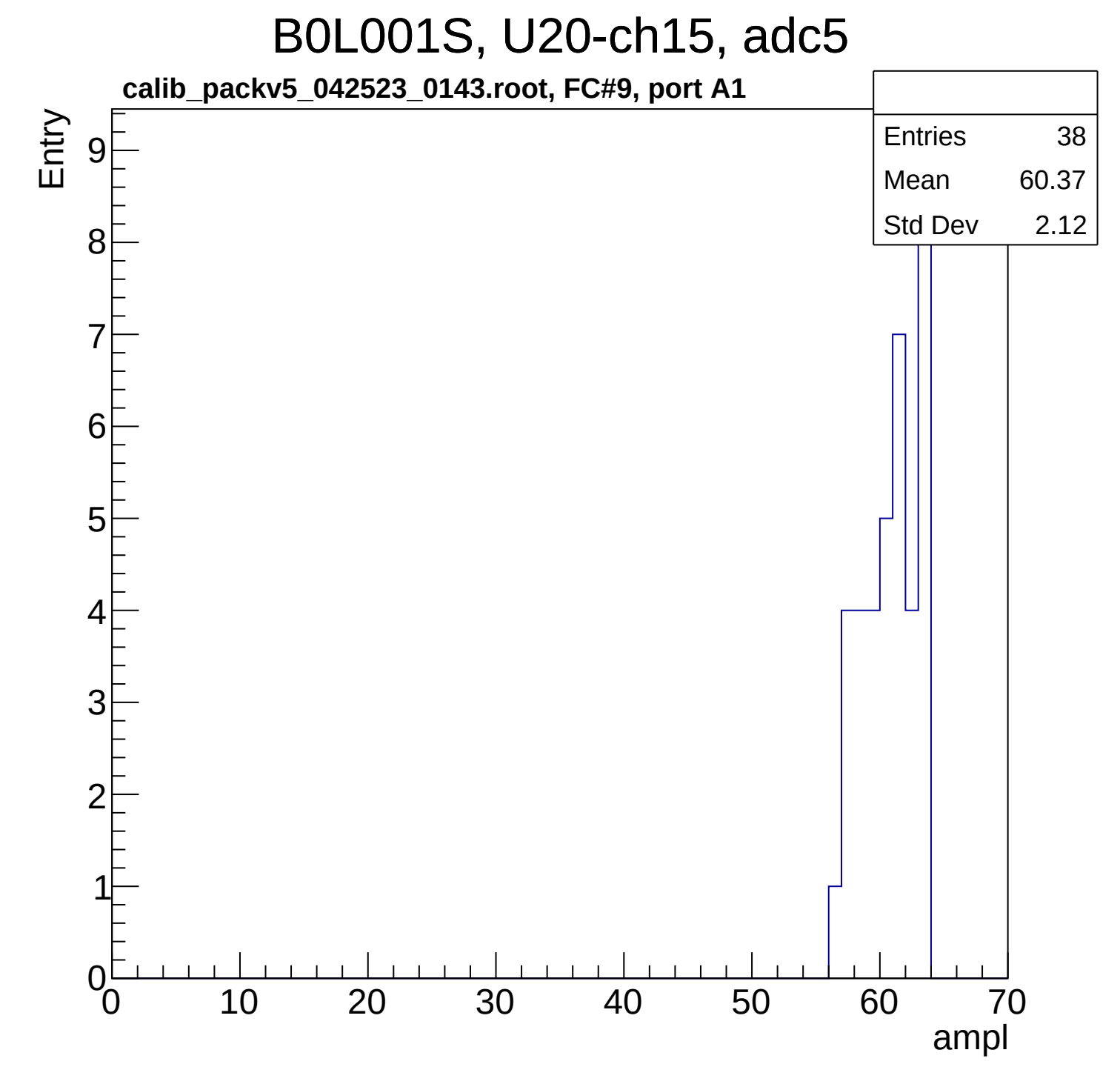
40
50
60
70


B0L001S, U20-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.37
Std Dev 2.12
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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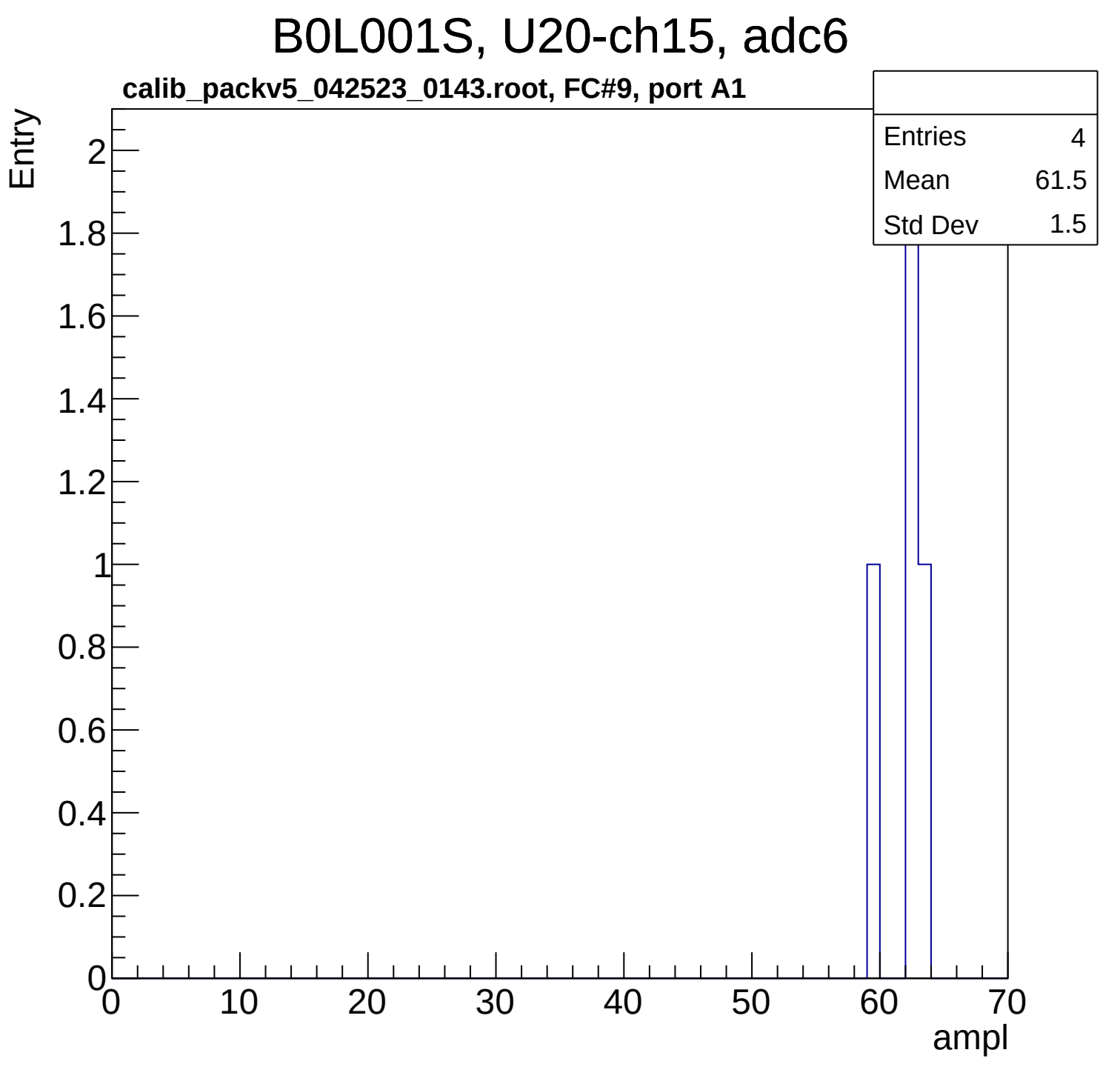
B0L001S, U20-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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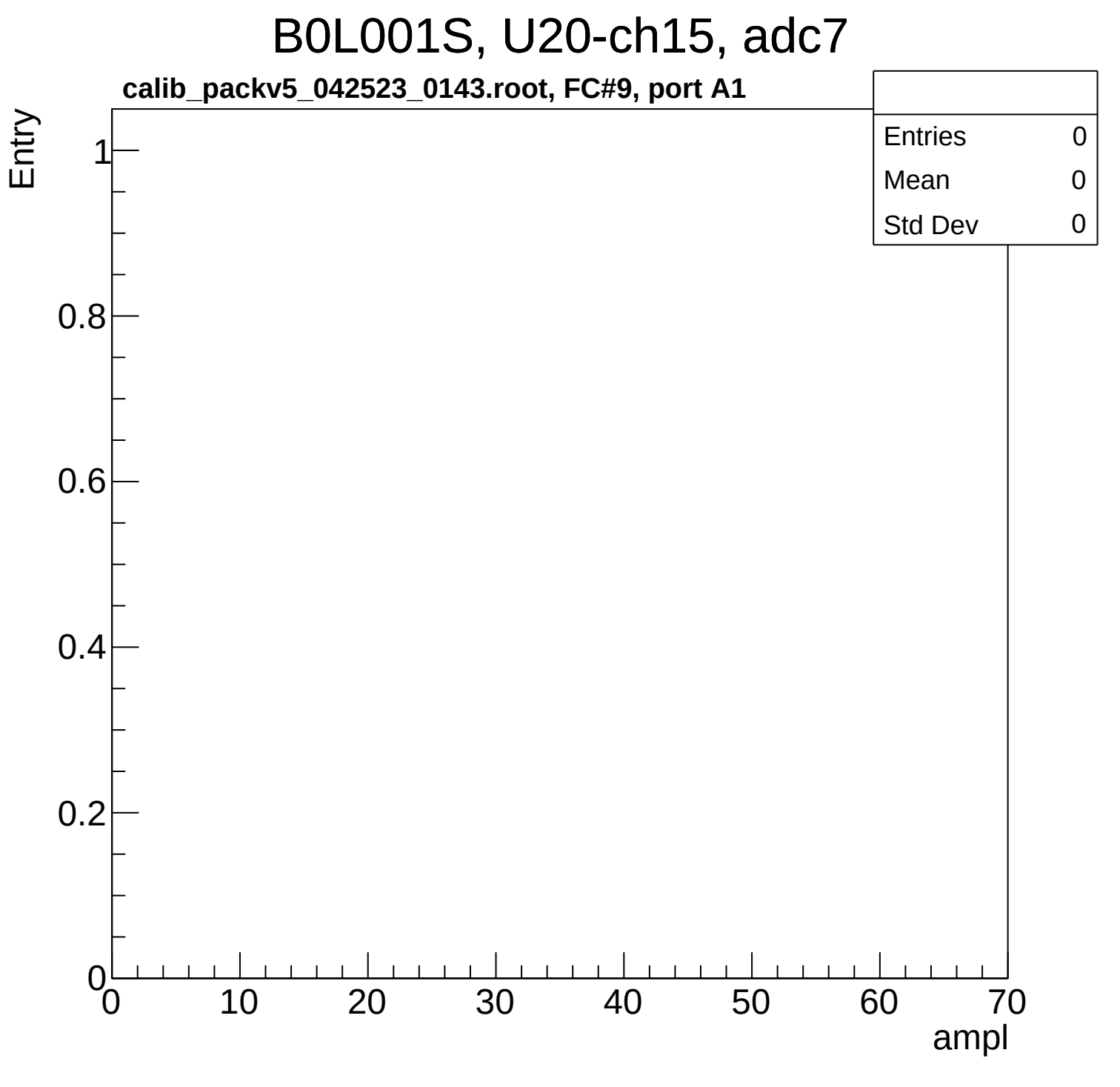
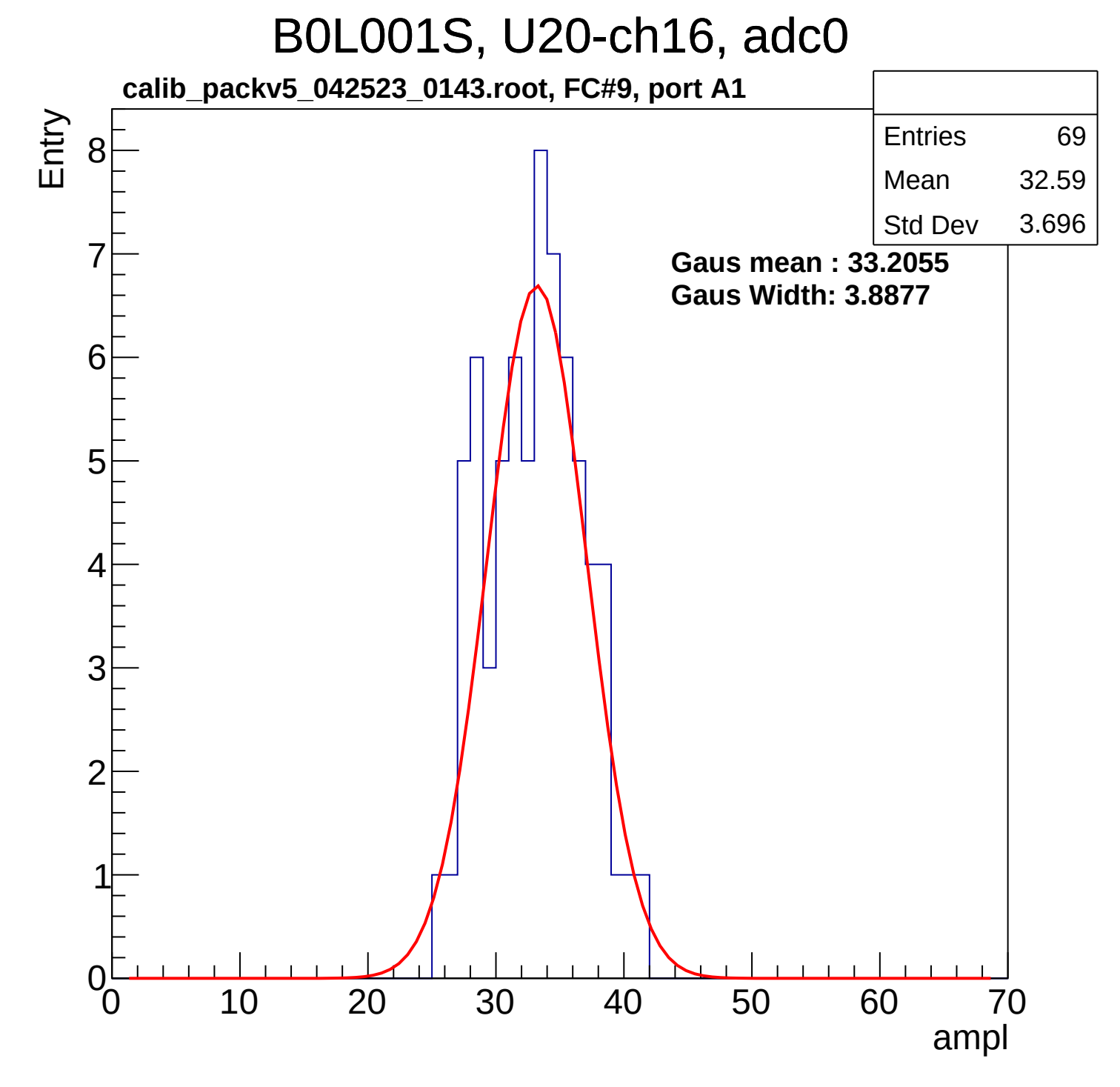

B0L001S, U20-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 32.59
Std Dev 3.696
Gaus mean : 33.2055
Gaus Width: 3.8877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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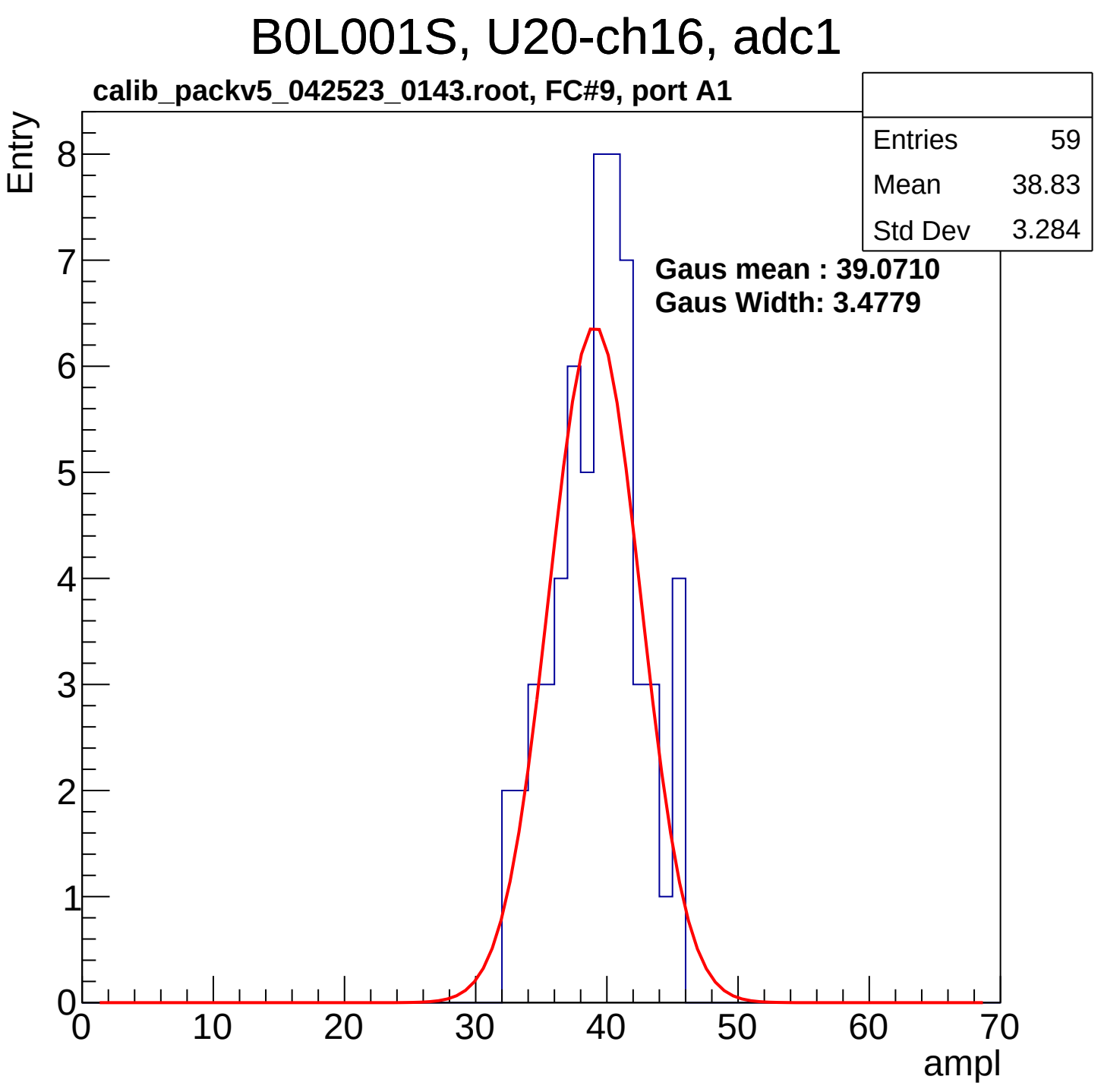
B0L001S, U20-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.83
Std Dev 3.284
Gaus mean : 39.0710
Gaus Width: 3.4779
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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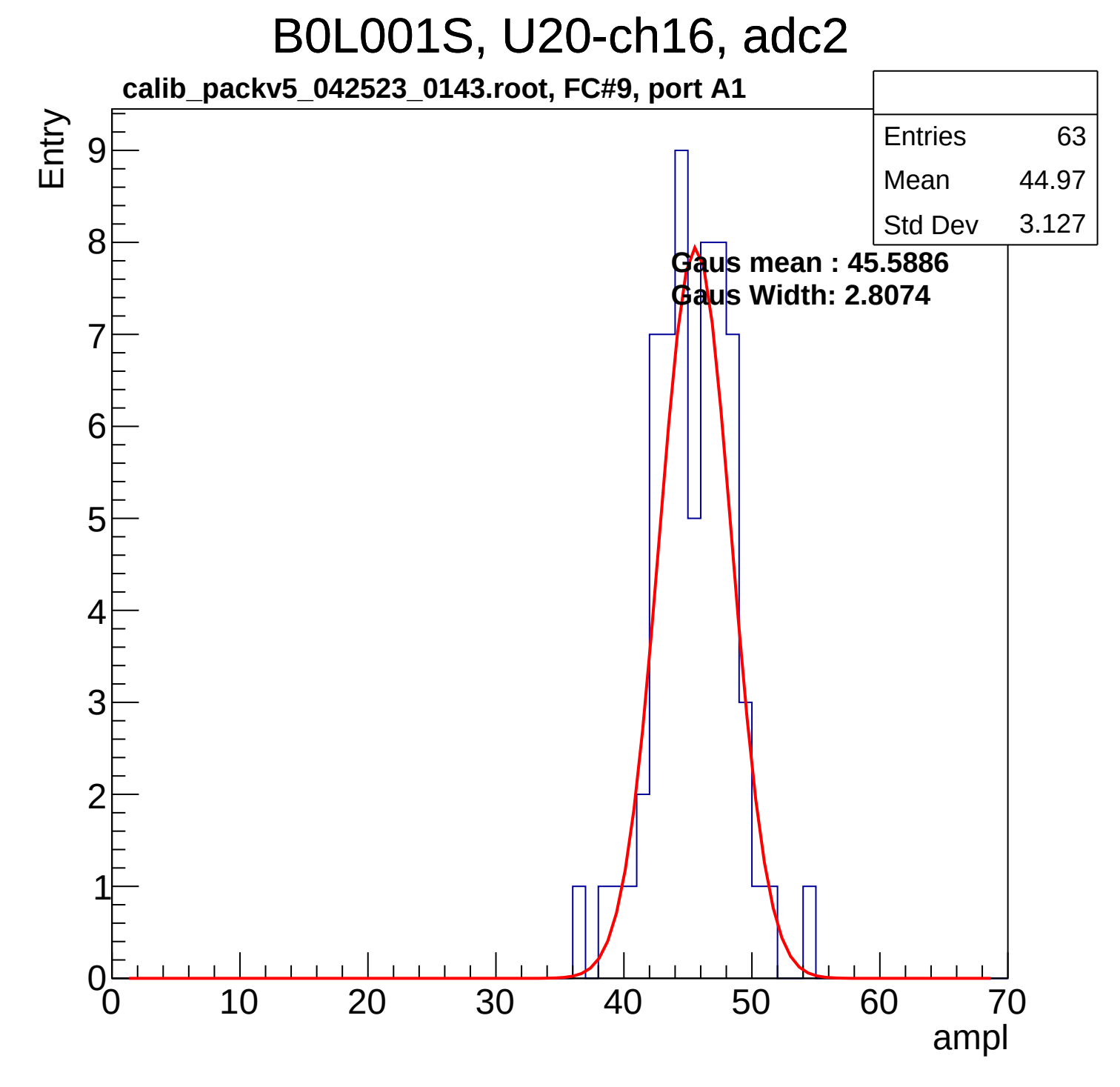
40
50
60
70


B0L001S, U20-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.97
Std Dev 3.127
Gaus mean : 45.5886
Gaus Width: 2.8074
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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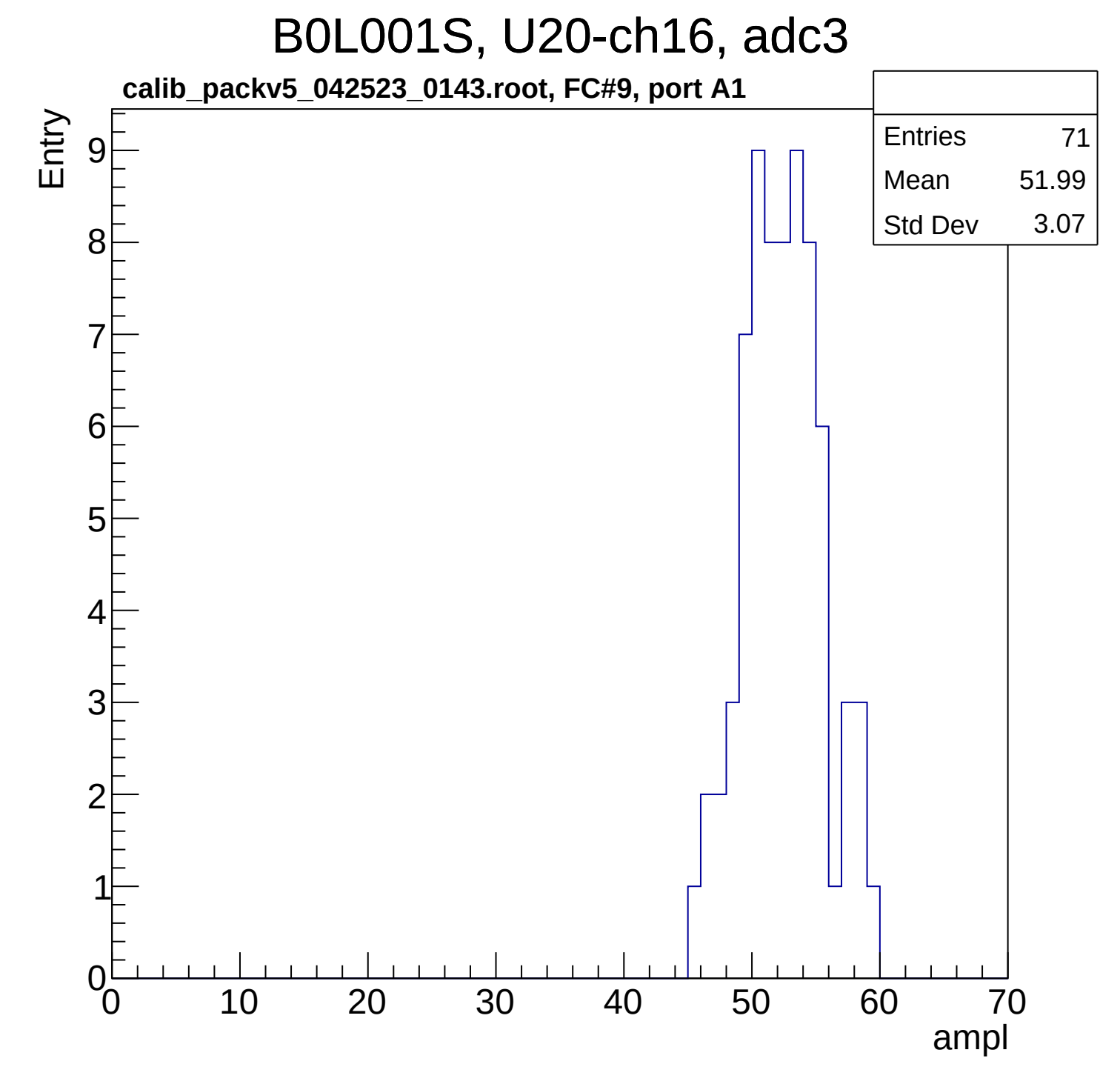
B0L001S, U20-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.99
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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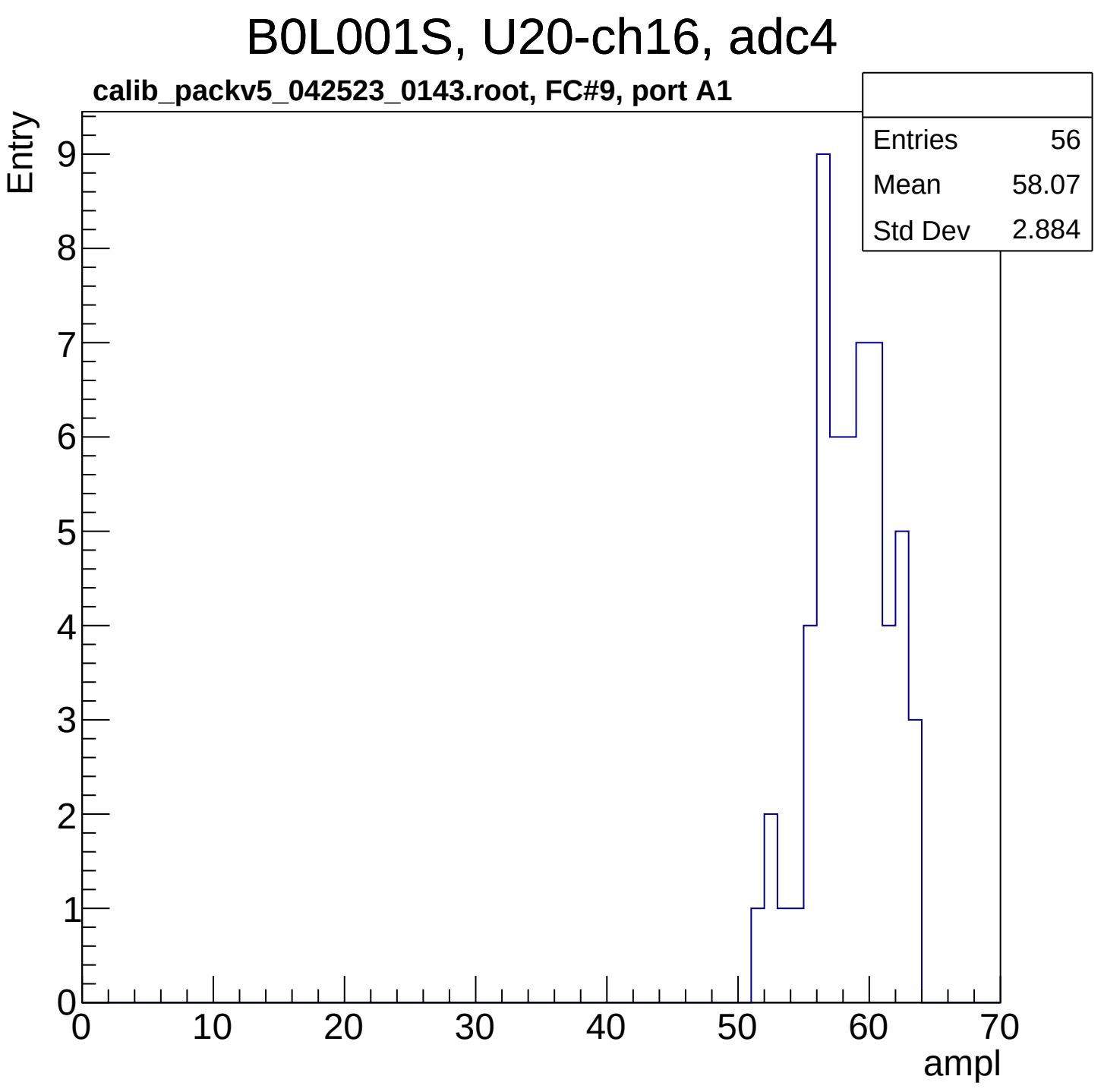
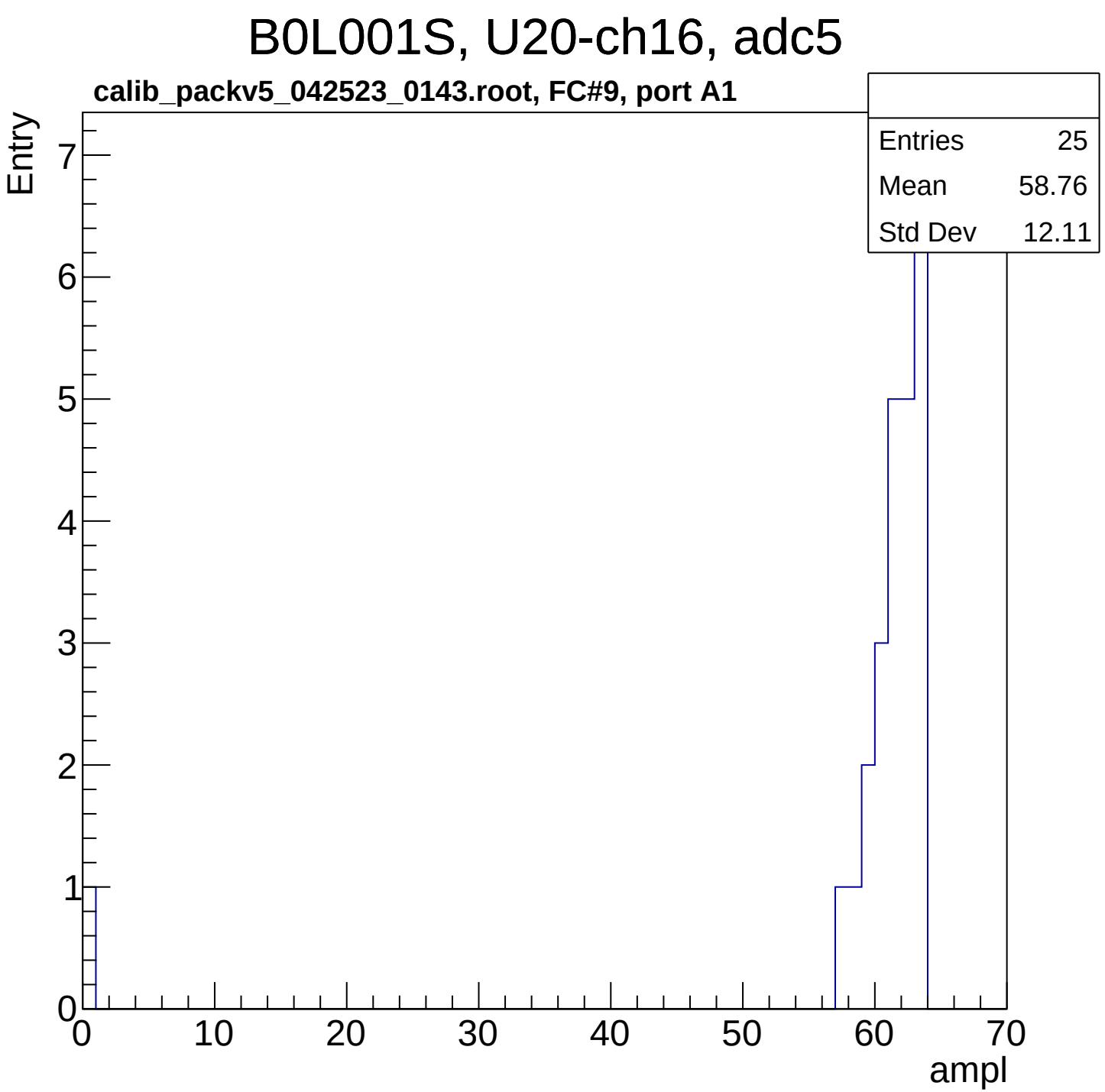
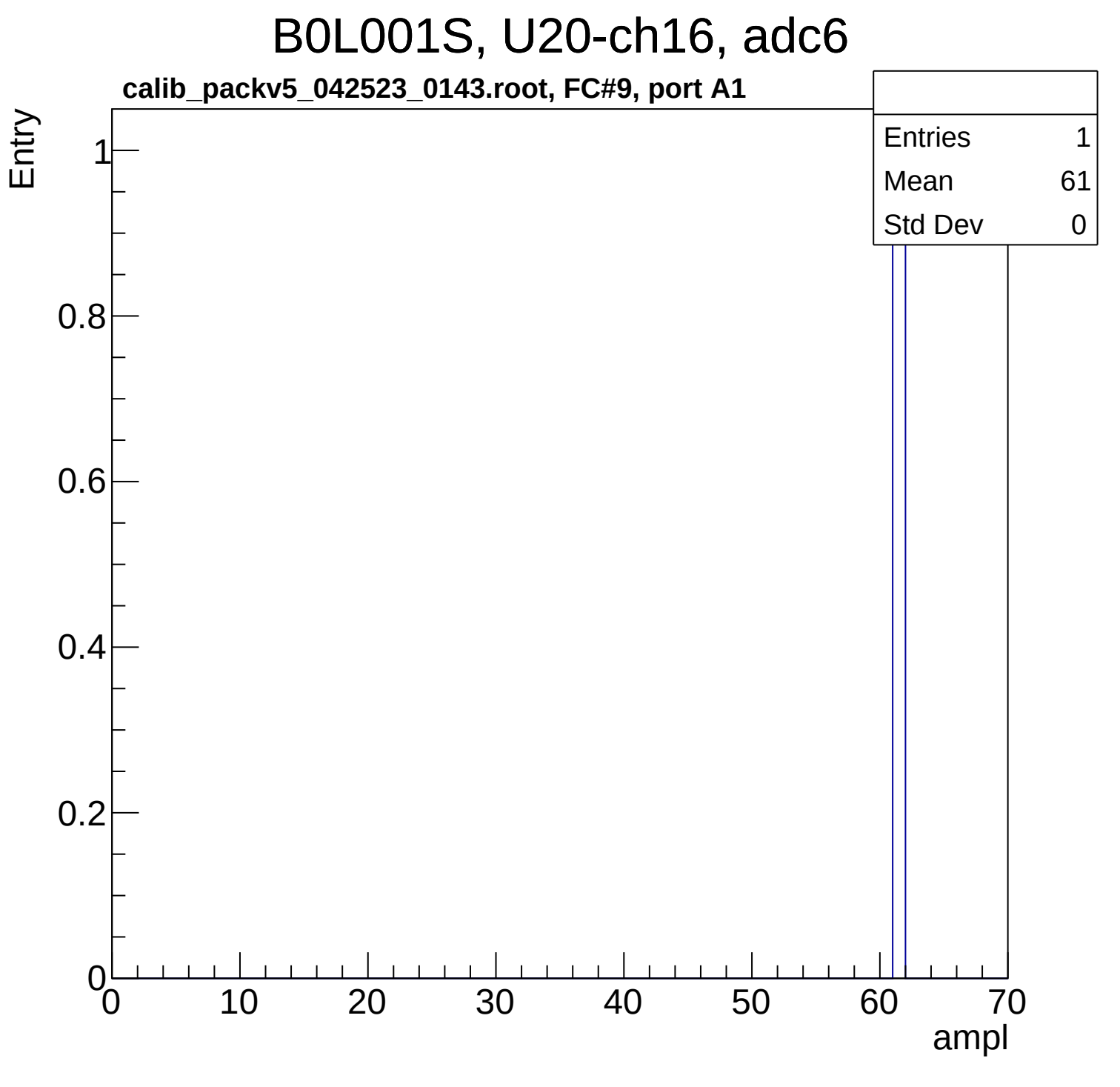
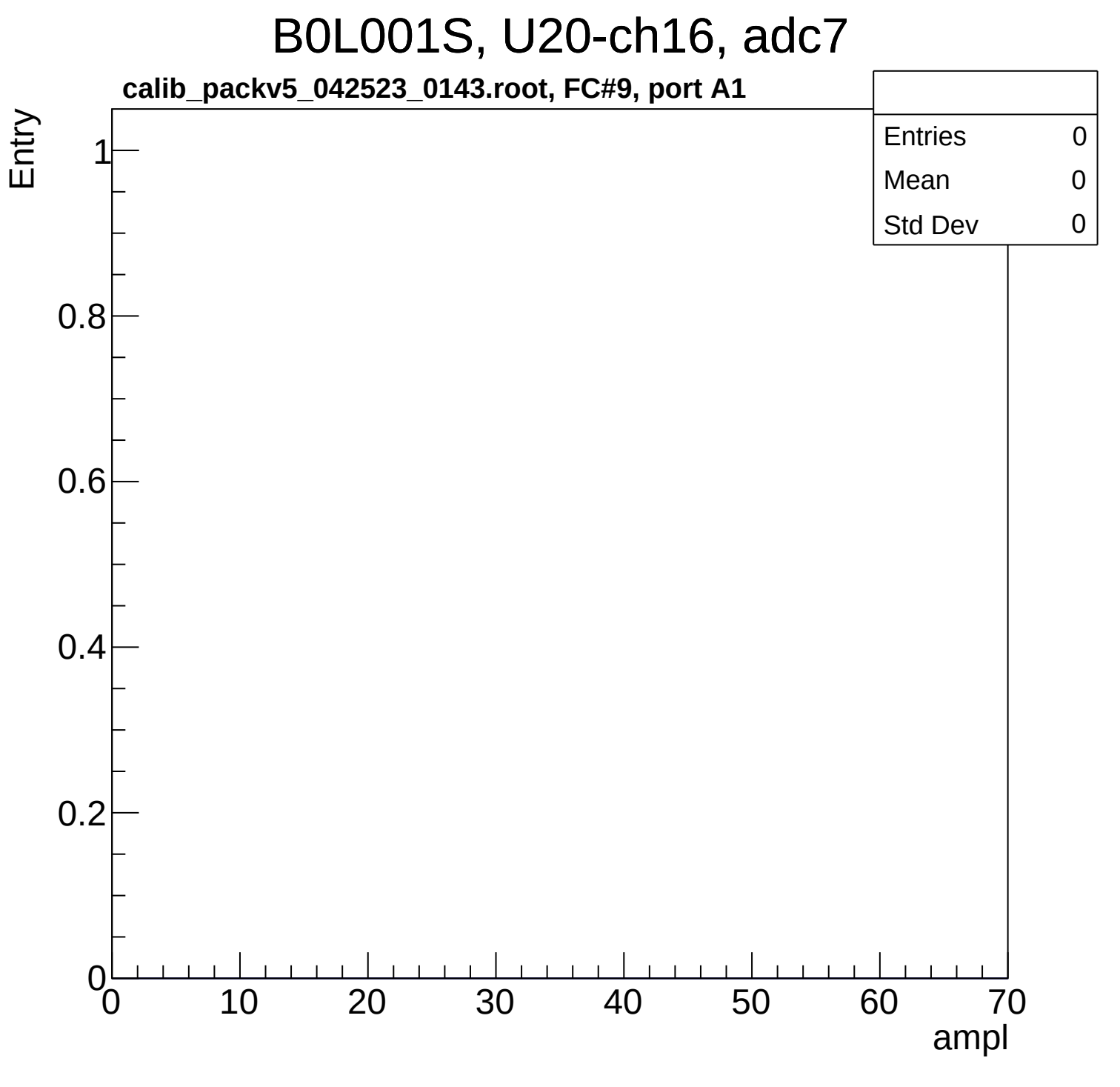
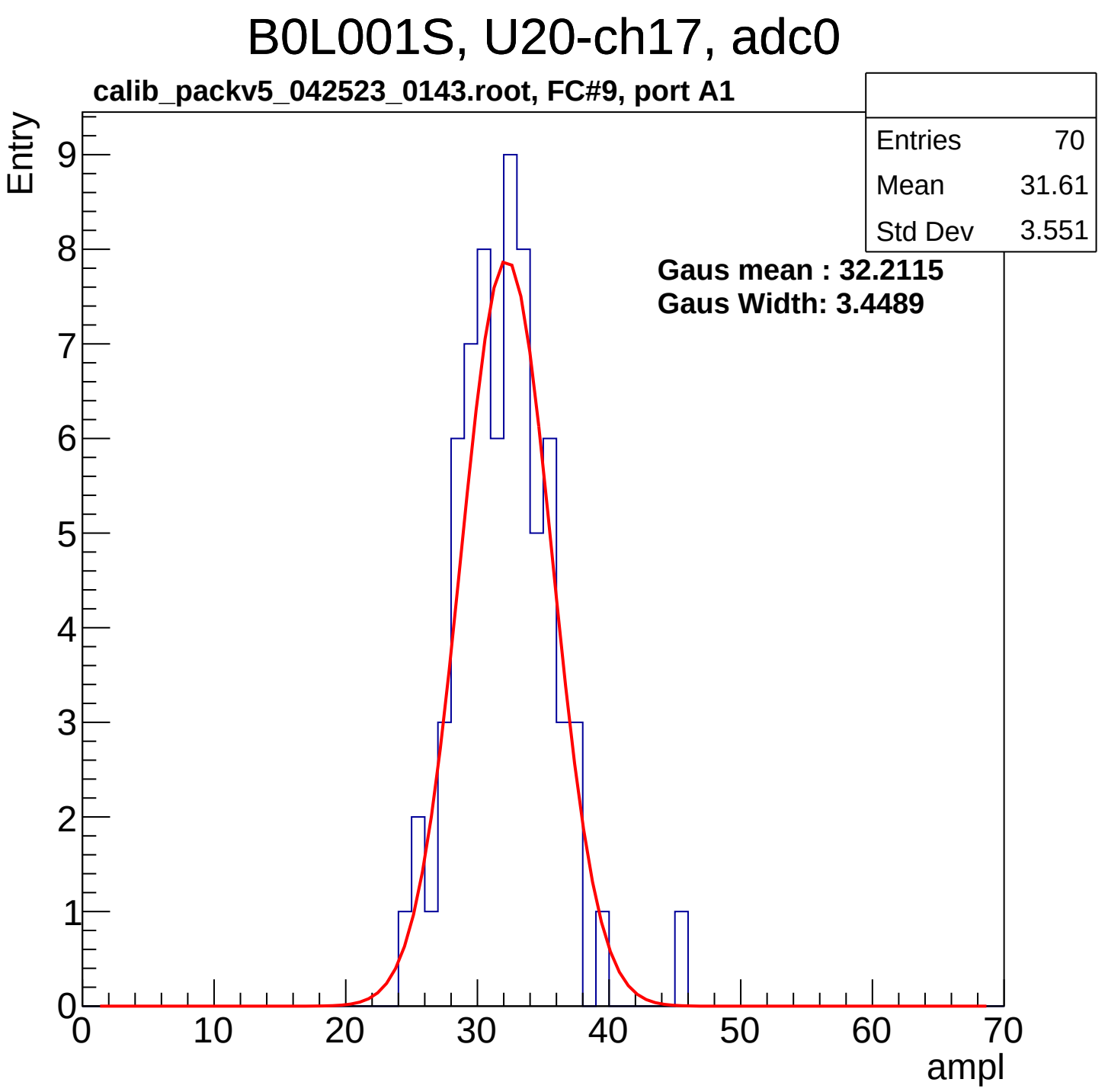
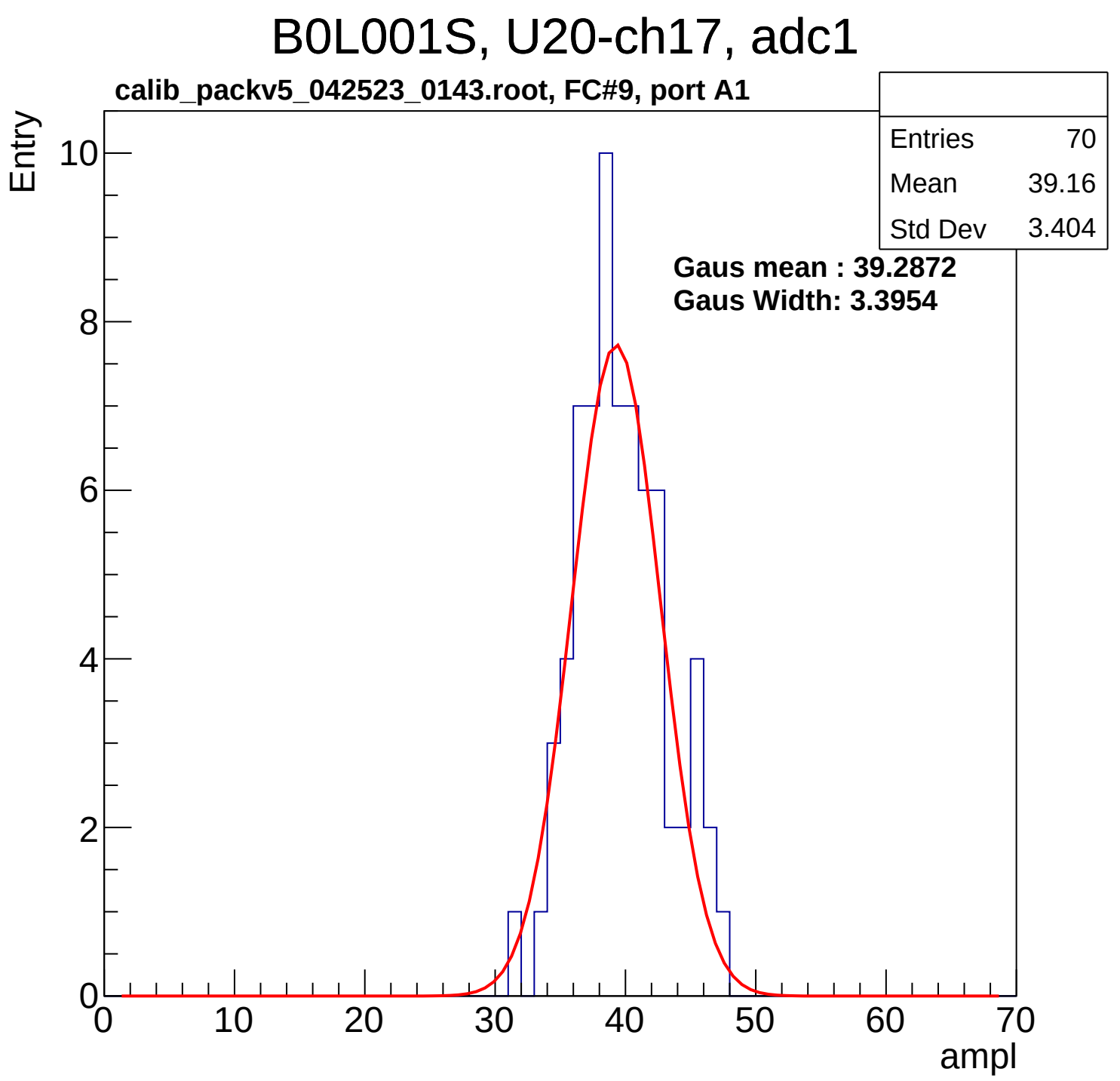

B0L001S, U20-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.07
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

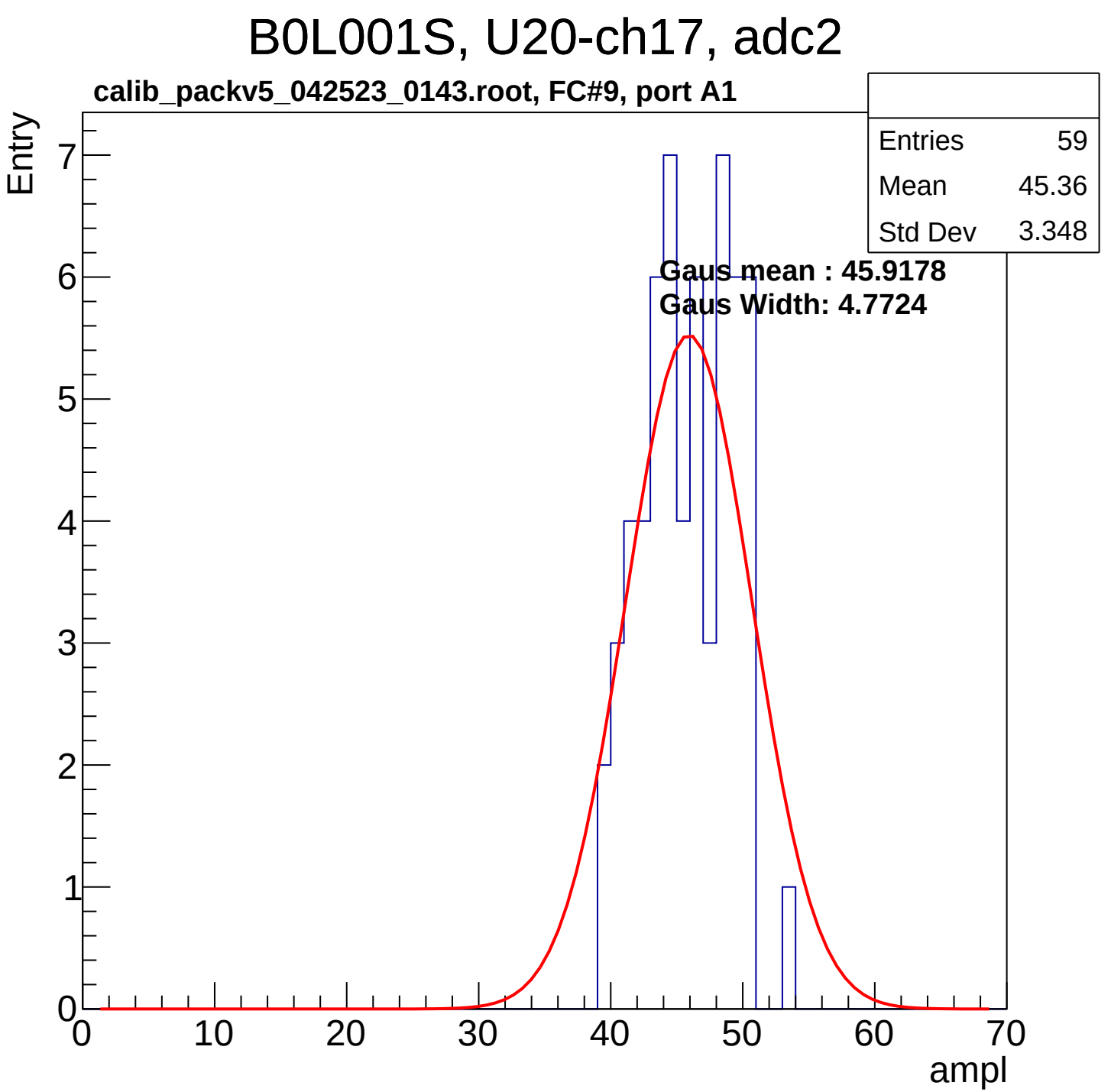

B0L001S, U20-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.76
Std Dev 12.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

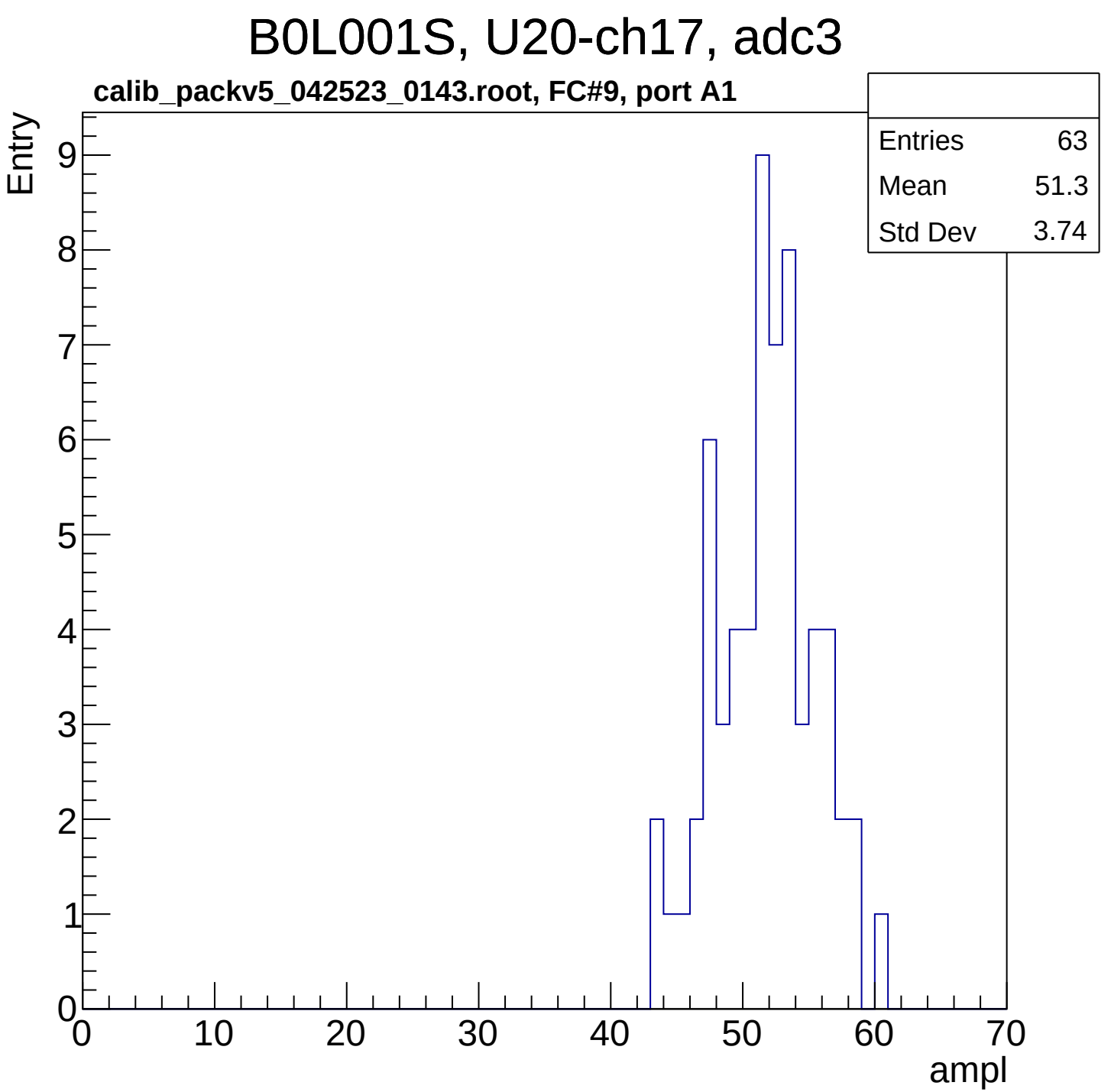

B0L001S, U20-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.61
Std Dev 3.551
Gaus mean : 32.2115
Gaus Width: 3.4489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 39.16
Std Dev 3.404
Gaus mean : 39.2872
Gaus Width: 3.3954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.36
Std Dev 3.348
Gaus mean : 45.9178
Gaus Width: 4.7724
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.3
Std Dev 3.74
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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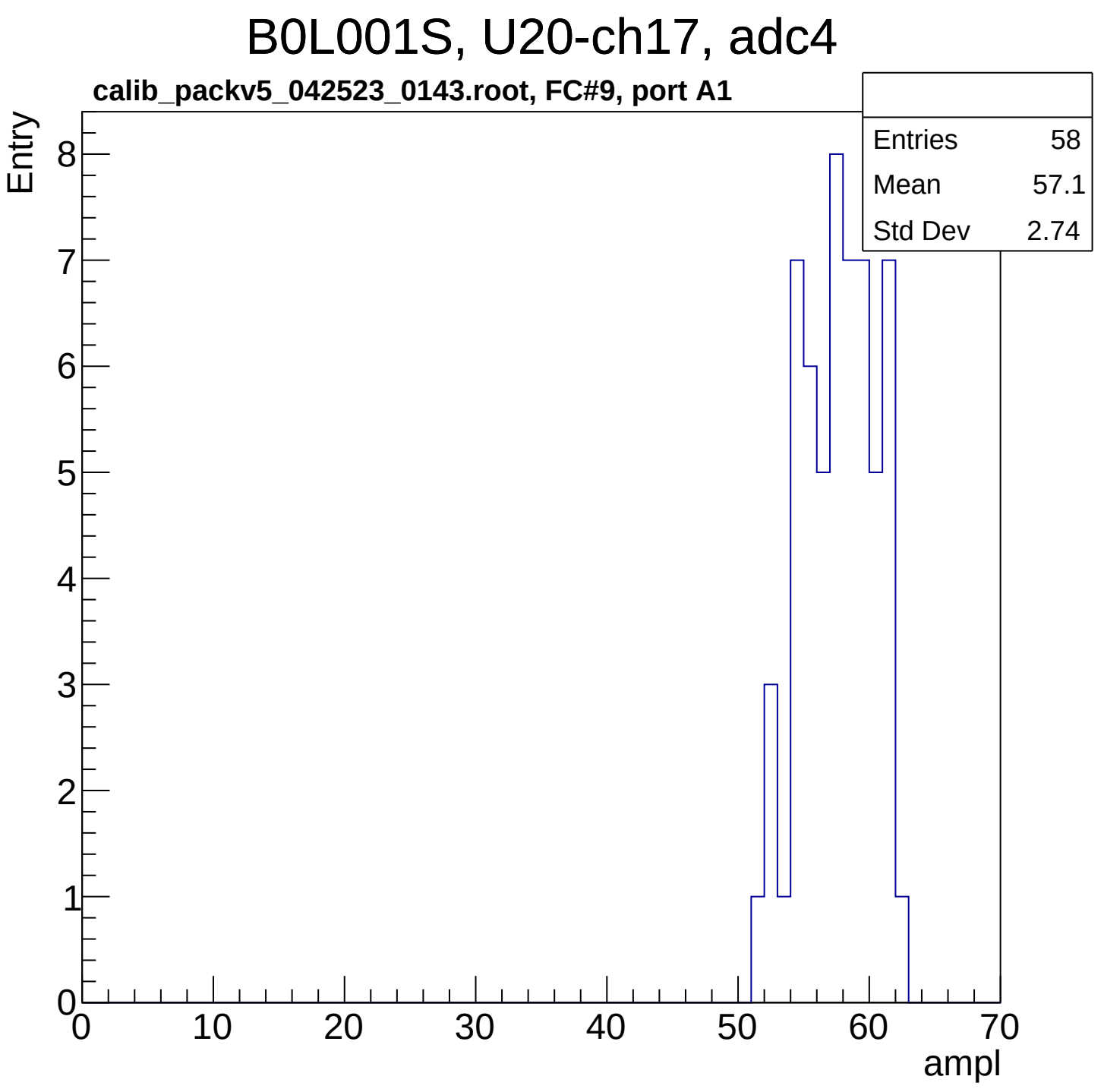
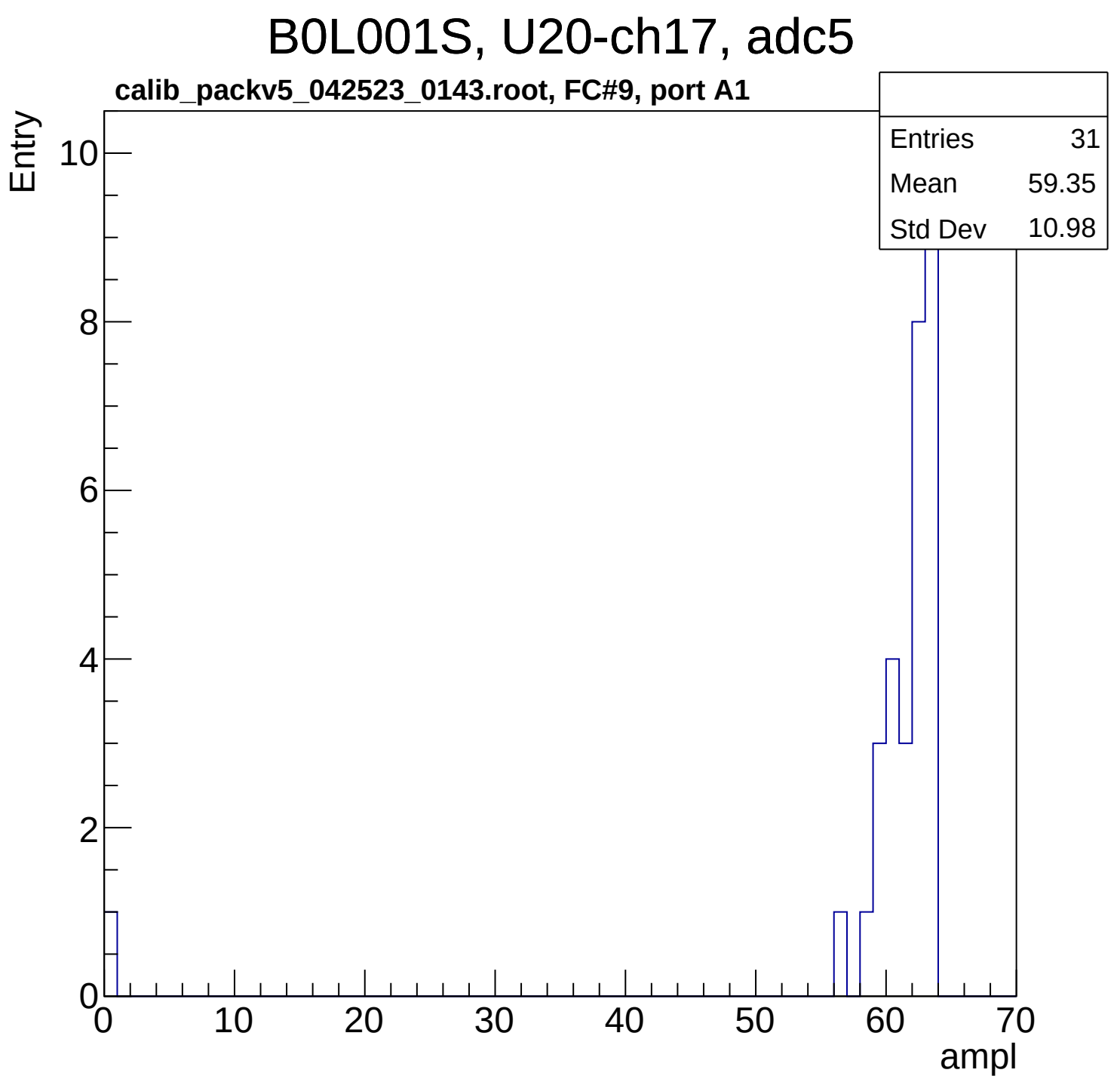
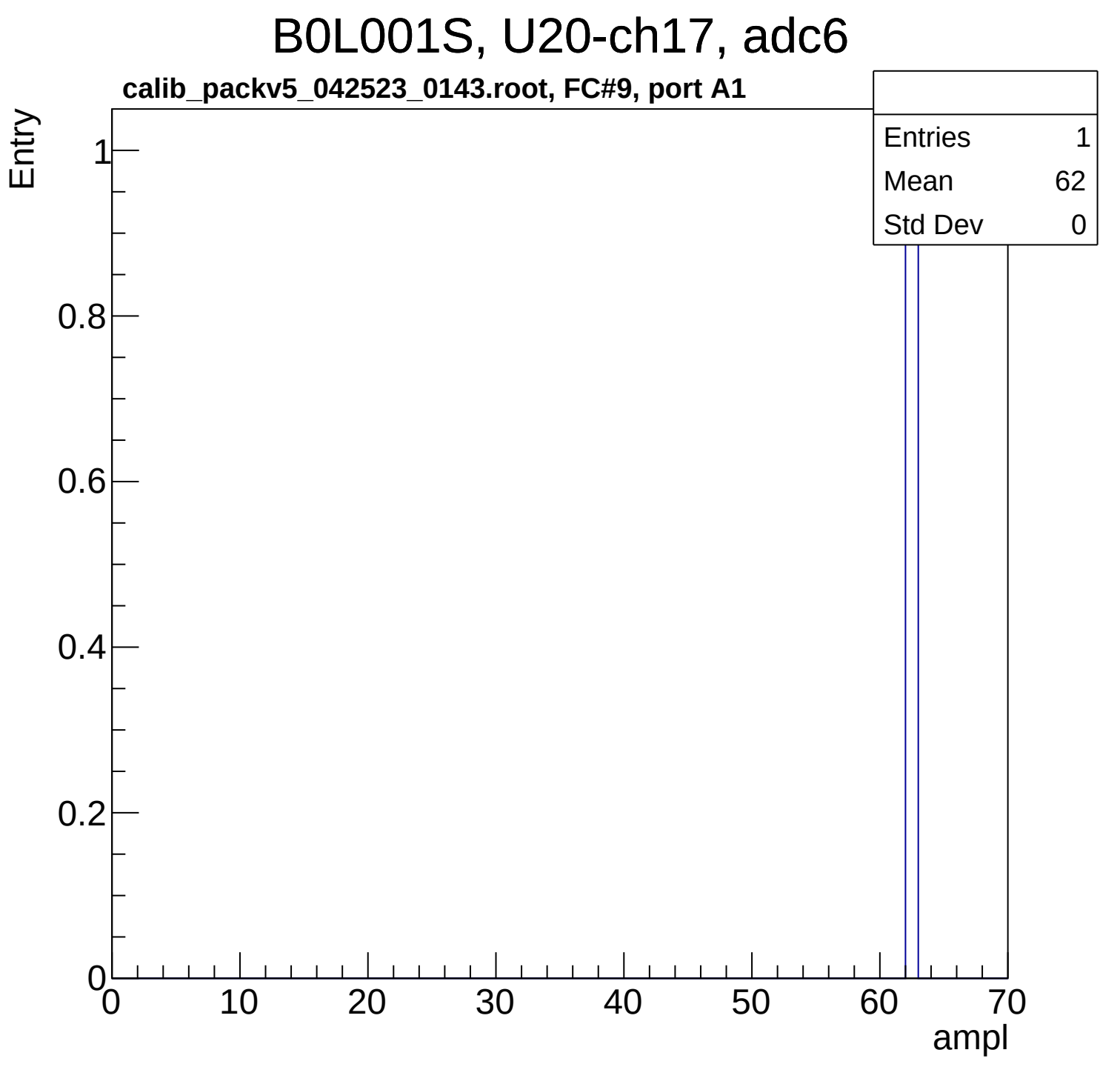

B0L001S, U20-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.1
Std Dev 2.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.35
Std Dev 10.98
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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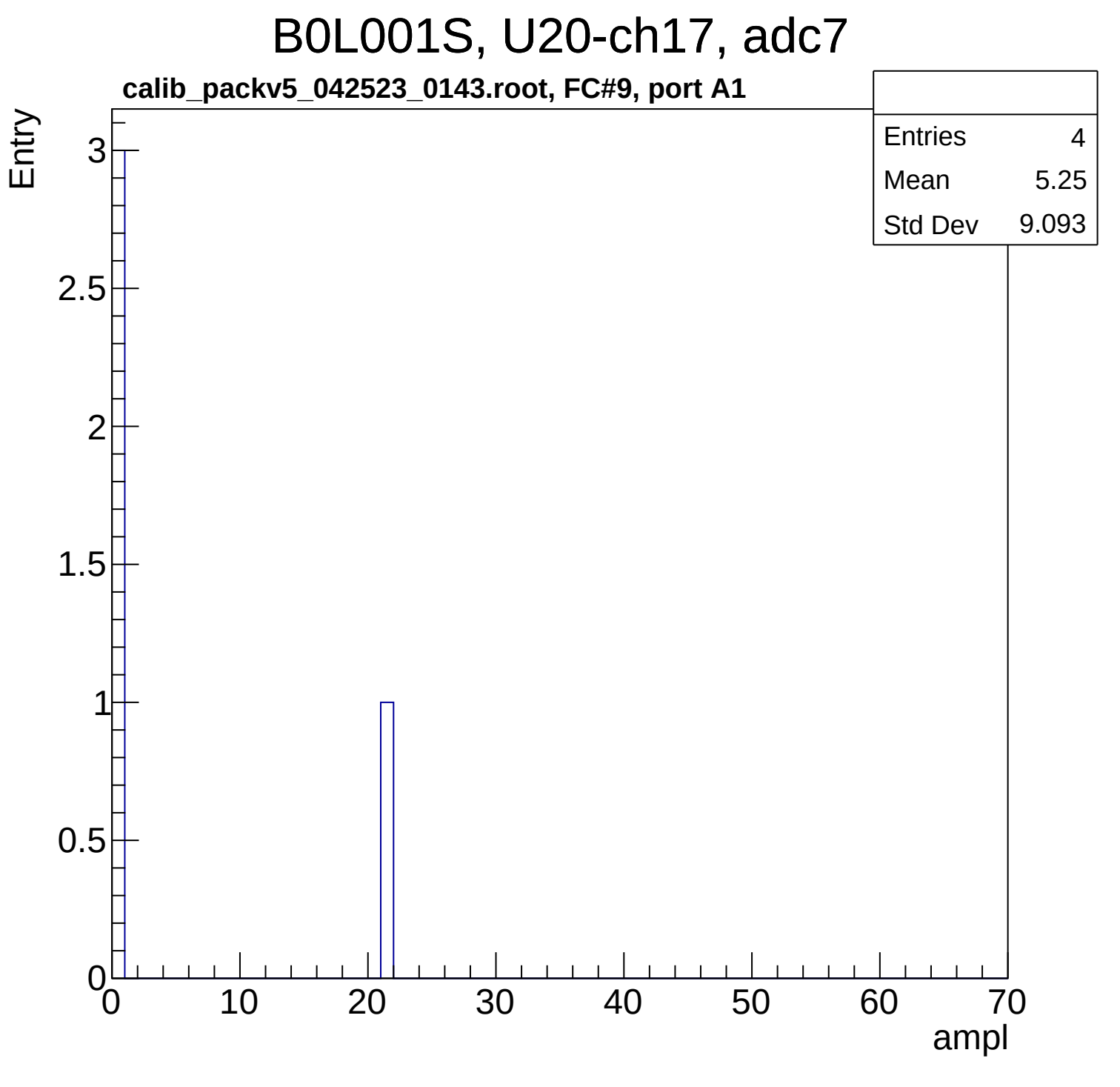
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 5.25
Std Dev 9.093
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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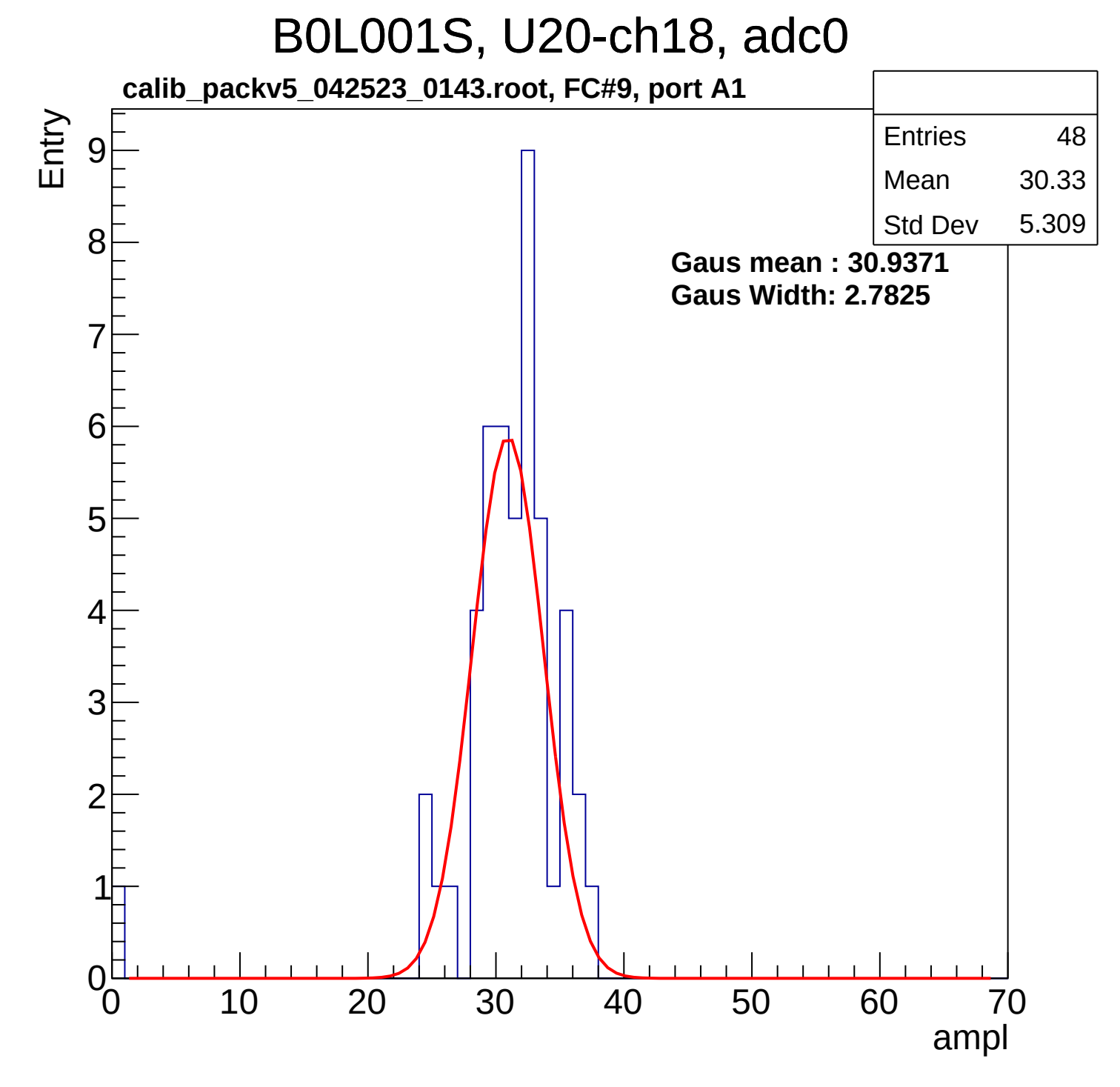
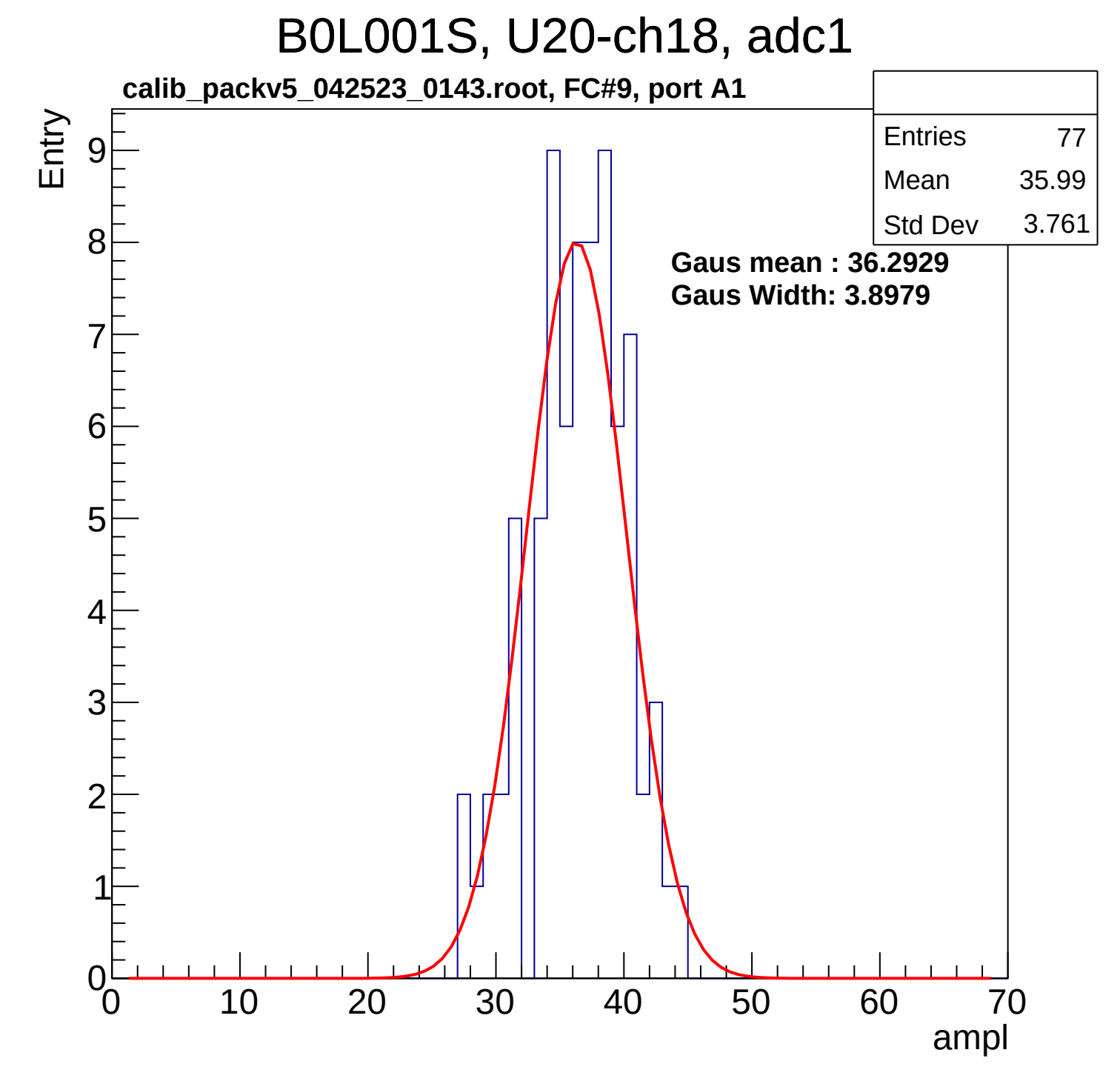
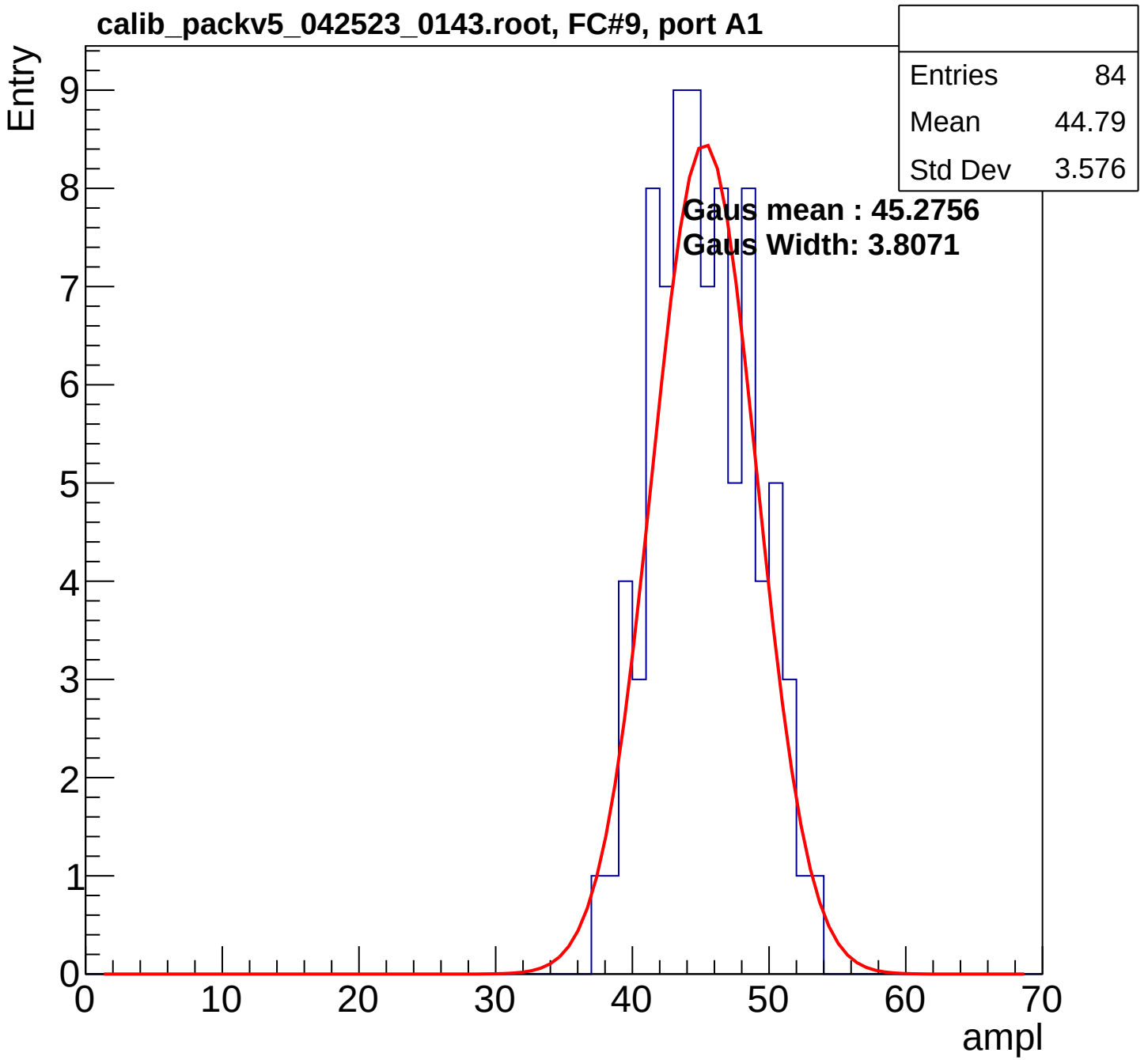
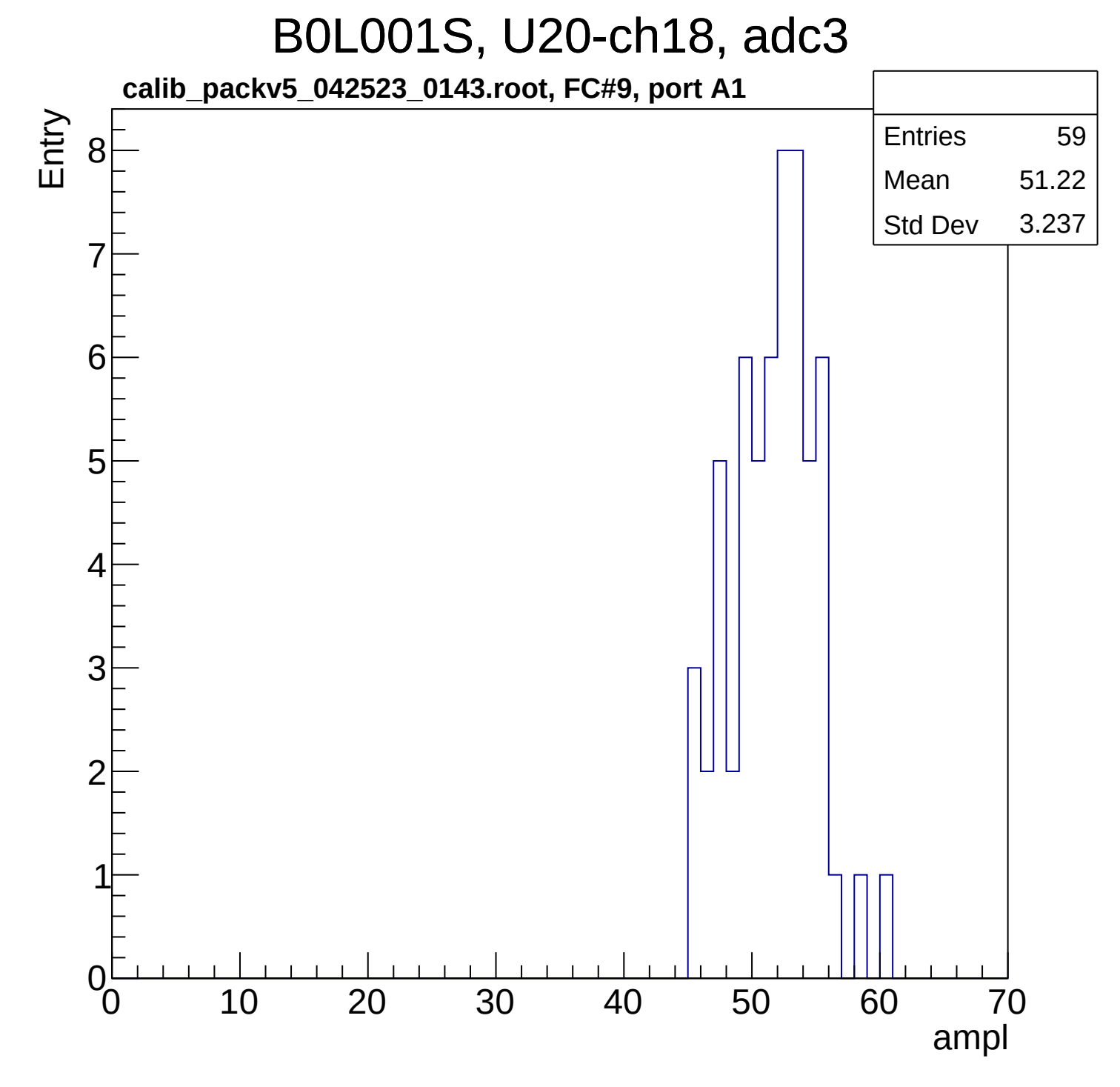
40
50
60
70


B0L001S, U20-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 30.33
Std Dev 5.309
Gaus mean : 30.9371
Gaus Width: 2.7825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 35.99
Std Dev 3.761
Gaus mean : 36.2929
Gaus Width: 3.8979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 44.79
Std Dev 3.576
Gaus mean : 45.2756
Gaus Width: 3.8071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.22
Std Dev 3.237
Entry
ampl
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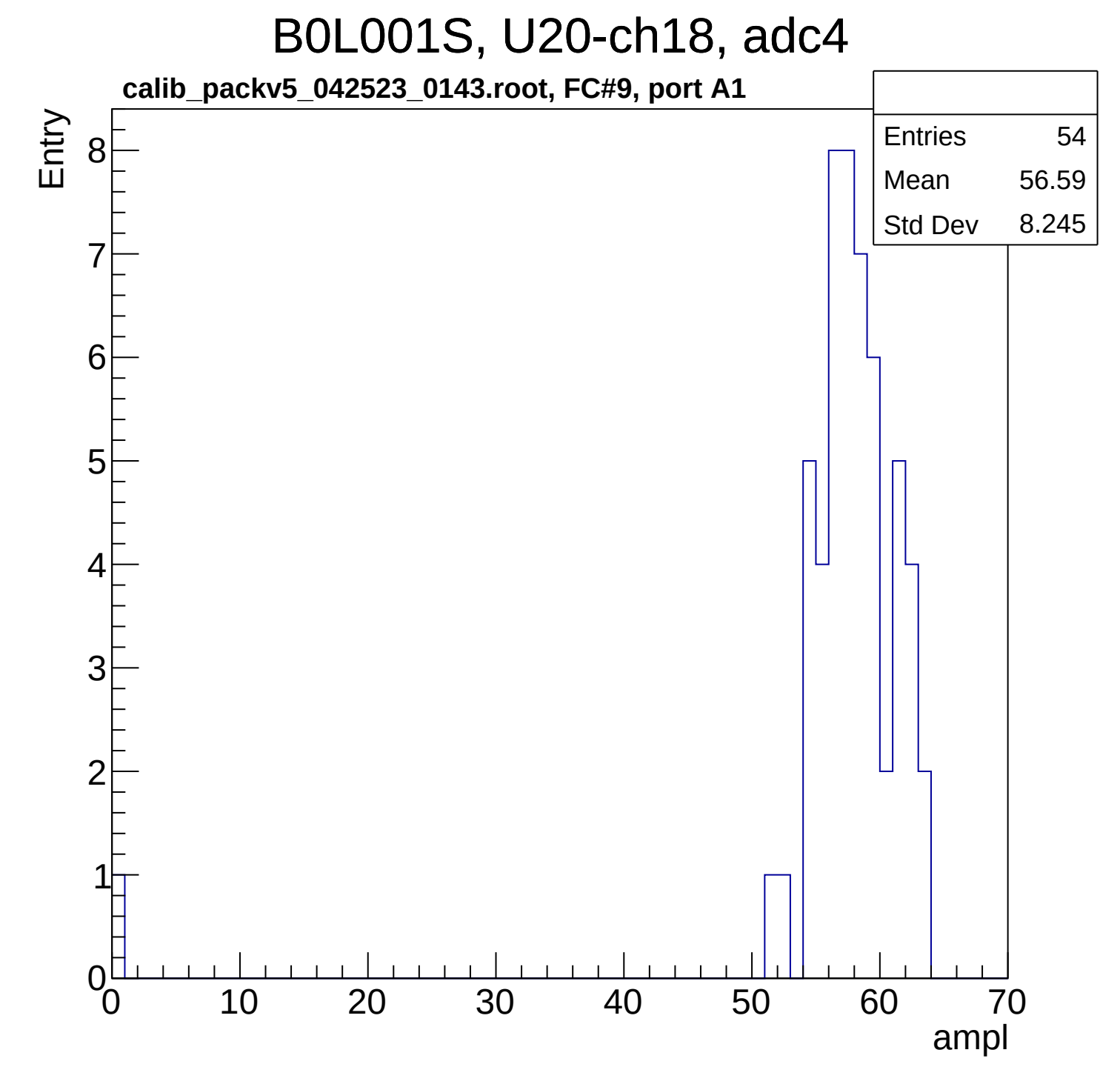
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.59
Std Dev 8.245
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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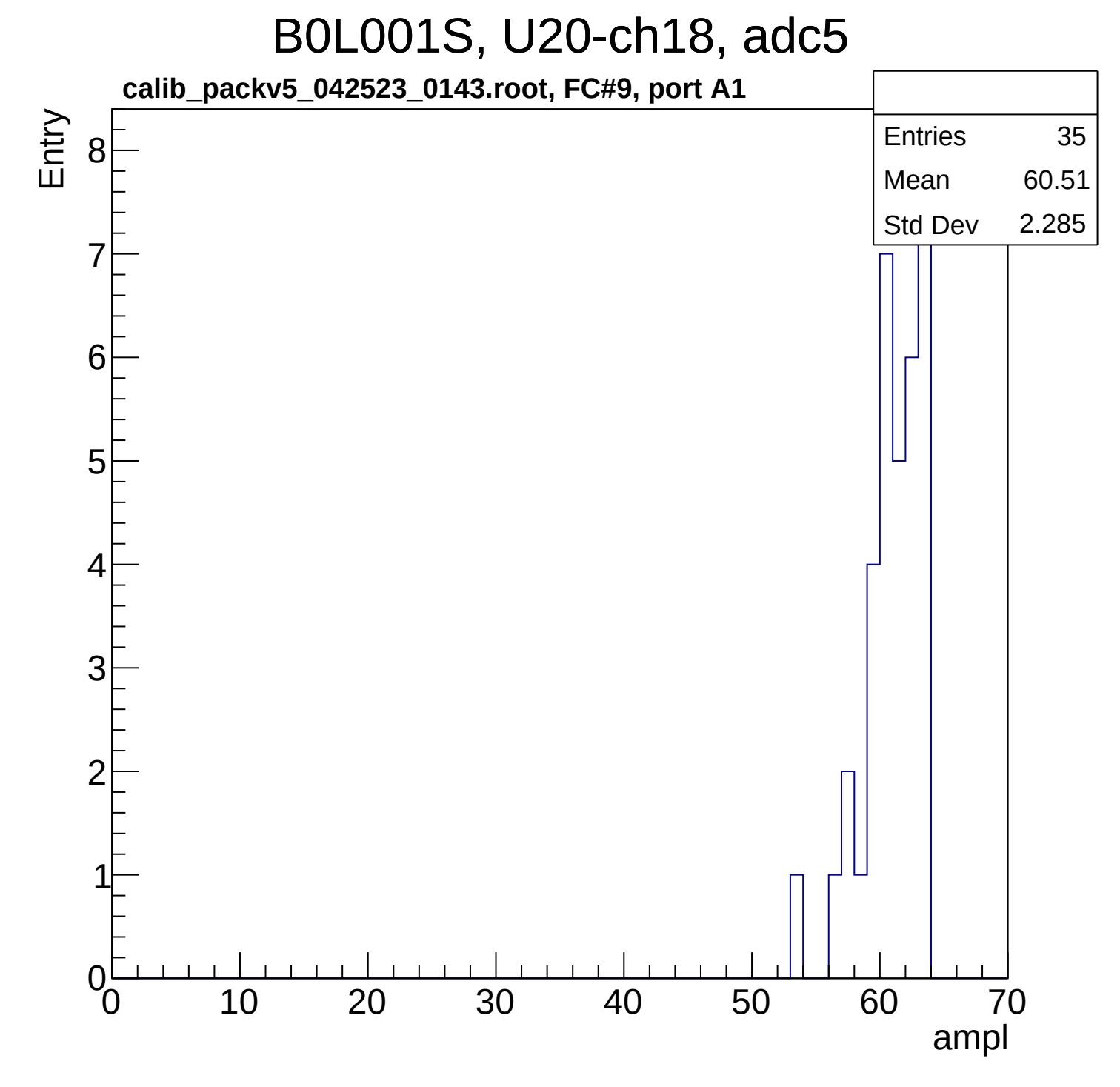
B0L001S, U20-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.51
Std Dev 2.285
Entry
ampl
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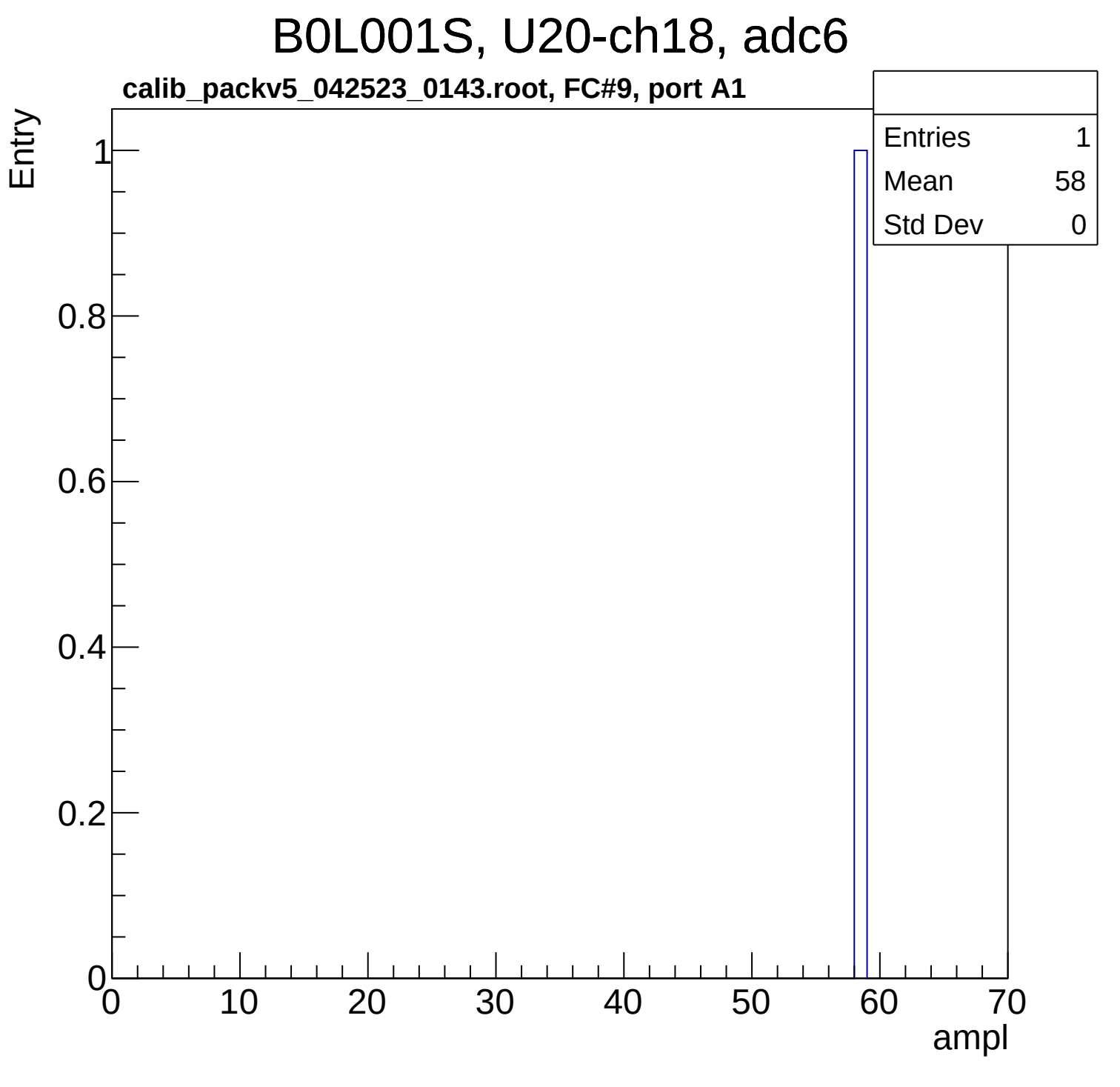
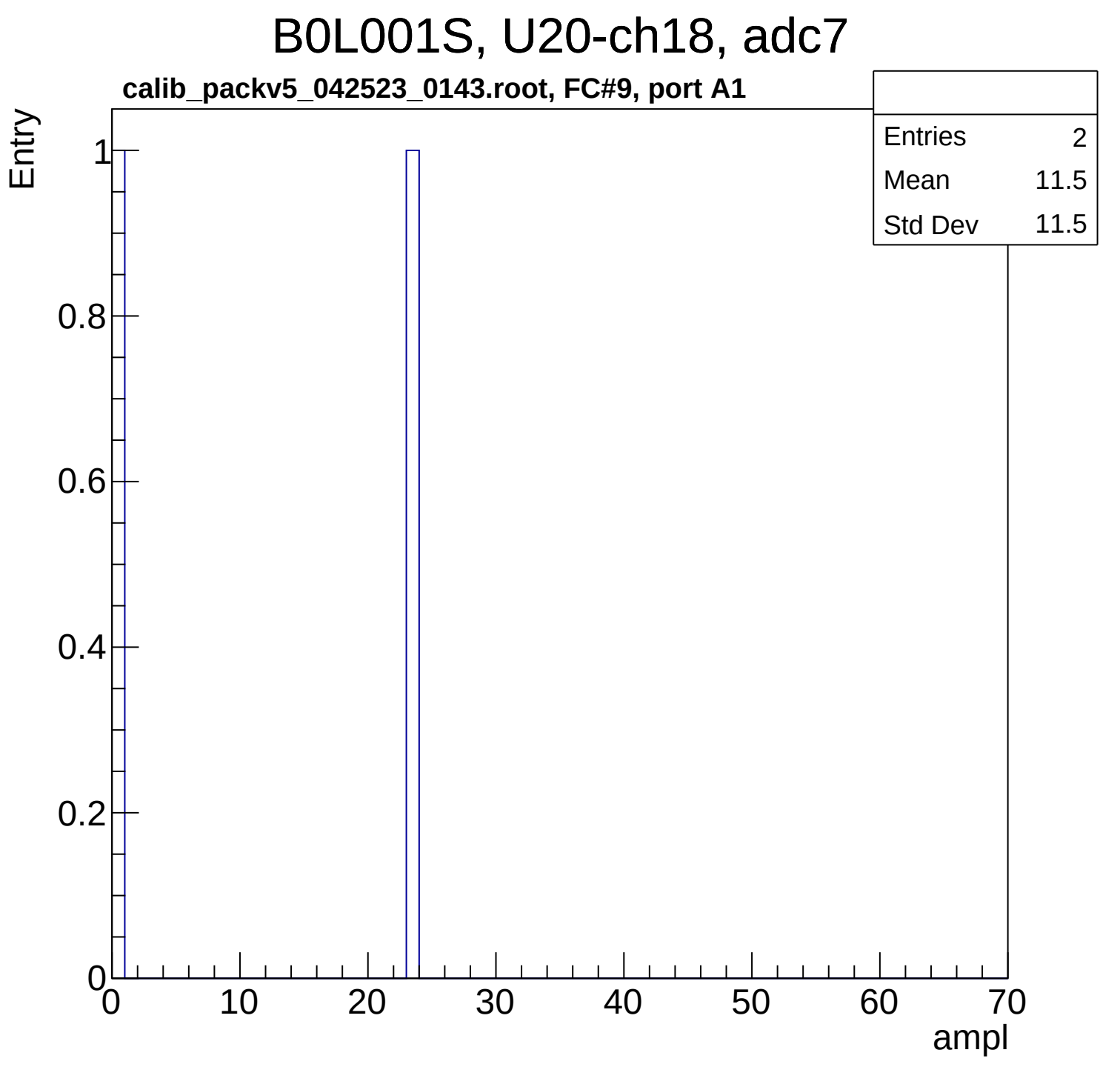
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 58
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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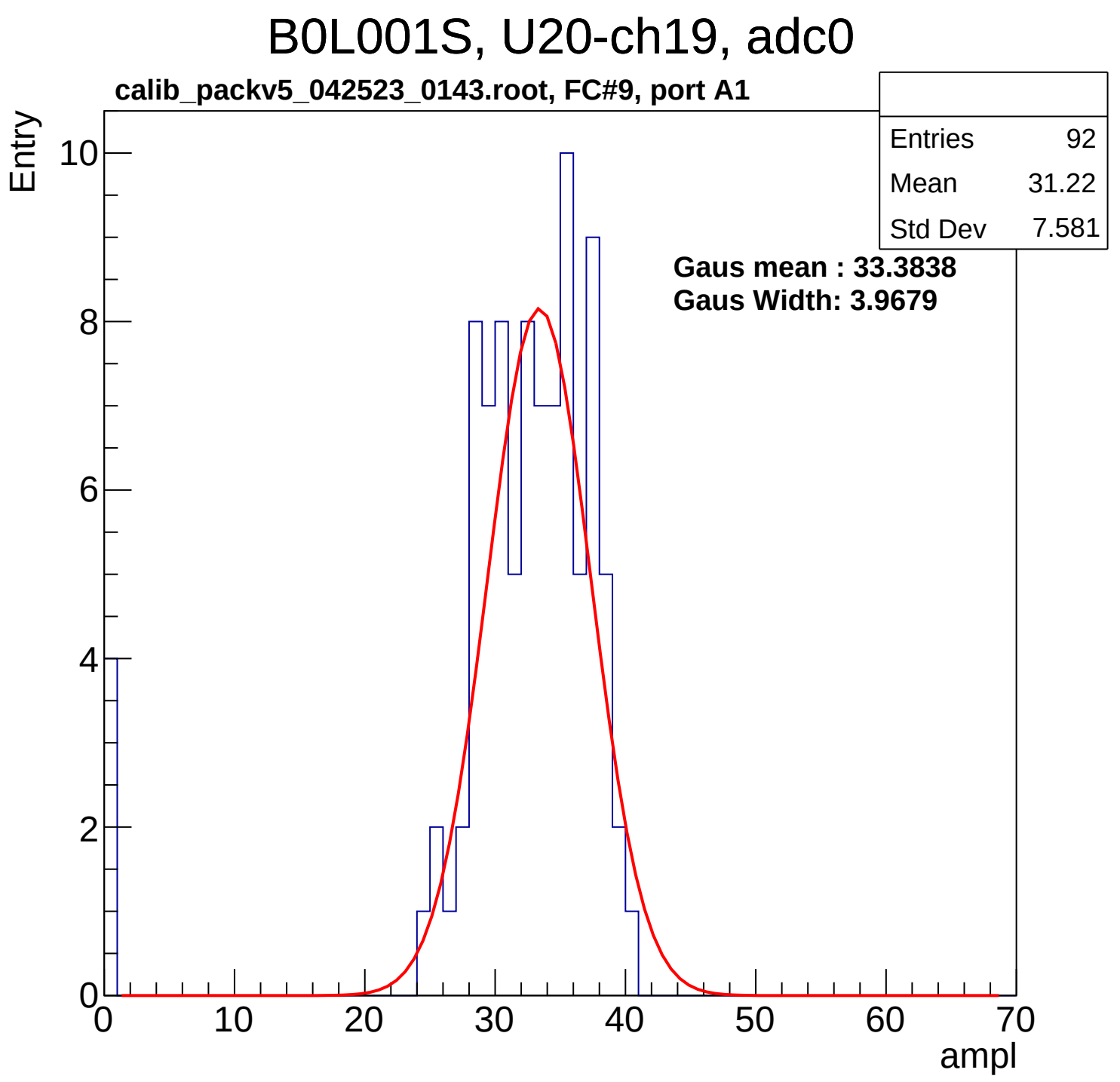
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 92
Mean 31.22
Std Dev 7.581
Gaus mean : 33.3838
Gaus Width: 3.9679
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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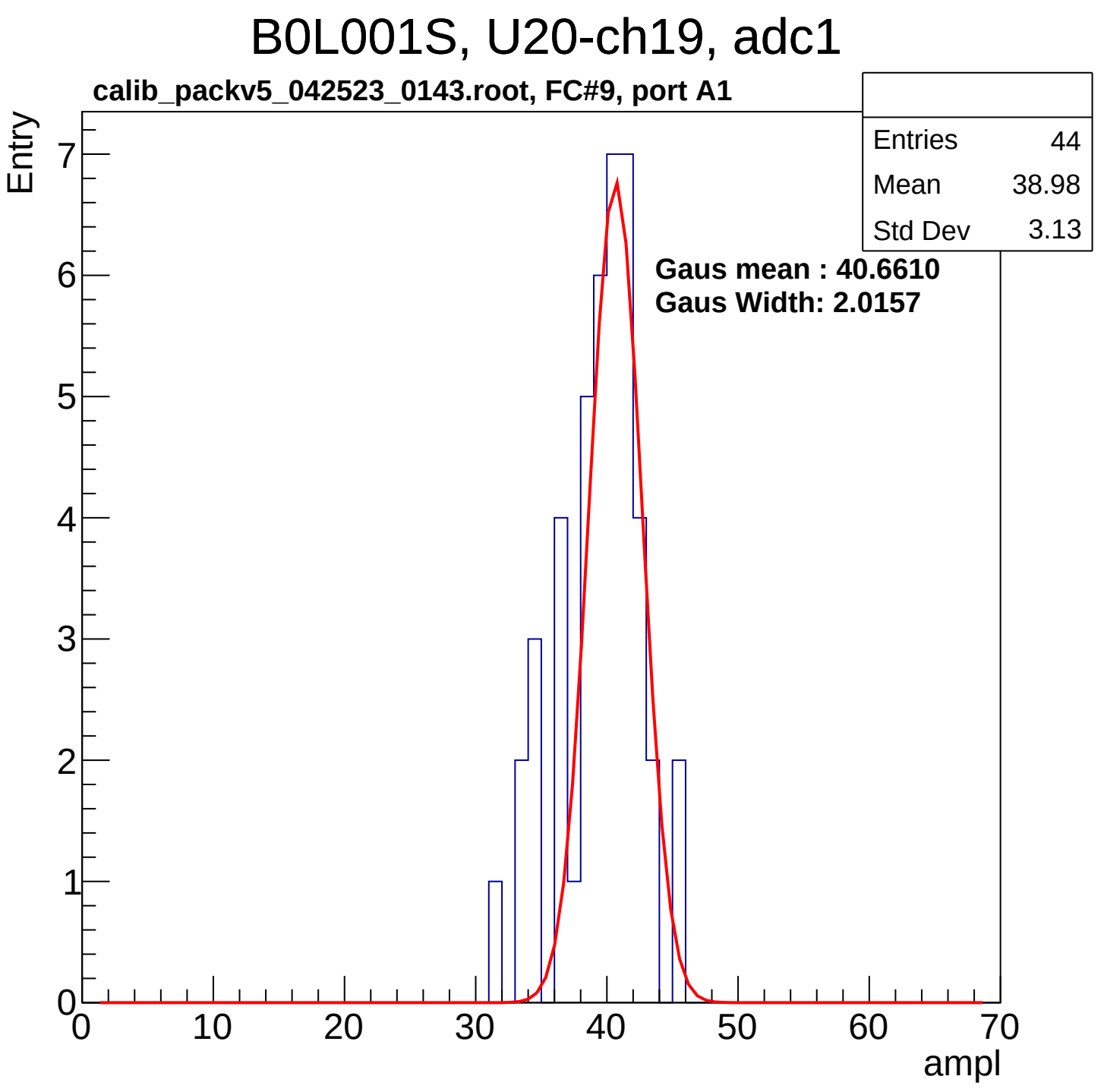
B0L001S, U20-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 38.98
Std Dev 3.13
Gaus mean : 40.6610
Gaus Width: 2.0157
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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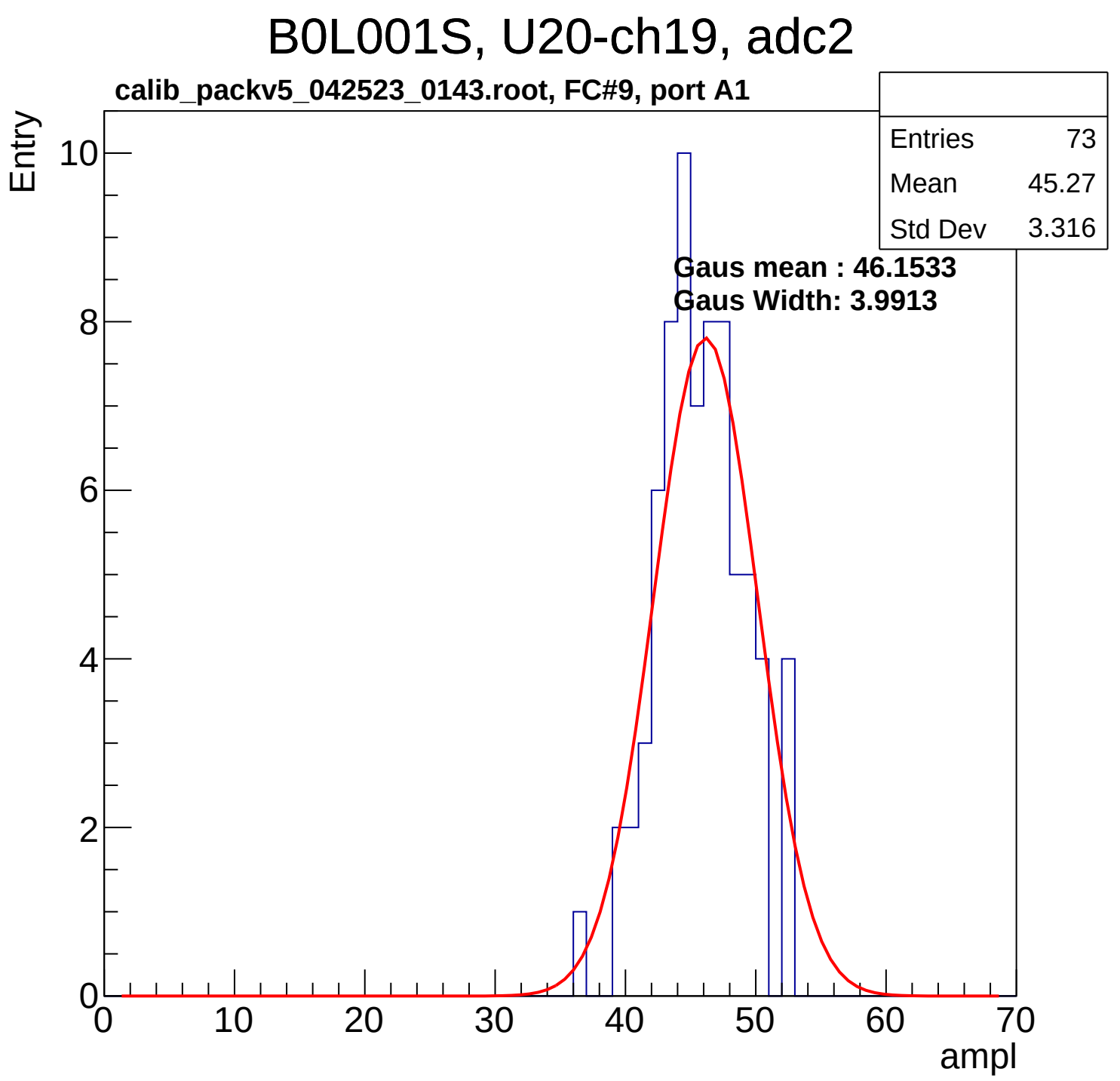
B0L001S, U20-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.27
Std Dev 3.316
Gaus mean : 46.1533
Gaus Width: 3.9913
Entry
ampl
0
2
4
6
8
10
0
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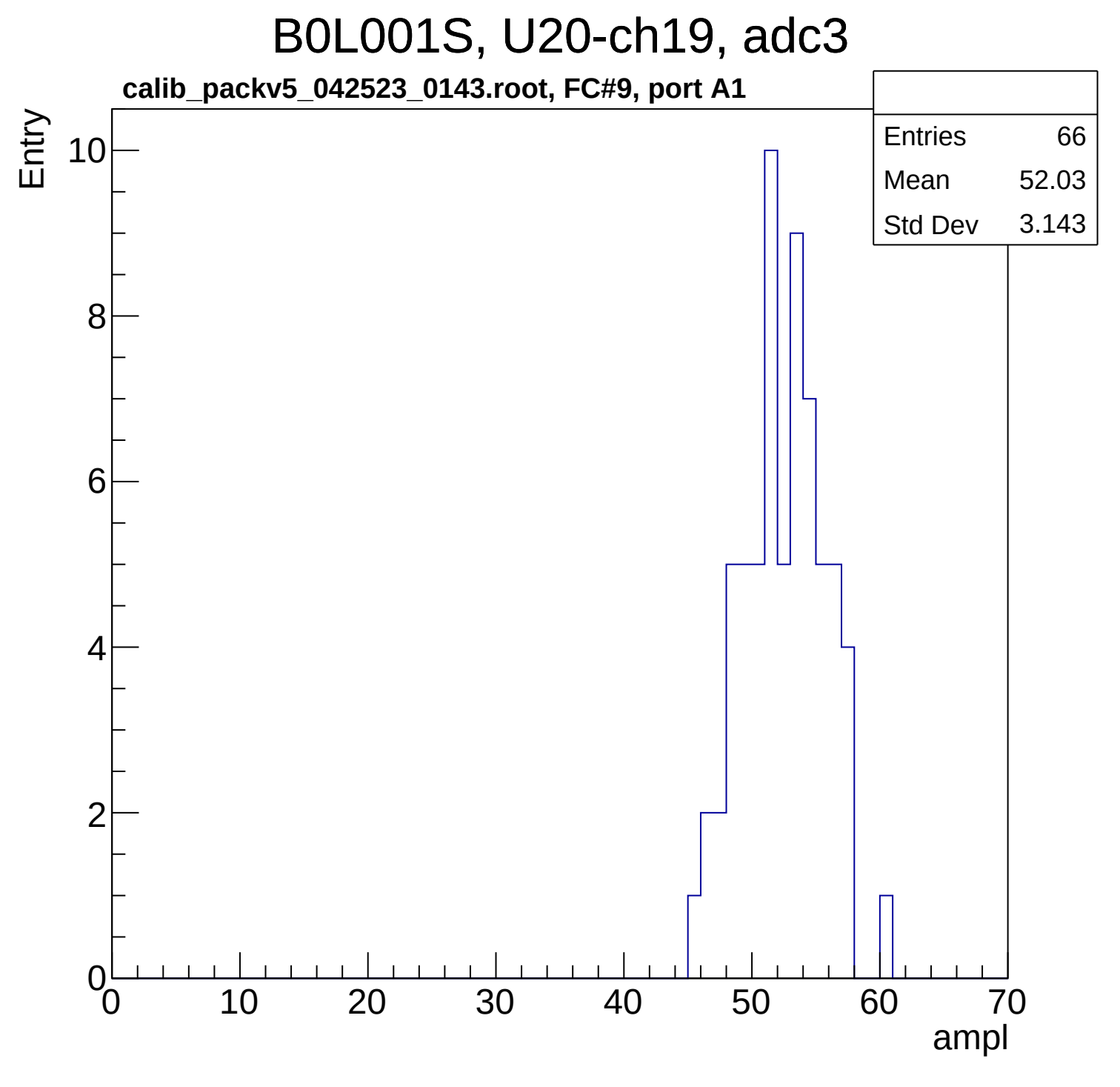
B0L001S, U20-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.03
Std Dev 3.143
Entry
ampl
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2
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0
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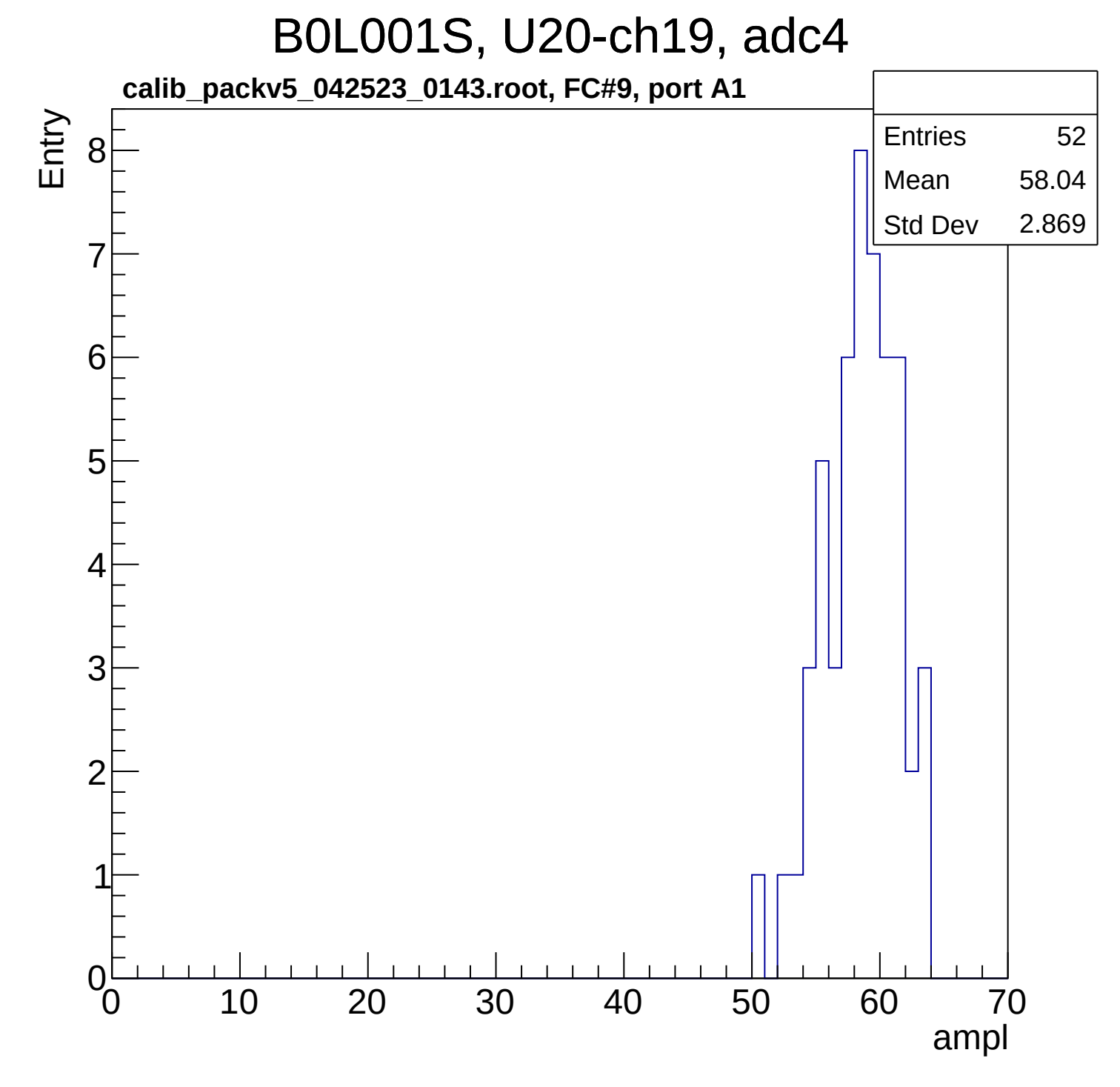
B0L001S, U20-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.04
Std Dev 2.869
Entry
ampl
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1
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0
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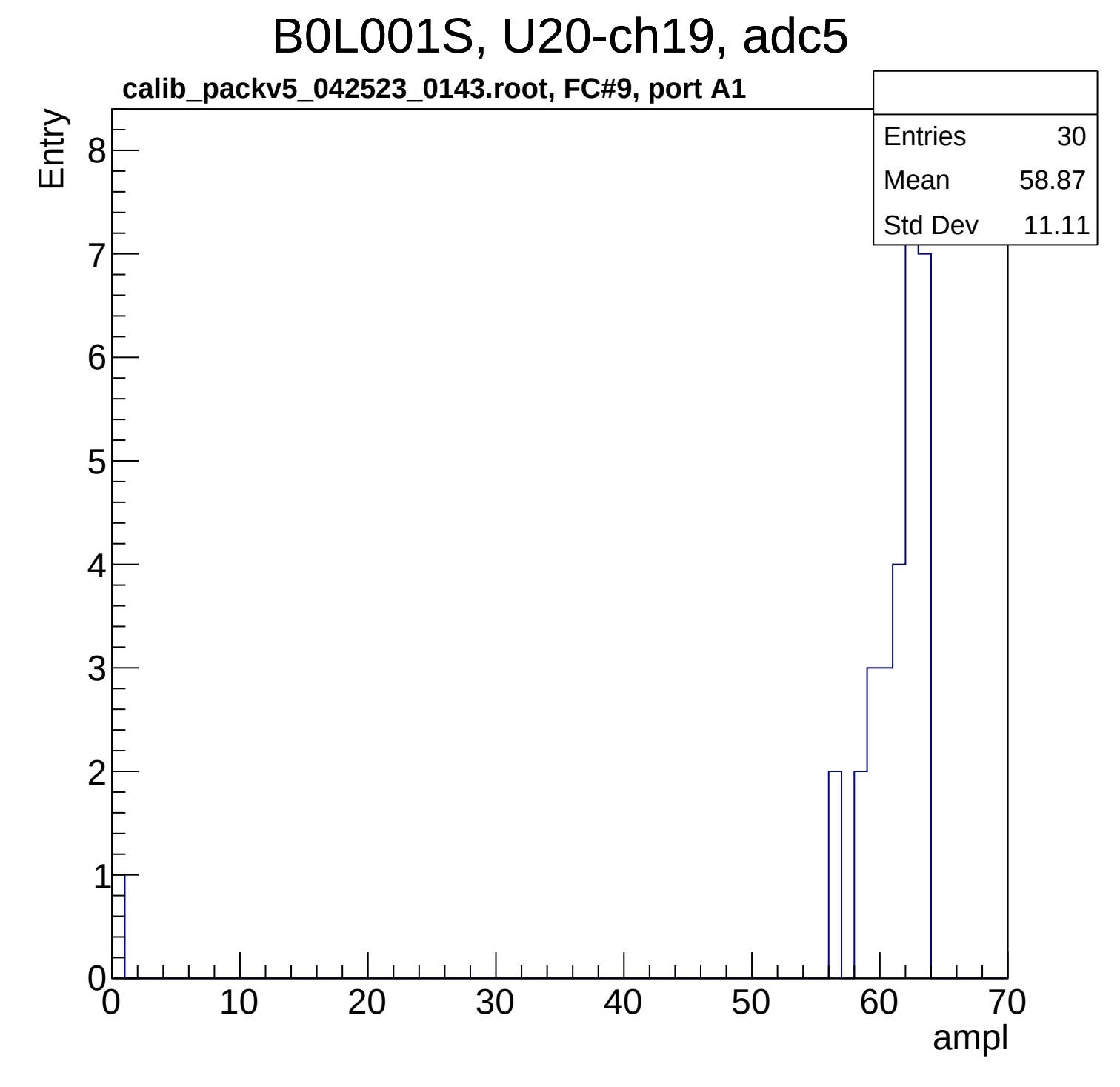
B0L001S, U20-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.87
Std Dev 11.11
Entry
ampl
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1
2
3
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0
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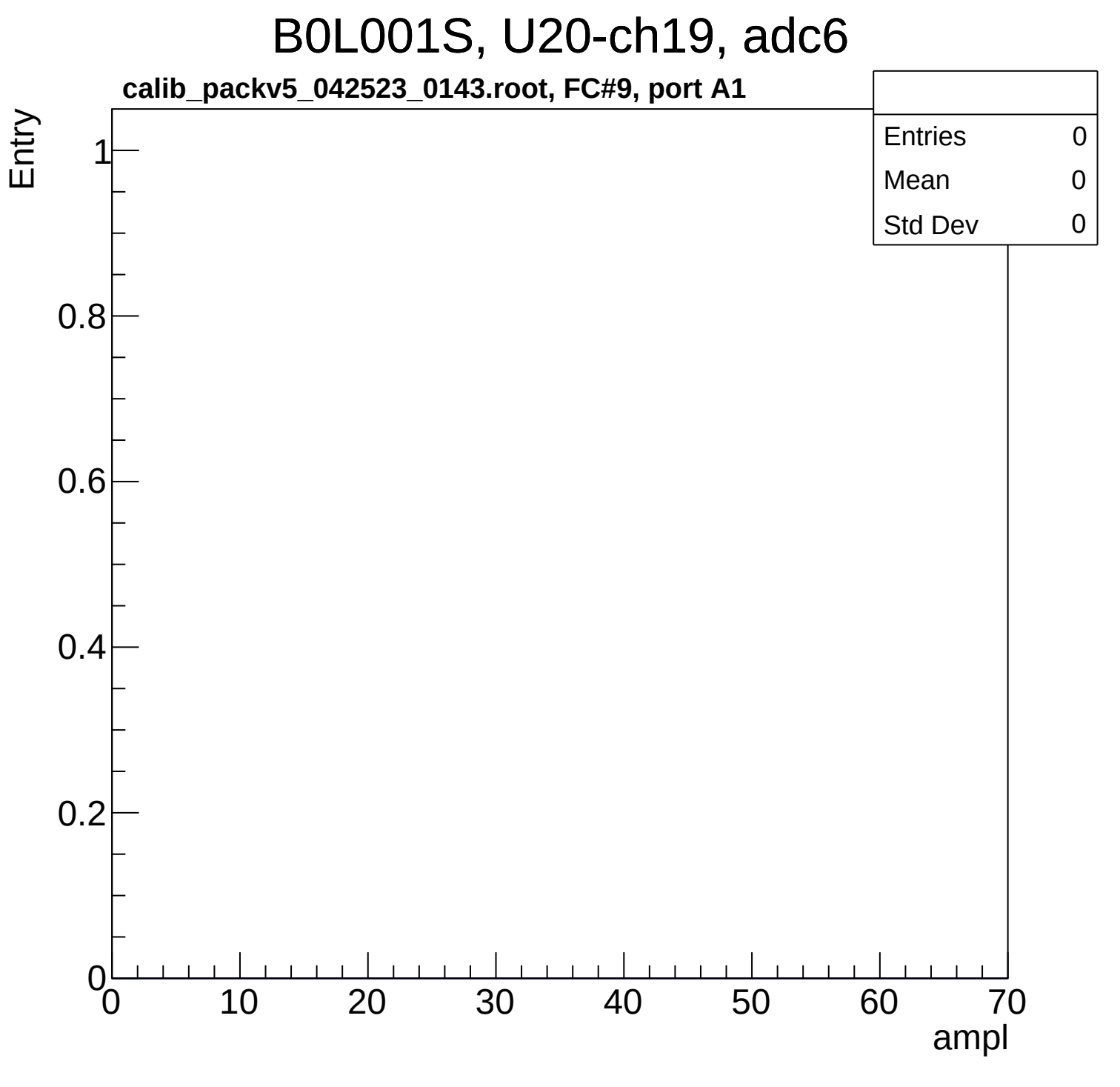
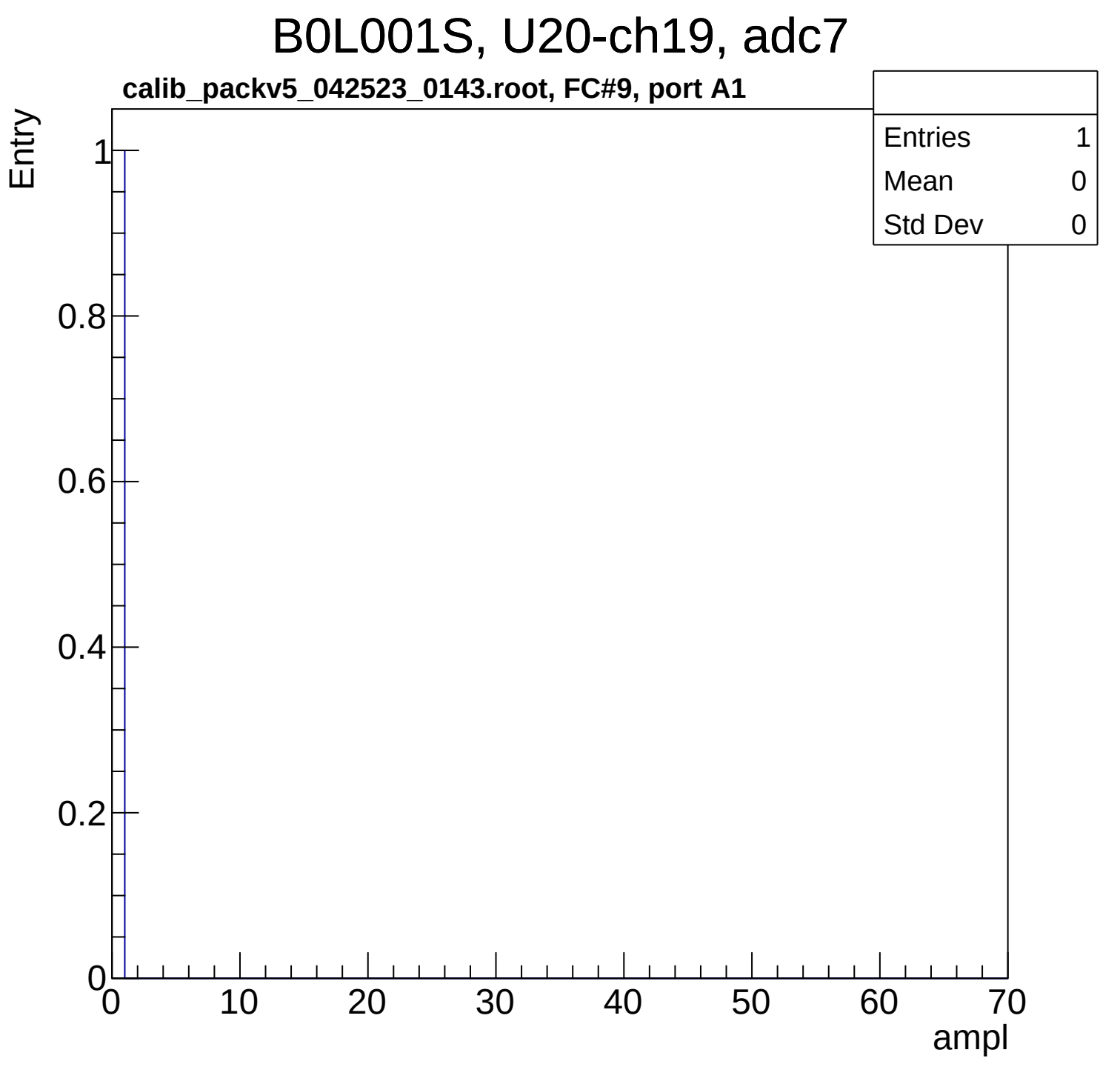
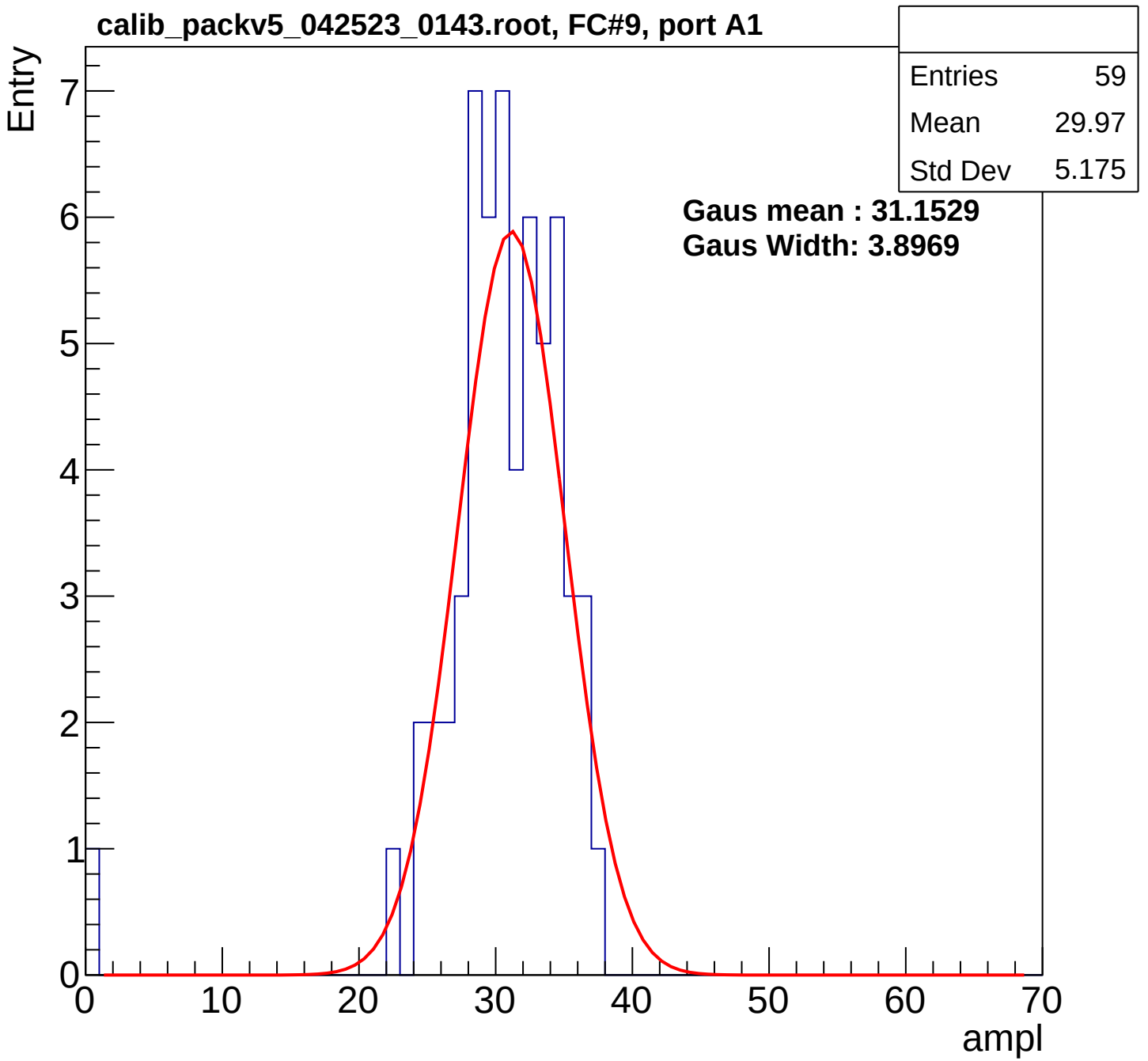

B0L001S, U20-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.97
Std Dev 5.175
Gaus mean : 31.1529
Gaus Width: 3.8969
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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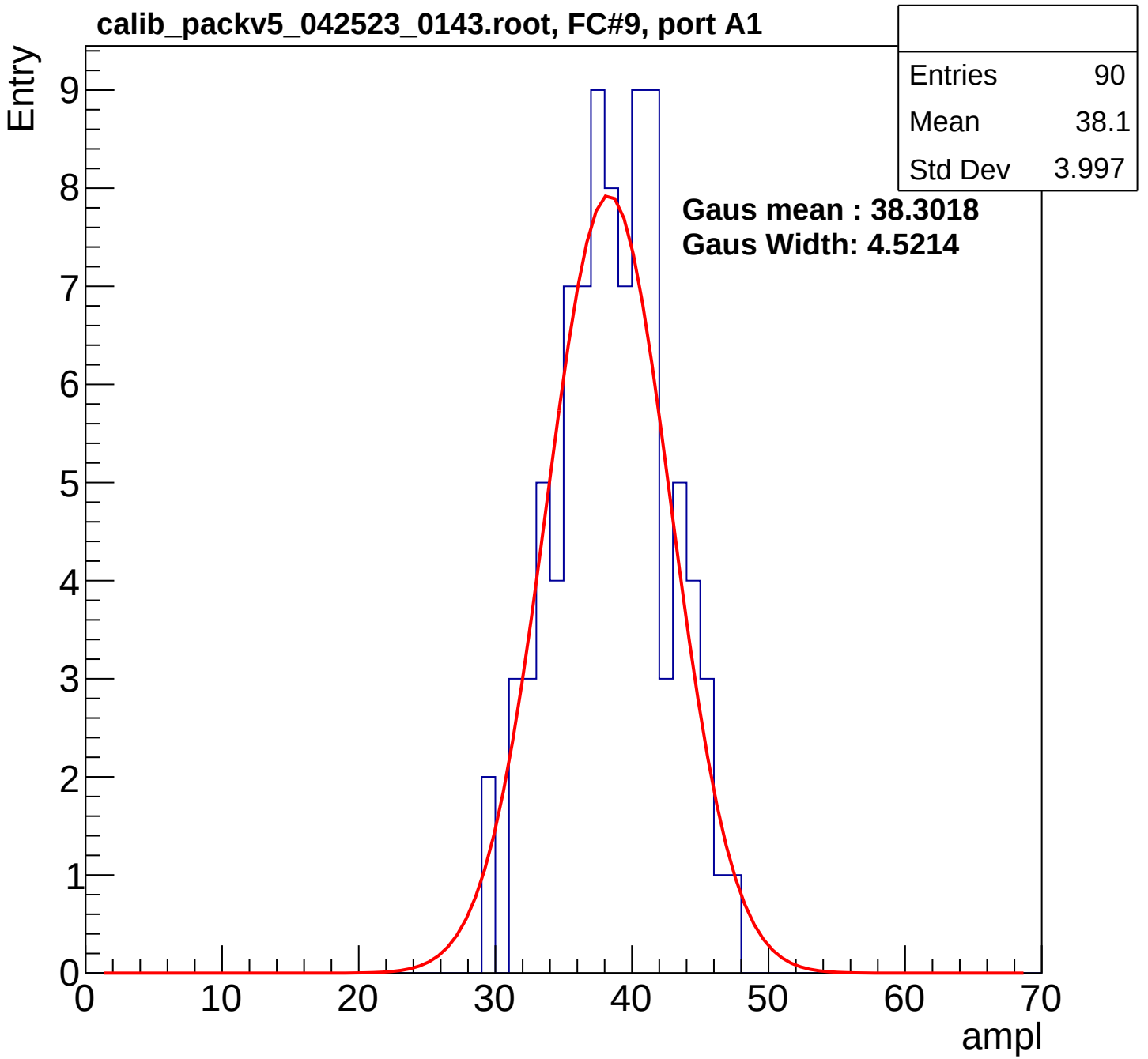
B0L001S, U20-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 38.1
Std Dev 3.997
Gaus mean : 38.3018
Gaus Width: 4.5214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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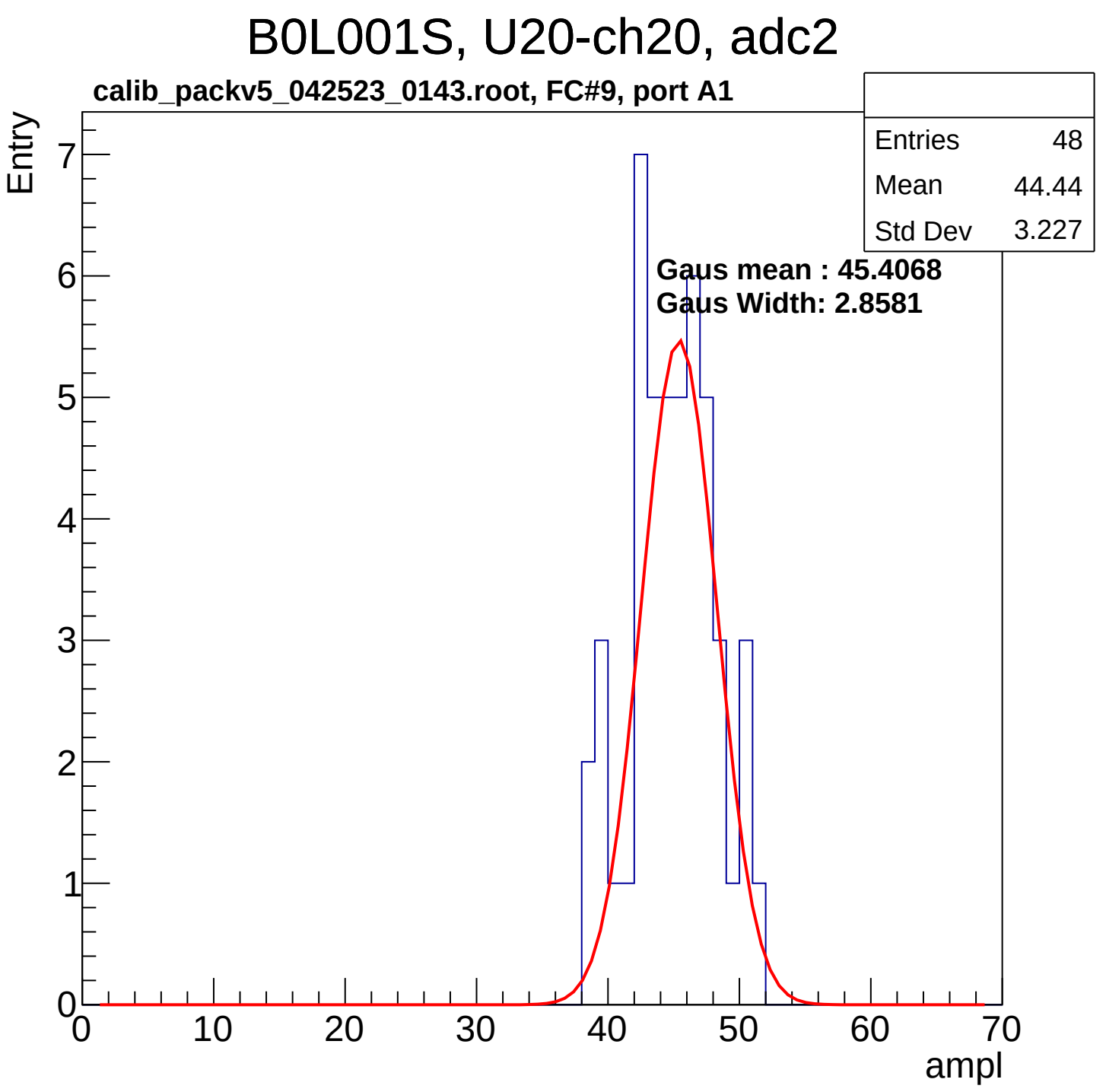
B0L001S, U20-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 44.44
Std Dev 3.227
Gaus mean : 45.4068
Gaus Width: 2.8581
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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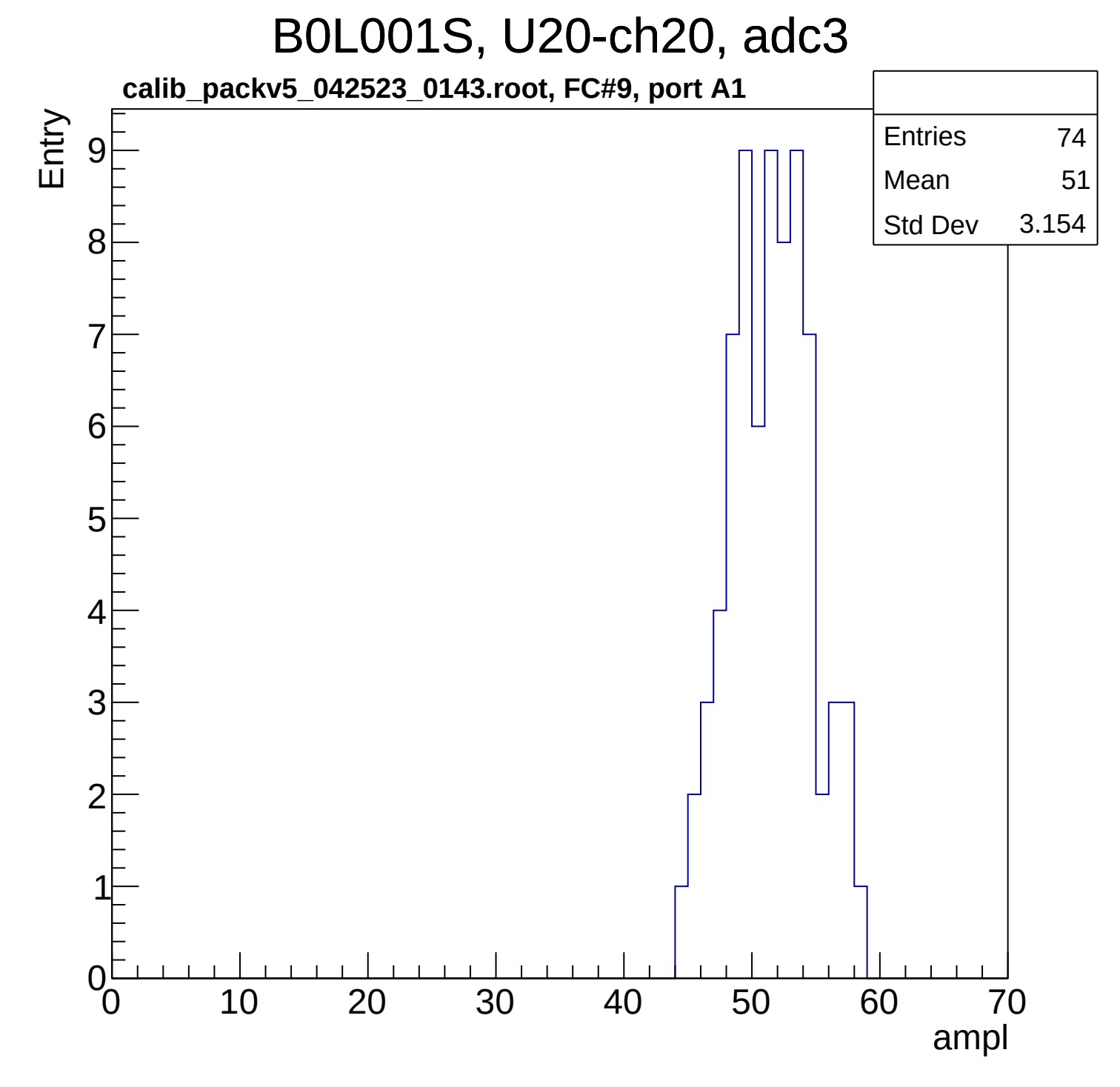
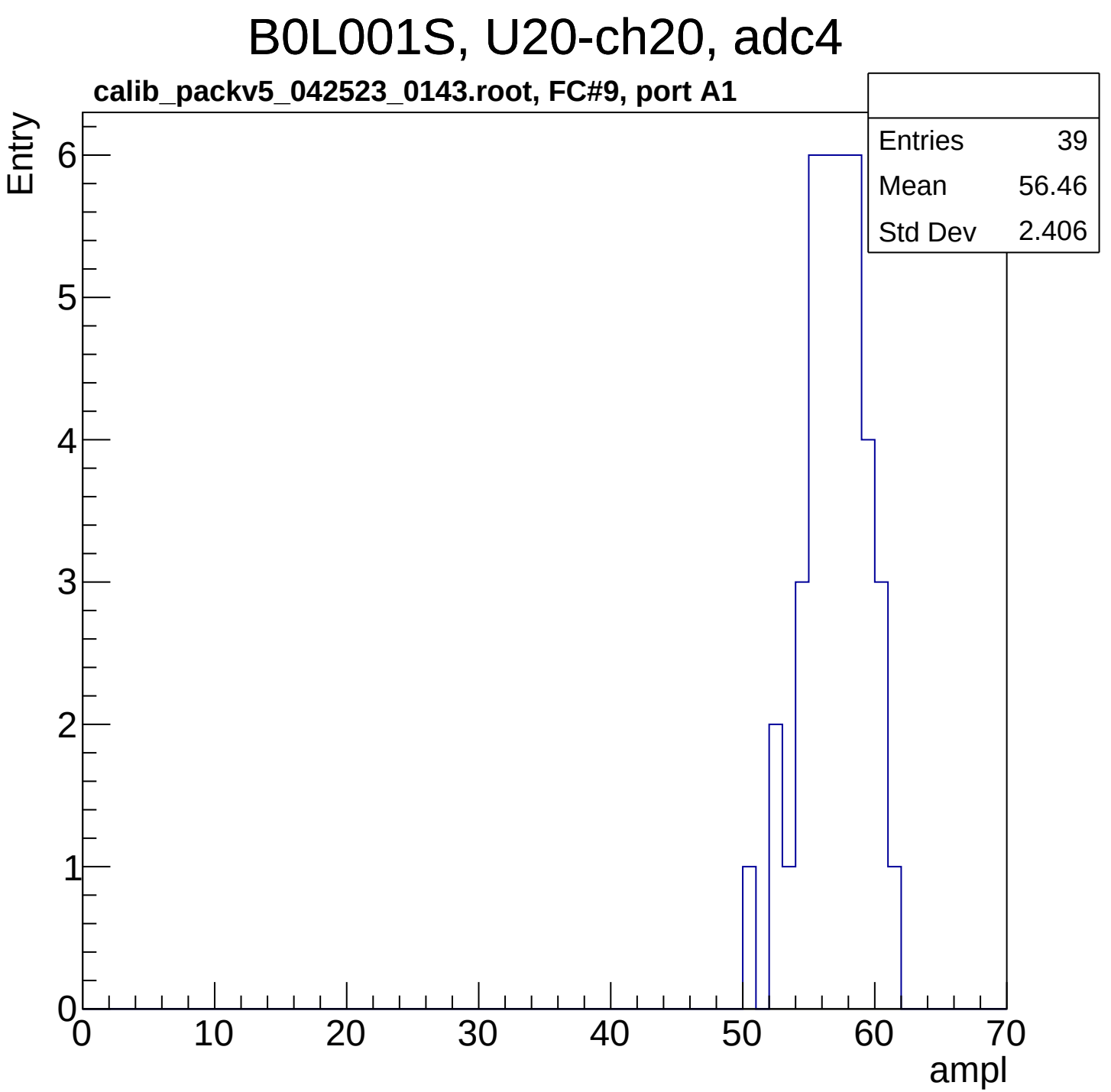

B0L001S, U20-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 56.46
Std Dev 2.406
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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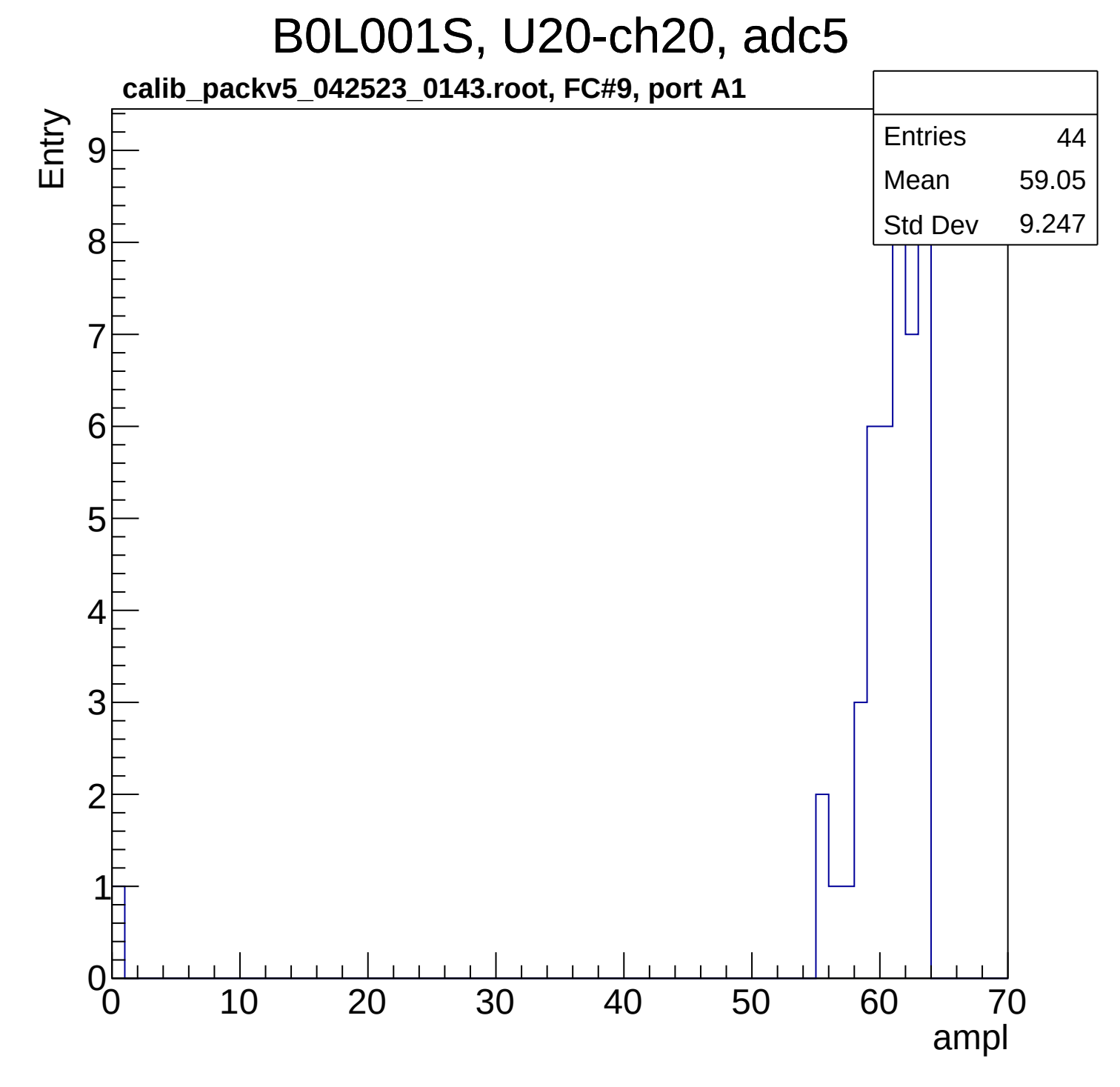
B0L001S, U20-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.05
Std Dev 9.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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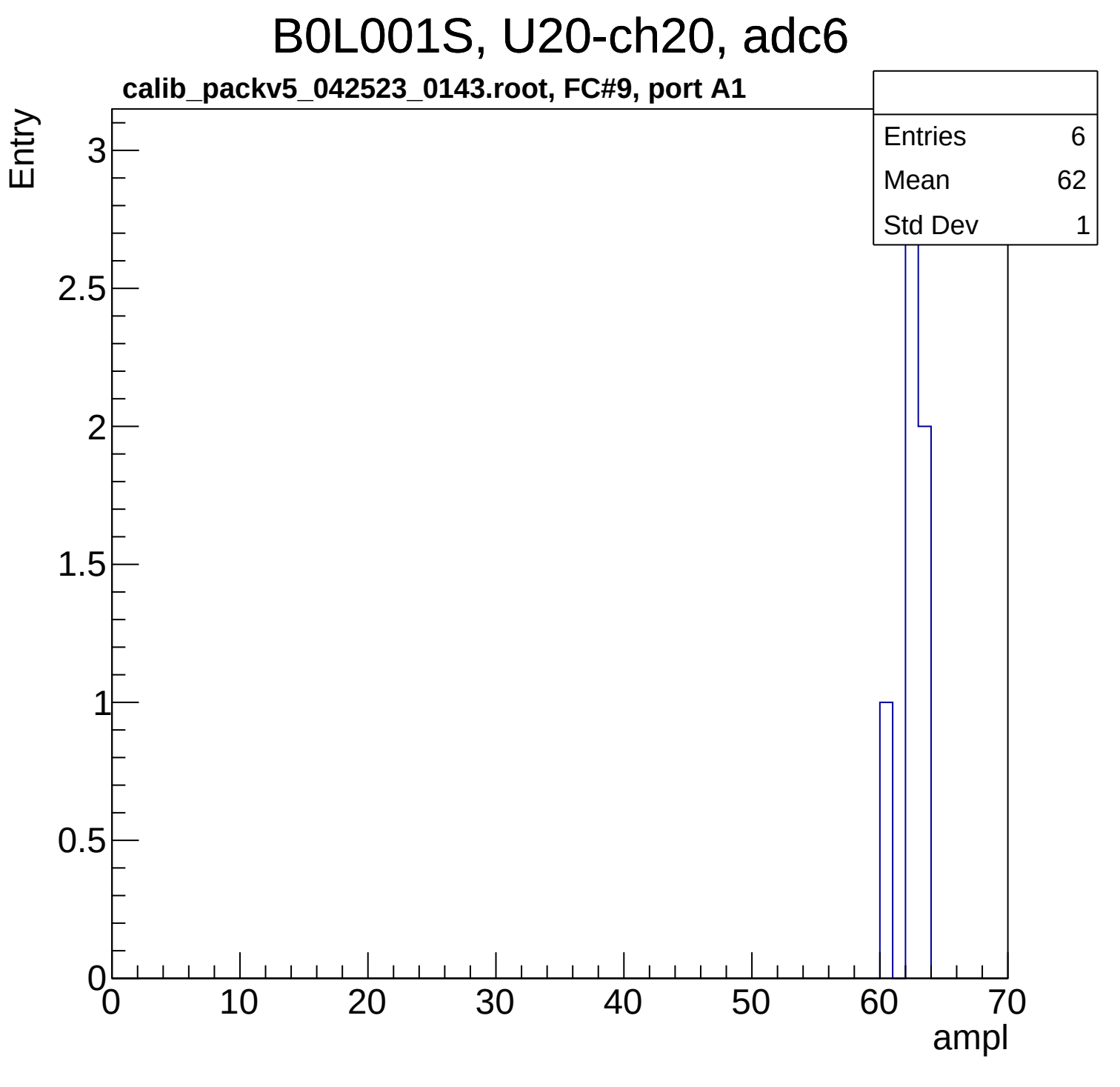
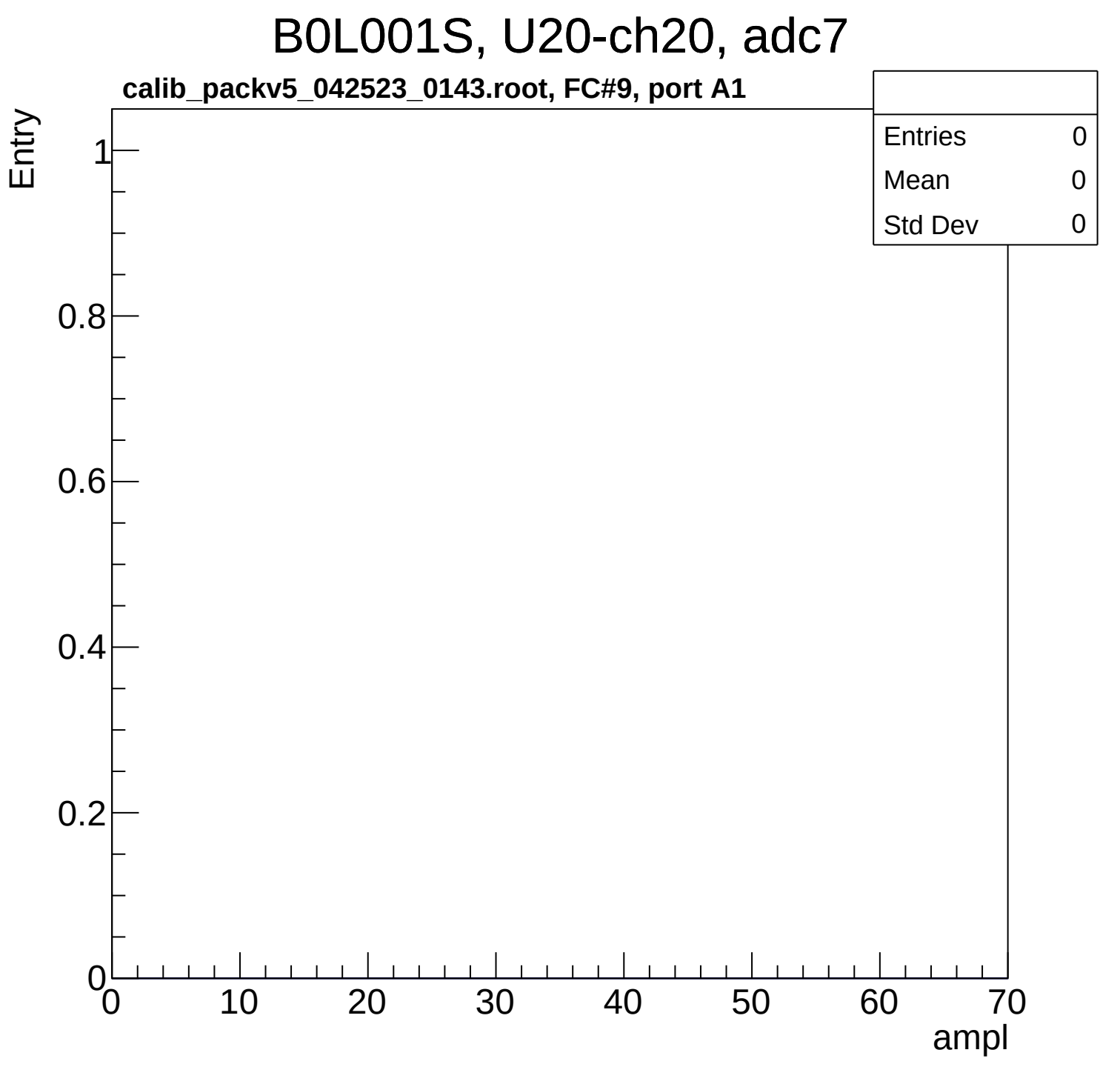
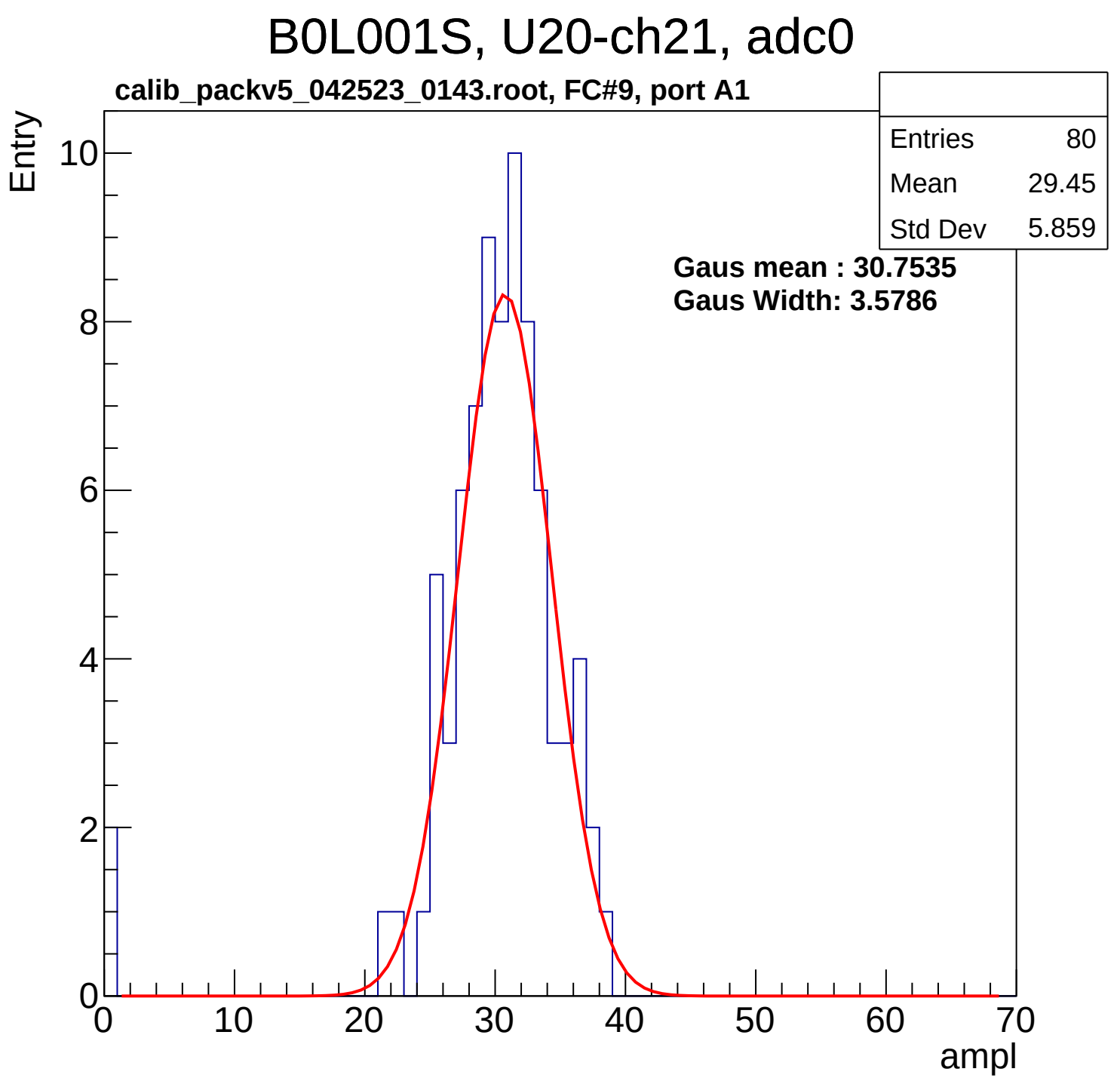
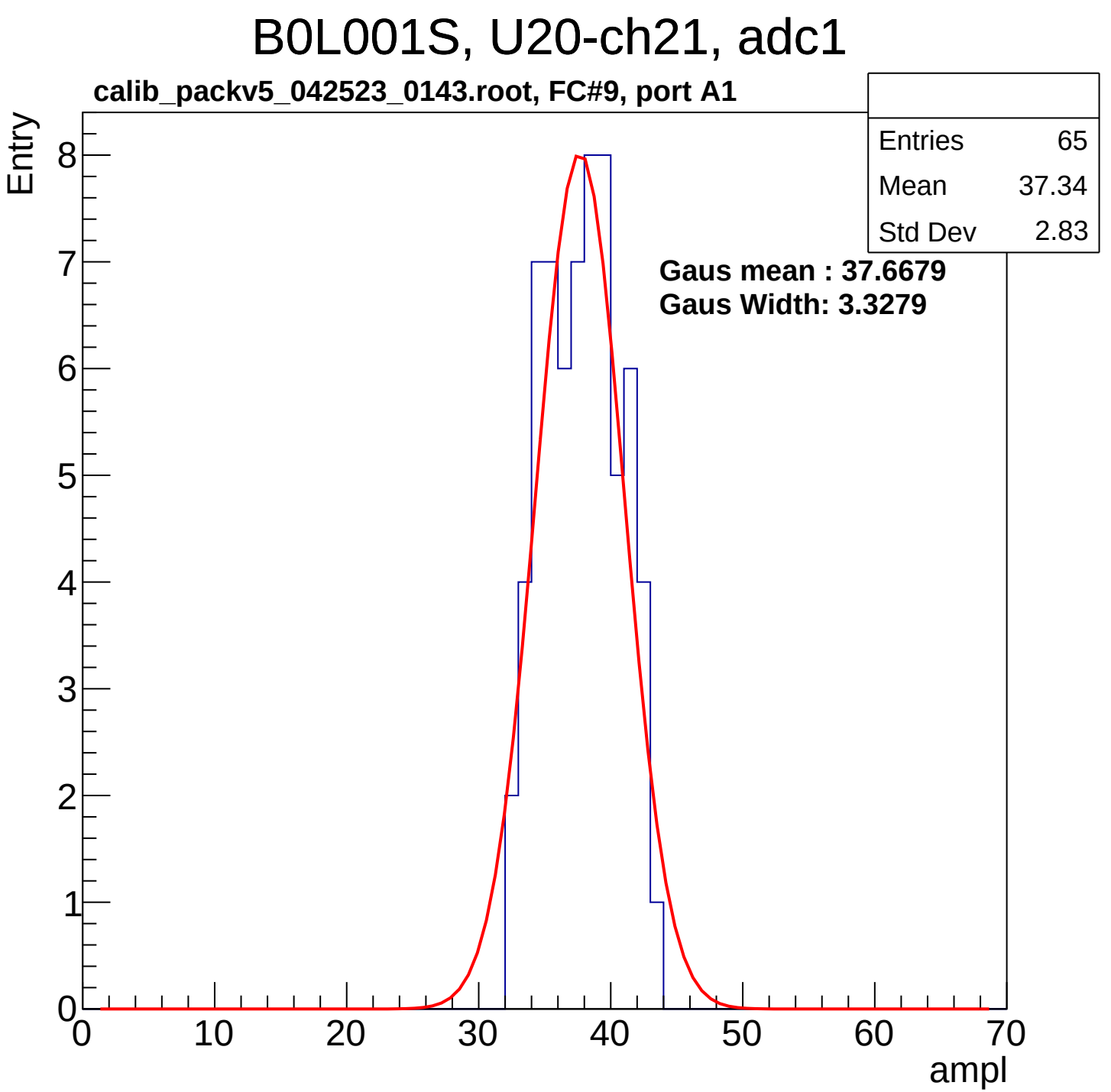
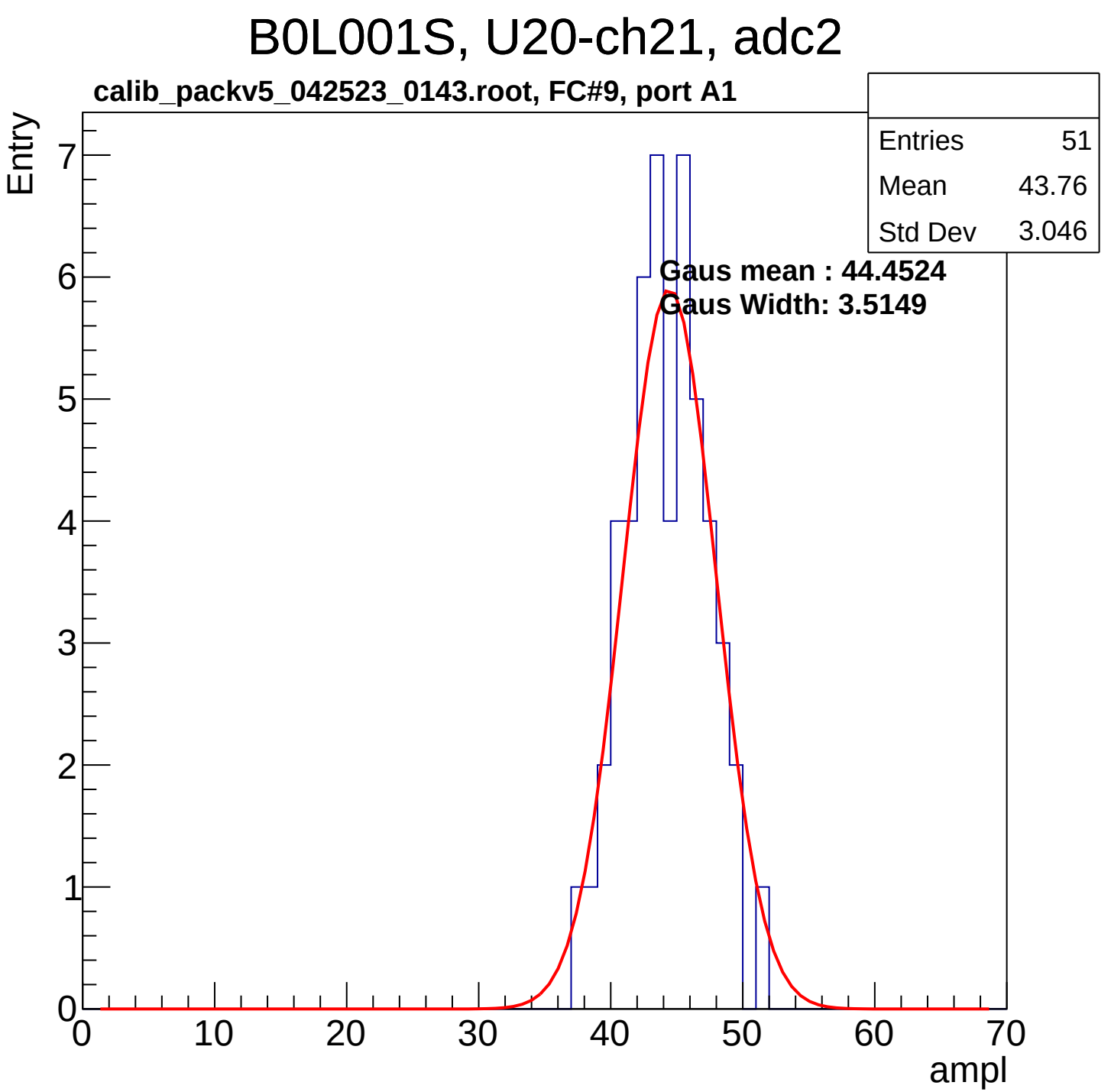
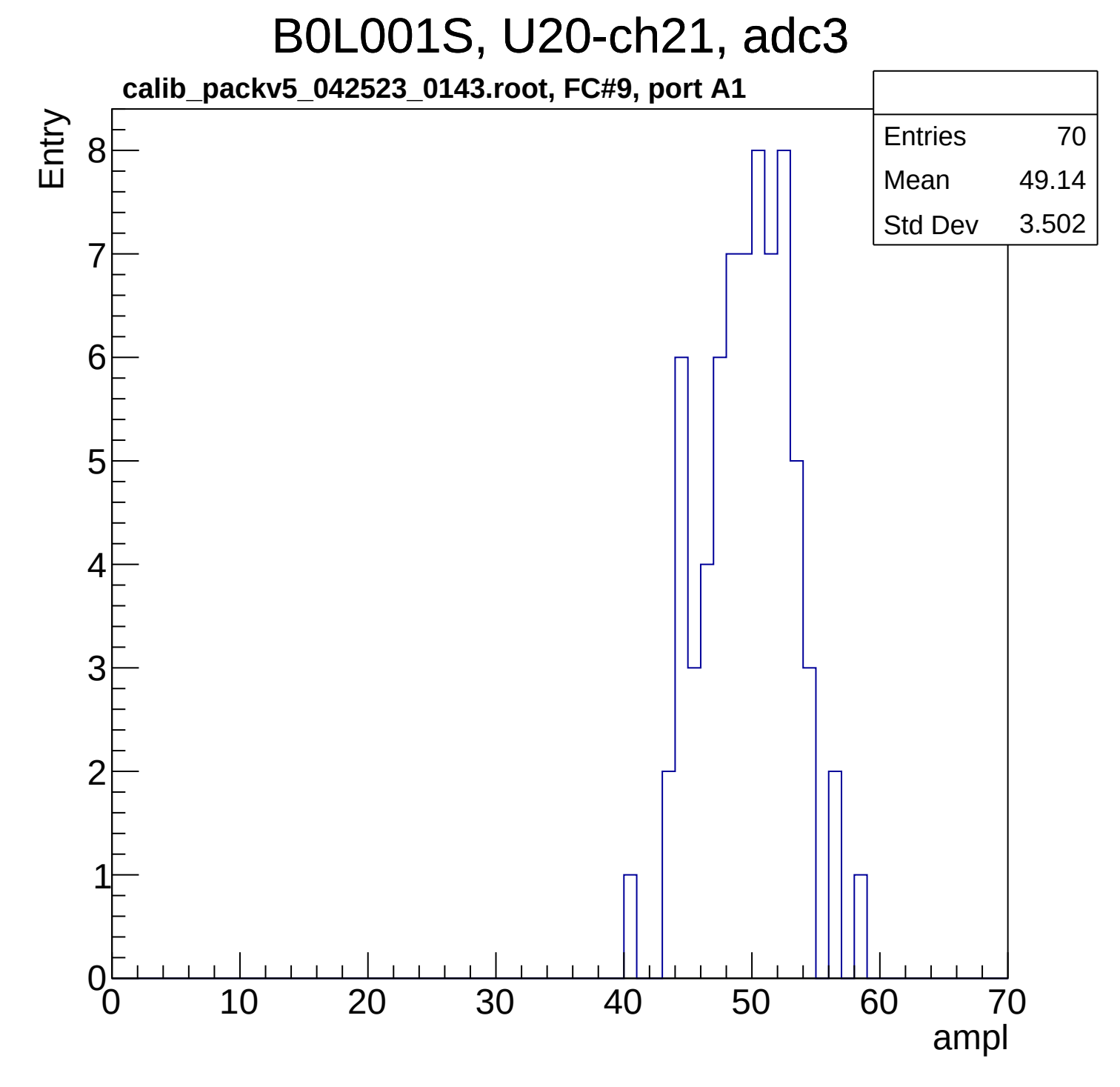

B0L001S, U20-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.45
Std Dev 5.859
Gaus mean : 30.7535
Gaus Width: 3.5786
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.34
Std Dev 2.83
Gaus mean : 37.6679
Gaus Width: 3.3279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.76
Std Dev 3.046
Gaus mean : 44.4524
Gaus Width: 3.5149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.14
Std Dev 3.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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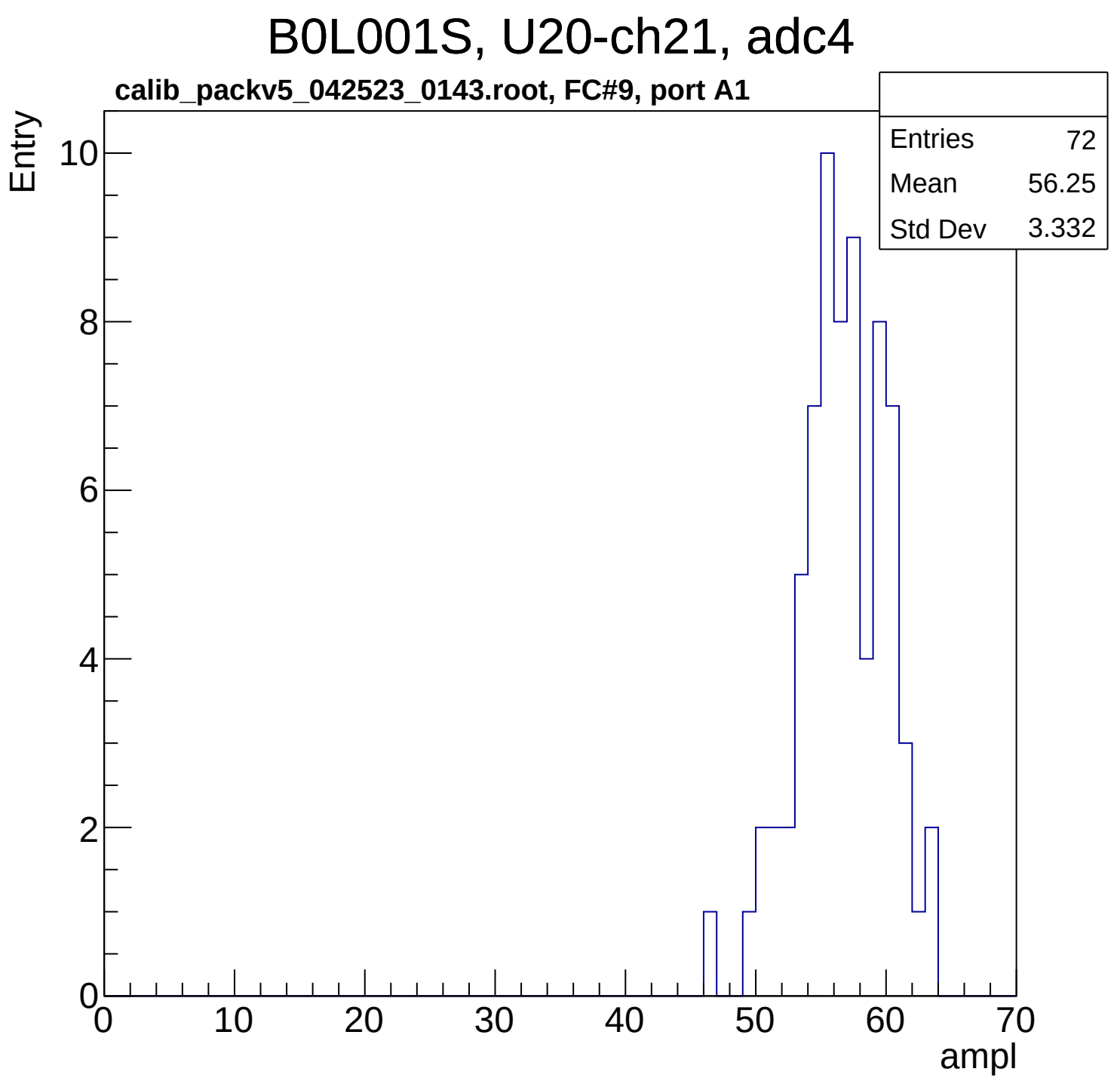
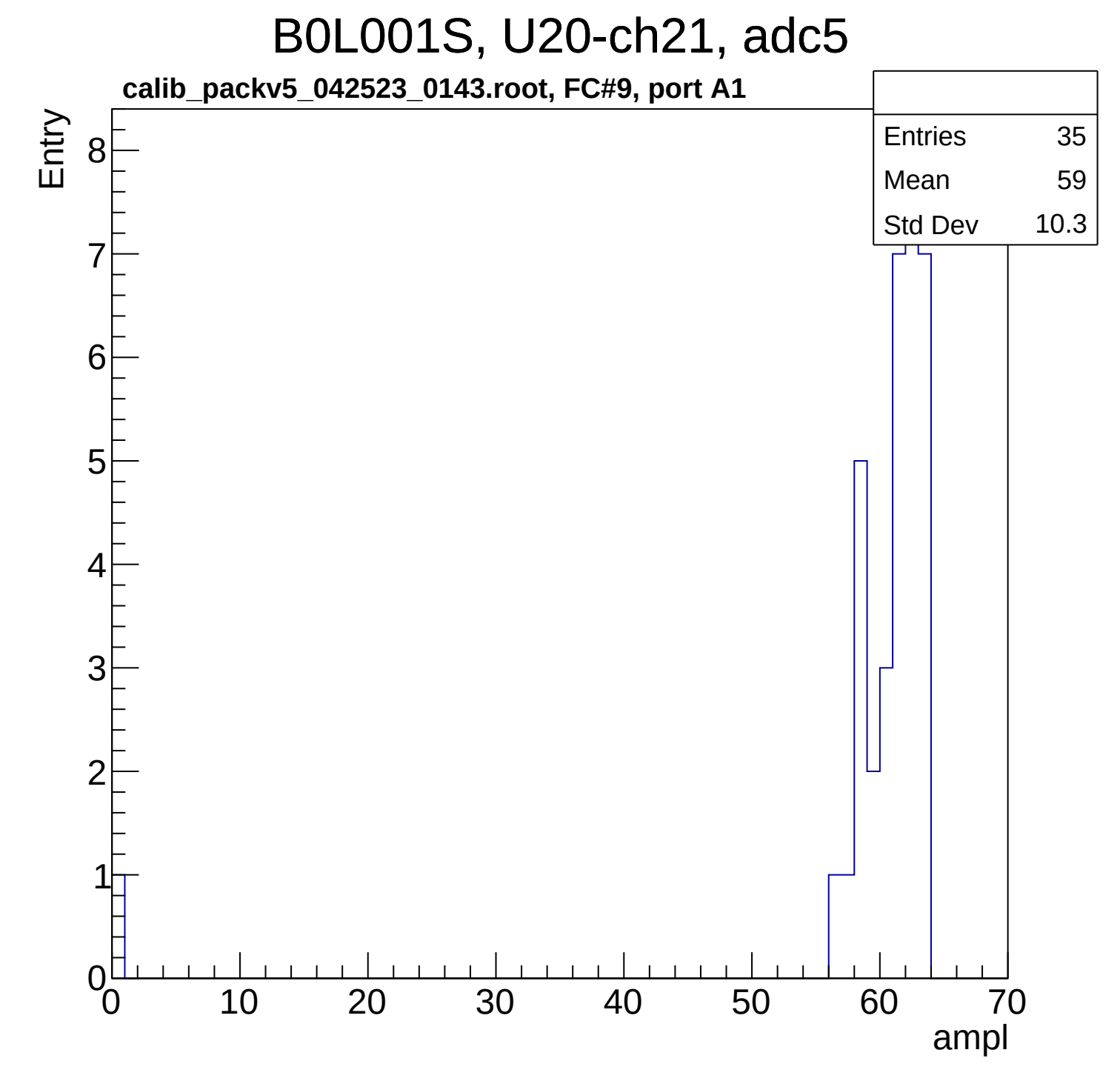
40
50
60
70


B0L001S, U20-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 56.25
Std Dev 3.332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59
Std Dev 10.3
Entry
ampl
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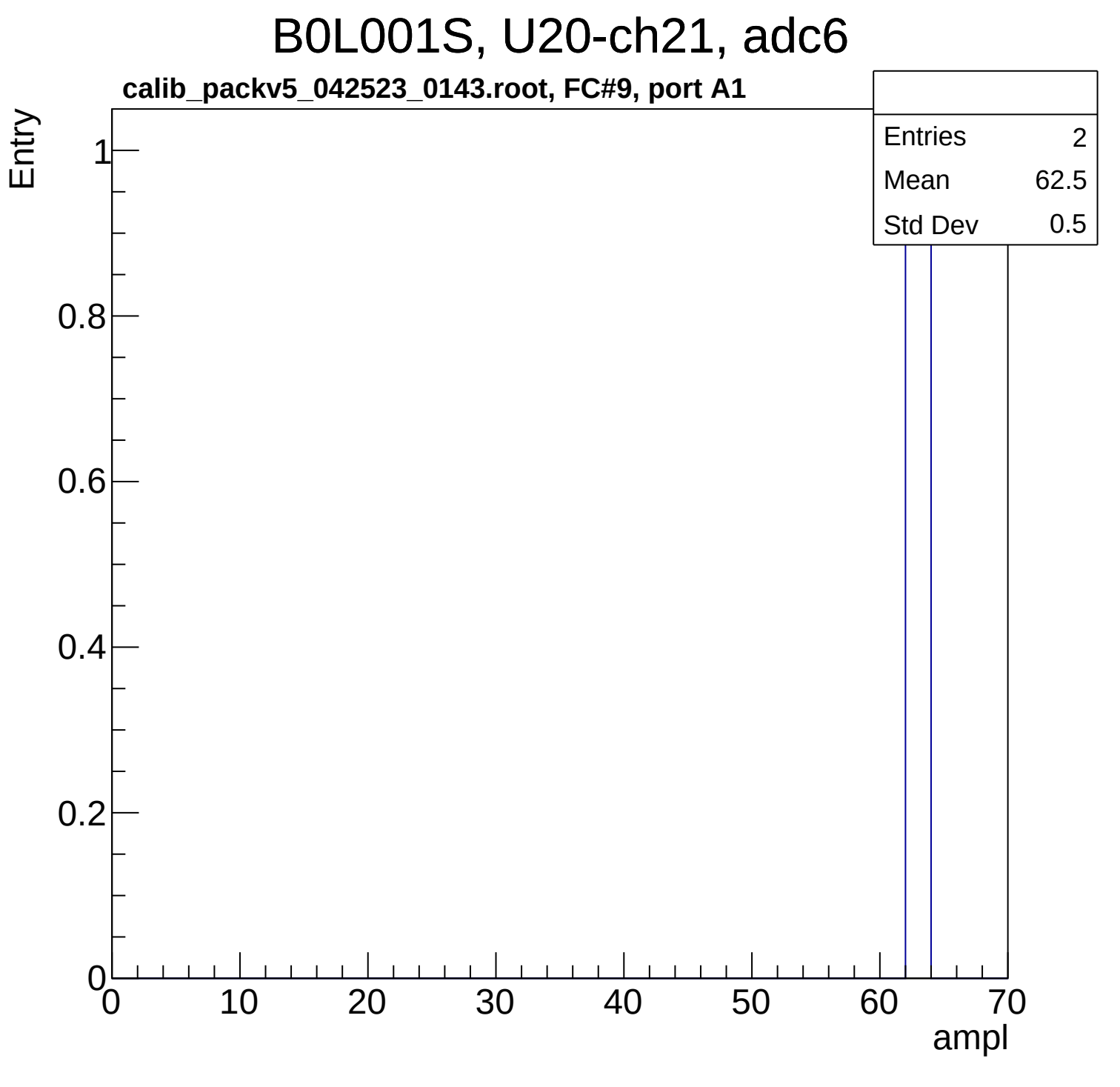
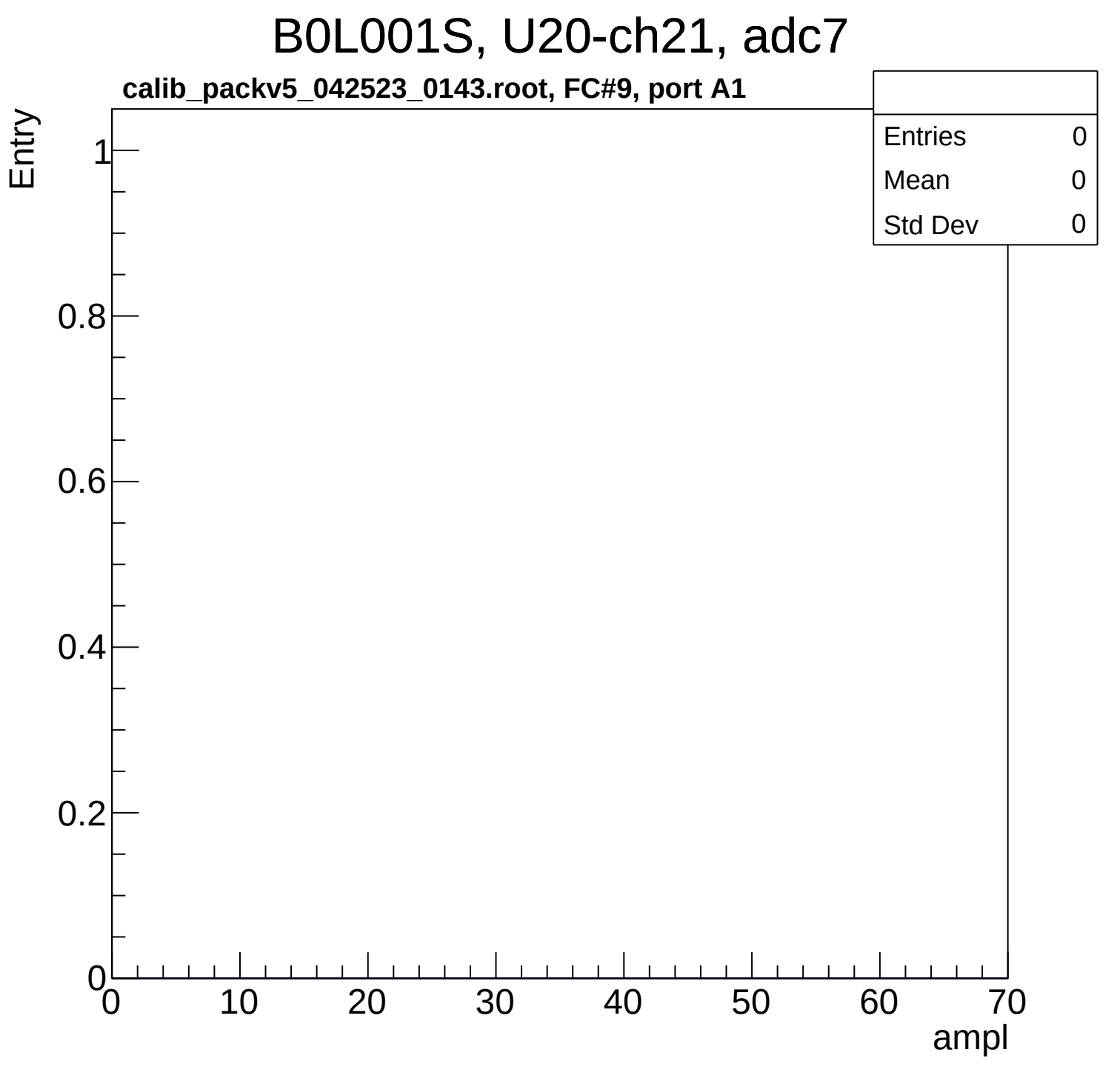
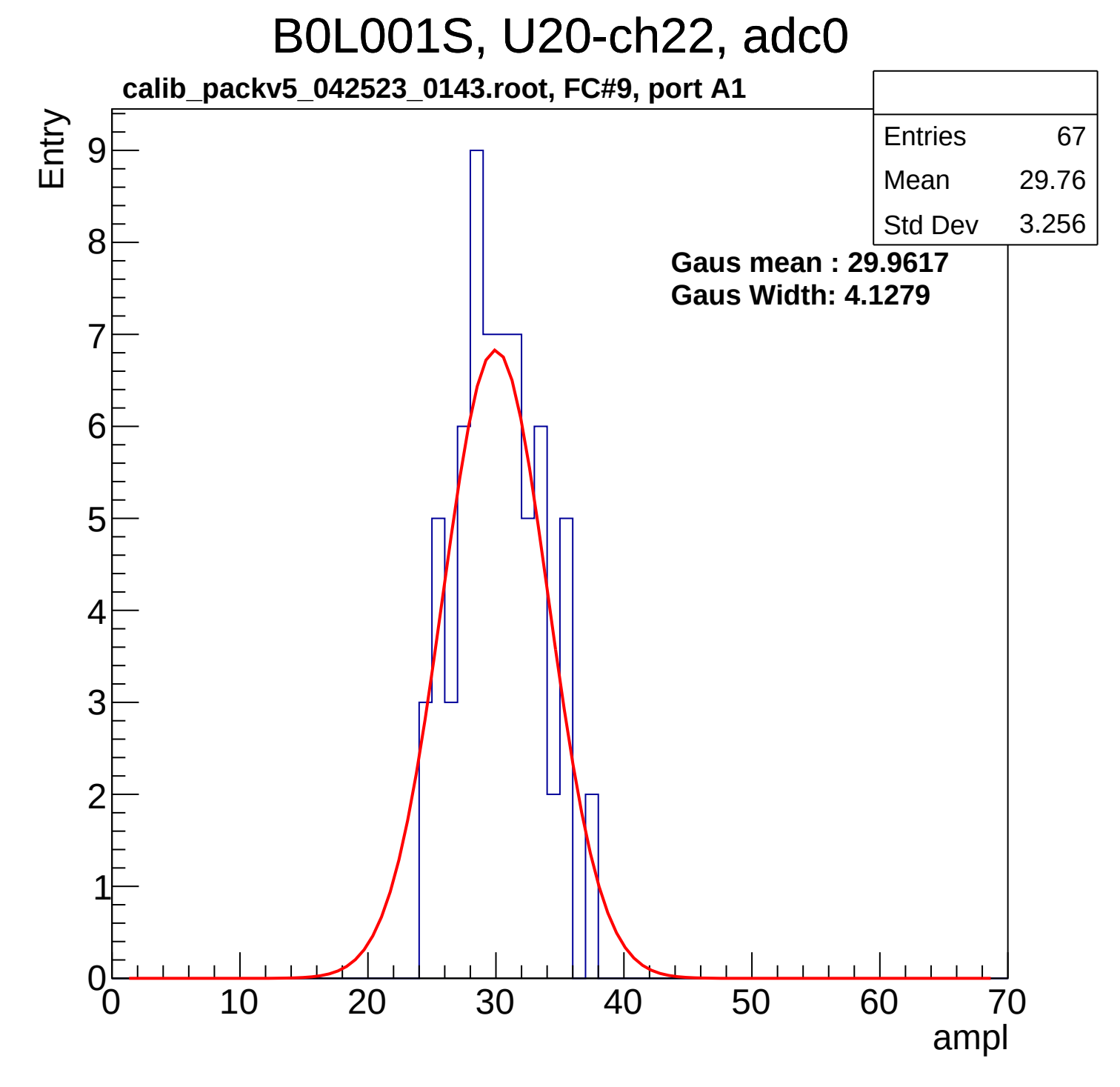
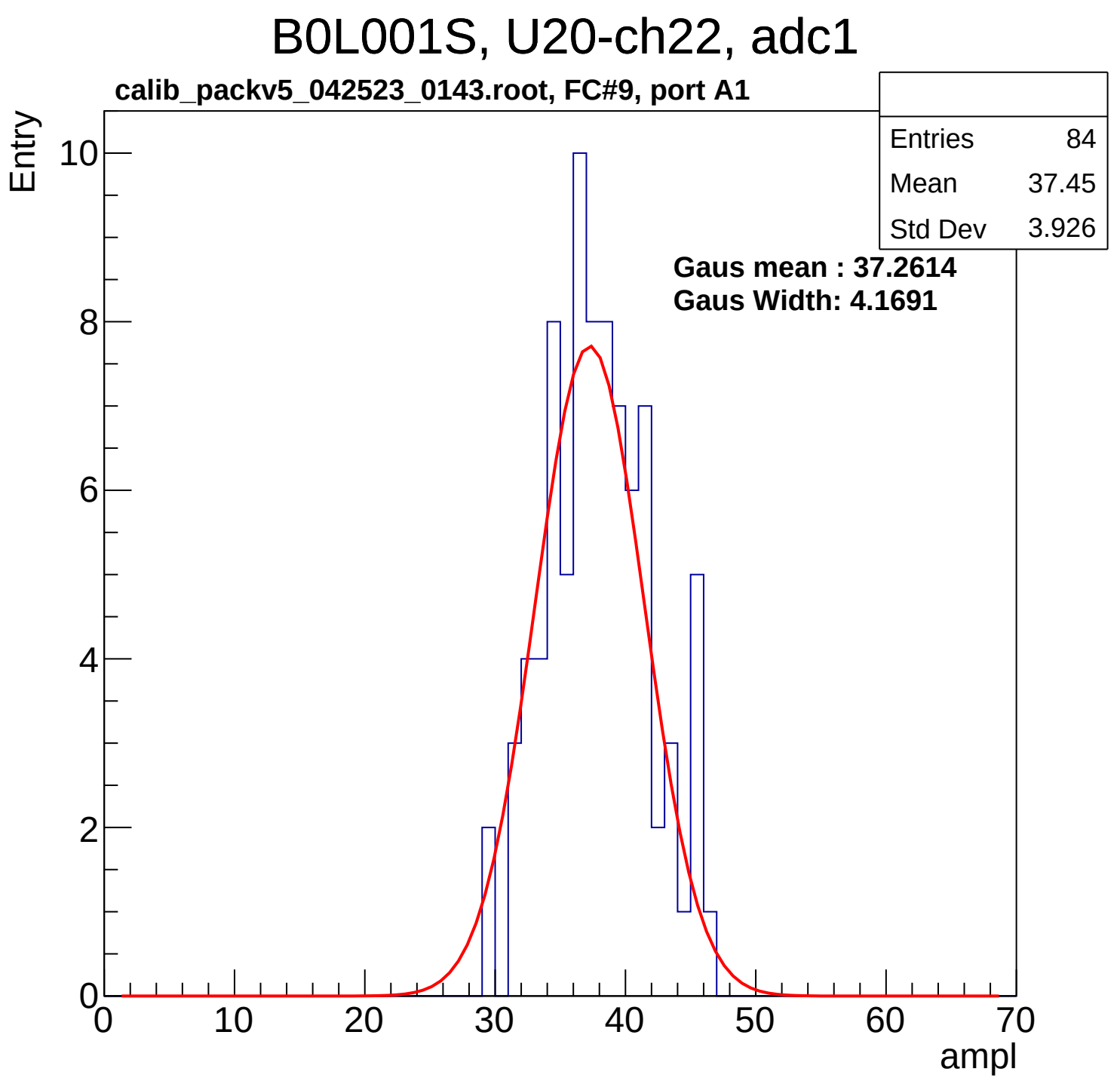
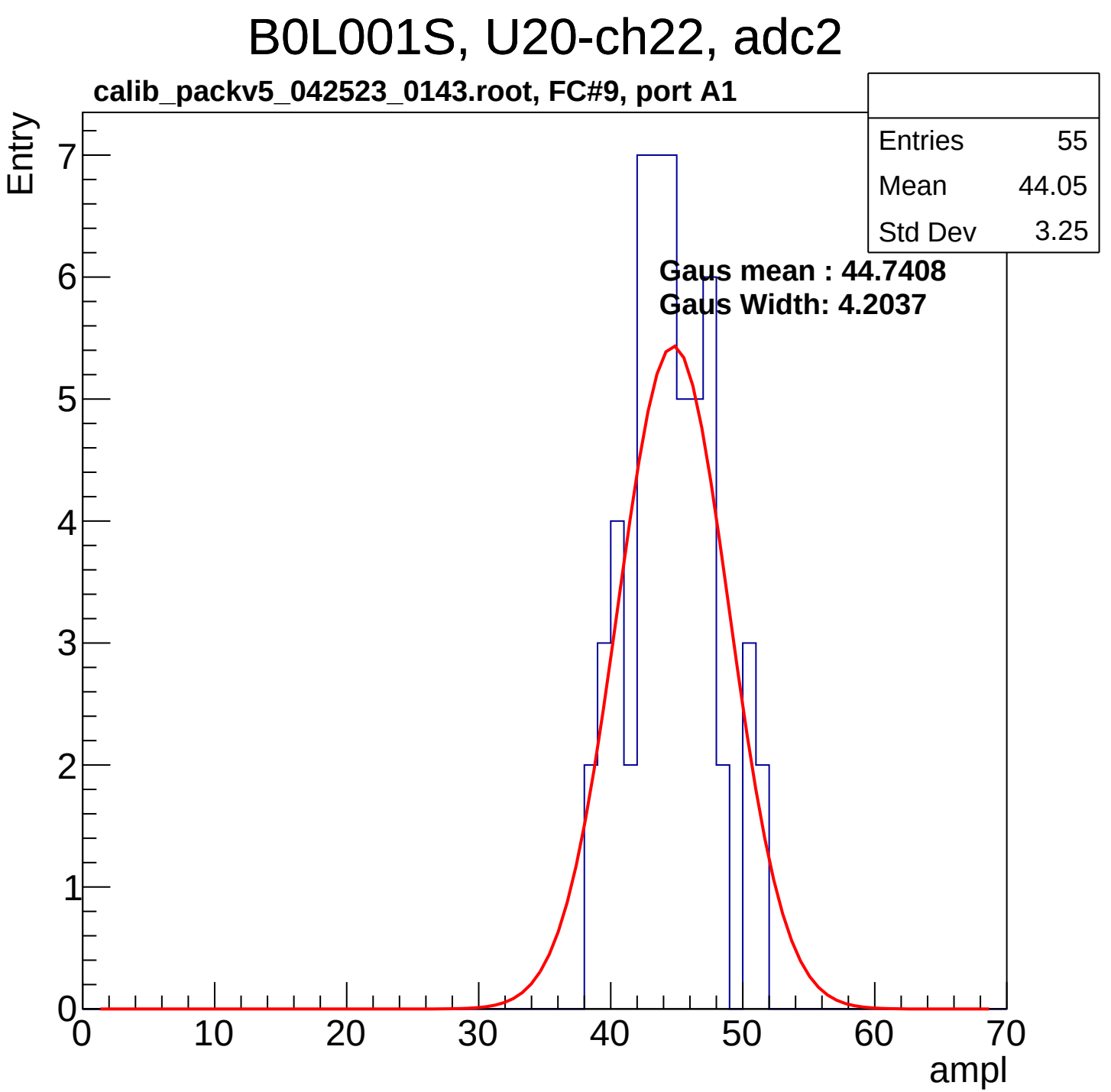
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

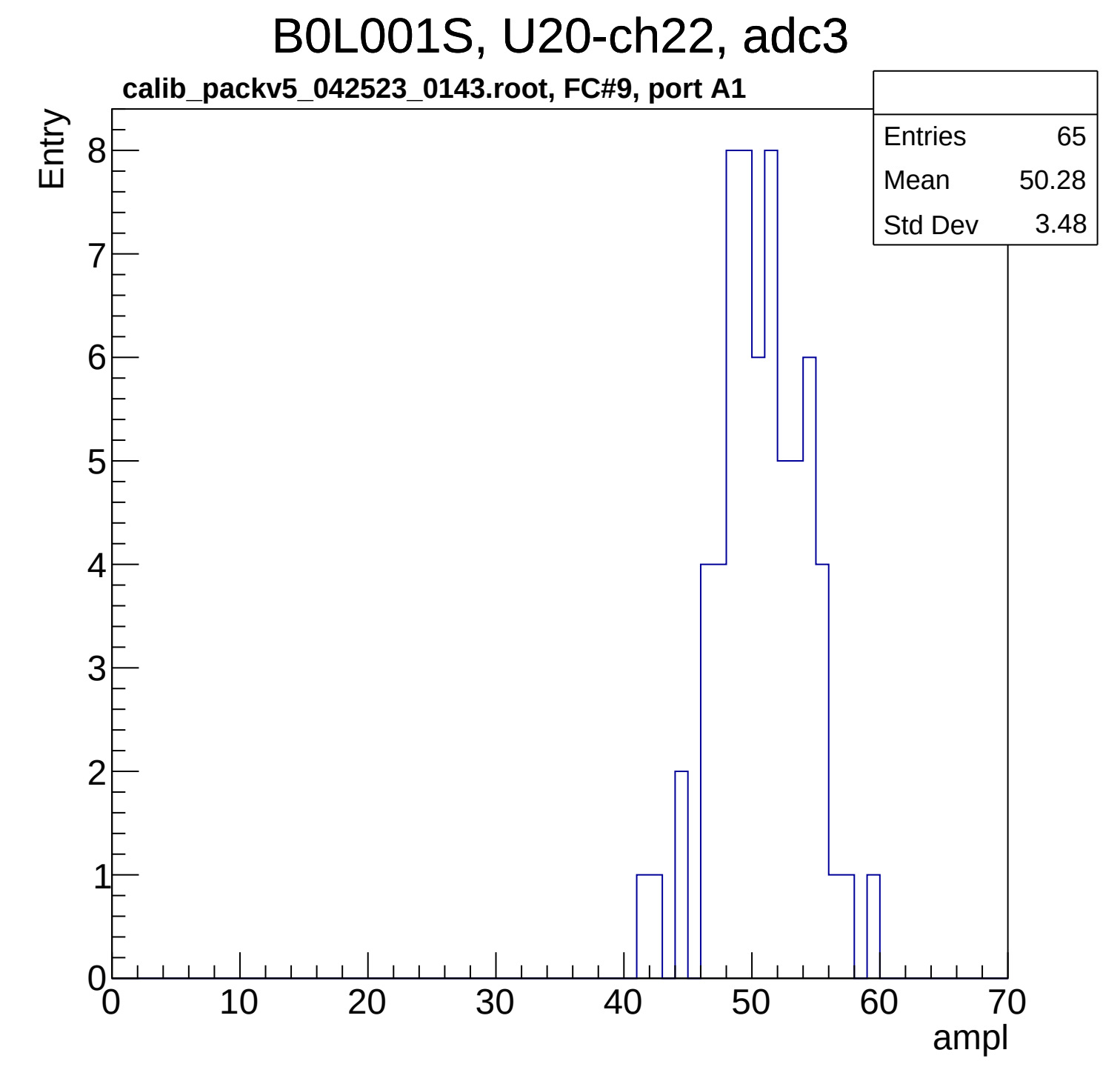

B0L001S, U20-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.76
Std Dev 3.256
Gaus mean : 29.9617
Gaus Width: 4.1279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 37.45
Std Dev 3.926
Gaus mean : 37.2614
Gaus Width: 4.1691
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.05
Std Dev 3.25
Gaus mean : 44.7408
Gaus Width: 4.2037
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.28
Std Dev 3.48
Entry
ampl
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1
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3
4
5
6
7
8
0
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20
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50
60
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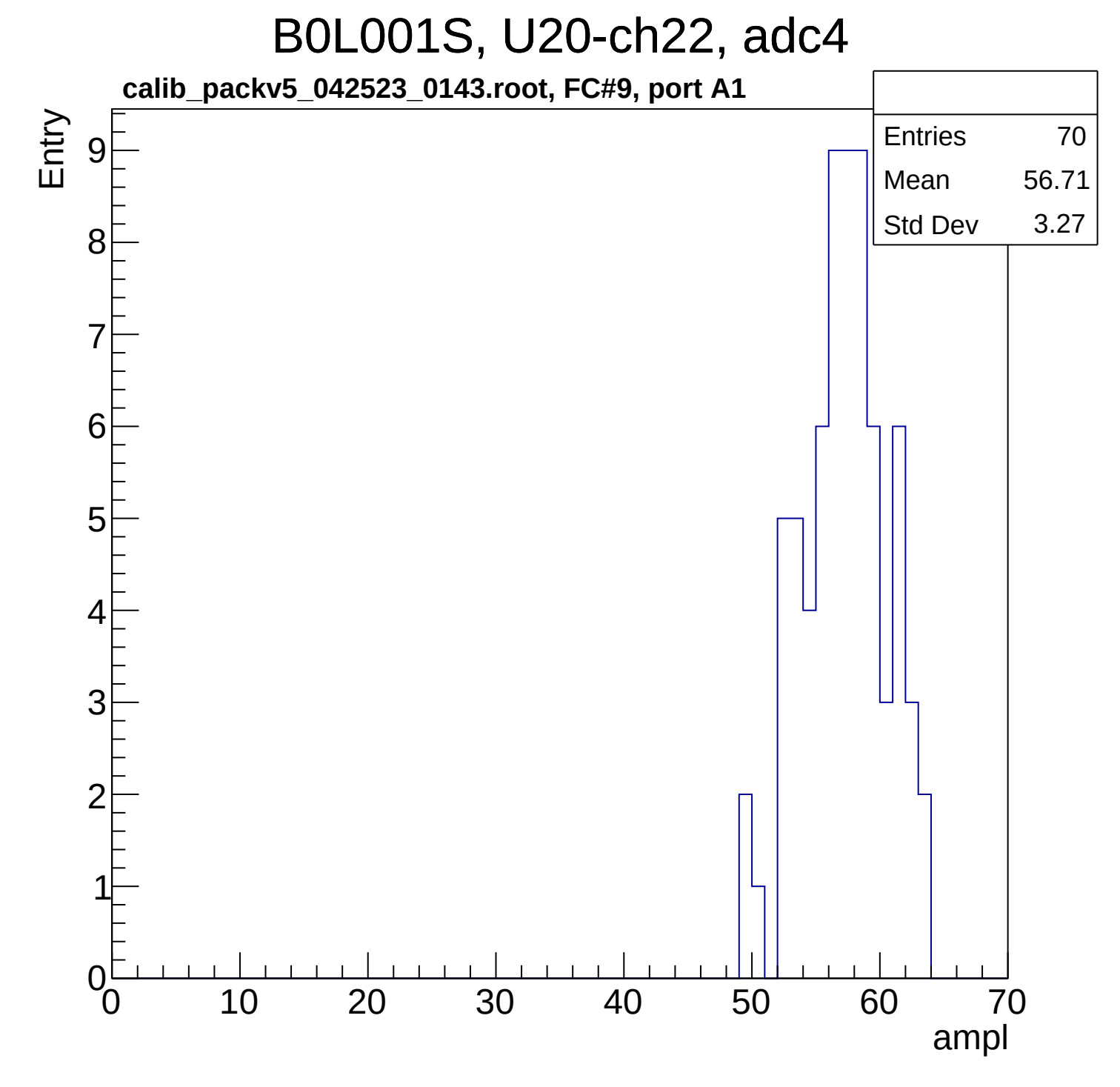
B0L001S, U20-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 56.71
Std Dev 3.27
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
50
60
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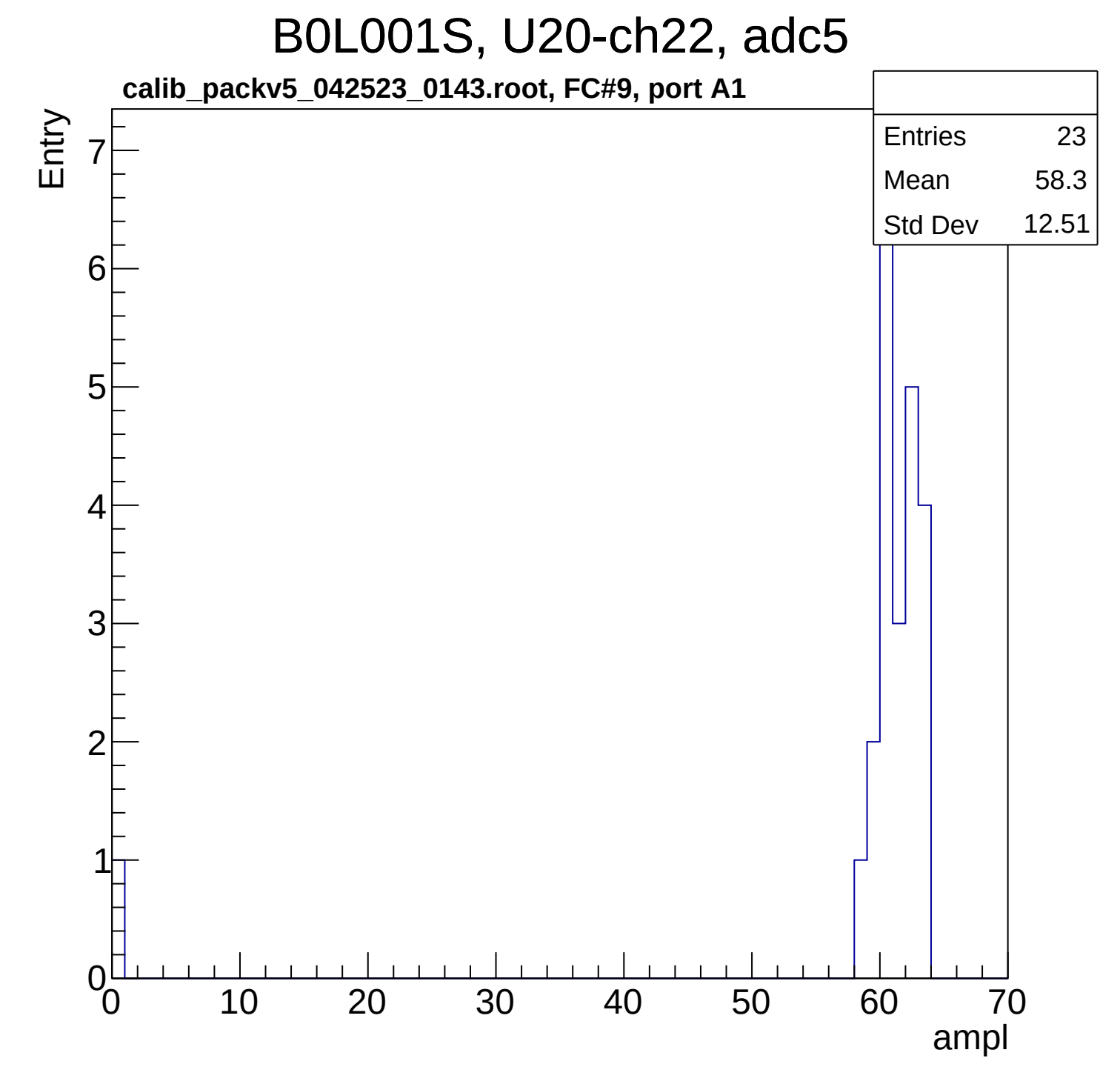
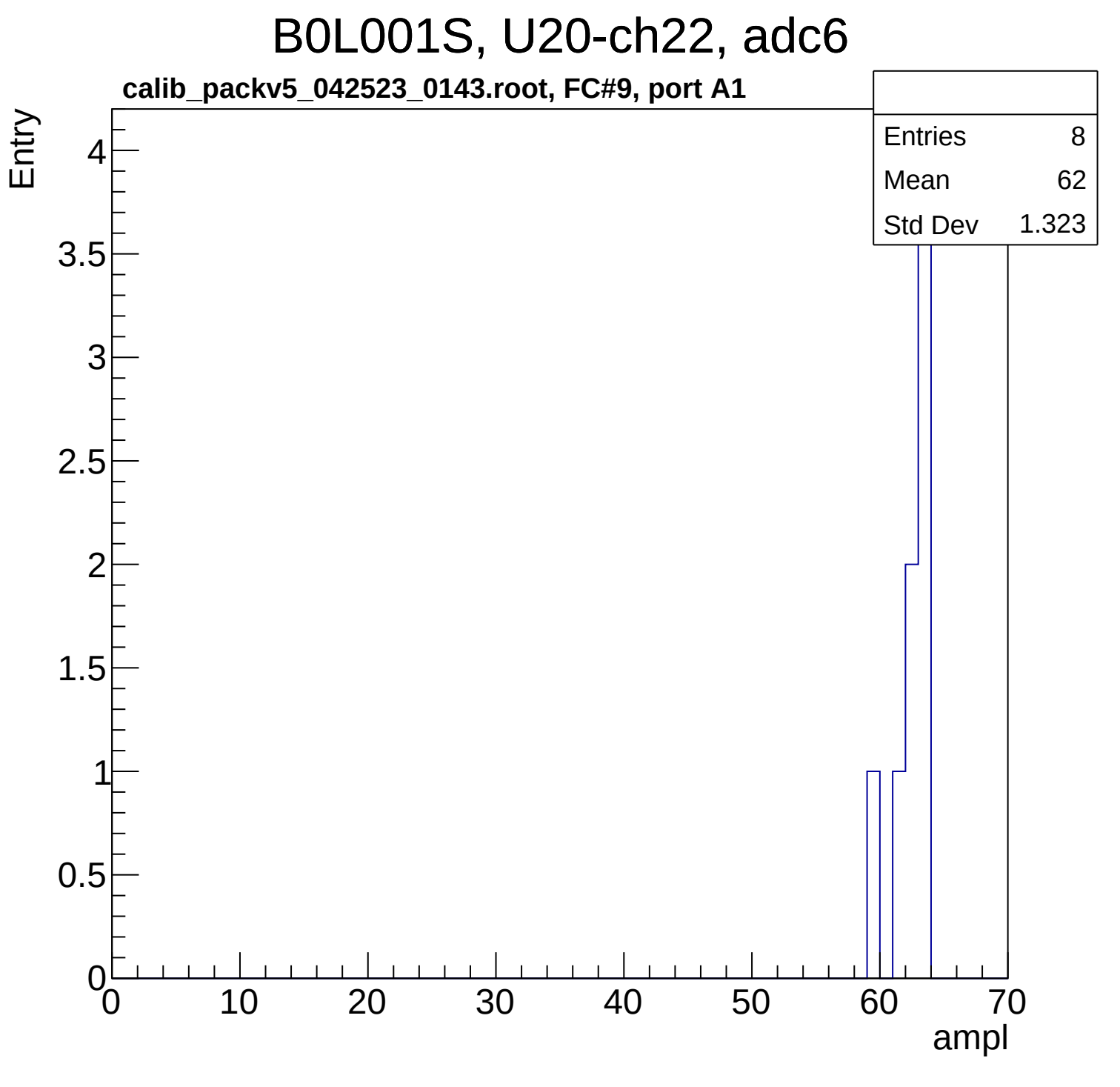
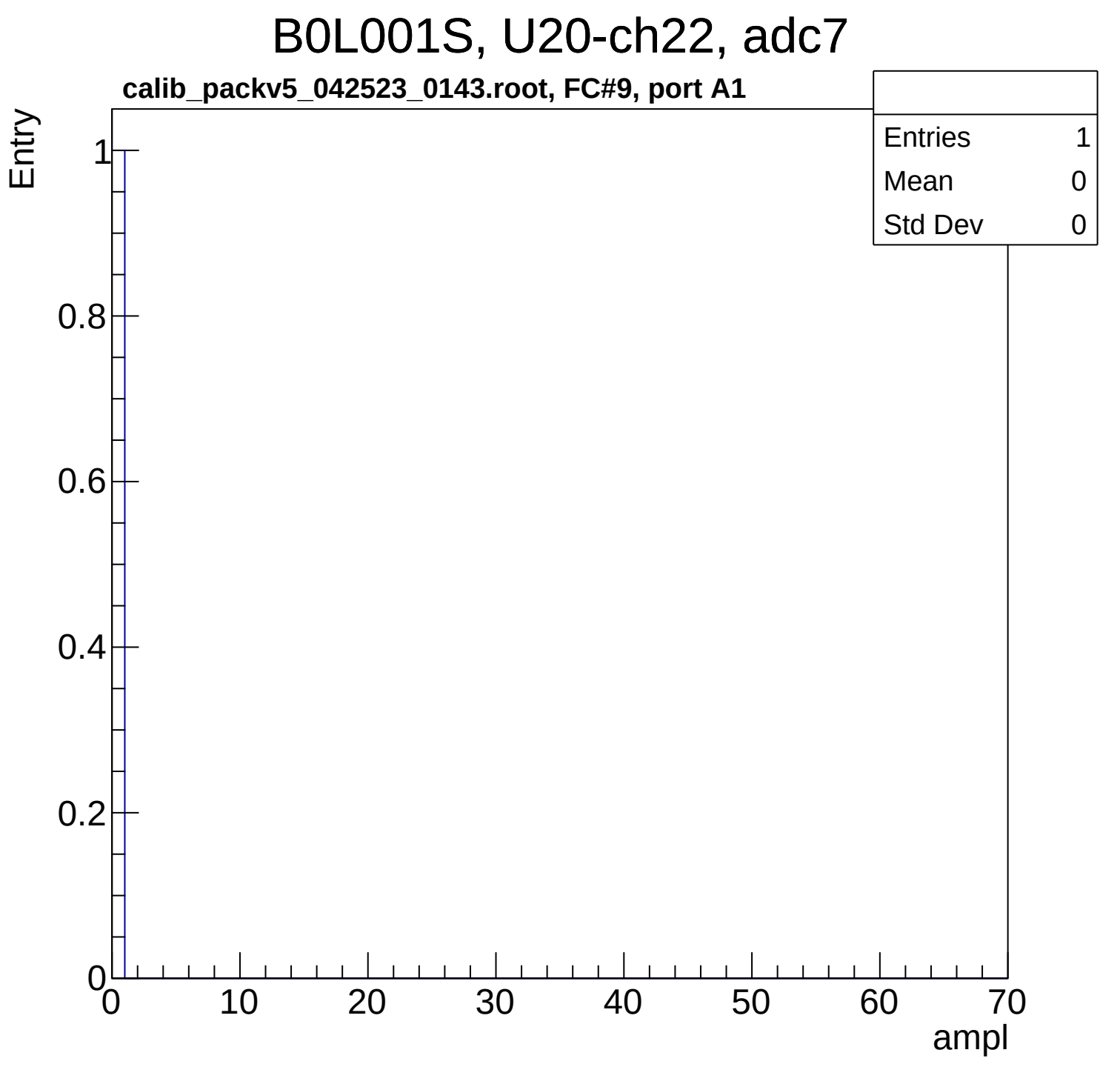
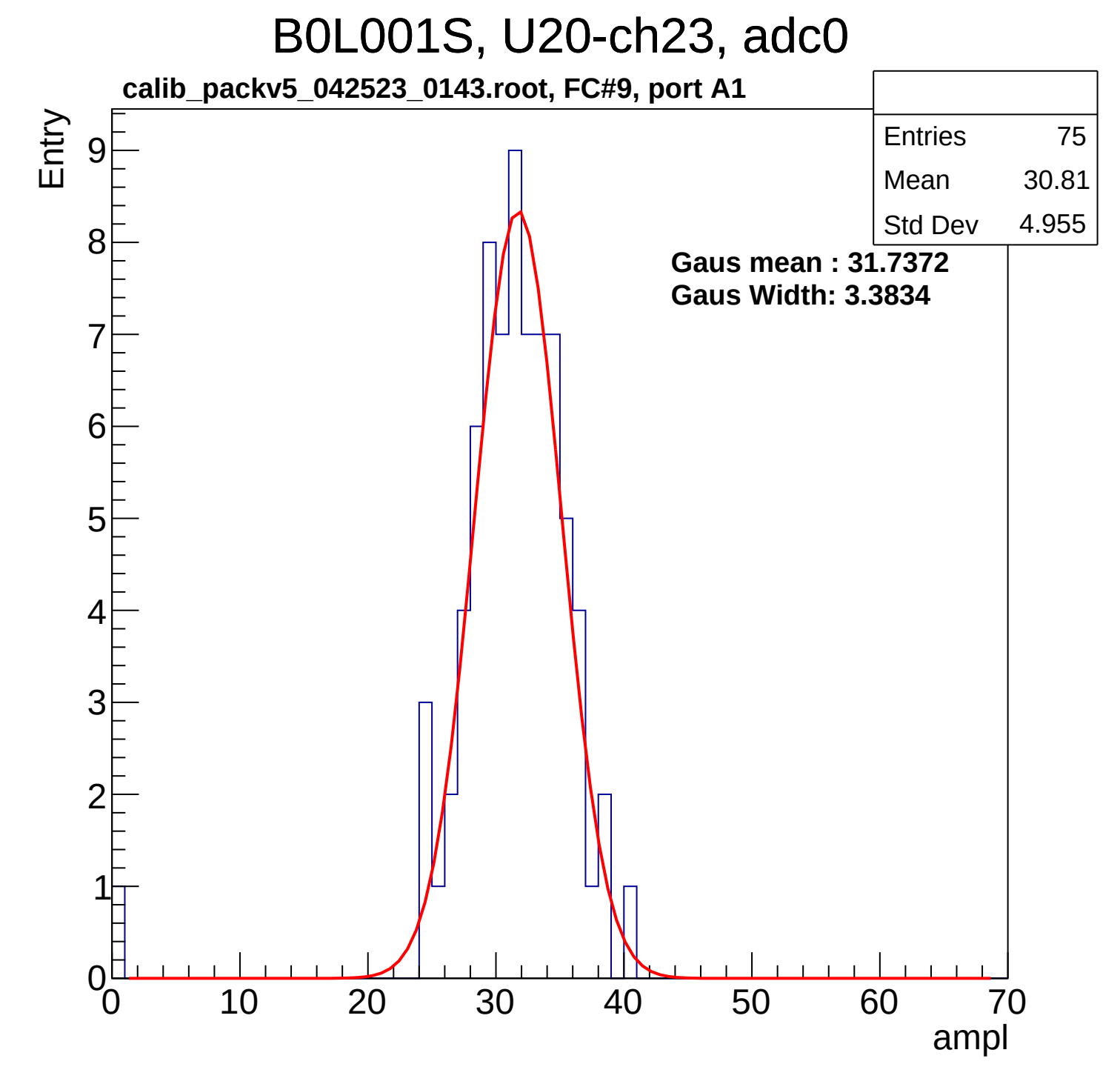
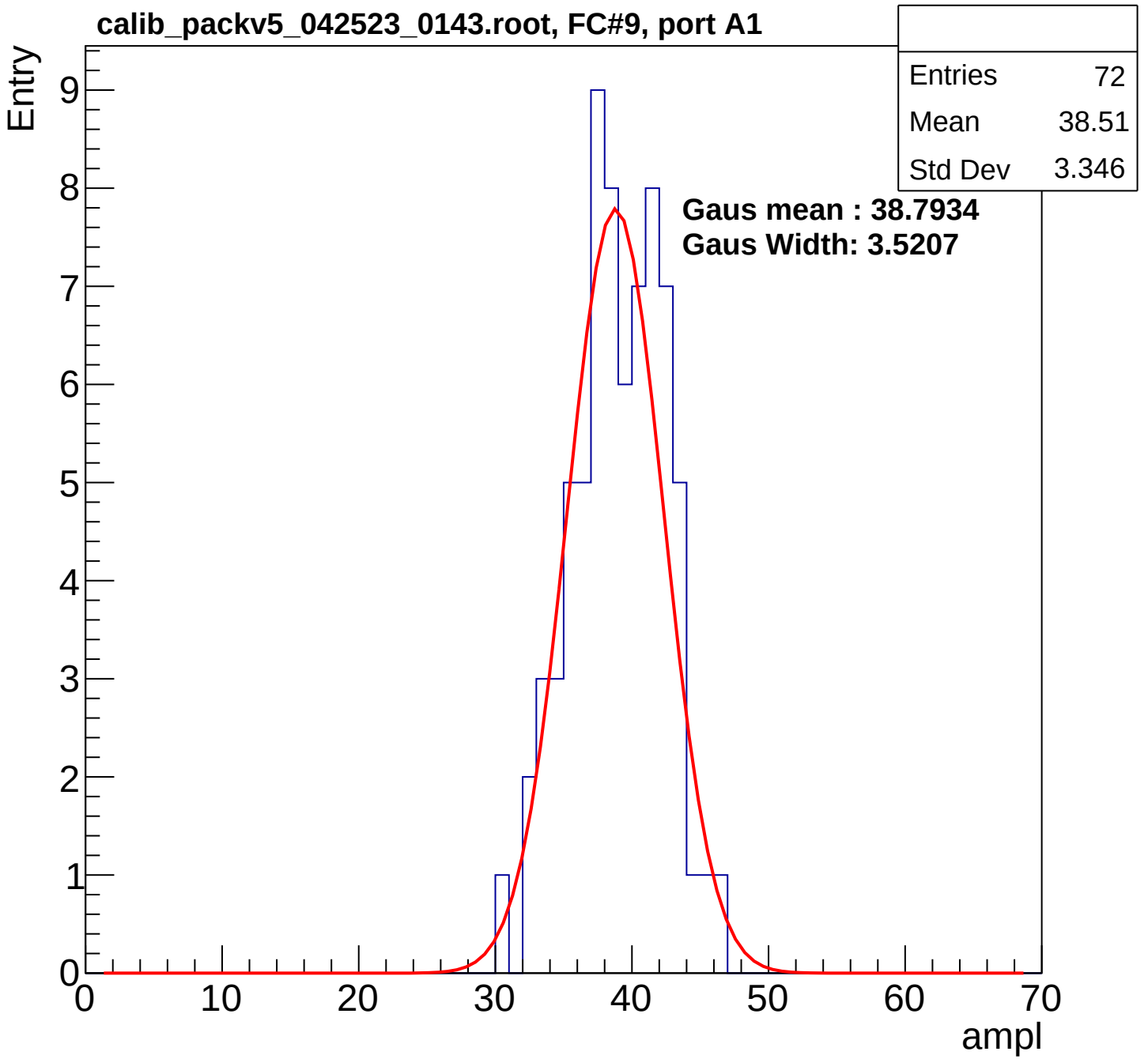
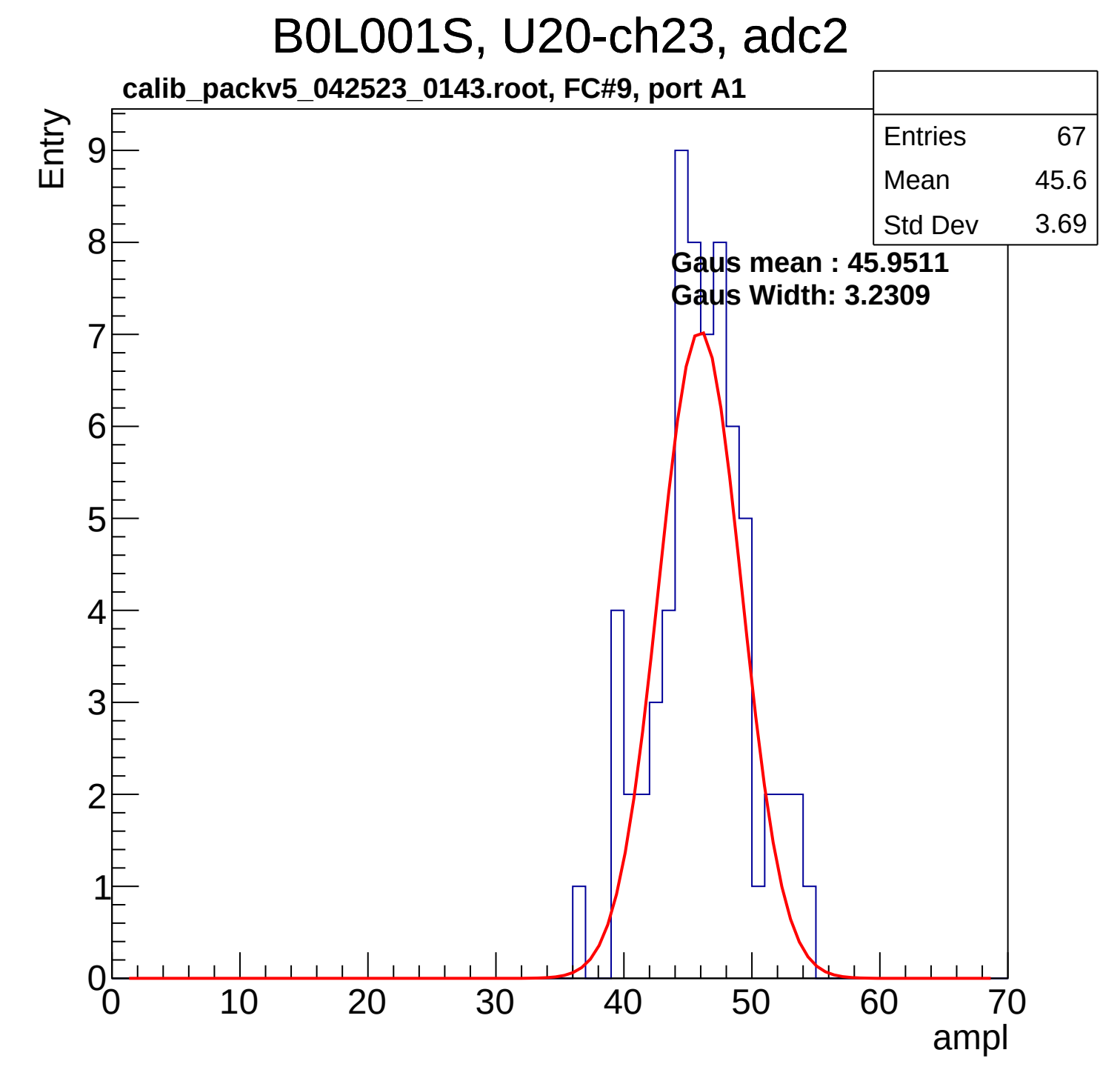

B0L001S, U20-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 58.3
Std Dev 12.51
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62
Std Dev 1.323
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U20-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.81
Std Dev 4.955
Gaus mean : 31.7372
Gaus Width: 3.3834
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.51
Std Dev 3.346
Gaus mean : 38.7934
Gaus Width: 3.5207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.6
Std Dev 3.69
Gaus mean : 45.9511
Gaus Width: 3.2309
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
20
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40
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60
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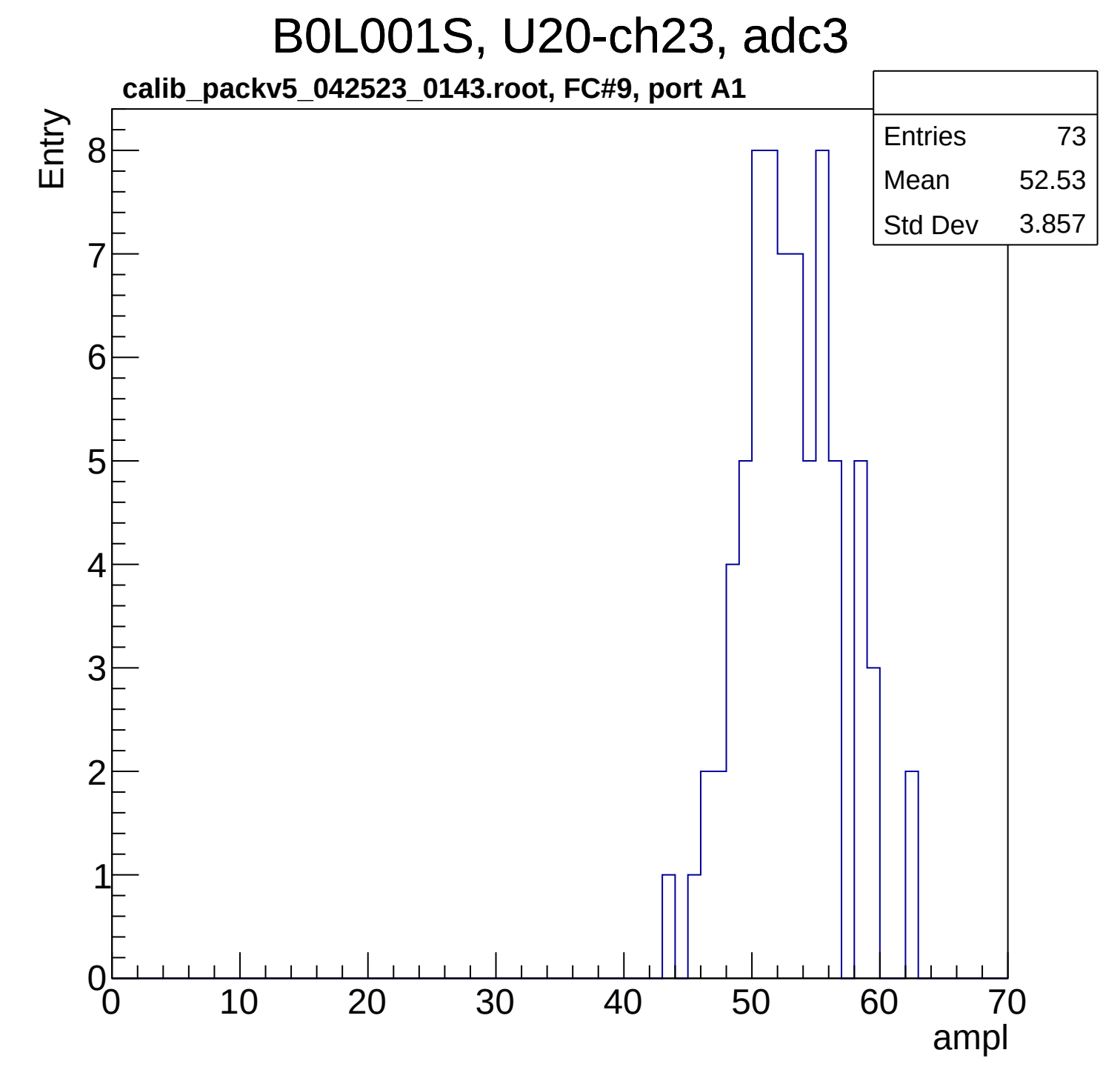
B0L001S, U20-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 52.53
Std Dev 3.857
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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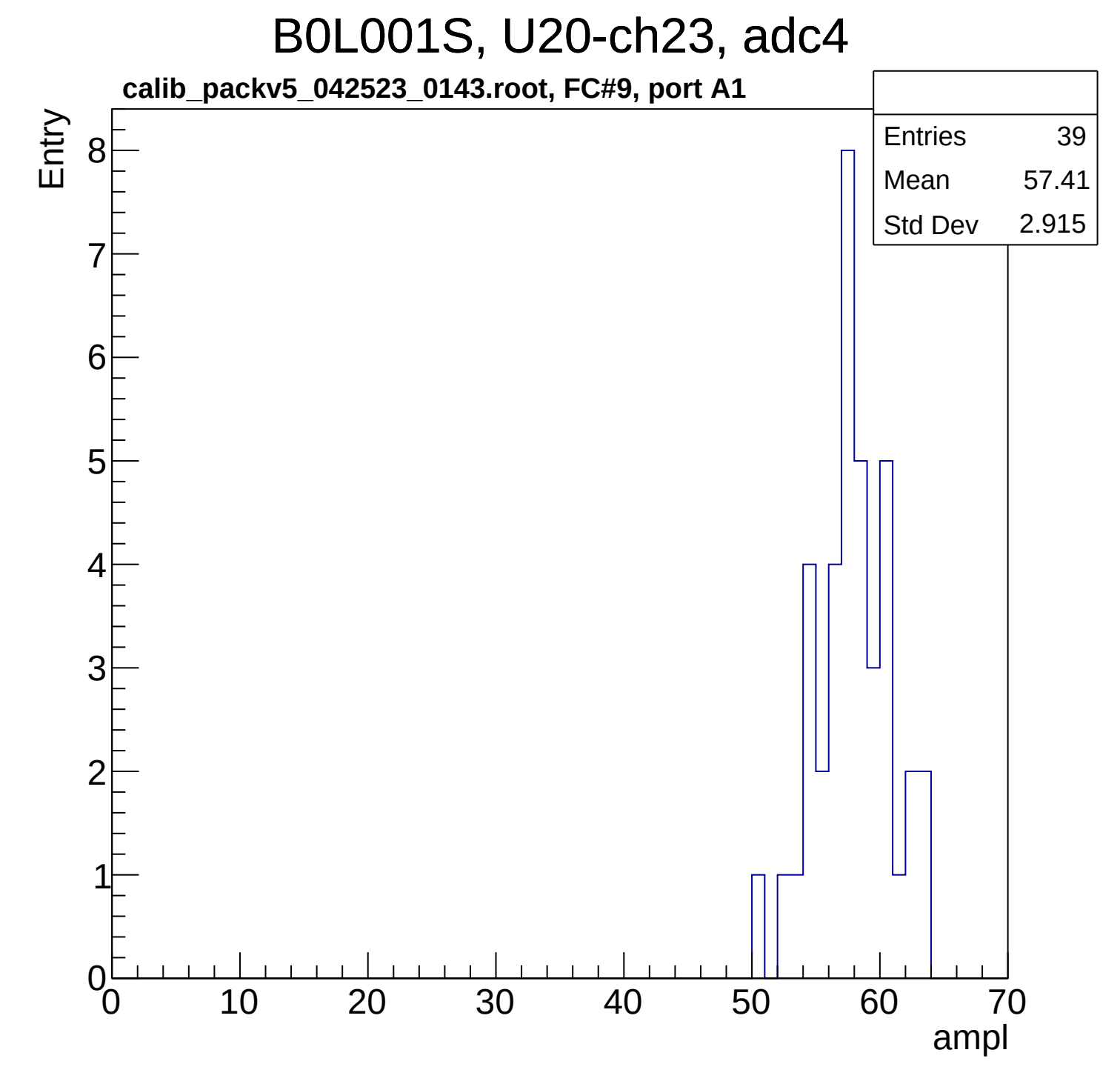
B0L001S, U20-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 57.41
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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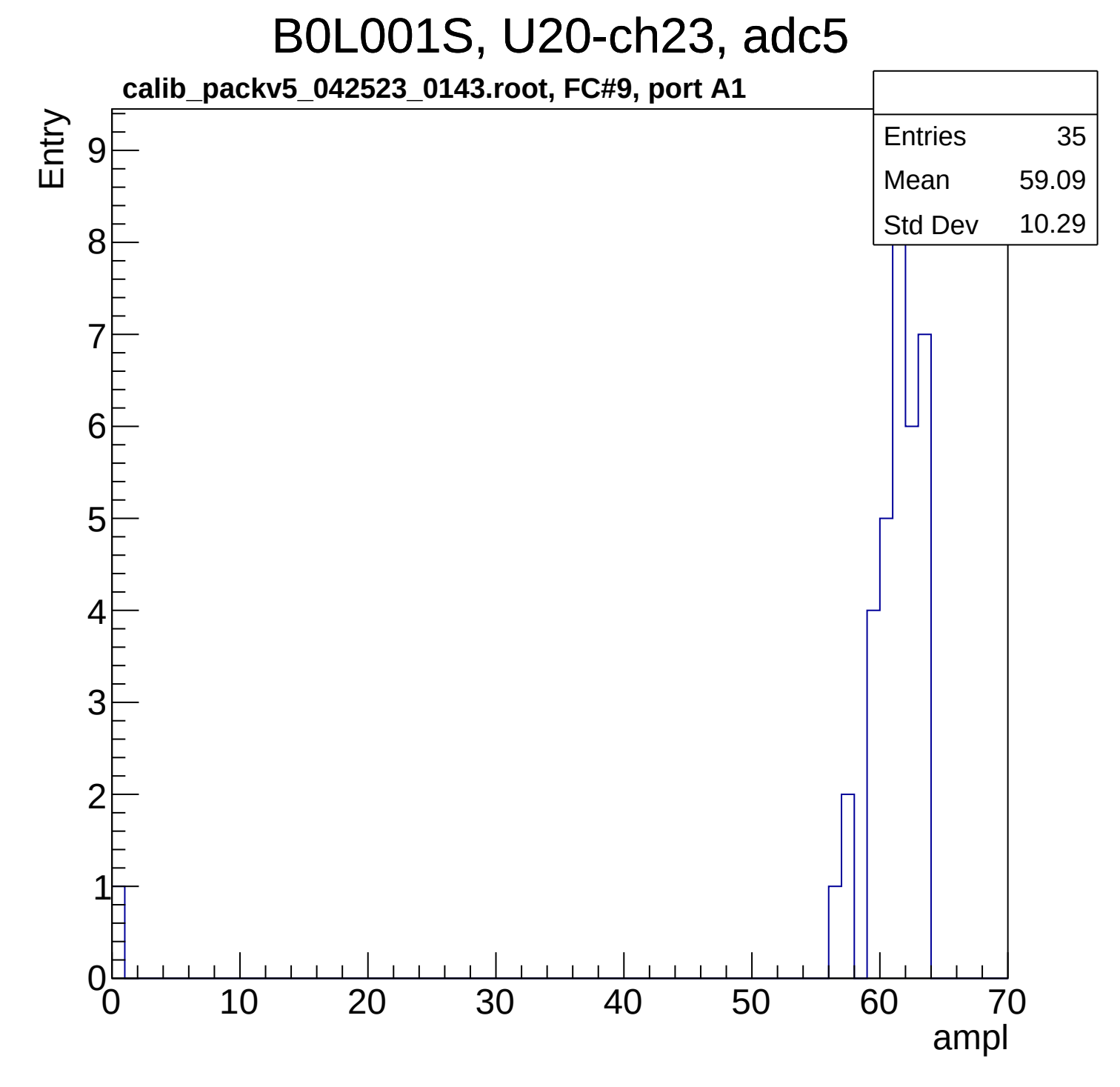
B0L001S, U20-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.09
Std Dev 10.29
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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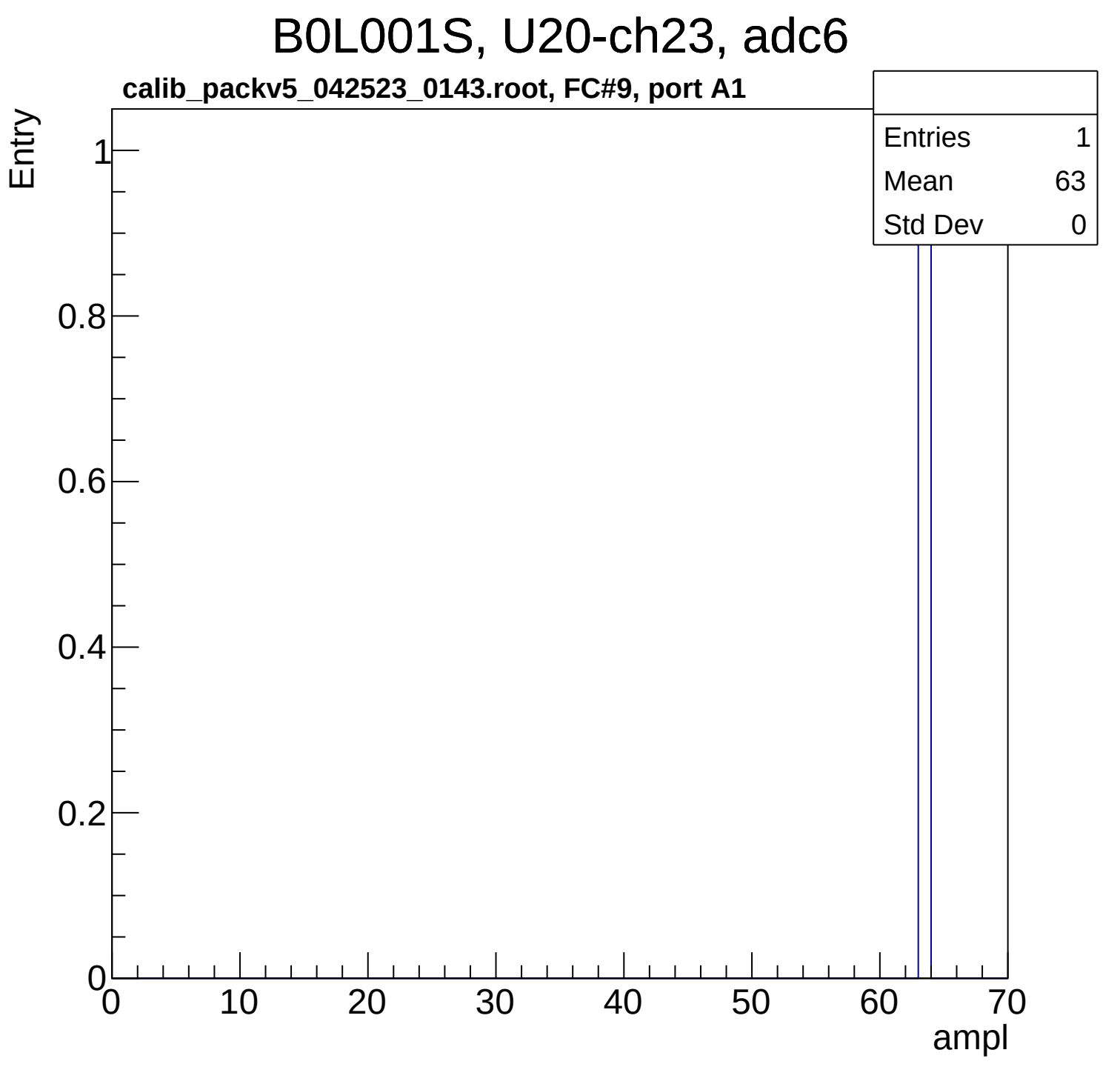
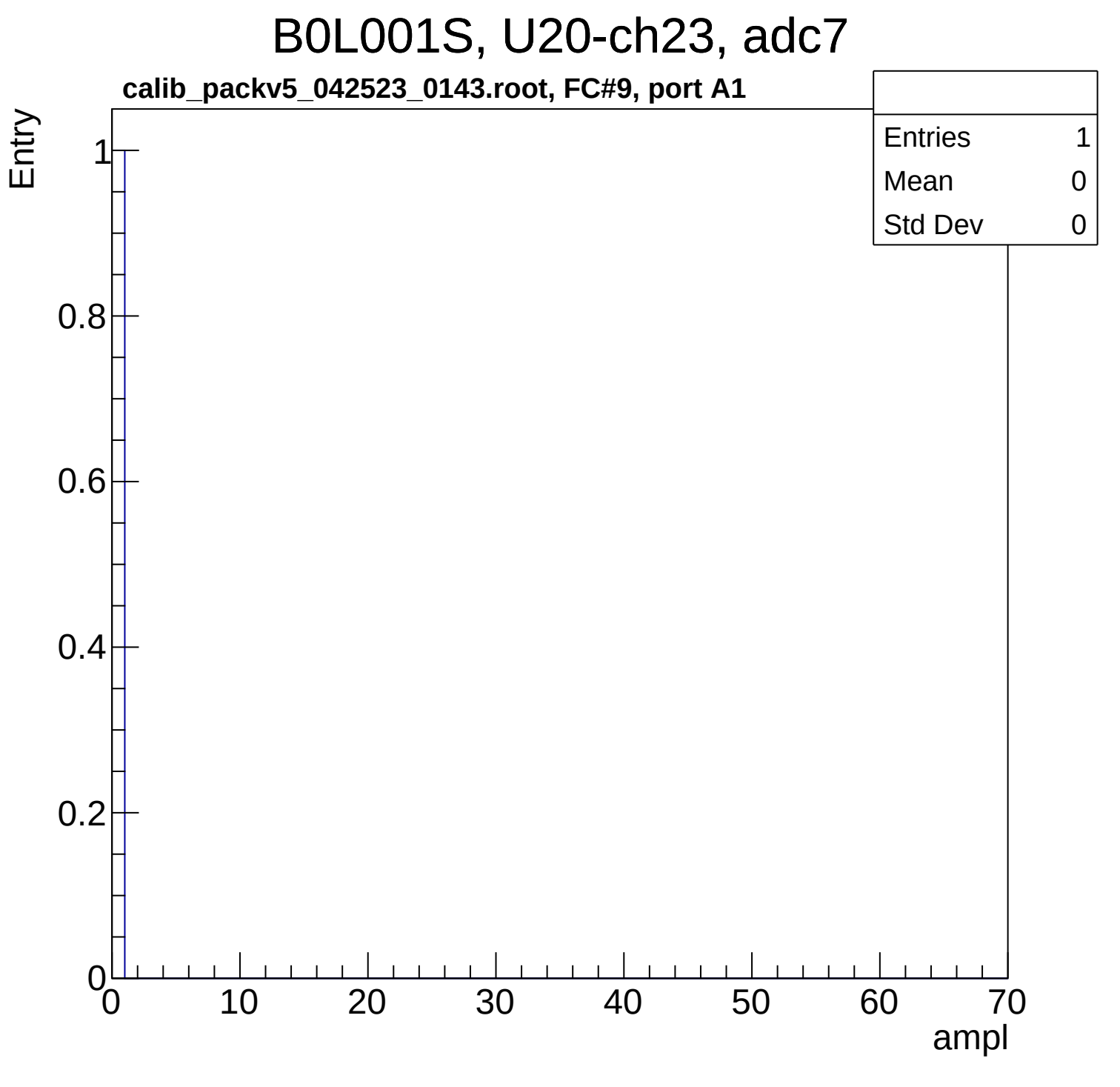
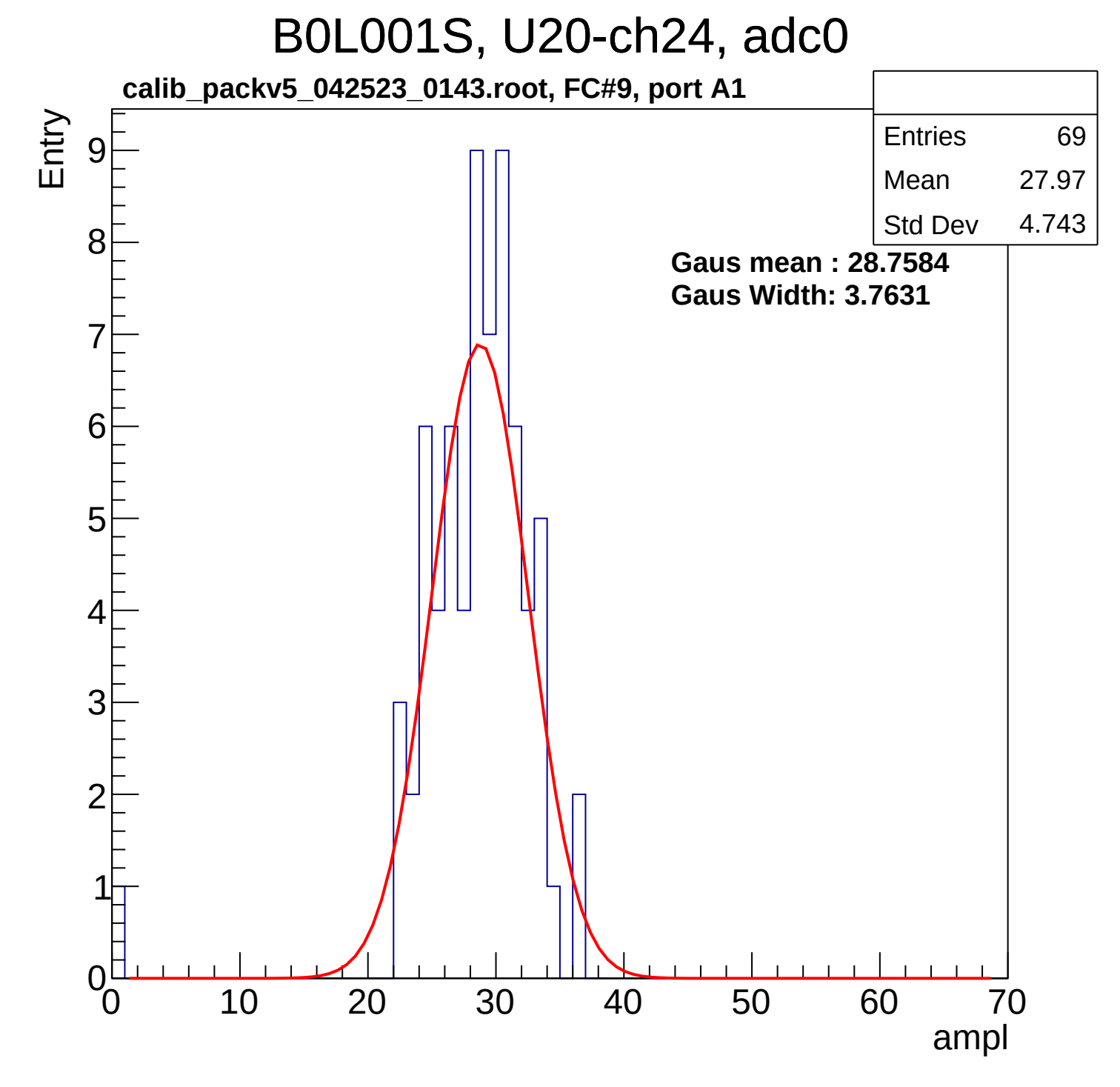
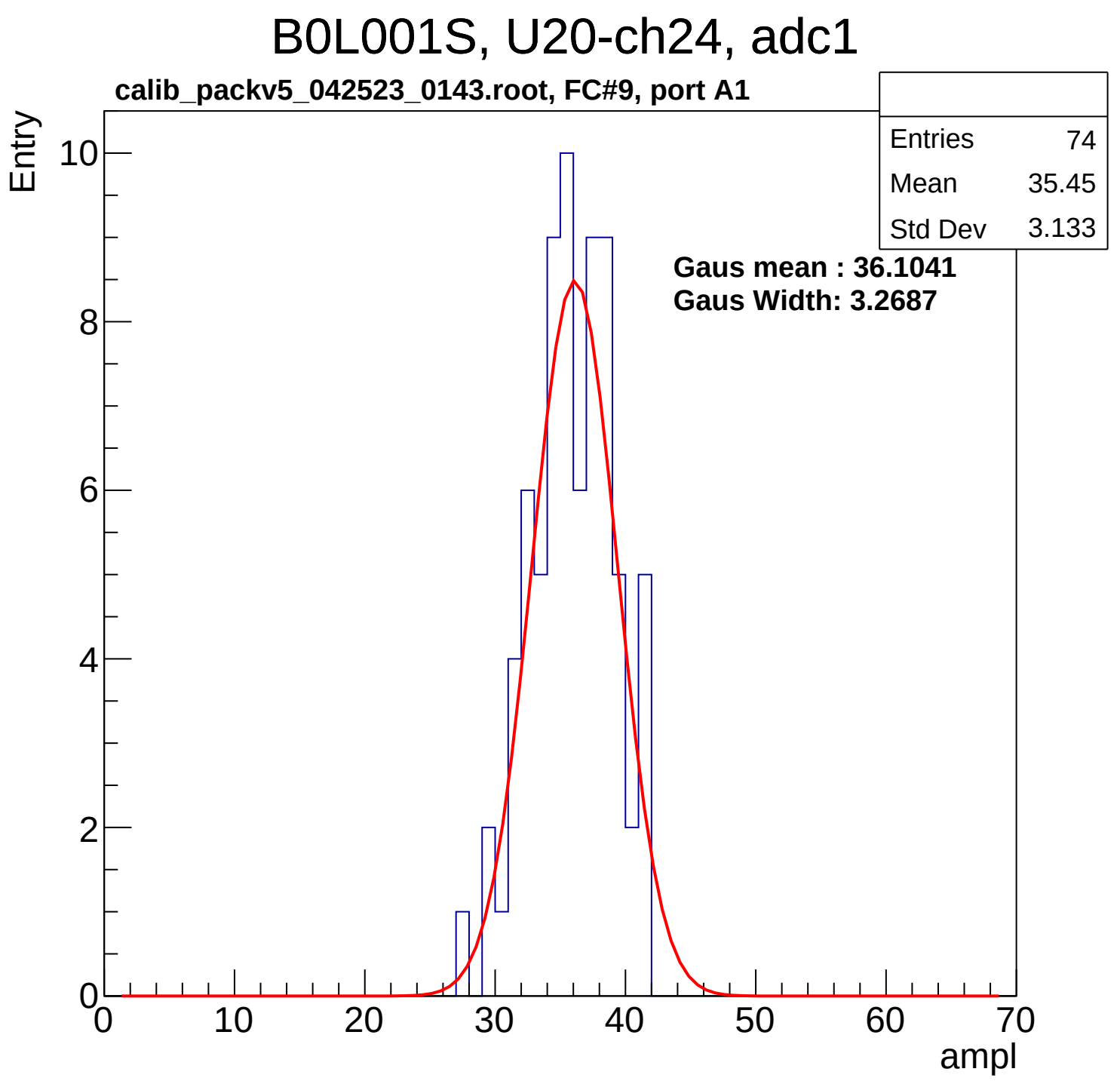
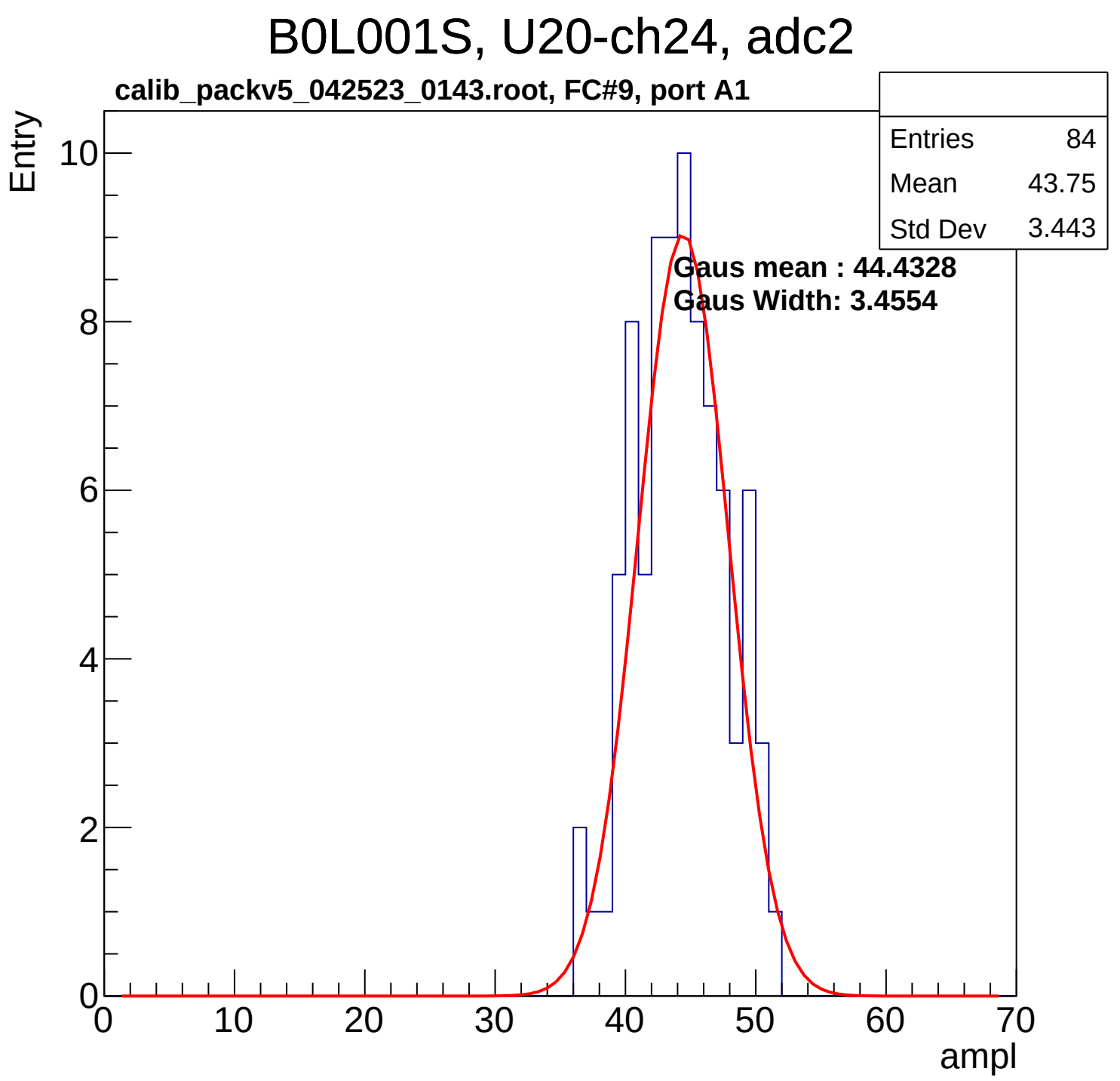
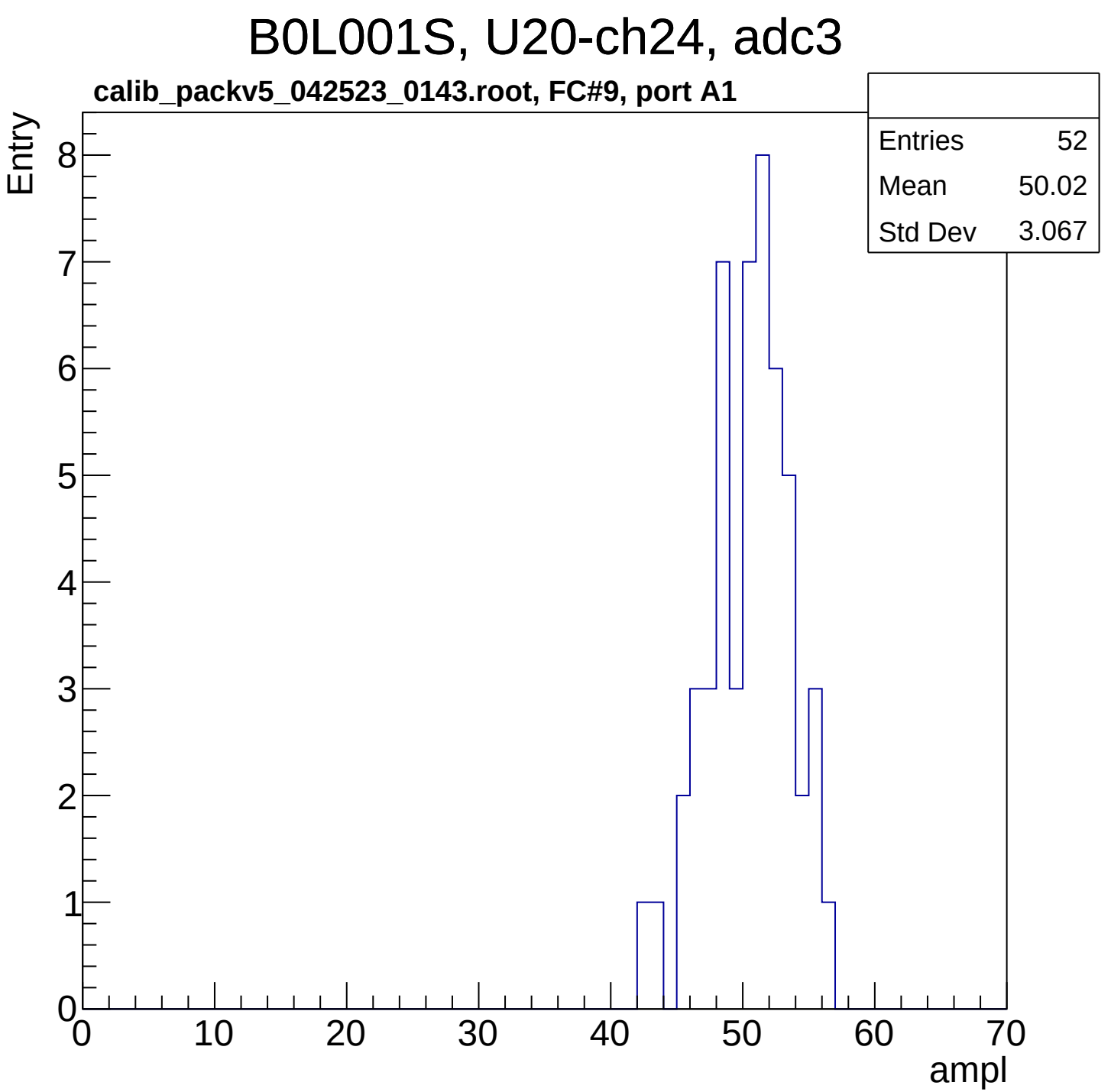

B0L001S, U20-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

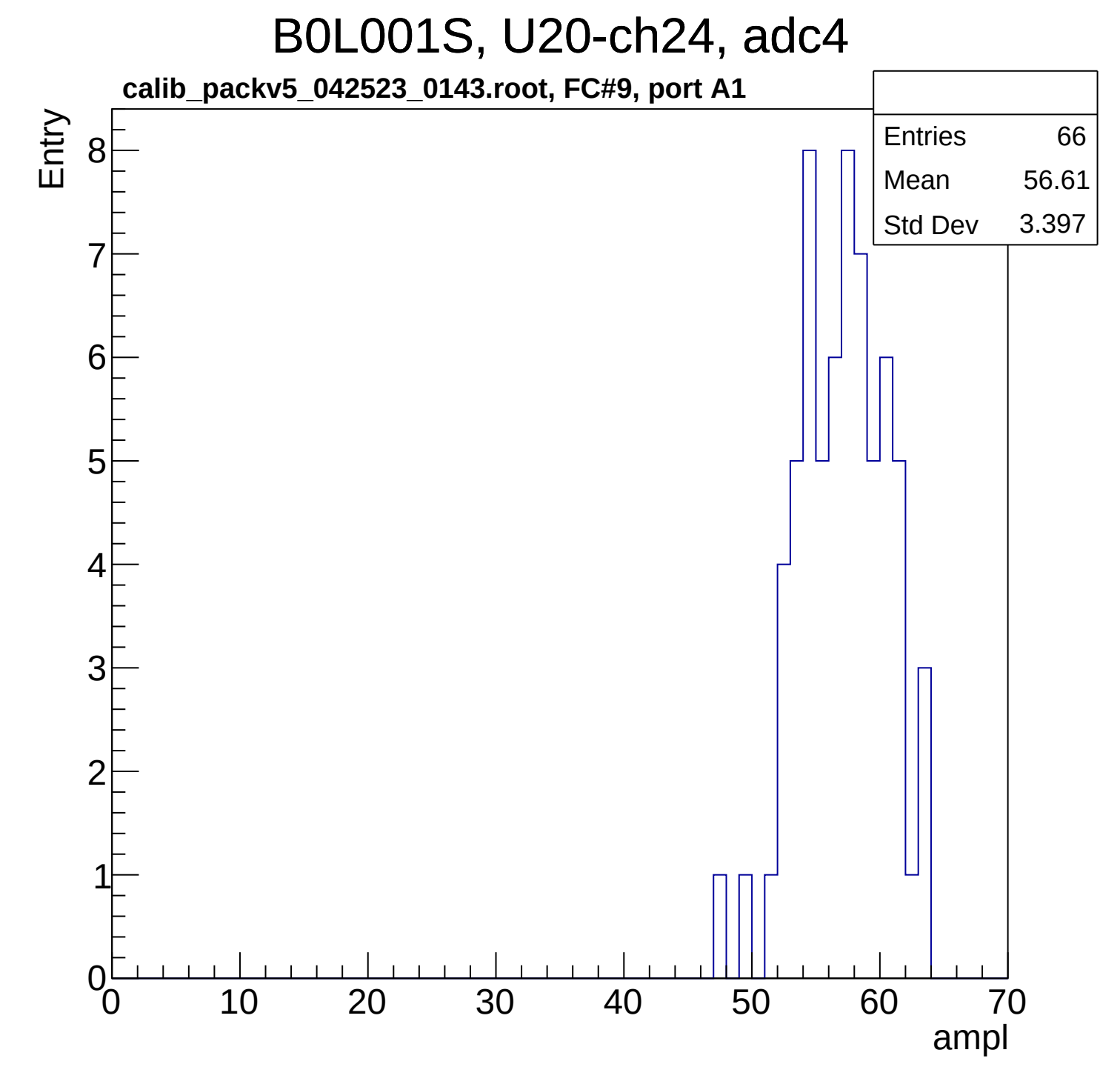

B0L001S, U20-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

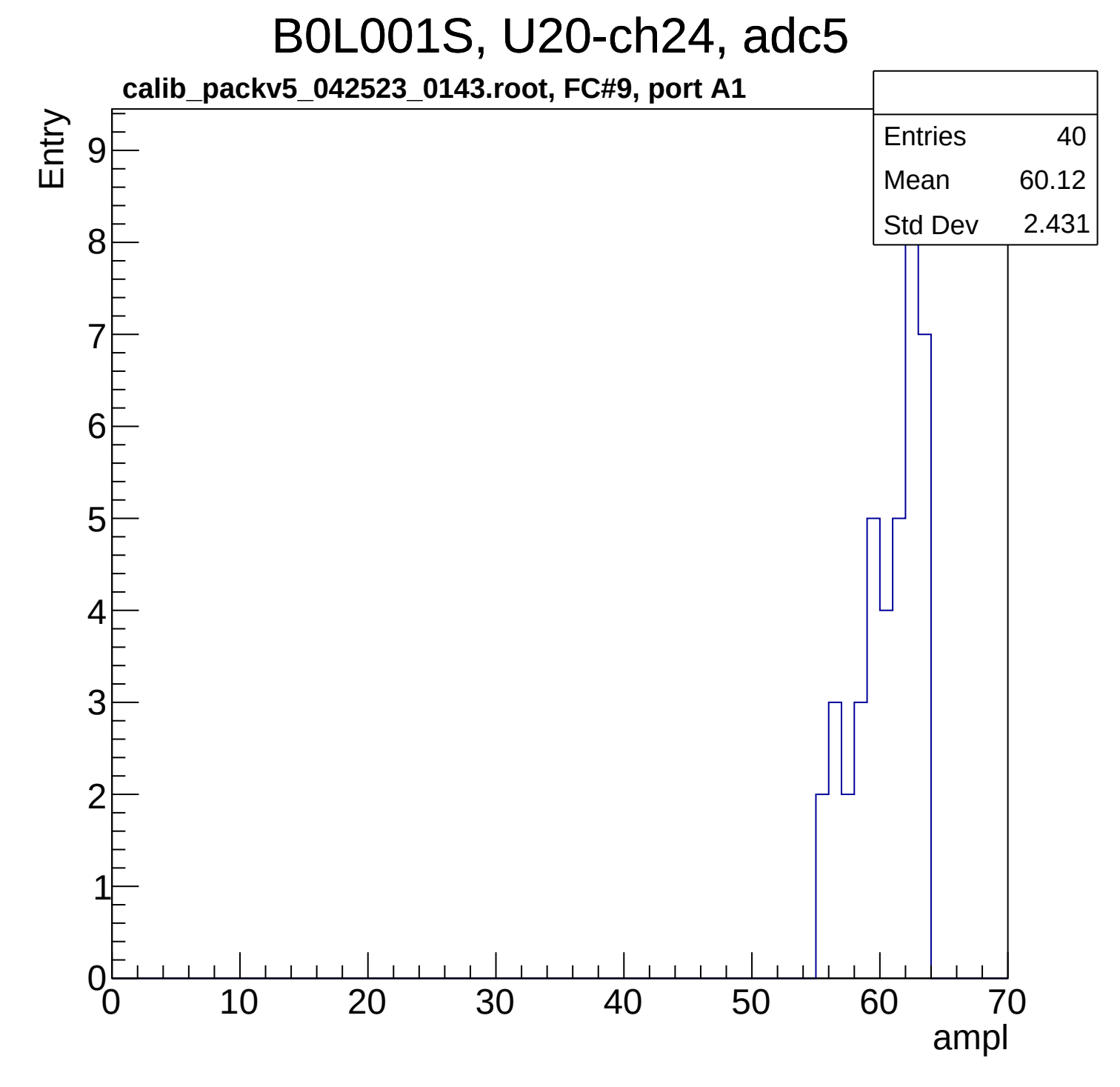

B0L001S, U20-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 27.97
Std Dev 4.743
Gaus mean : 28.7584
Gaus Width: 3.7631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

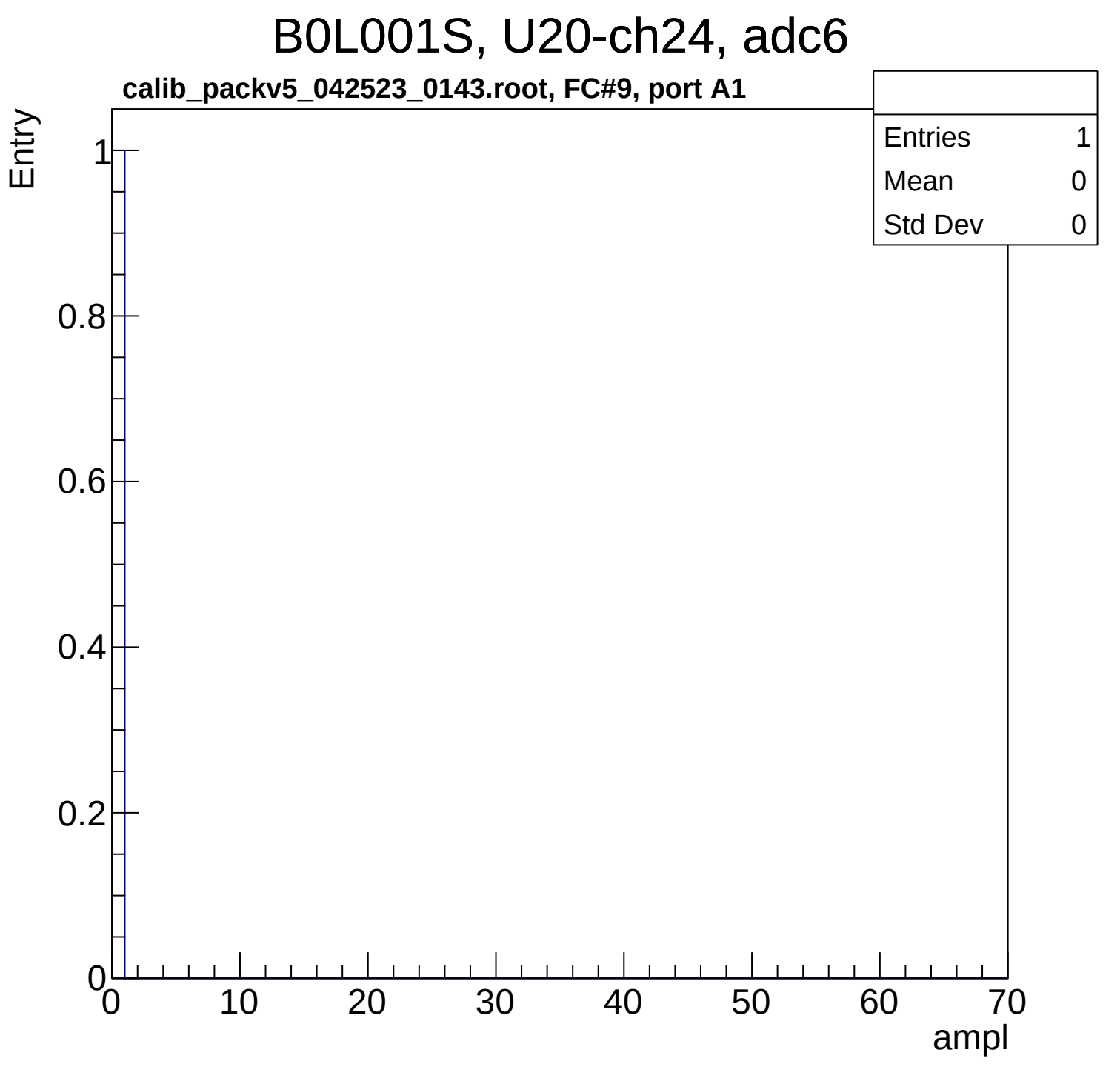

B0L001S, U20-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 35.45
Std Dev 3.133
Gaus mean : 36.1041
Gaus Width: 3.2687
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

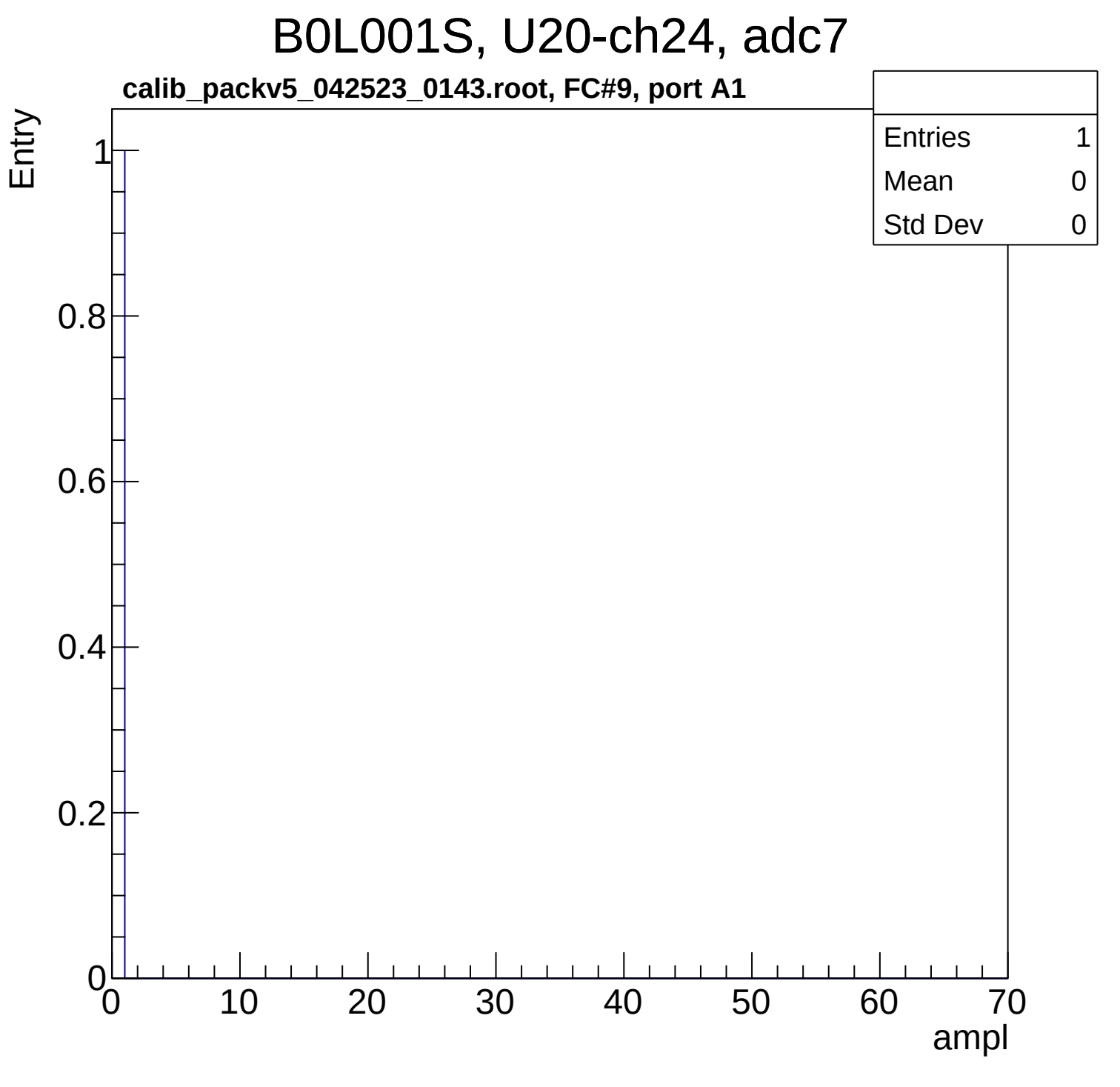

B0L001S, U20-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 43.75
Std Dev 3.443
Gaus mean : 44.4328
Gaus Width: 3.4554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

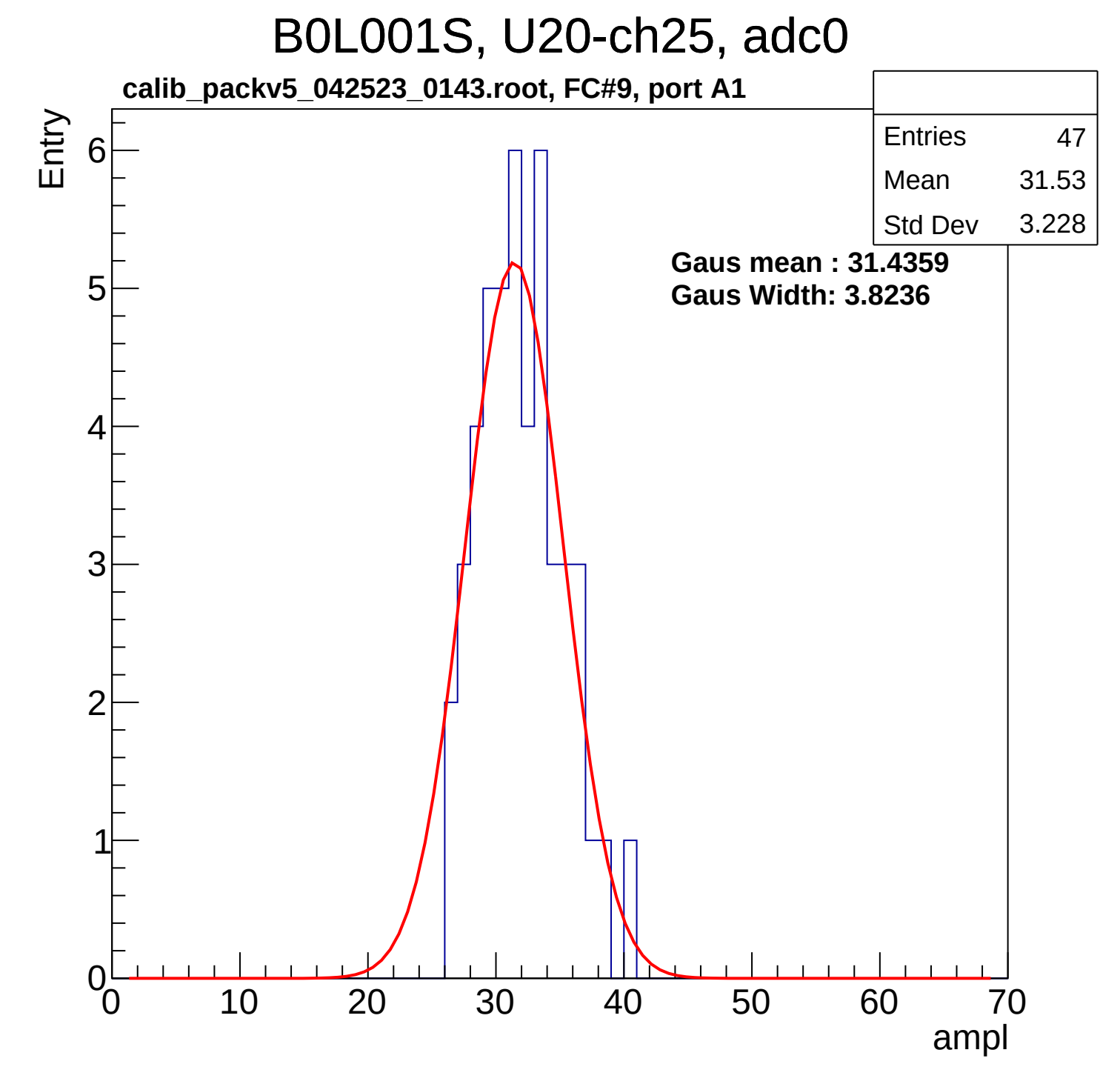

B0L001S, U20-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.02
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

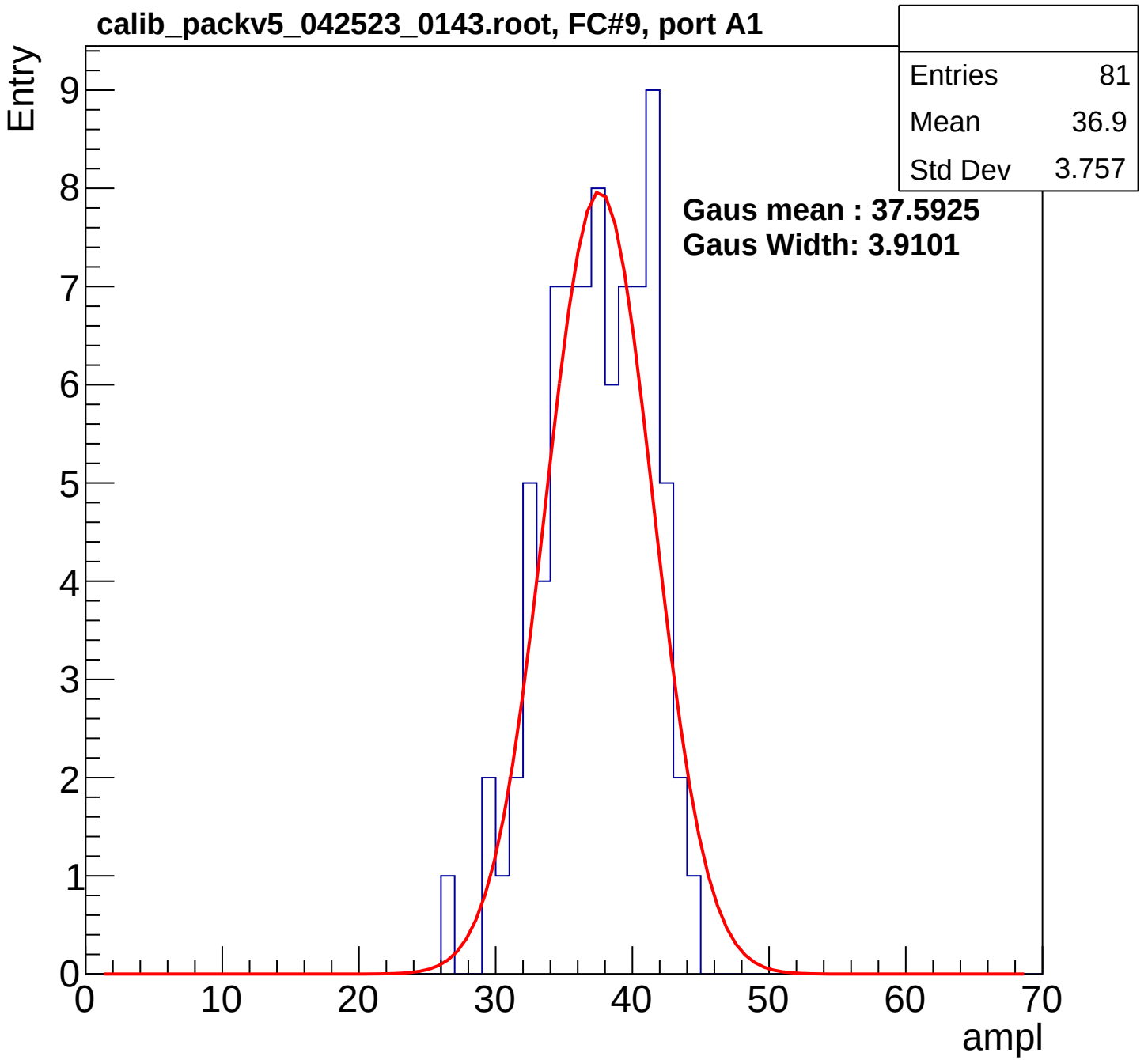

B0L001S, U20-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.61
Std Dev 3.397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

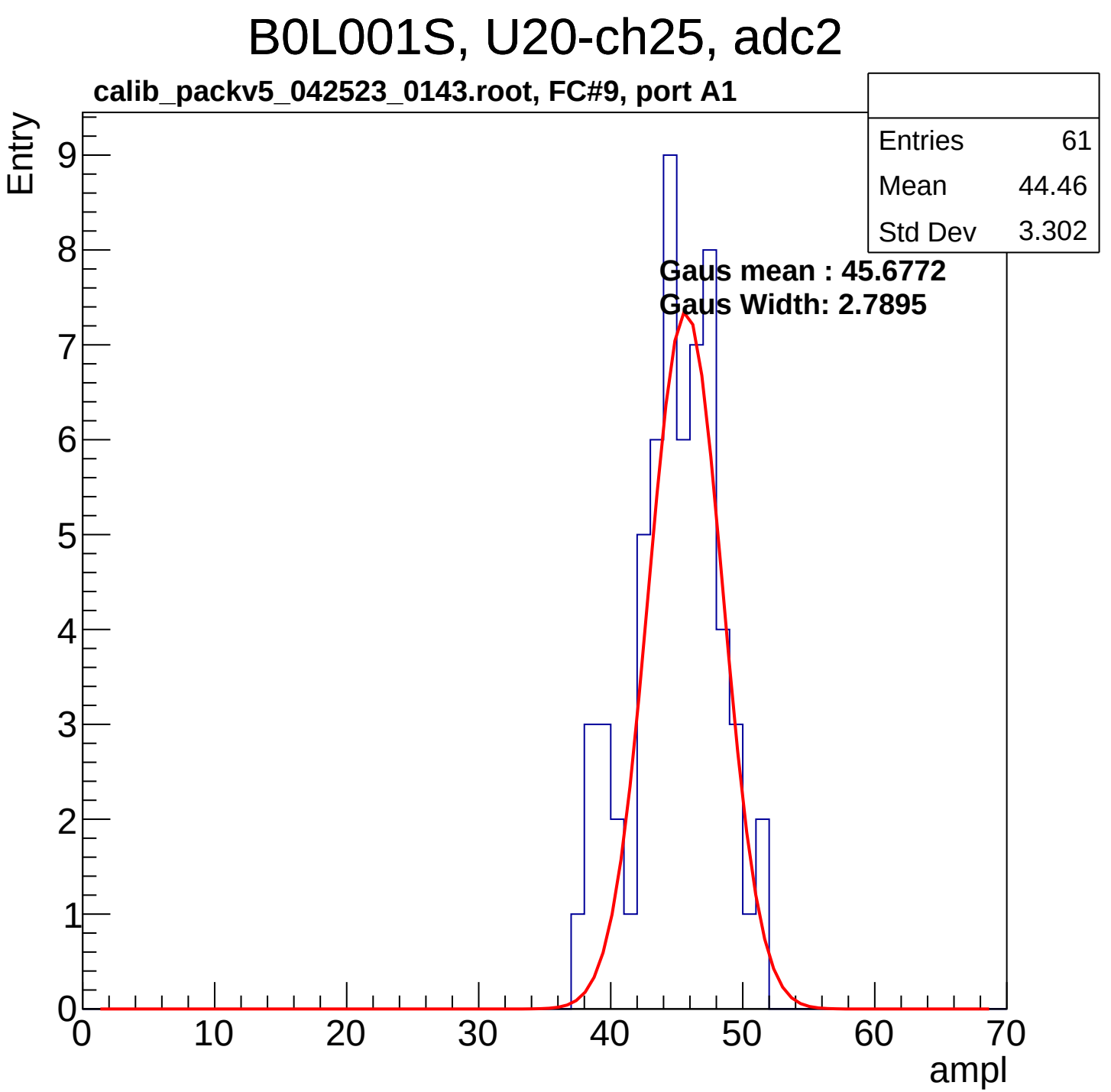

B0L001S, U20-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.12
Std Dev 2.431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

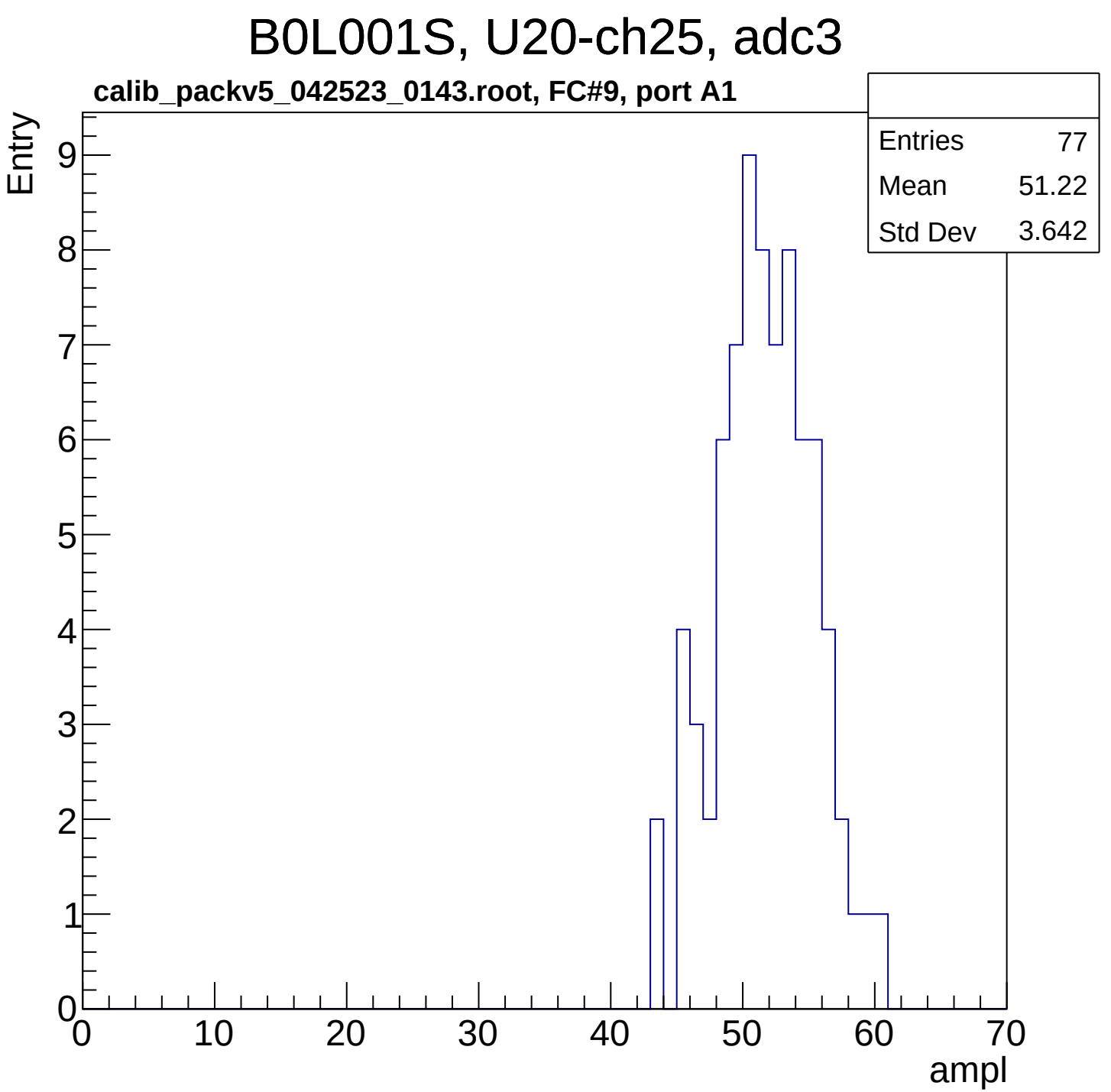

B0L001S, U20-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 31.53
Std Dev 3.228
Gaus mean : 31.4359
Gaus Width: 3.8236
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 36.9
Std Dev 3.757
Gaus mean : 37.5925
Gaus Width: 3.9101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.46
Std Dev 3.302
Gaus mean : 45.6772
Gaus Width: 2.7895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.22
Std Dev 3.642
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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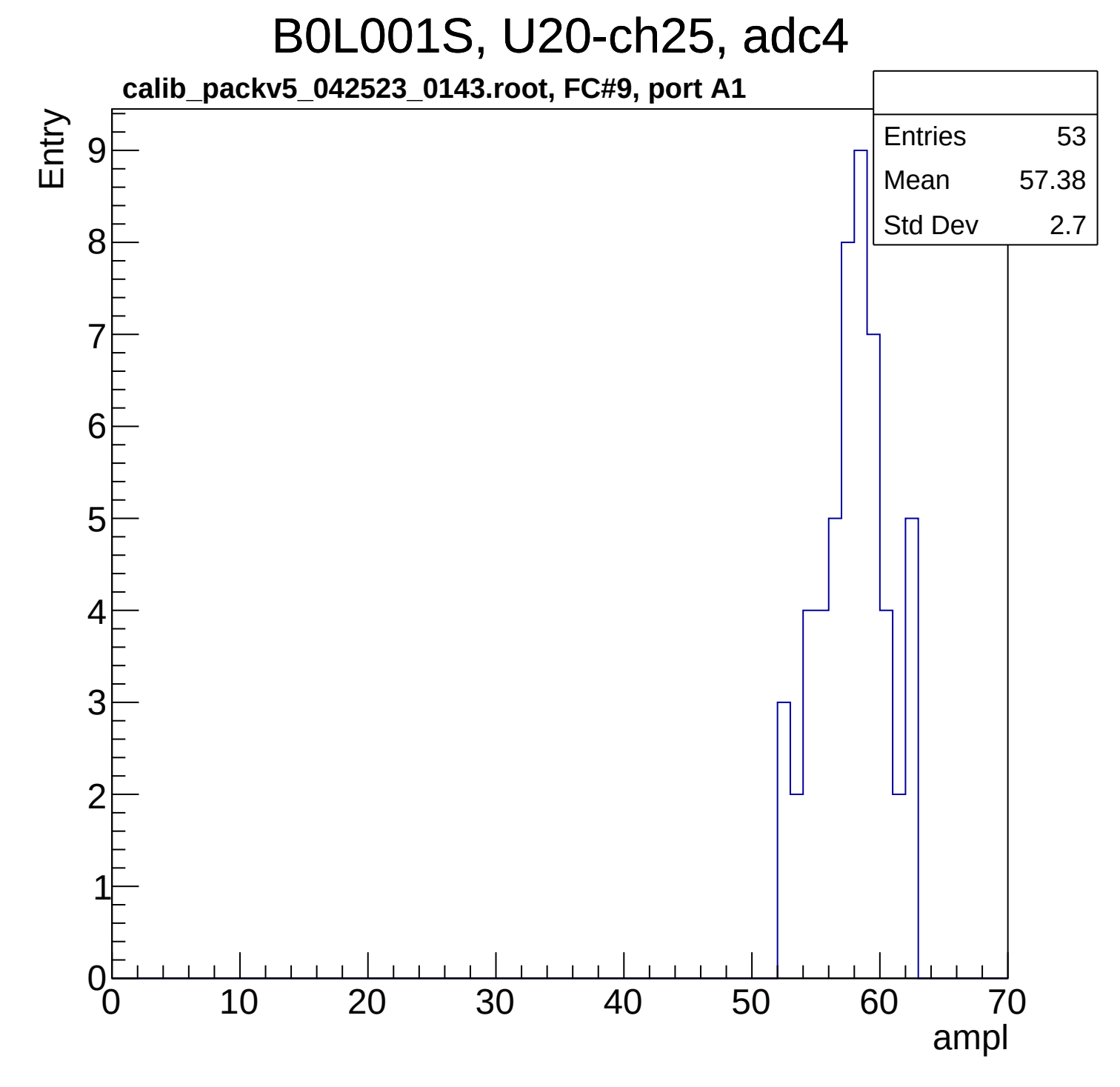
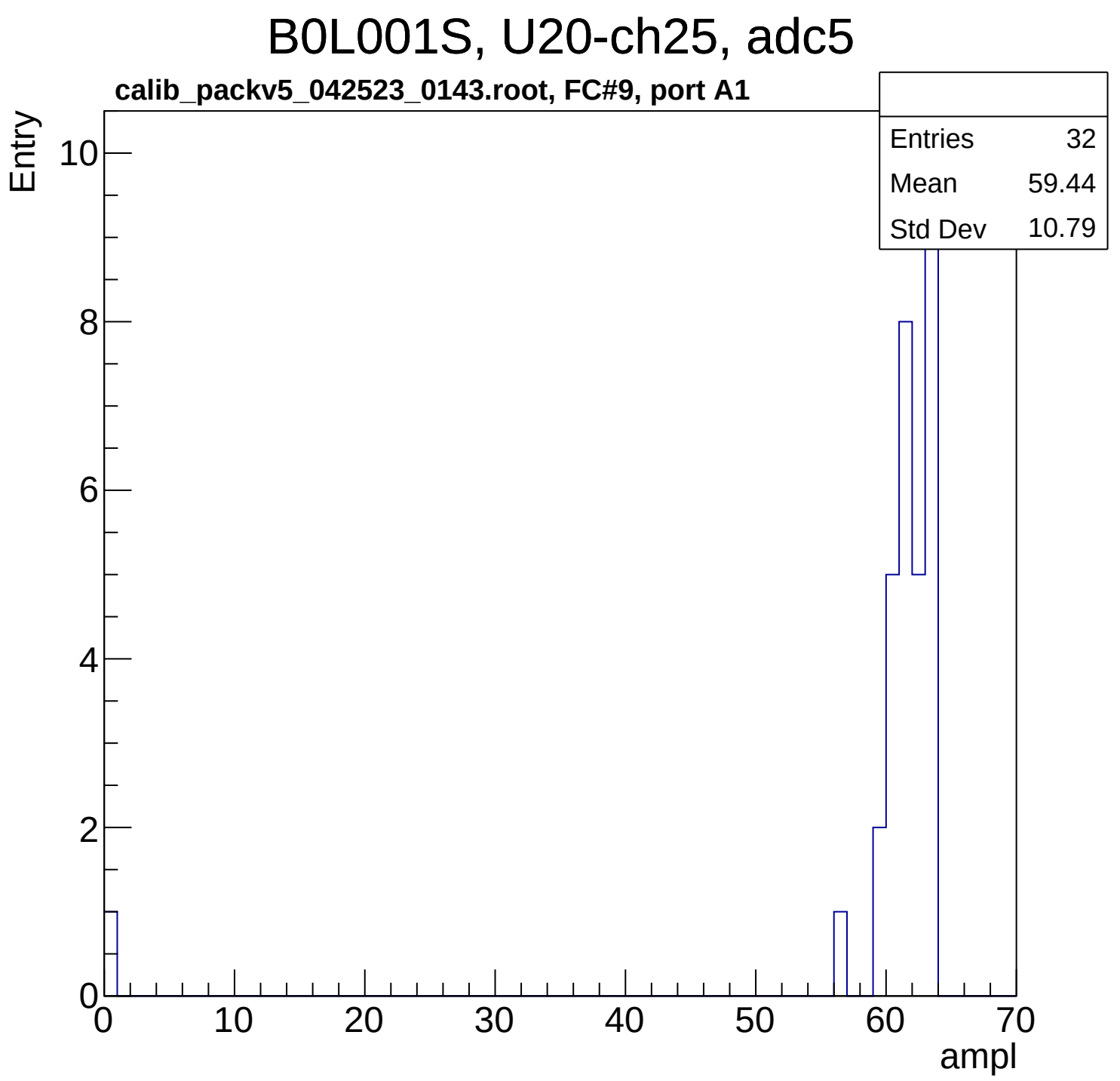
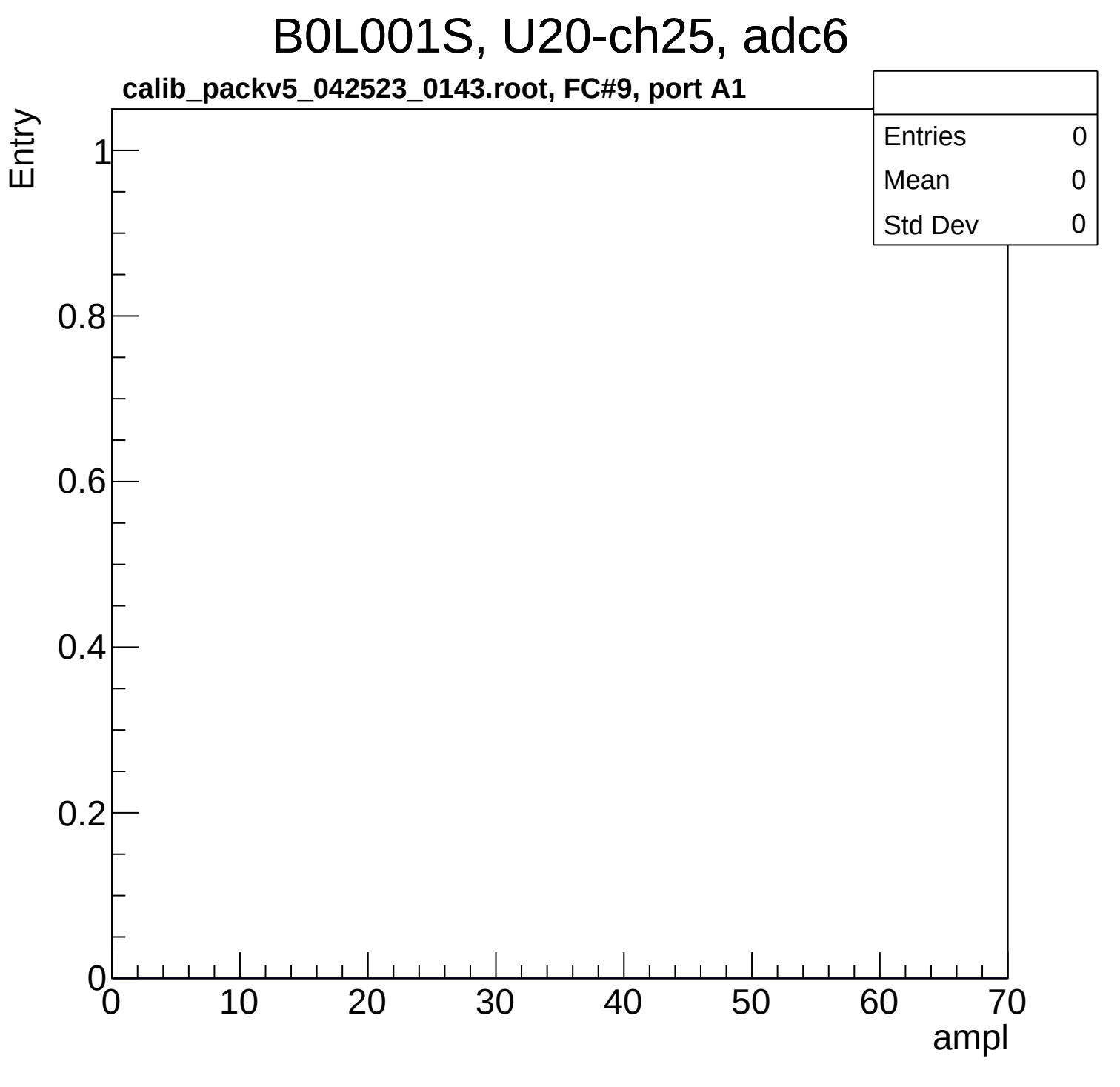

B0L001S, U20-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.38
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.44
Std Dev 10.79
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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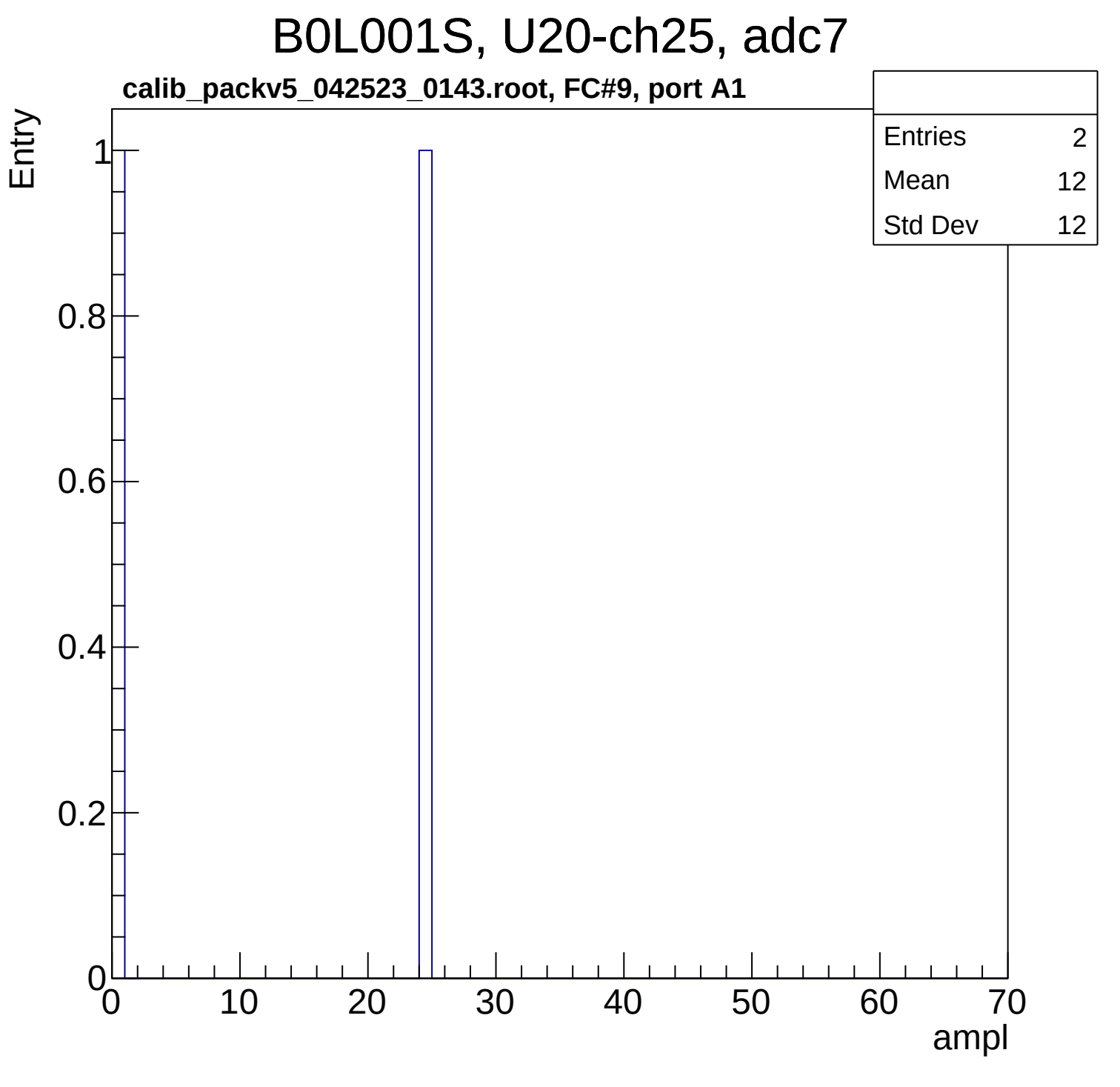
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
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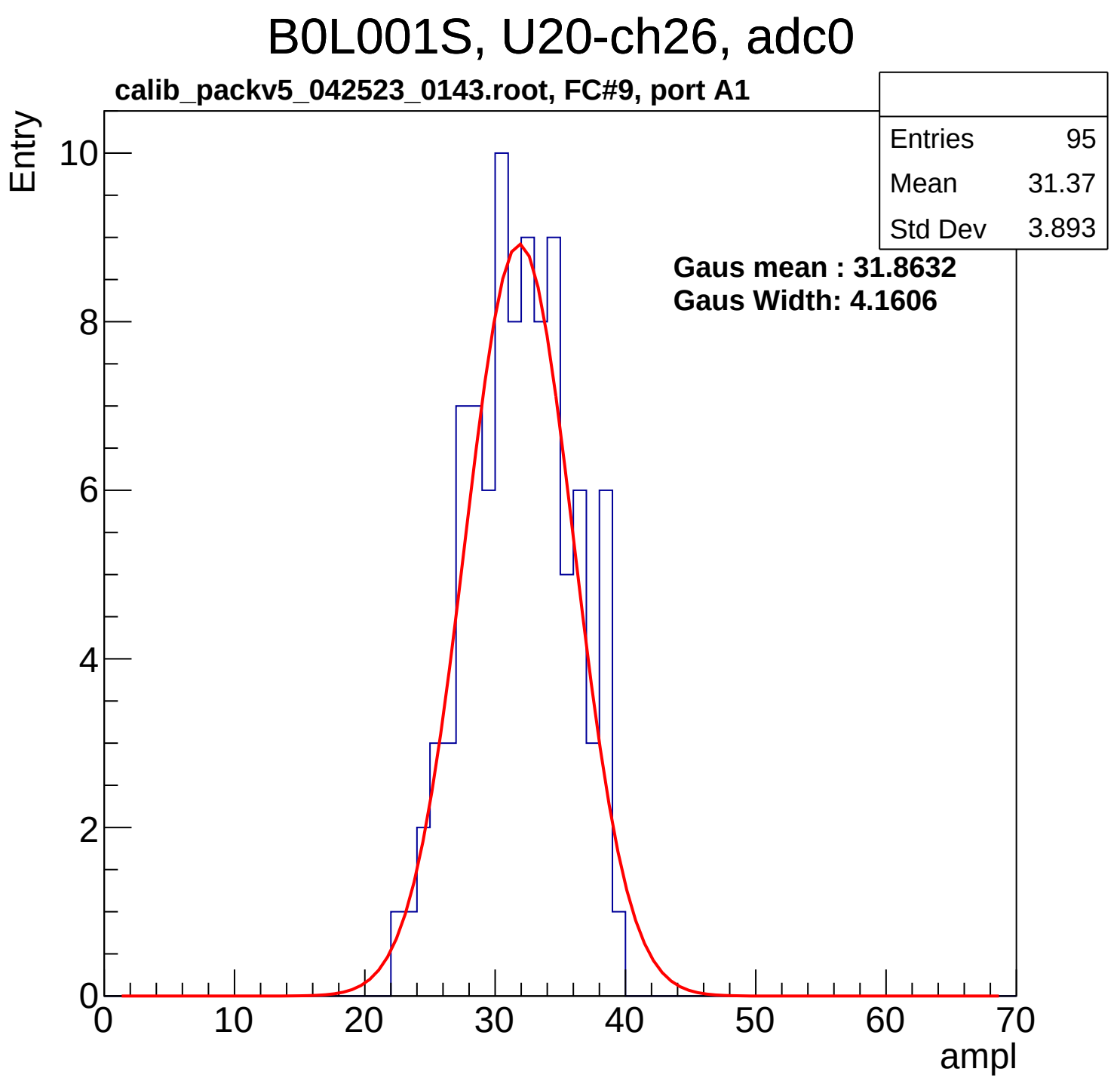
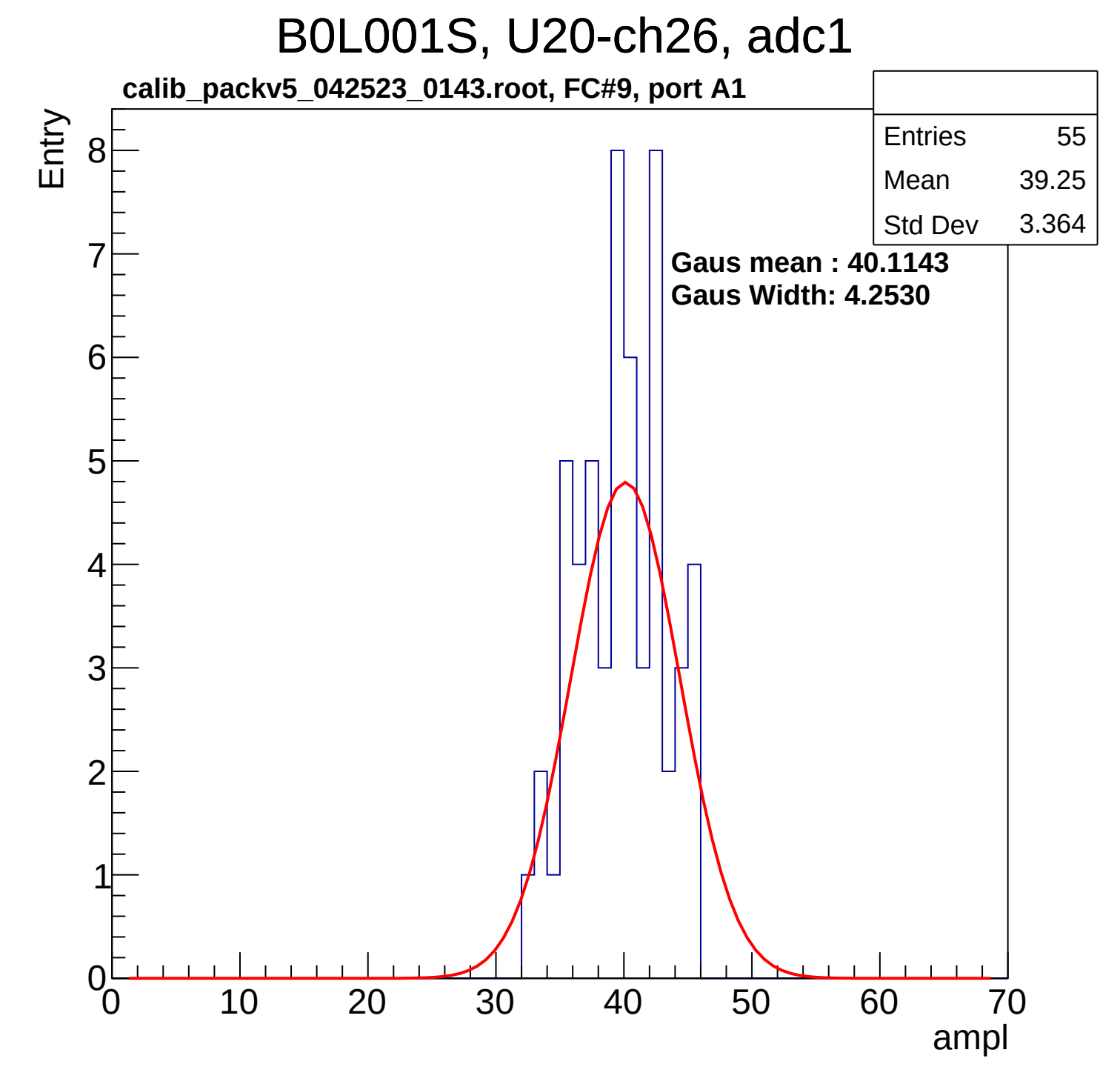
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 95
Mean 31.37
Std Dev 3.893
Gaus mean : 31.8632
Gaus Width: 4.1606
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 39.25
Std Dev 3.364
Gaus mean : 40.1143
Gaus Width: 4.2530
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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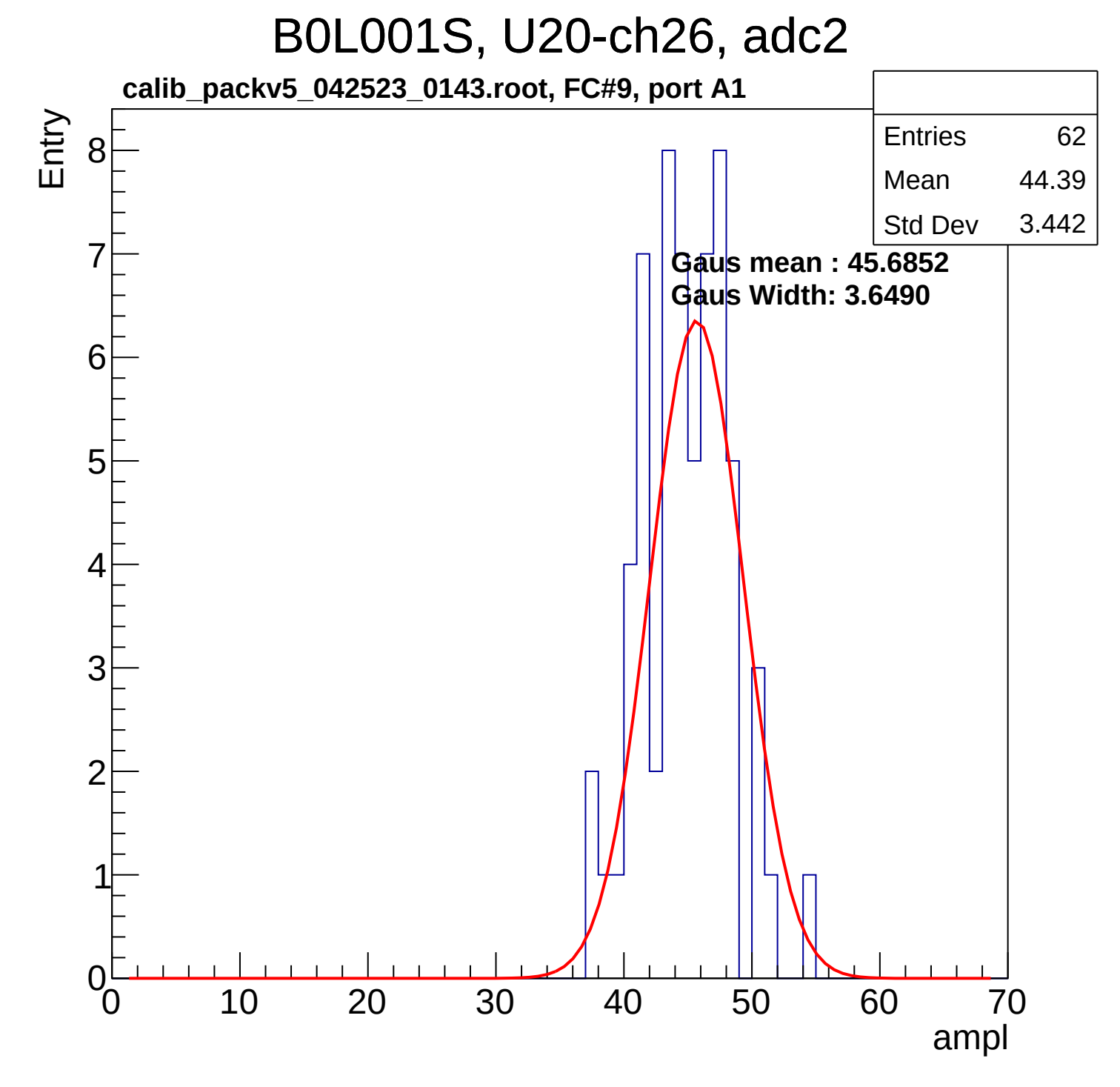
B0L001S, U20-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.39
Std Dev 3.442
Gaus mean : 45.6852
Gaus Width: 3.6490
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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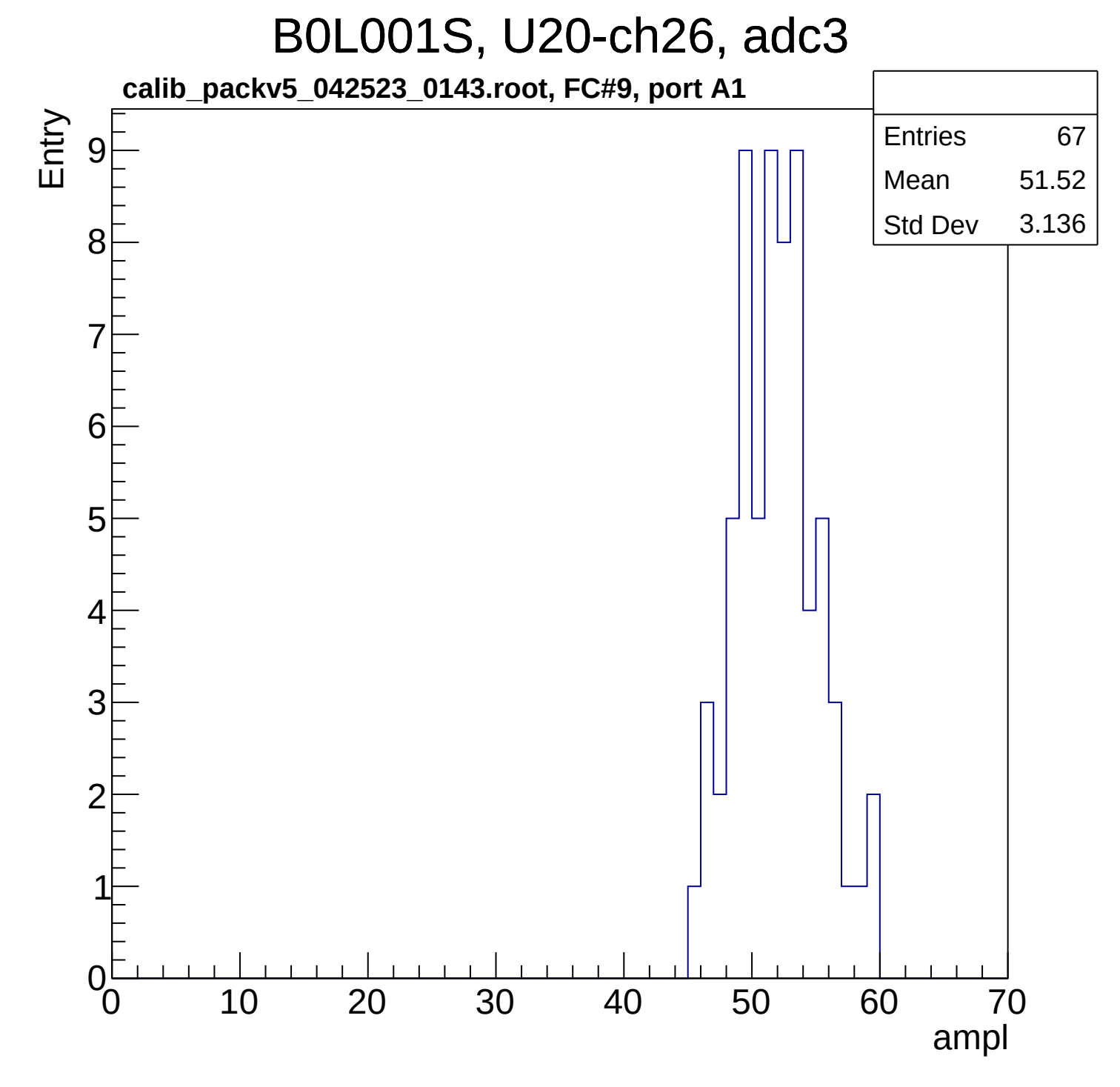
B0L001S, U20-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.52
Std Dev 3.136
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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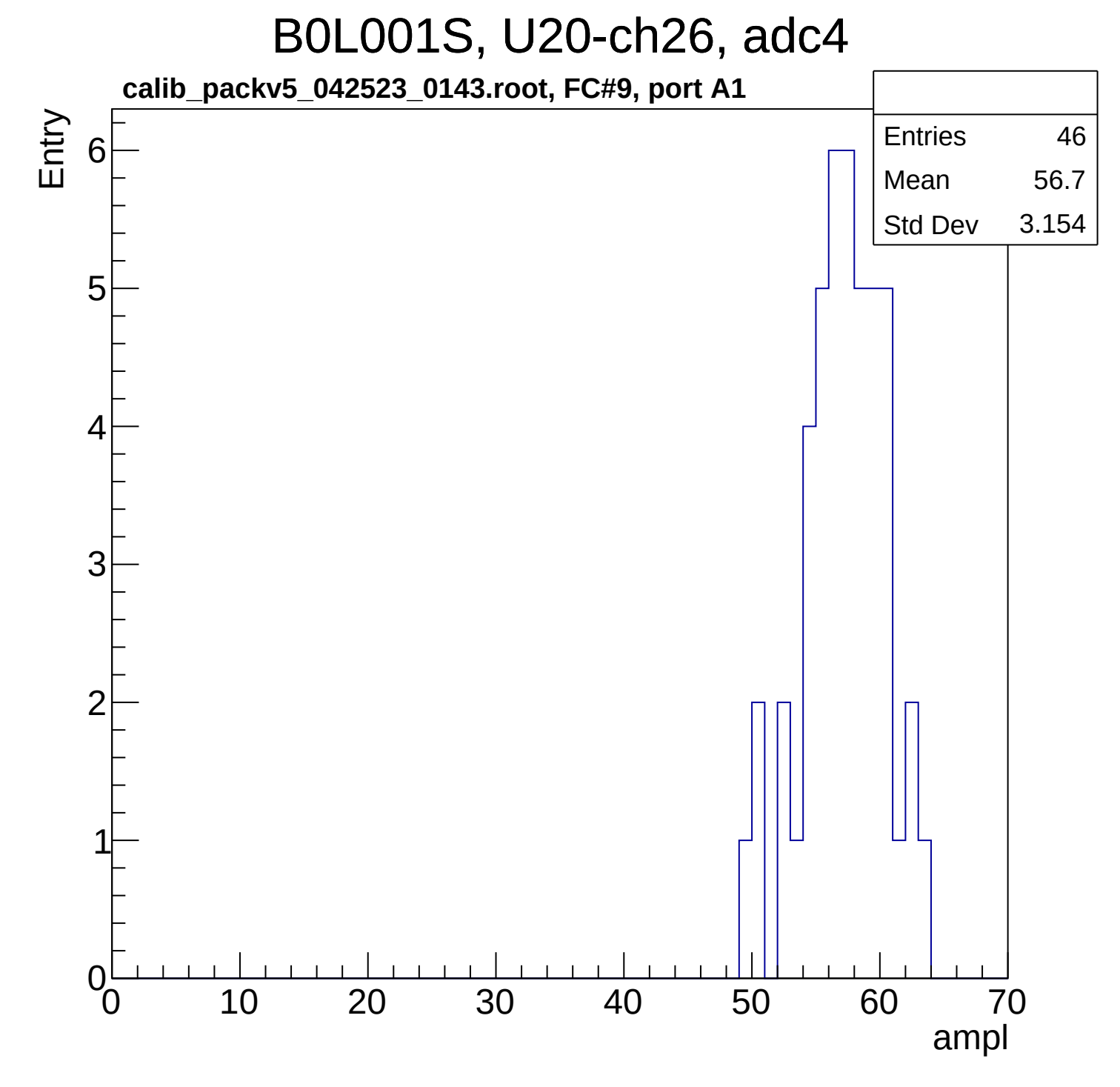
B0L001S, U20-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 56.7
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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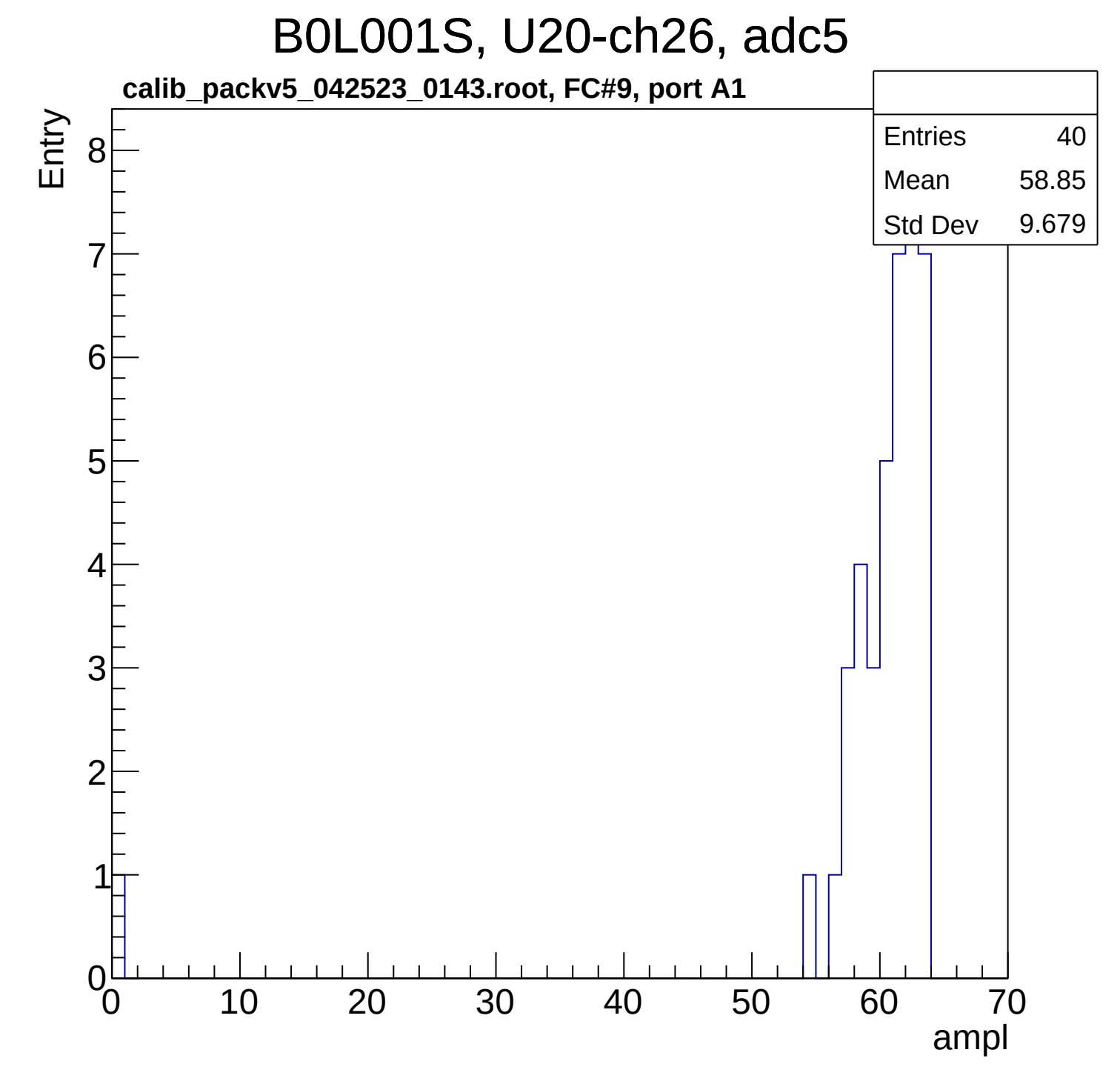
B0L001S, U20-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.85
Std Dev 9.679
Entry
ampl
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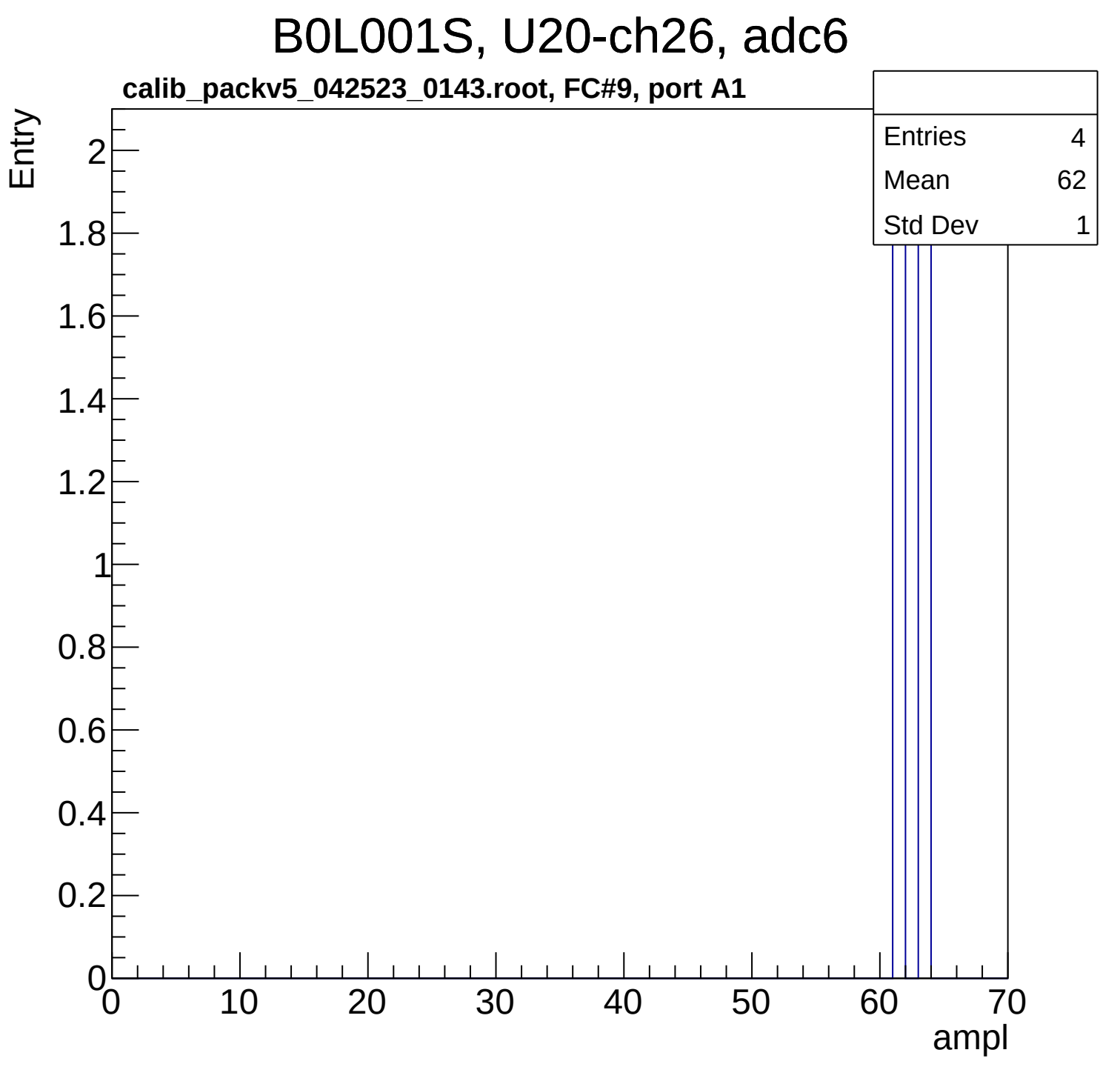
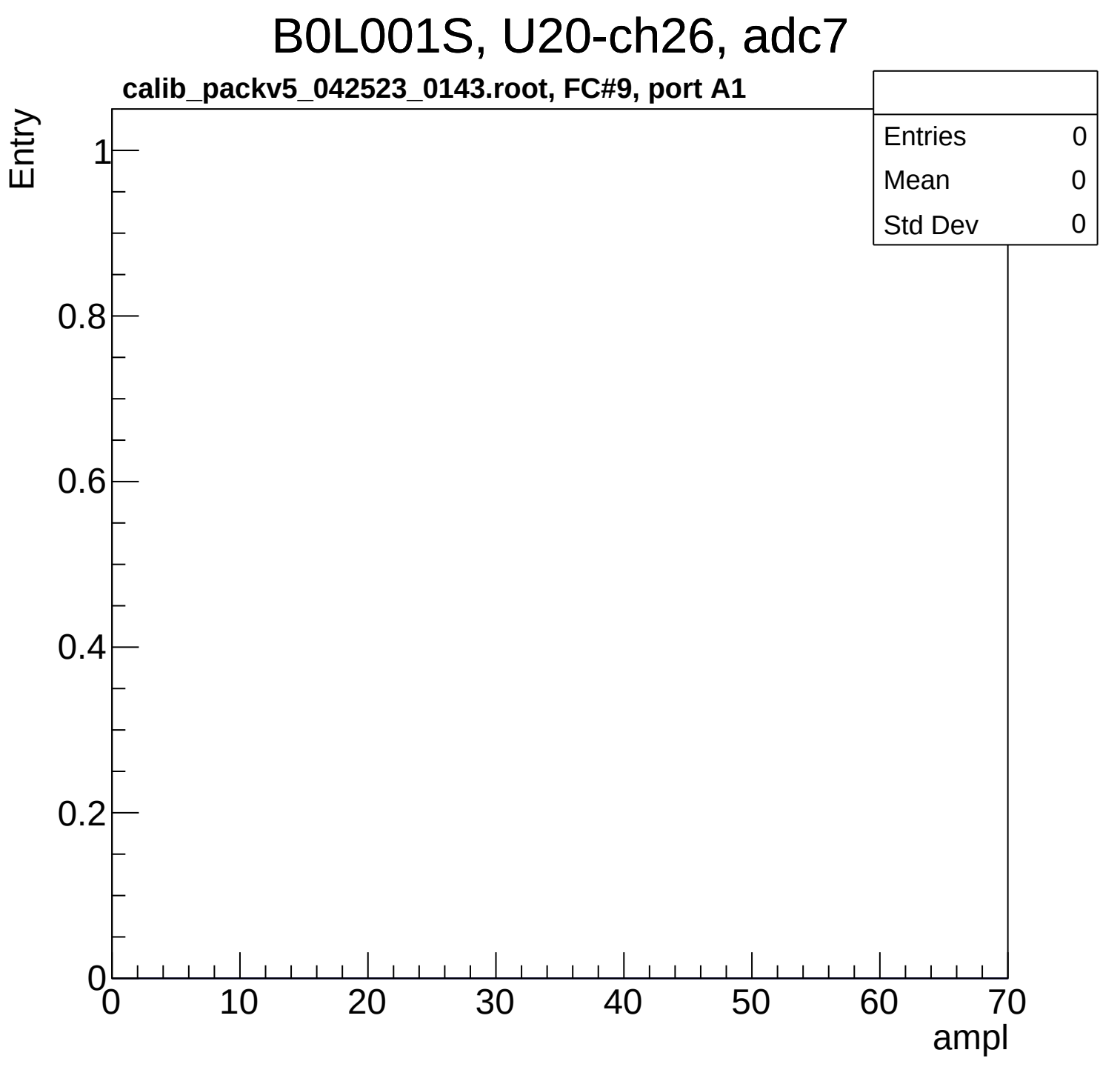
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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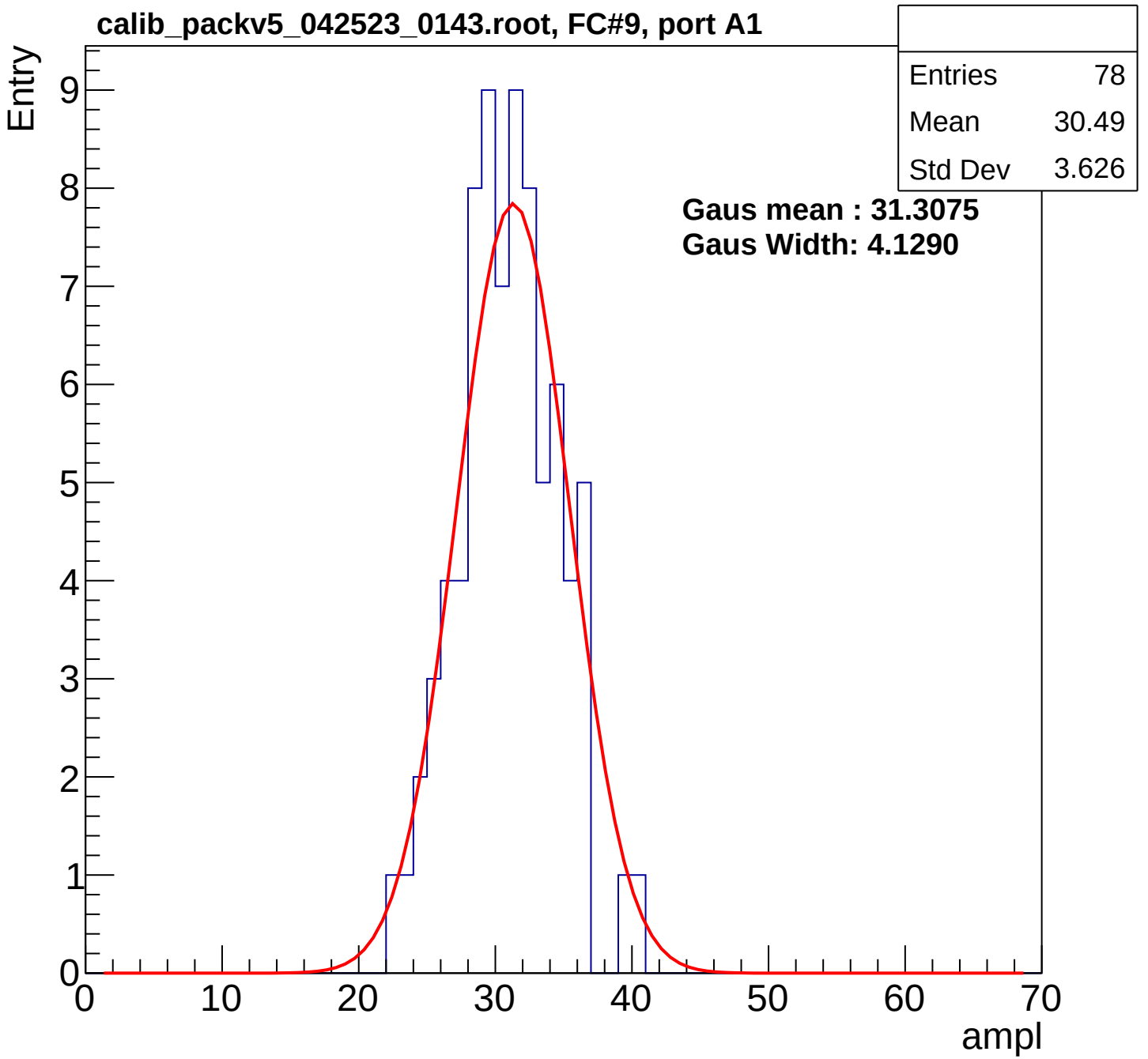
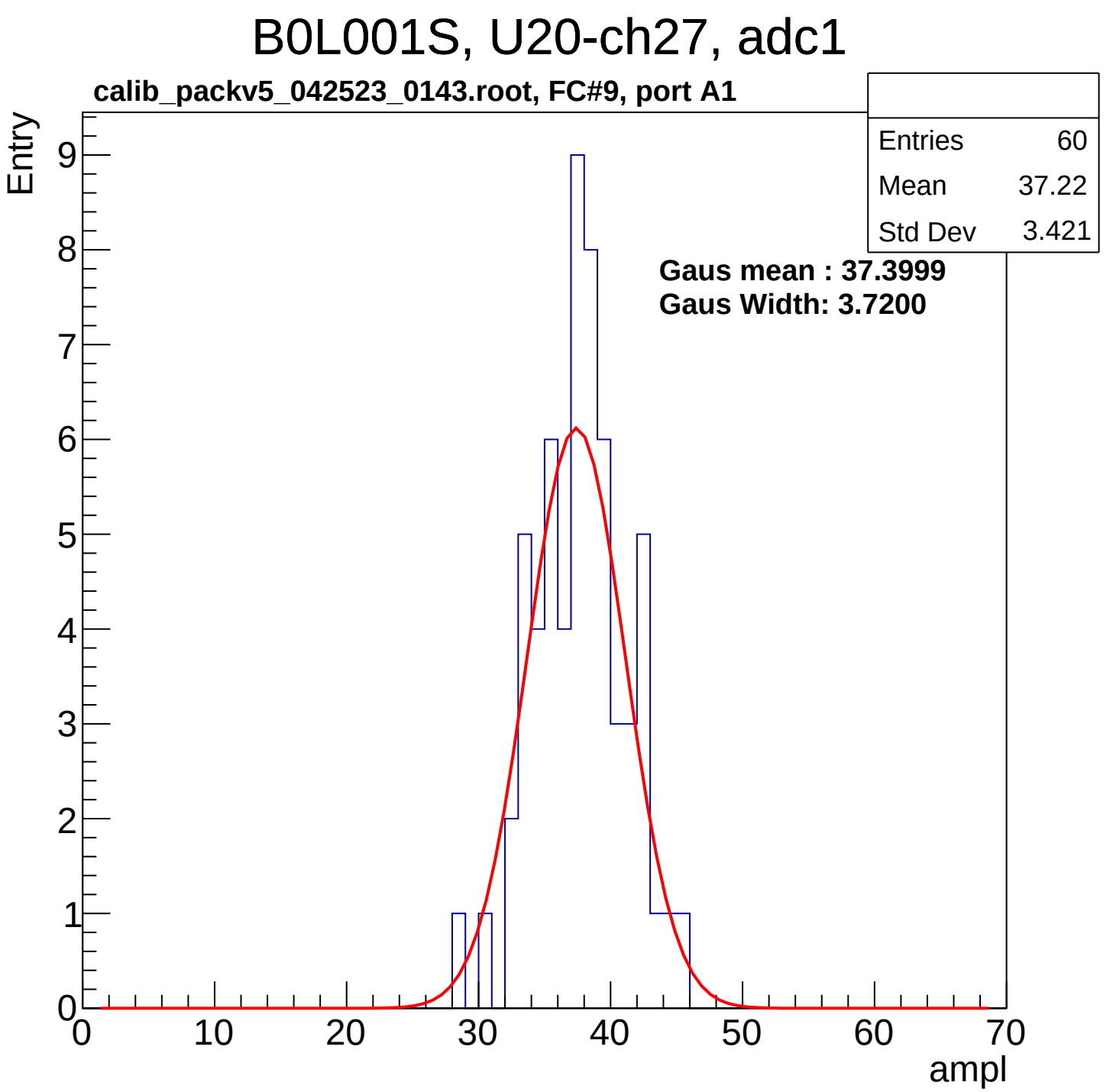
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.49
Std Dev 3.626
Gaus mean : 31.3075
Gaus Width: 4.1290
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.22
Std Dev 3.421
Gaus mean : 37.3999
Gaus Width: 3.7200
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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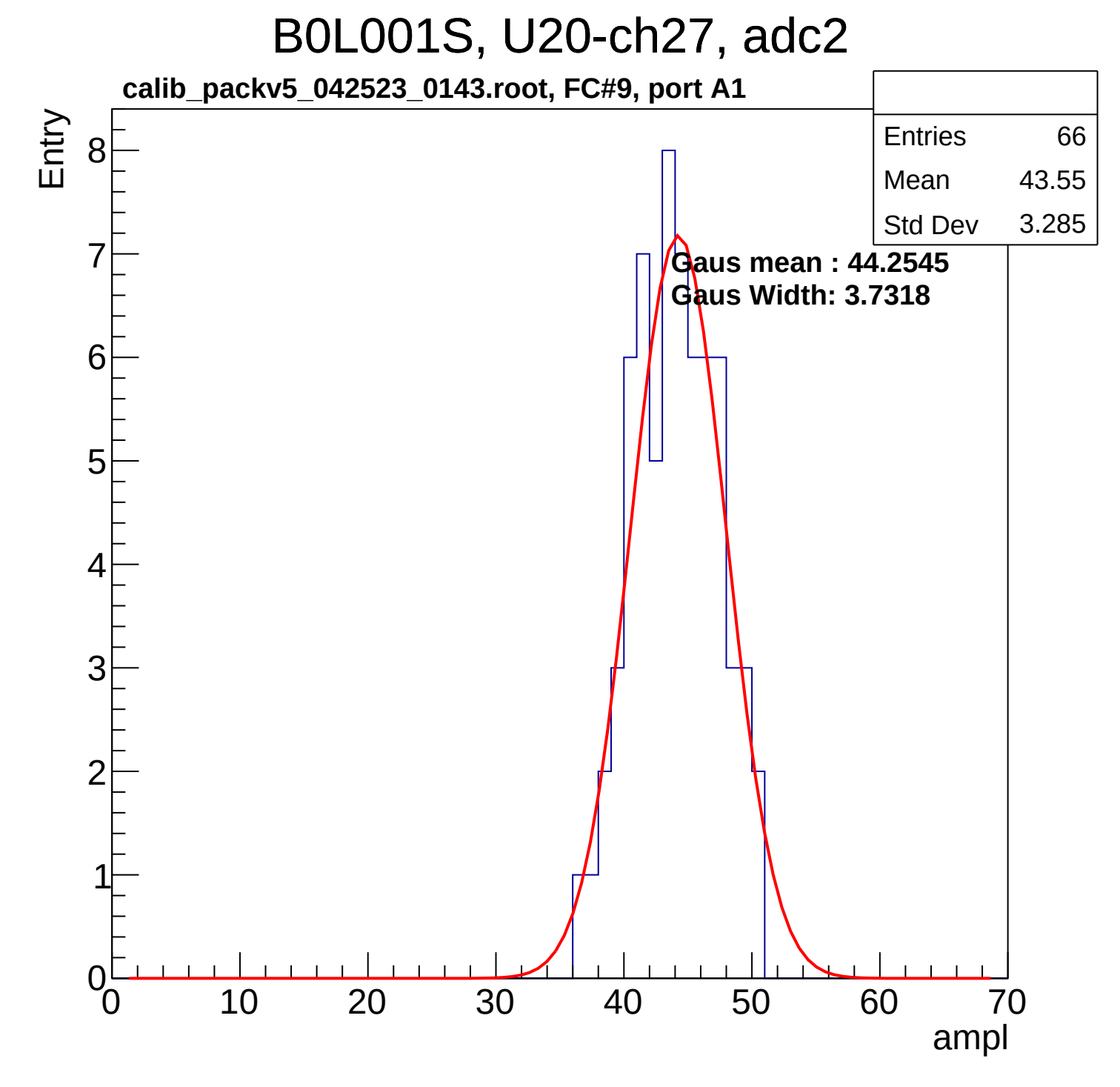
B0L001S, U20-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.55
Std Dev 3.285
Gaus mean : 44.2545
Gaus Width: 3.7318
Entry
ampl
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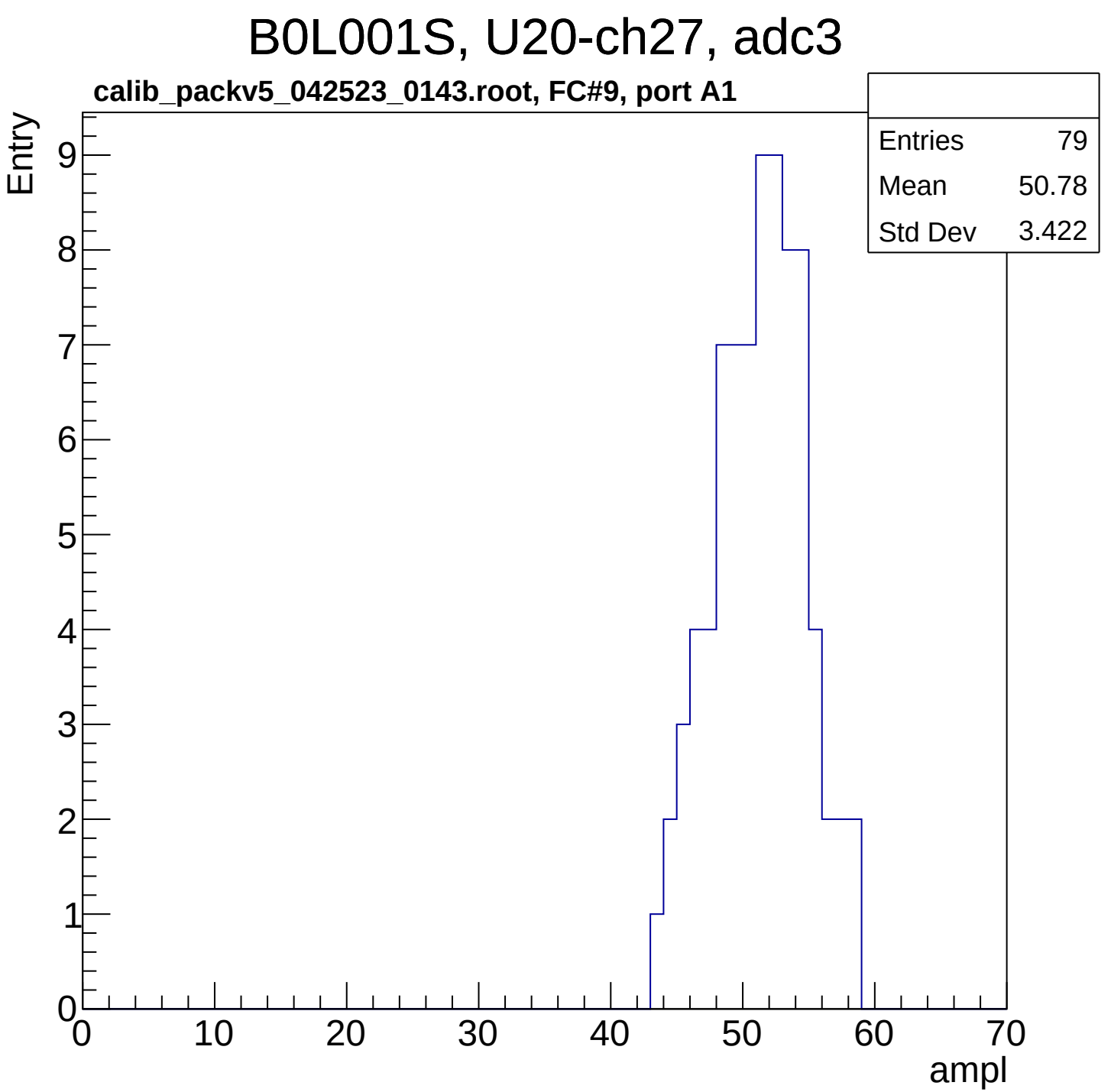
B0L001S, U20-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 50.78
Std Dev 3.422
Entry
ampl
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6
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0
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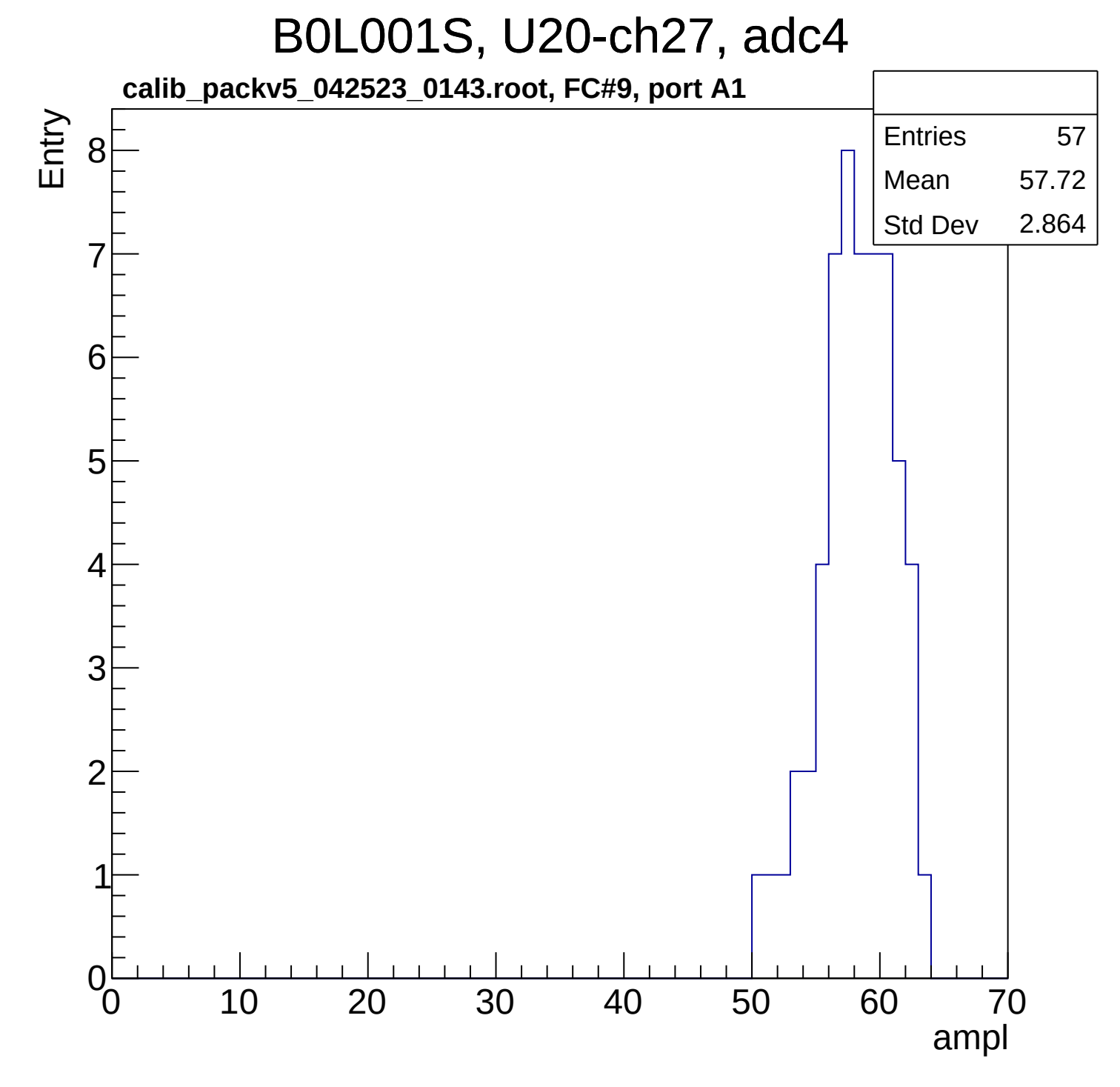
B0L001S, U20-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.72
Std Dev 2.864
Entry
ampl
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2
3
4
5
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0
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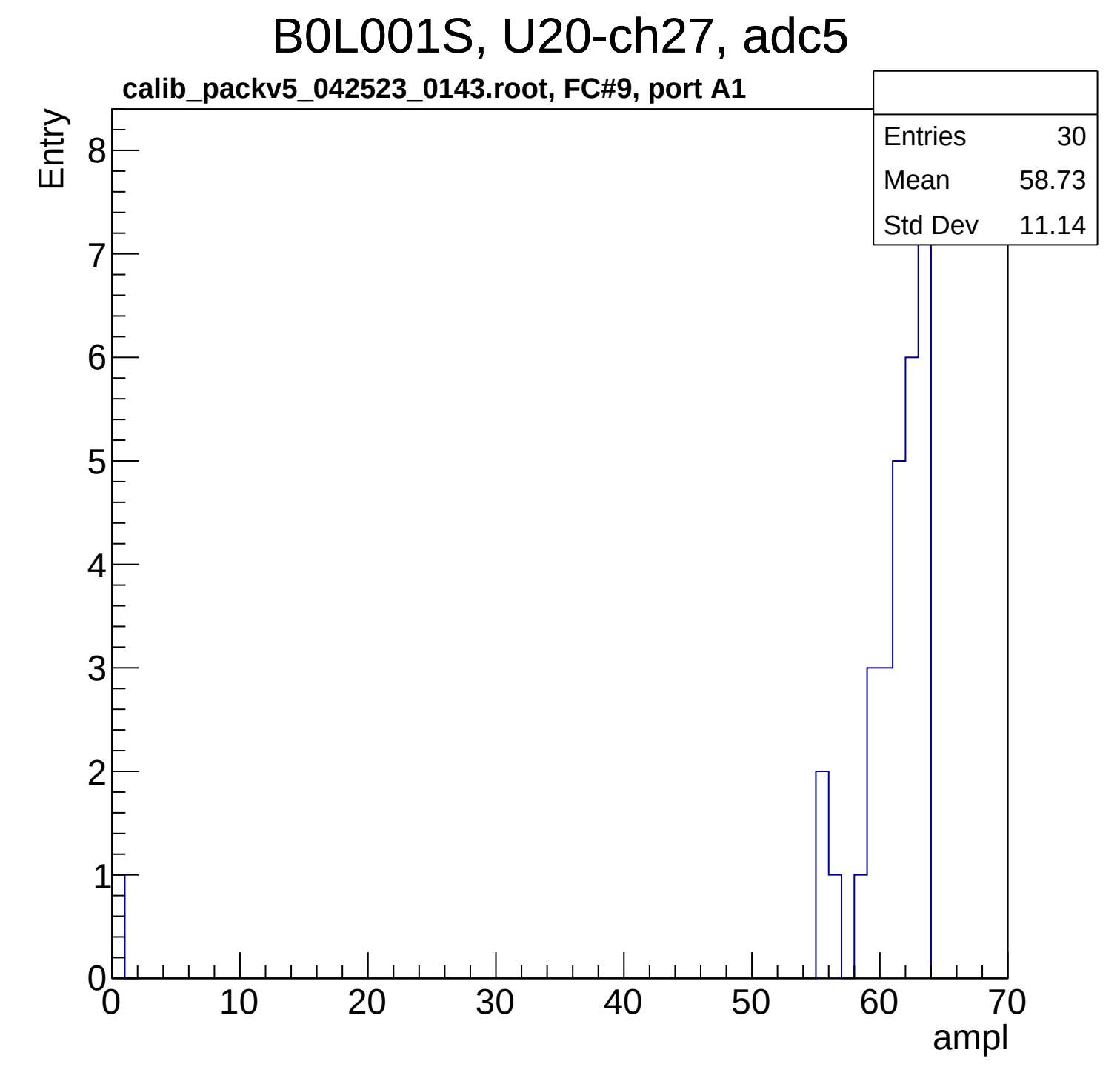
B0L001S, U20-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.73
Std Dev 11.14
Entry
ampl
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1
2
3
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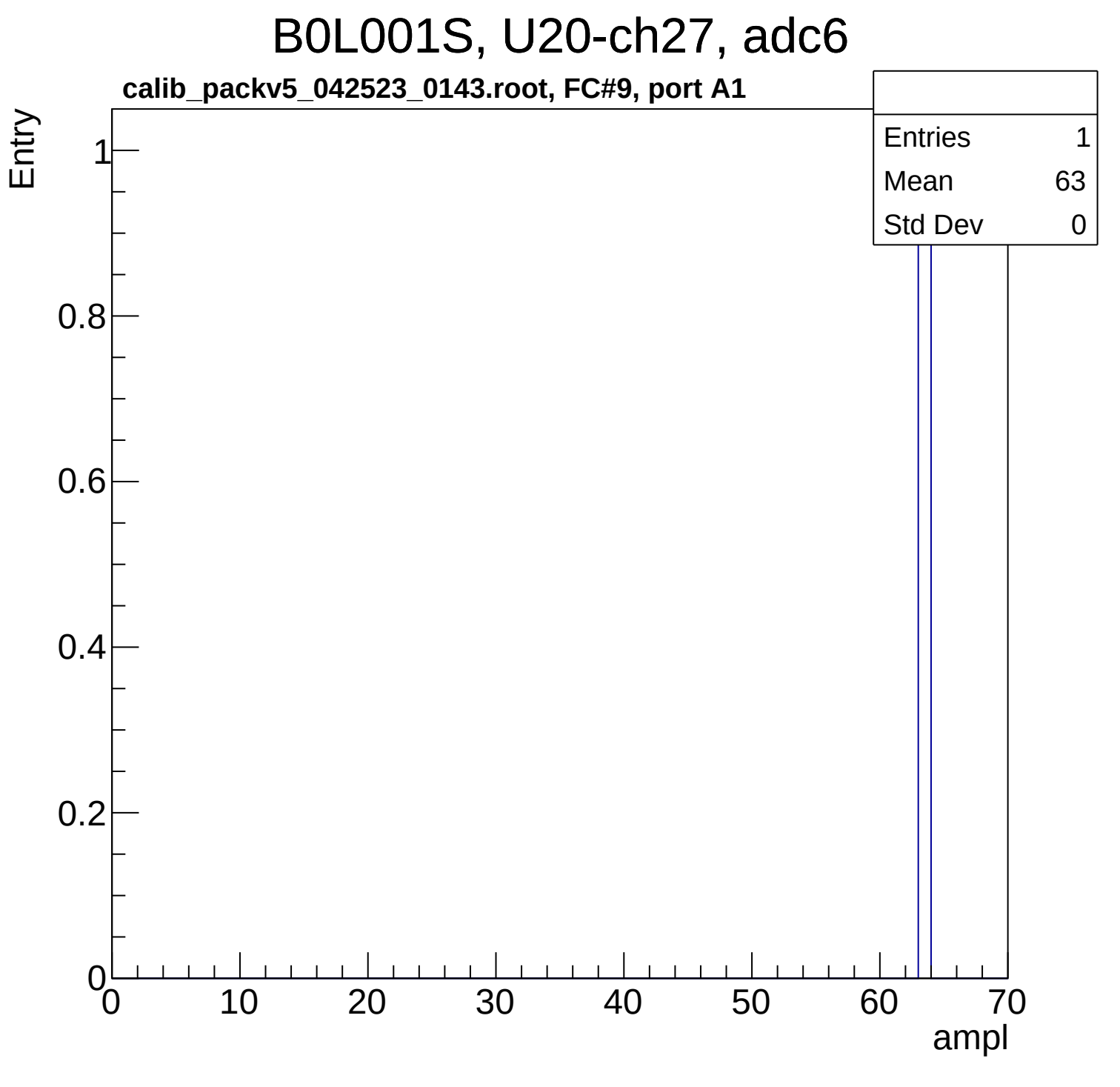
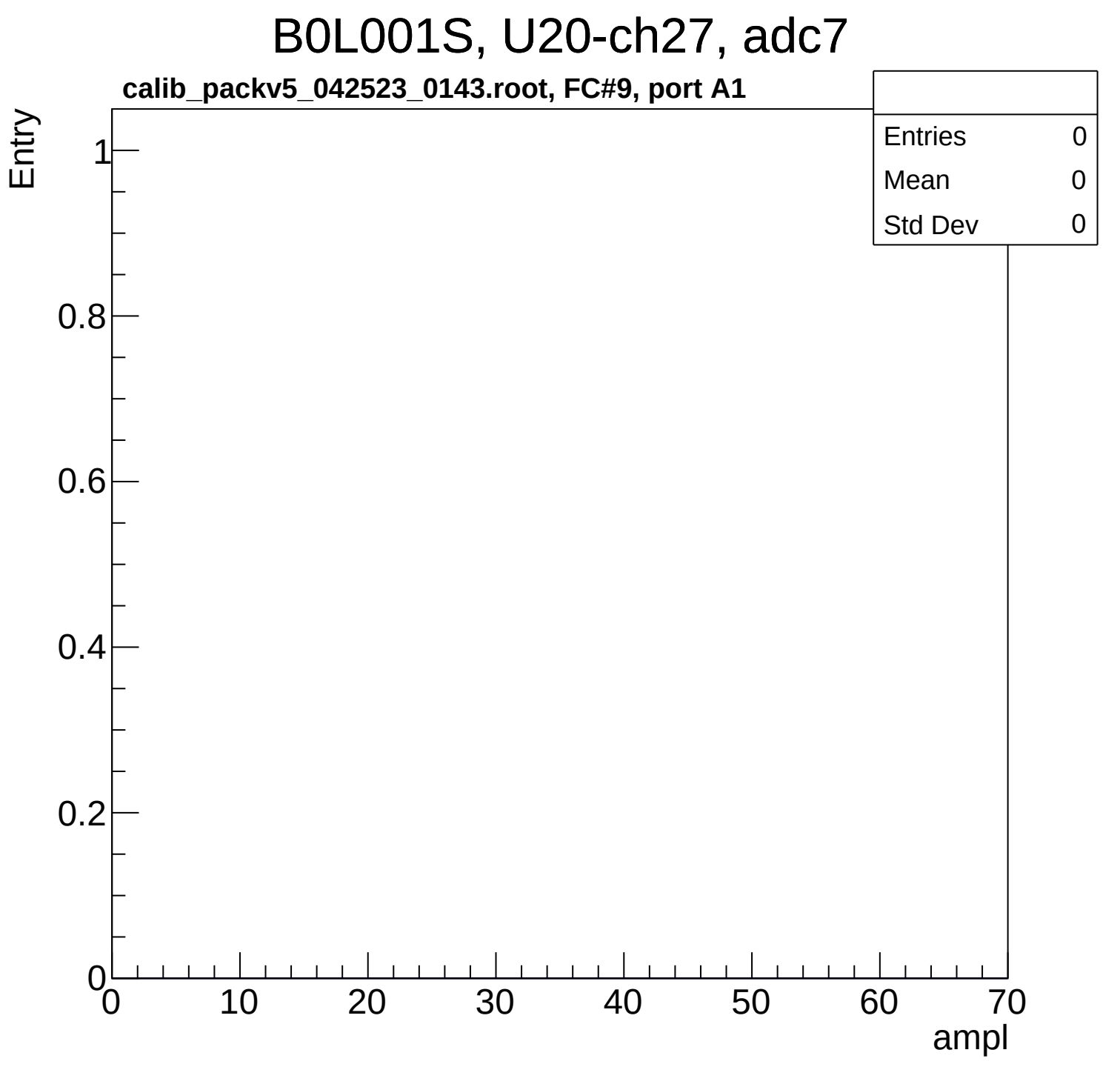
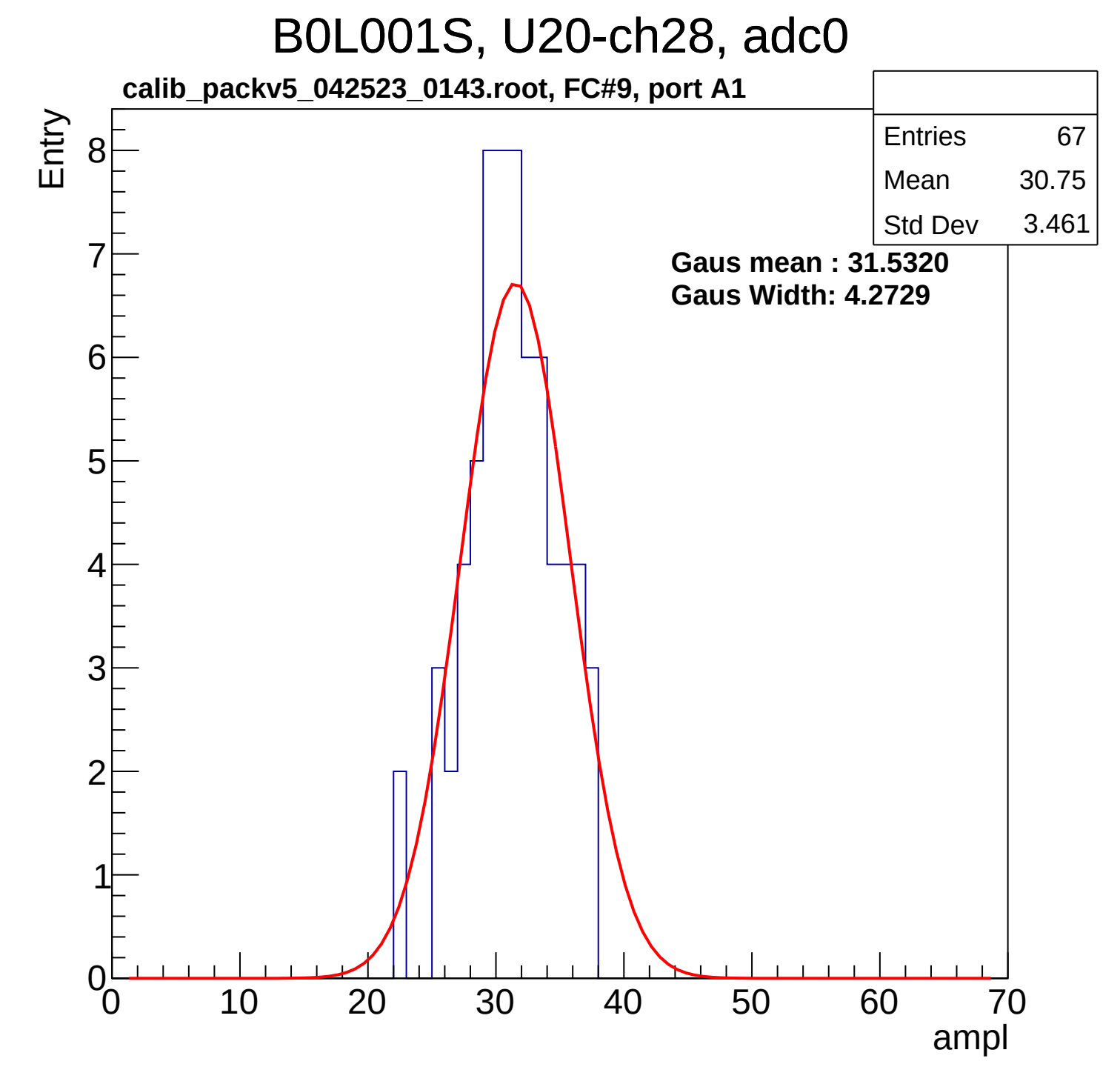

B0L001S, U20-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.75
Std Dev 3.461
Gaus mean : 31.5320
Gaus Width: 4.2729
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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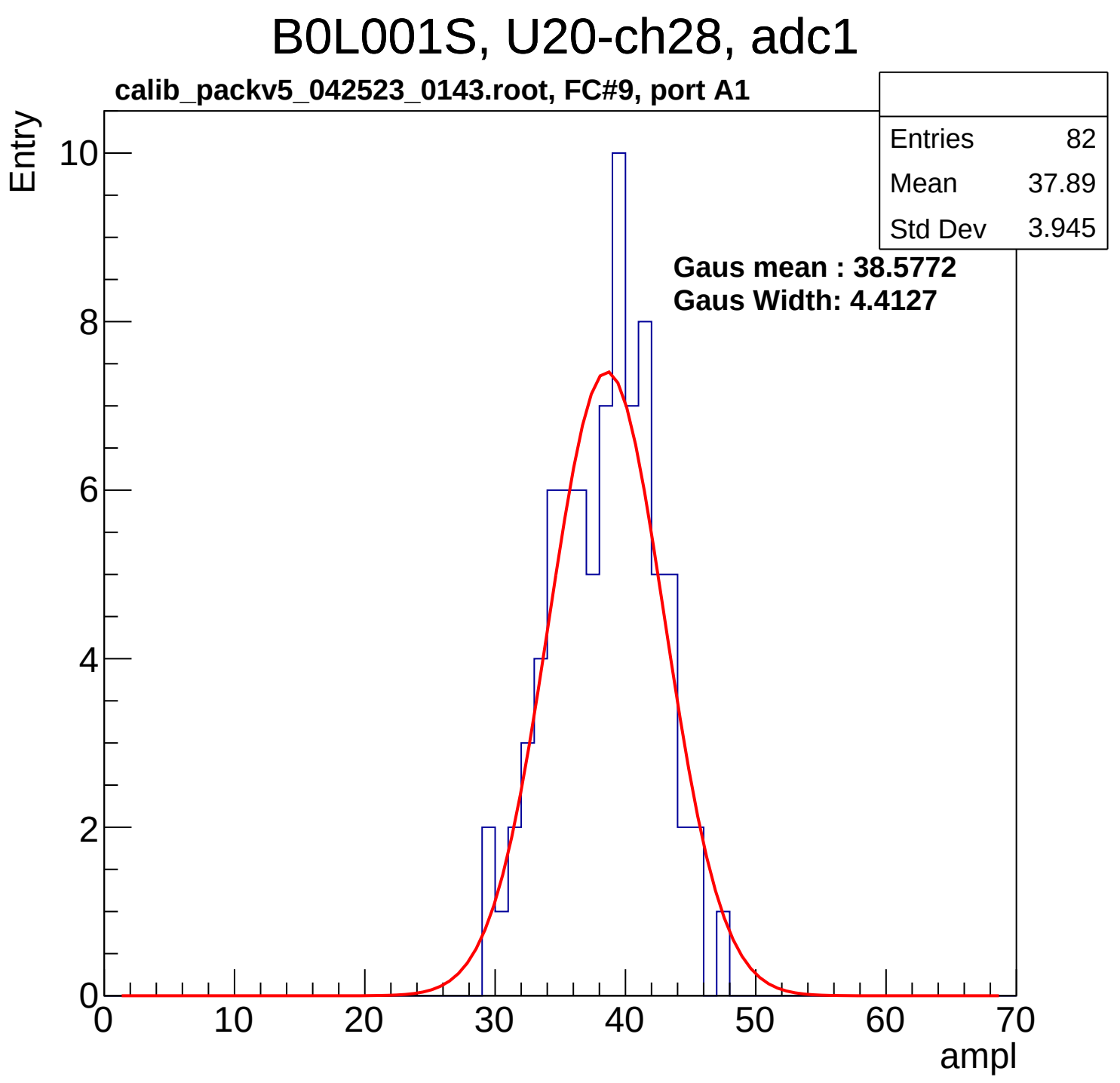
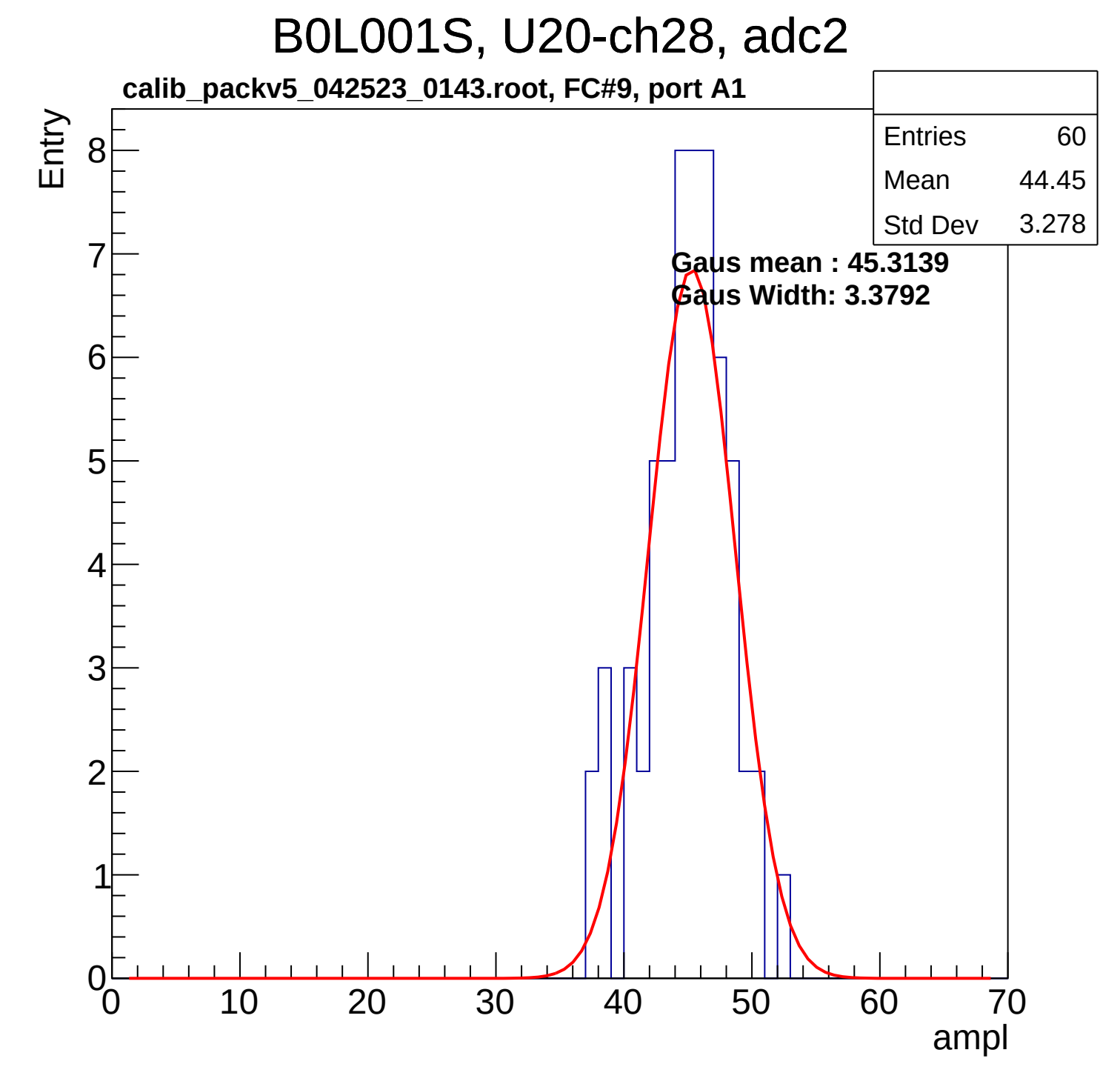

B0L001S, U20-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 37.89
Std Dev 3.945
Gaus mean : 38.5772
Gaus Width: 4.4127
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.45
Std Dev 3.278
Gaus mean : 45.3139
Gaus Width: 3.3792
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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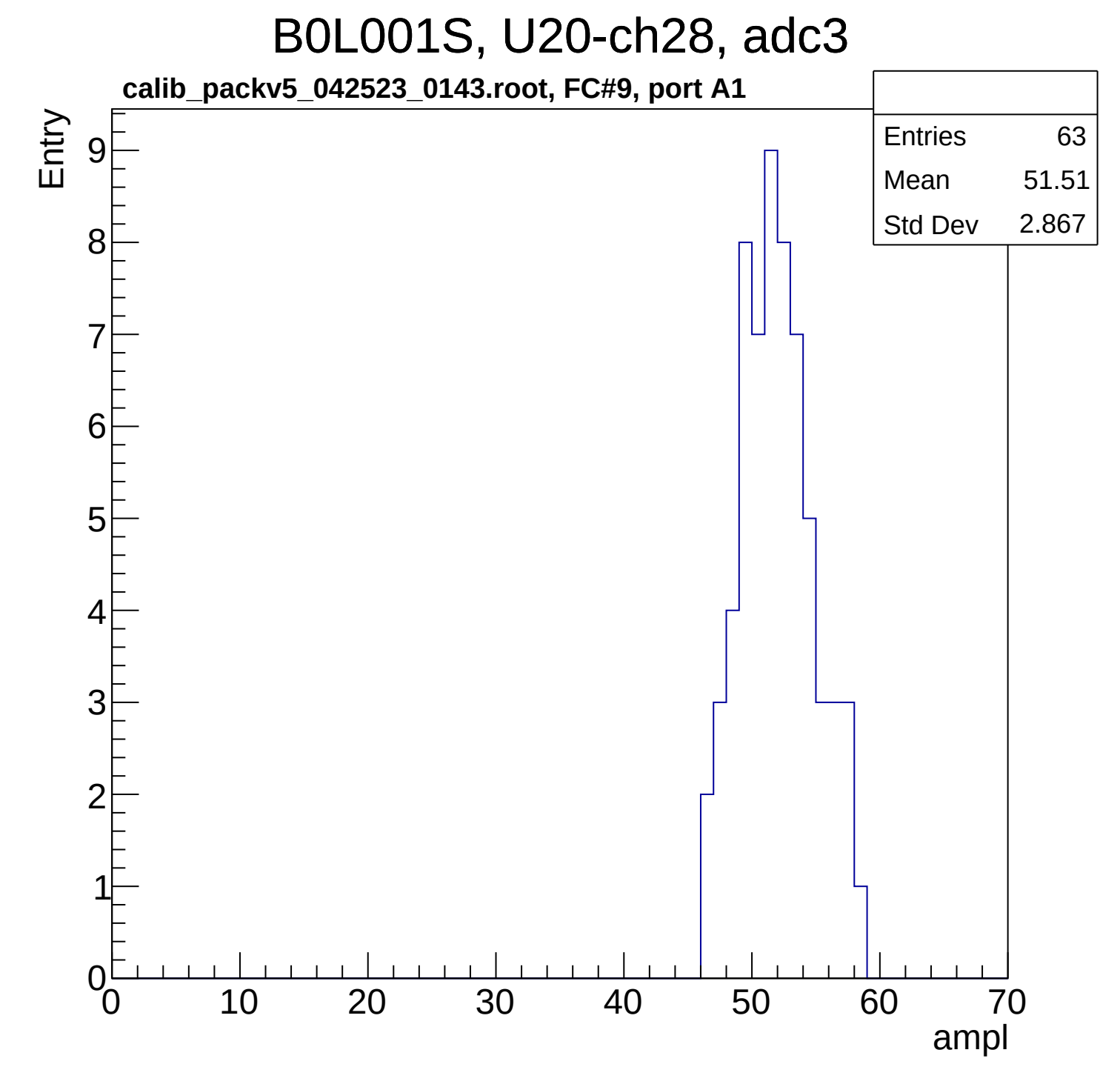
B0L001S, U20-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.51
Std Dev 2.867
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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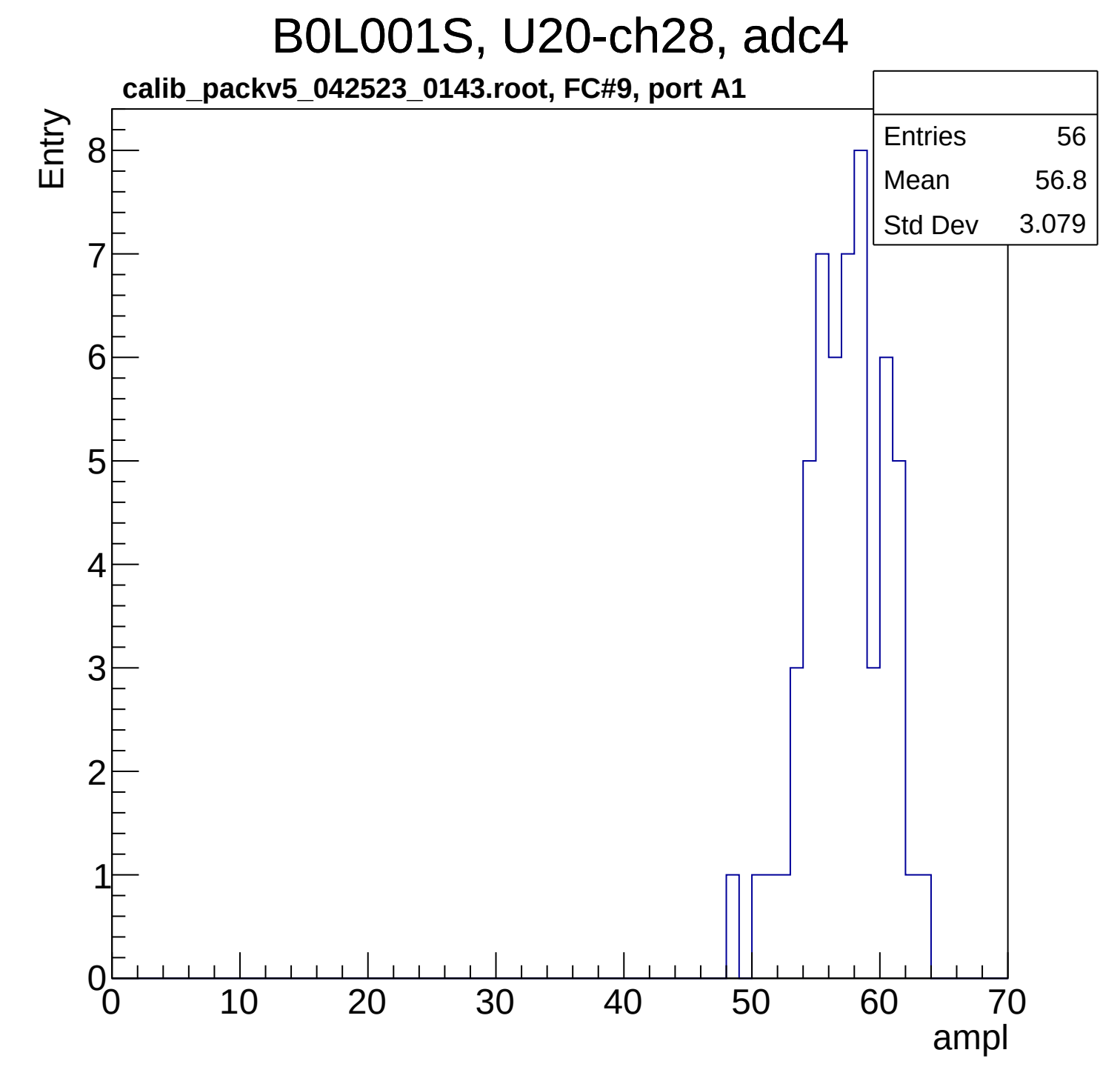
B0L001S, U20-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.8
Std Dev 3.079
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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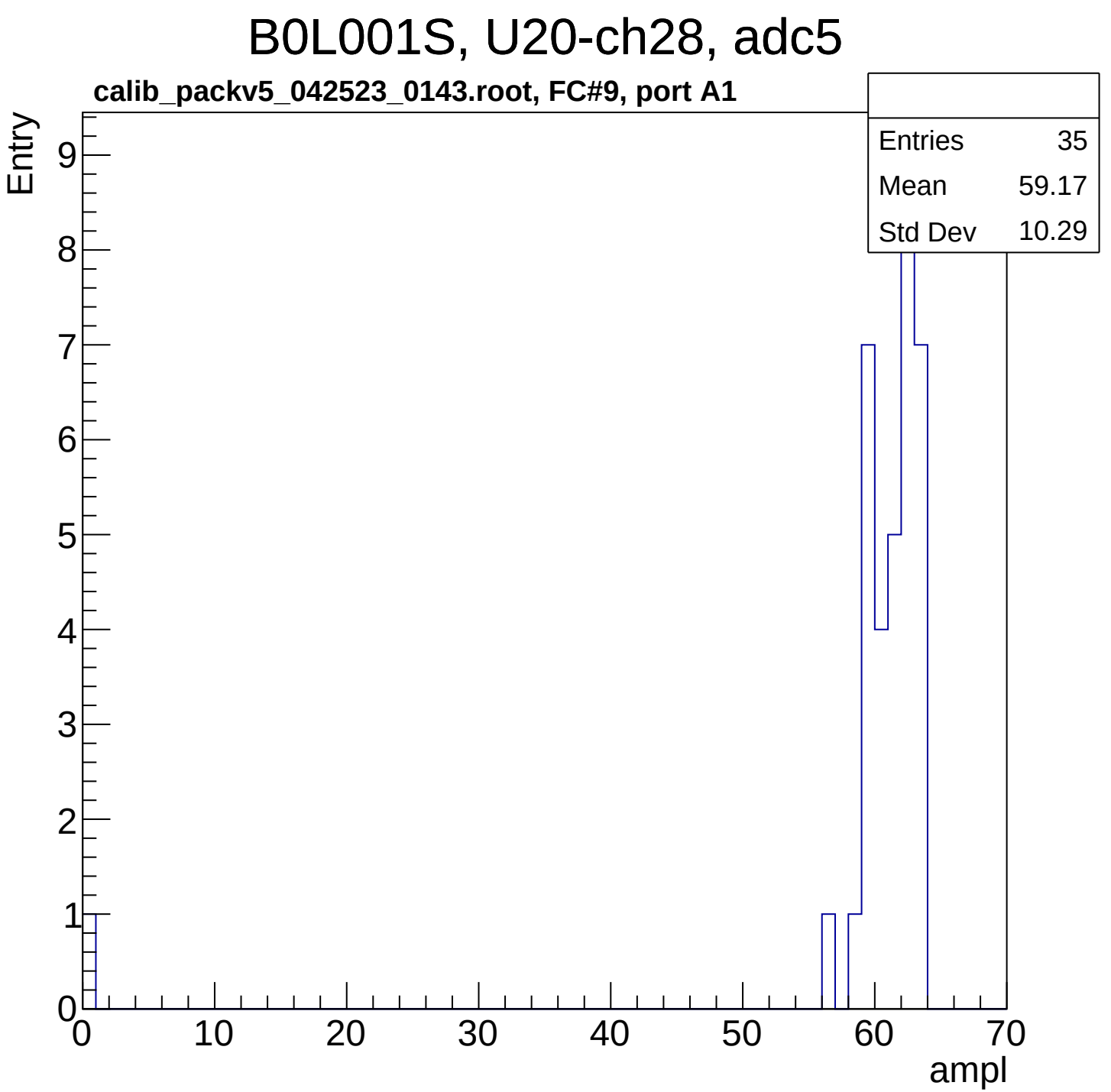
B0L001S, U20-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.17
Std Dev 10.29
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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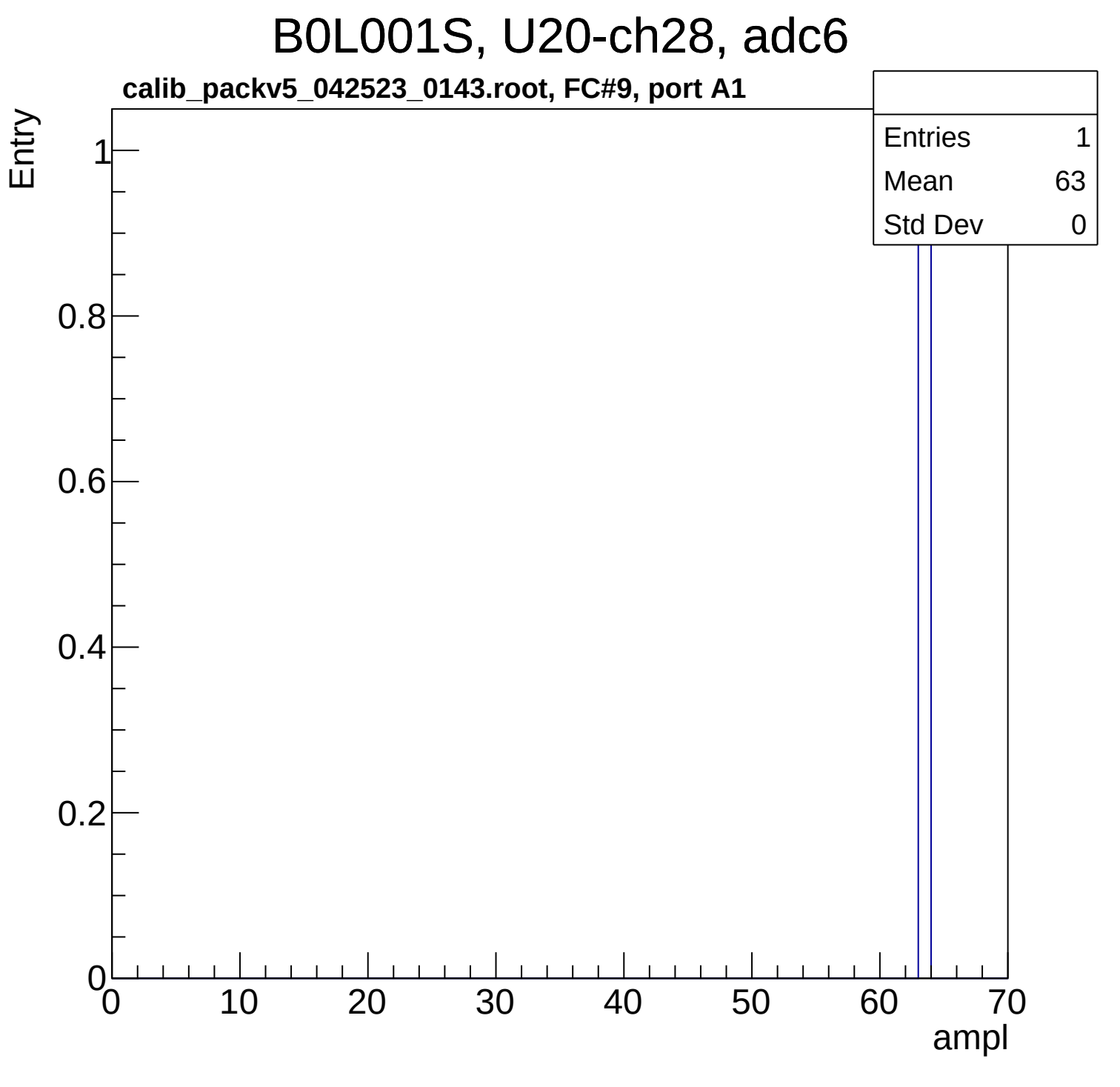
B0L001S, U20-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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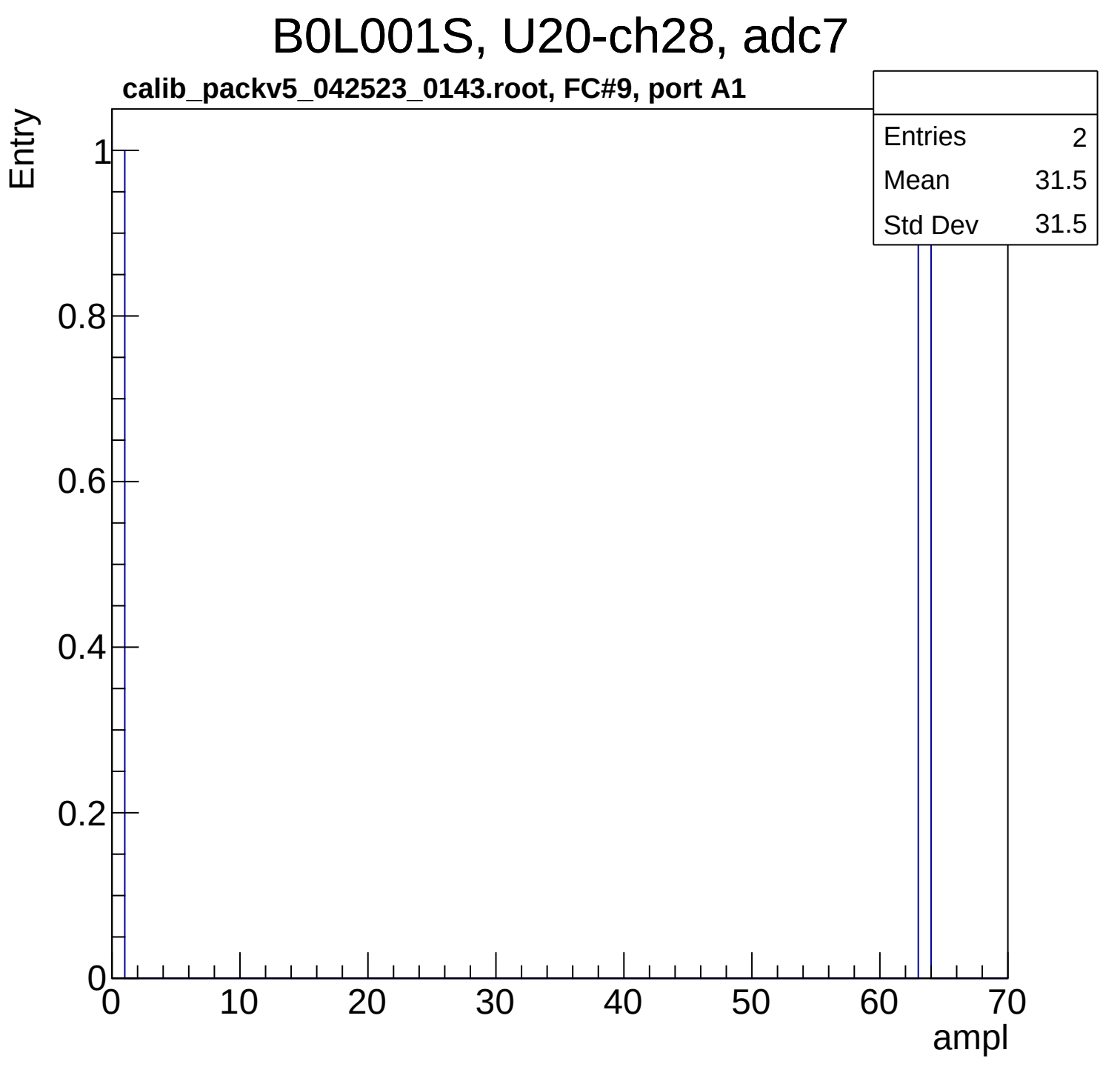
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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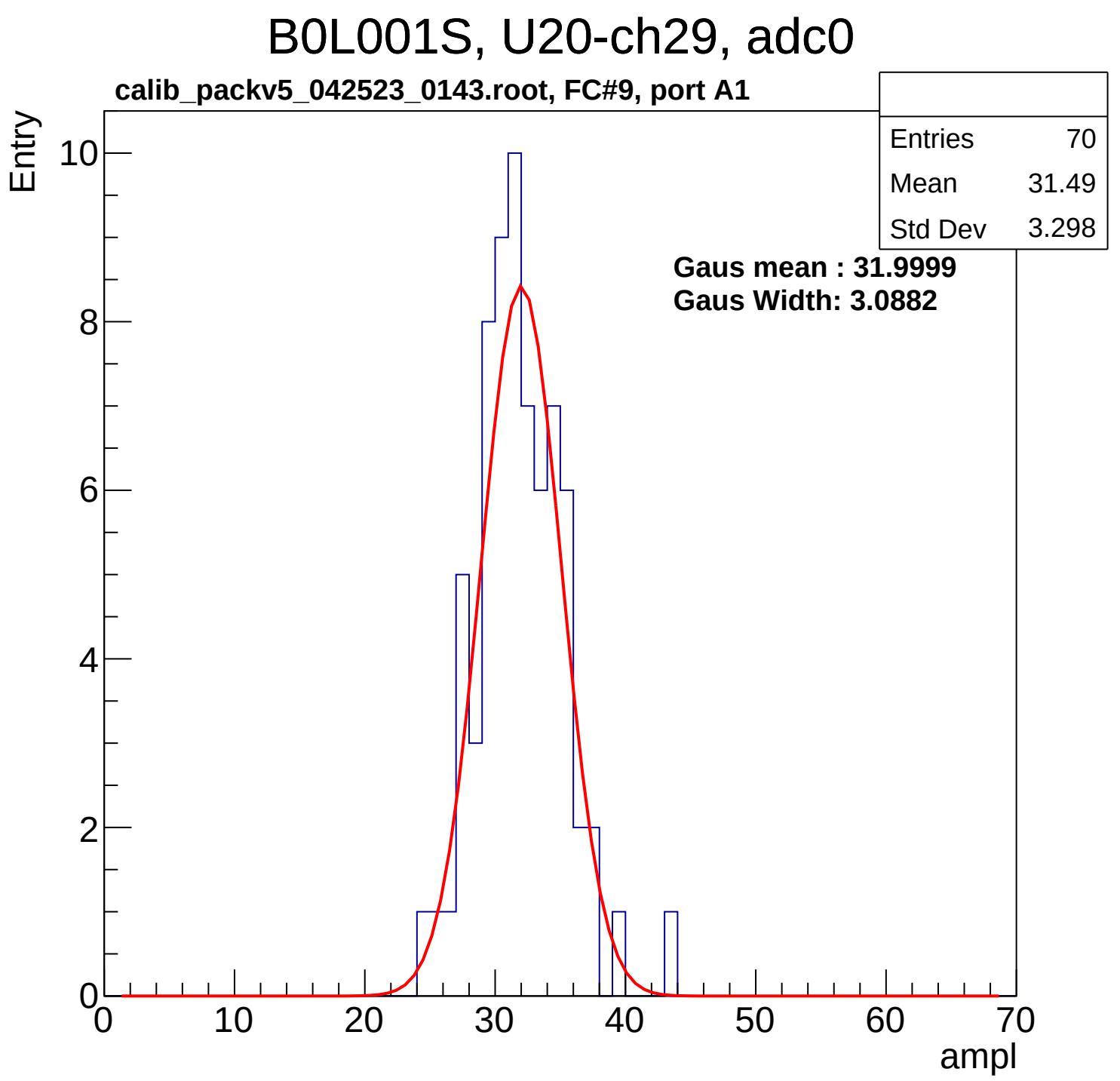
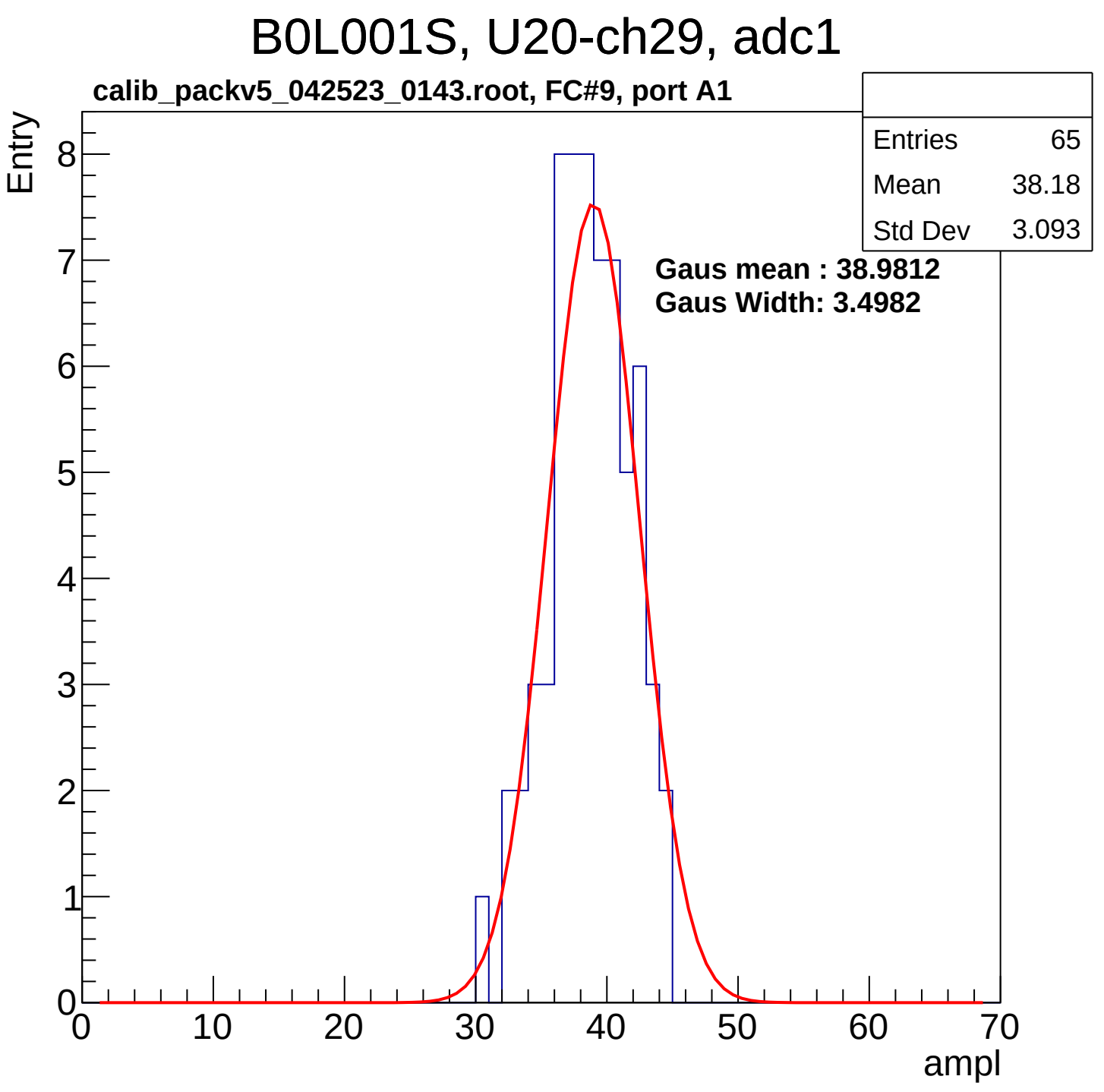
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.49
Std Dev 3.298
Gaus mean : 31.9999
Gaus Width: 3.0882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.18
Std Dev 3.093
Gaus mean : 38.9812
Gaus Width: 3.4982
Entry
ampl
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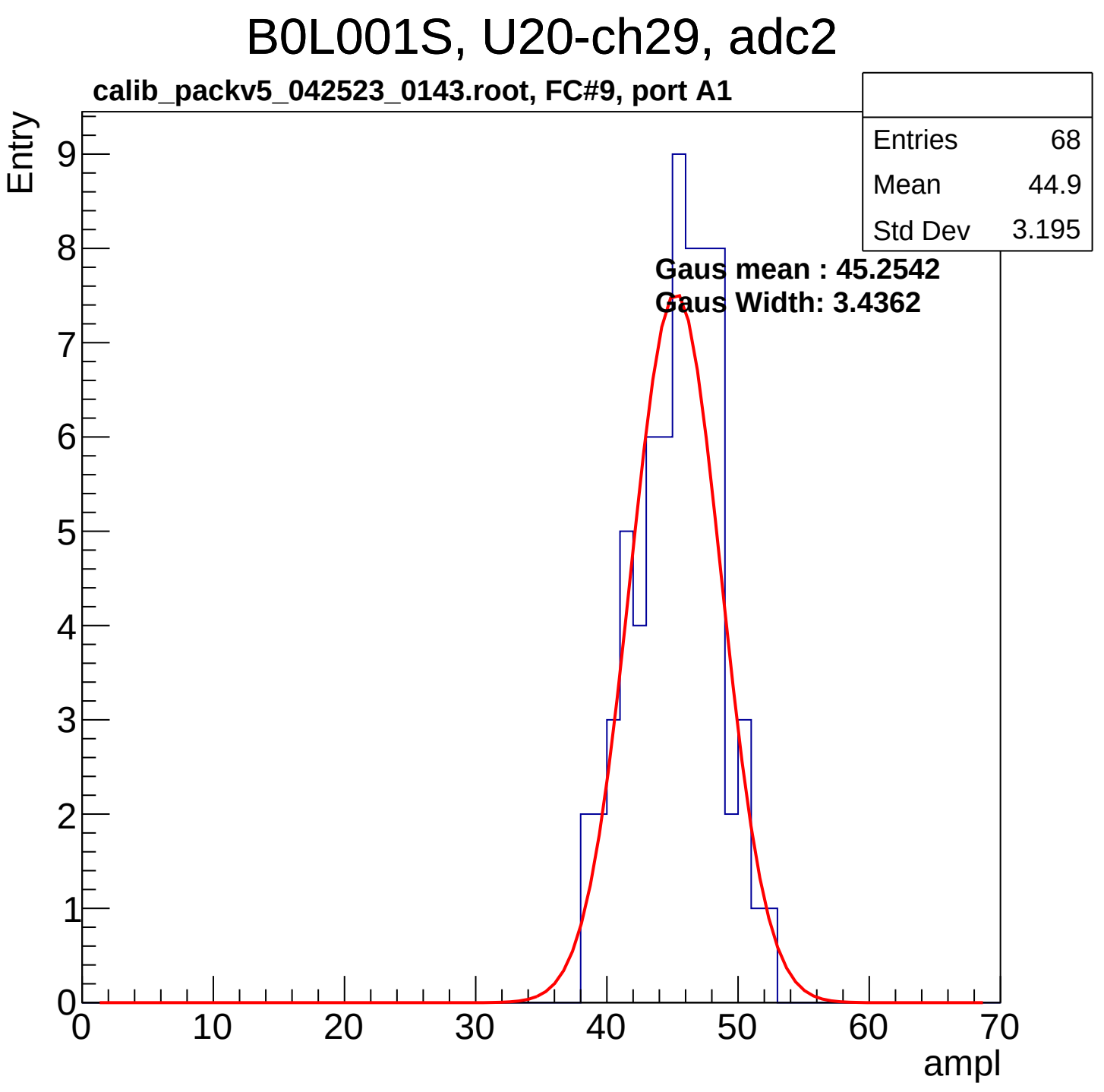
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.9
Std Dev 3.195
Gaus mean : 45.2542
Gaus Width: 3.4362
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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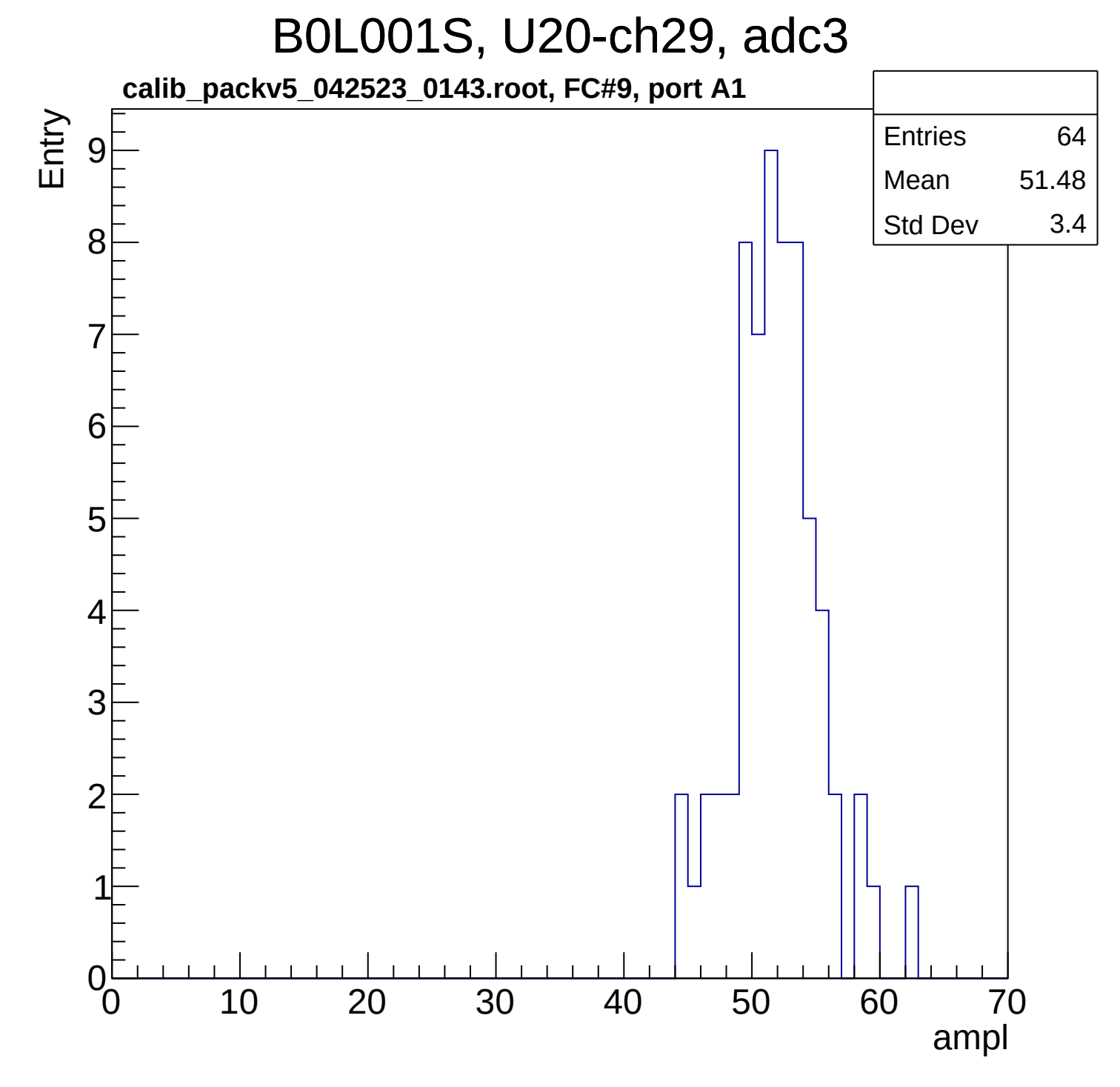
40
50
60
70


B0L001S, U20-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.48
Std Dev 3.4
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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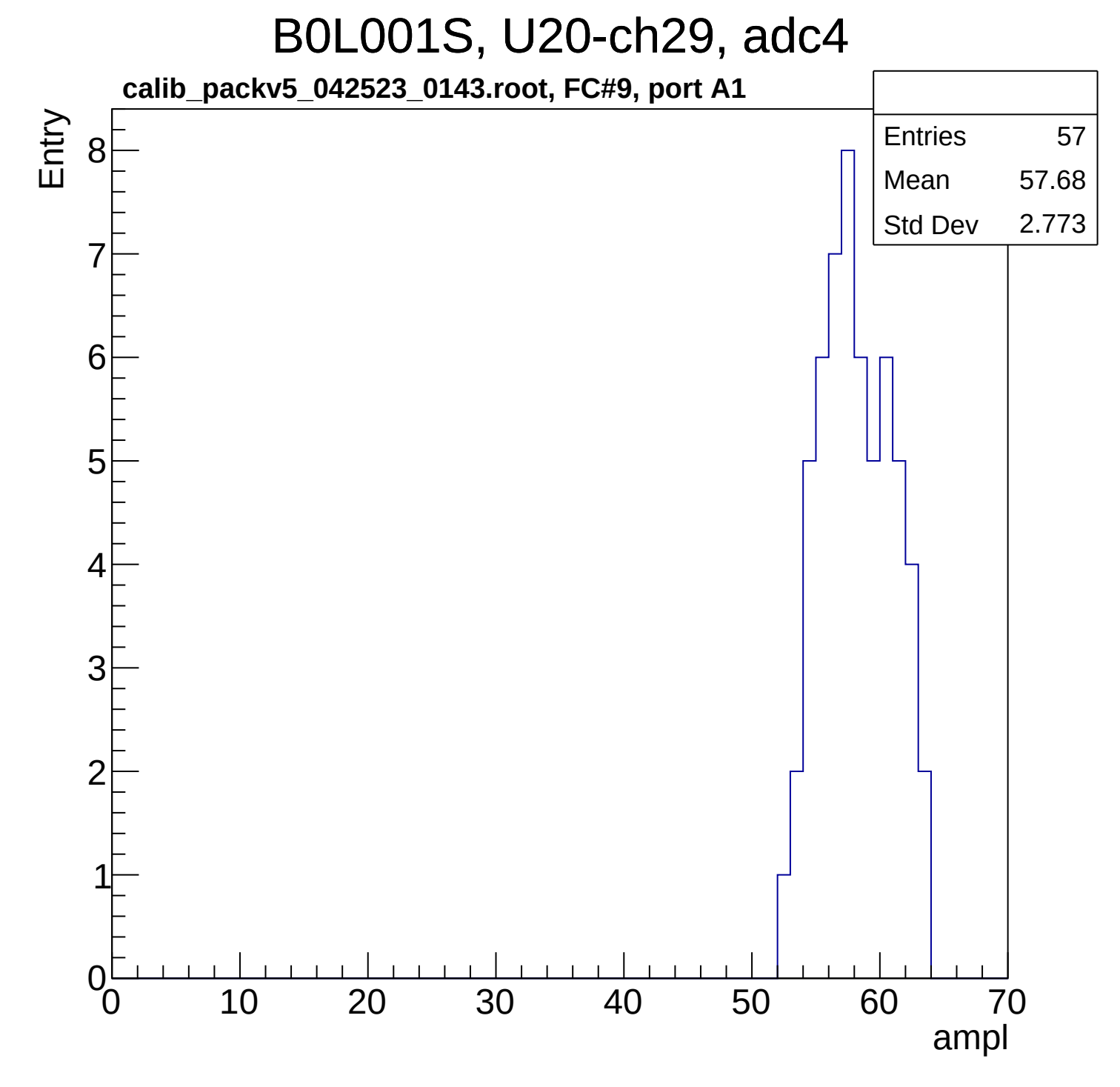
B0L001S, U20-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.68
Std Dev 2.773
Entry
ampl
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5
6
7
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0
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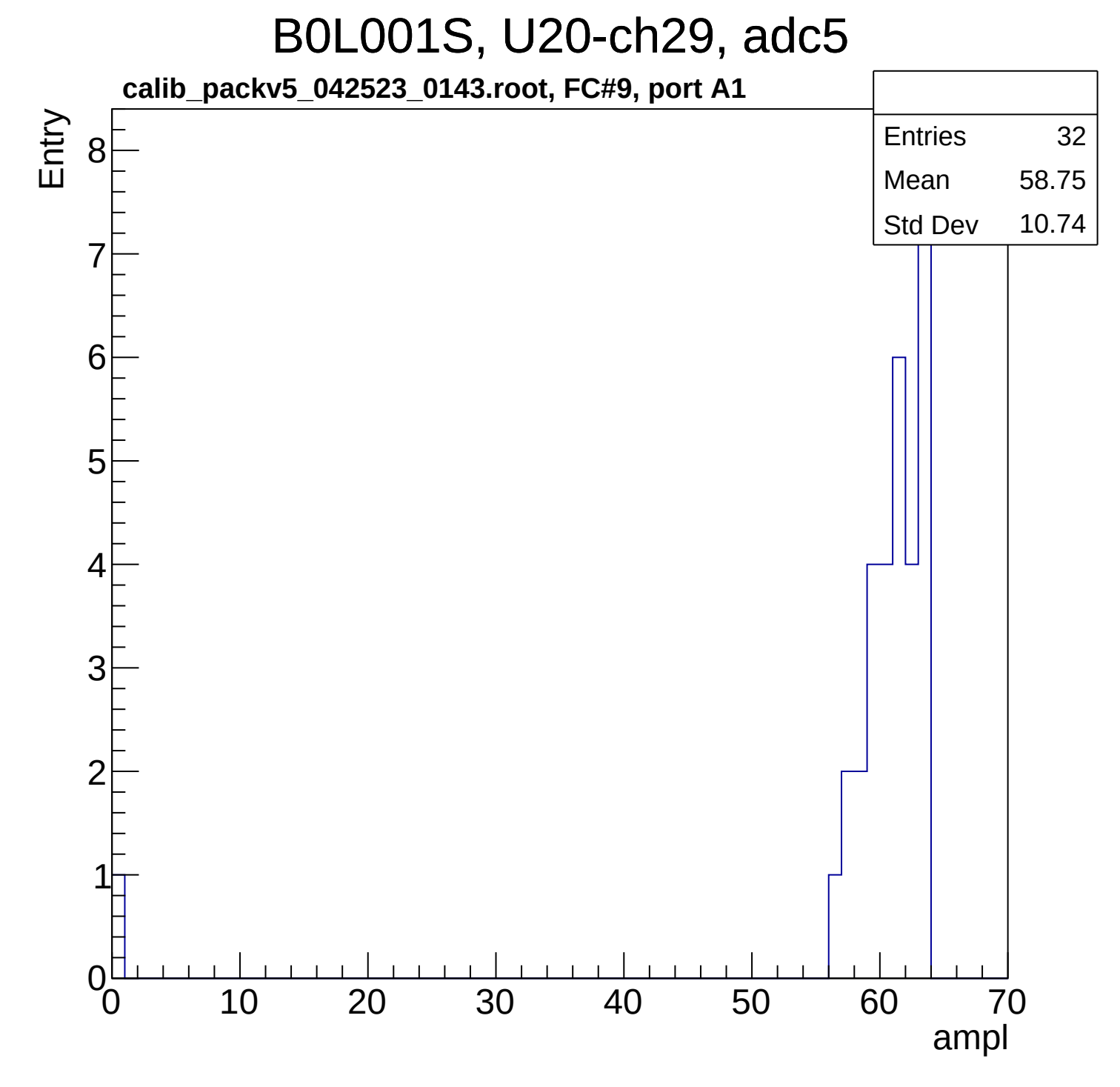
B0L001S, U20-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.75
Std Dev 10.74
Entry
ampl
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4
5
6
7
8
0
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60
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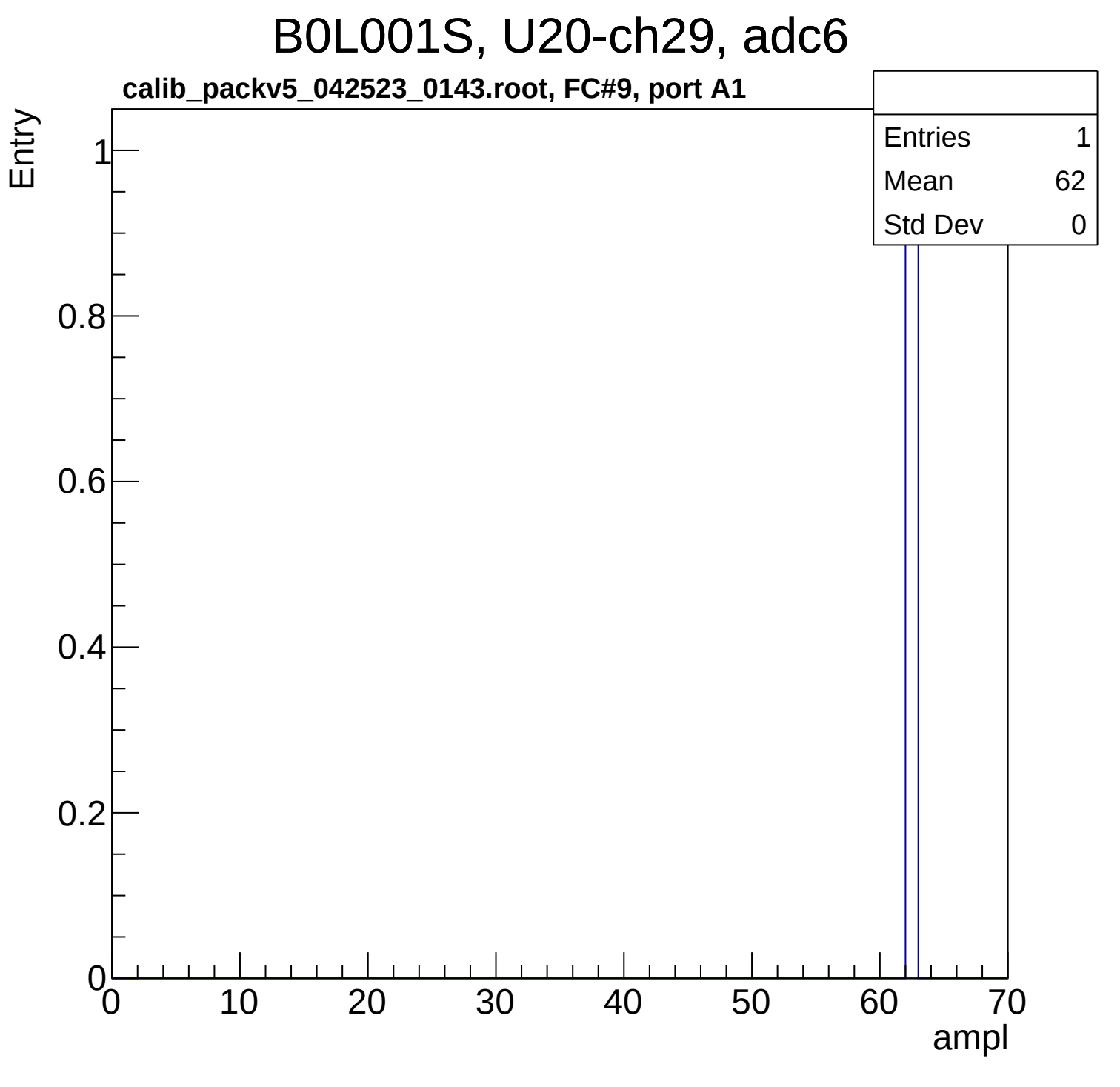
B0L001S, U20-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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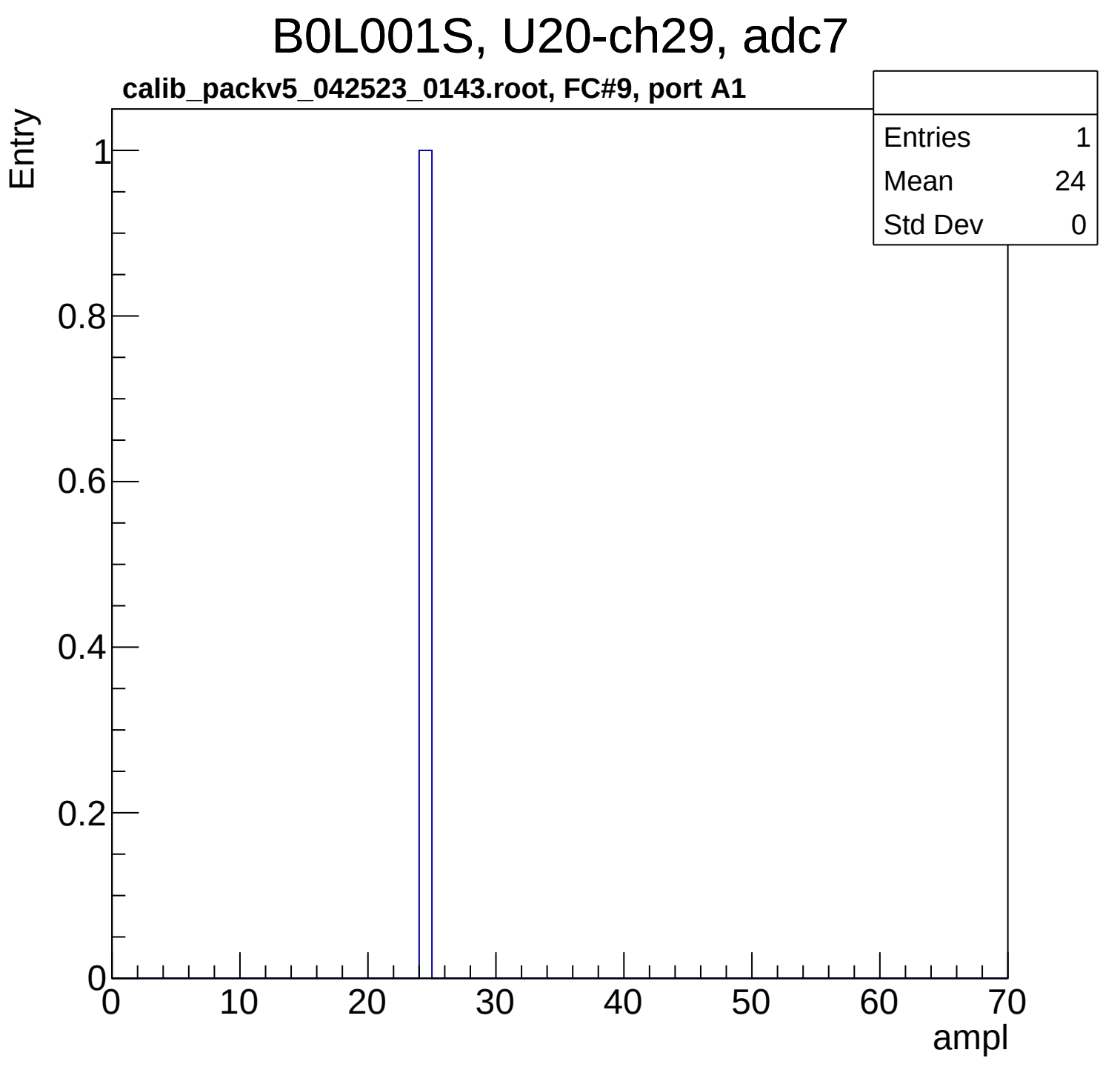
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
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0.6
0.4
0.2
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10
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30
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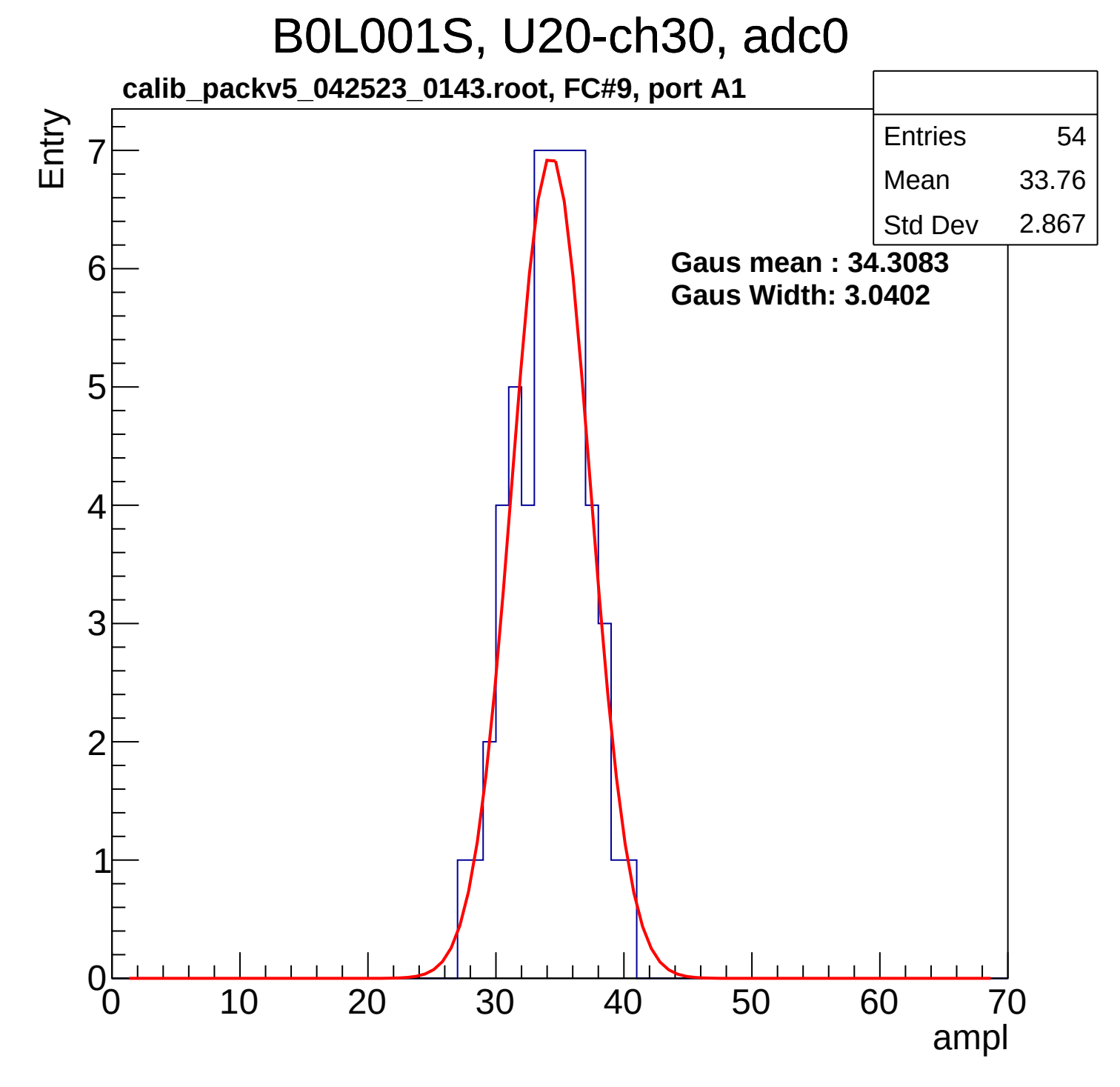
50
60
70
ampl


B0L001S, U20-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 33.76
Std Dev 2.867
Gaus mean : 34.3083
Gaus Width: 3.0402
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
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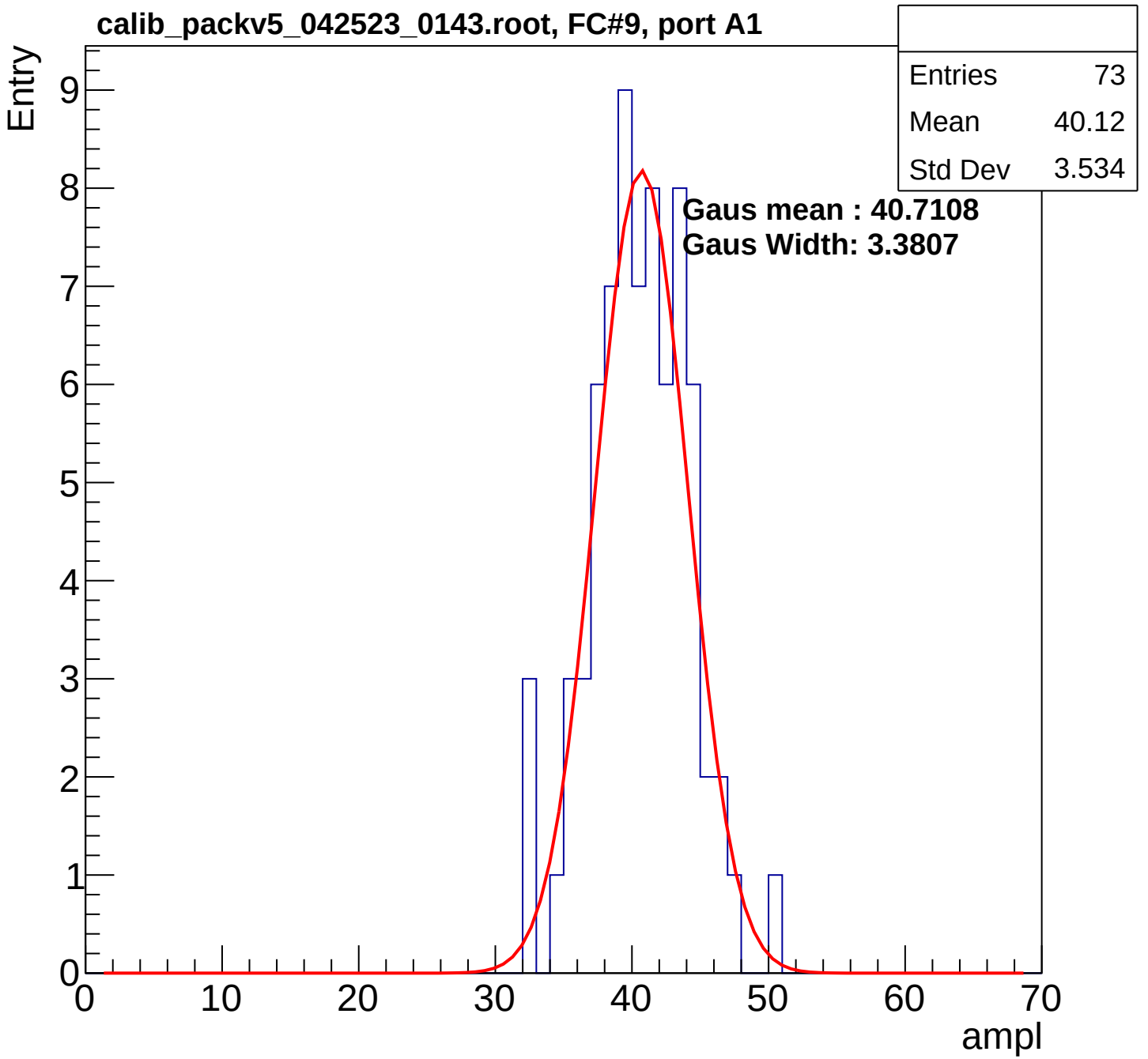
60
70


B0L001S, U20-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 40.12
Std Dev 3.534
Gaus mean : 40.7108
Gaus Width: 3.3807
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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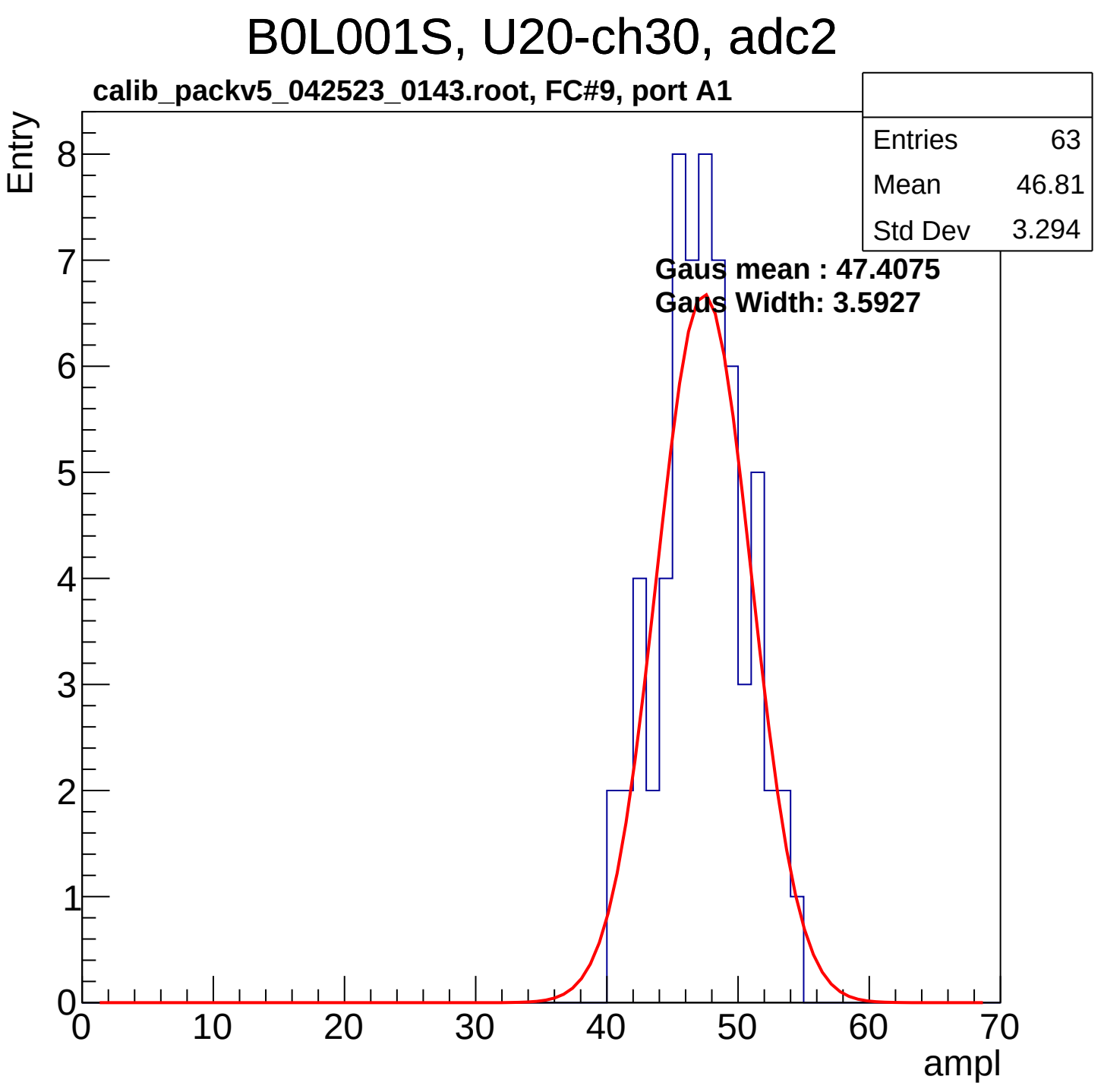
B0L001S, U20-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 46.81
Std Dev 3.294
Gaus mean : 47.4075
Gaus Width: 3.5927
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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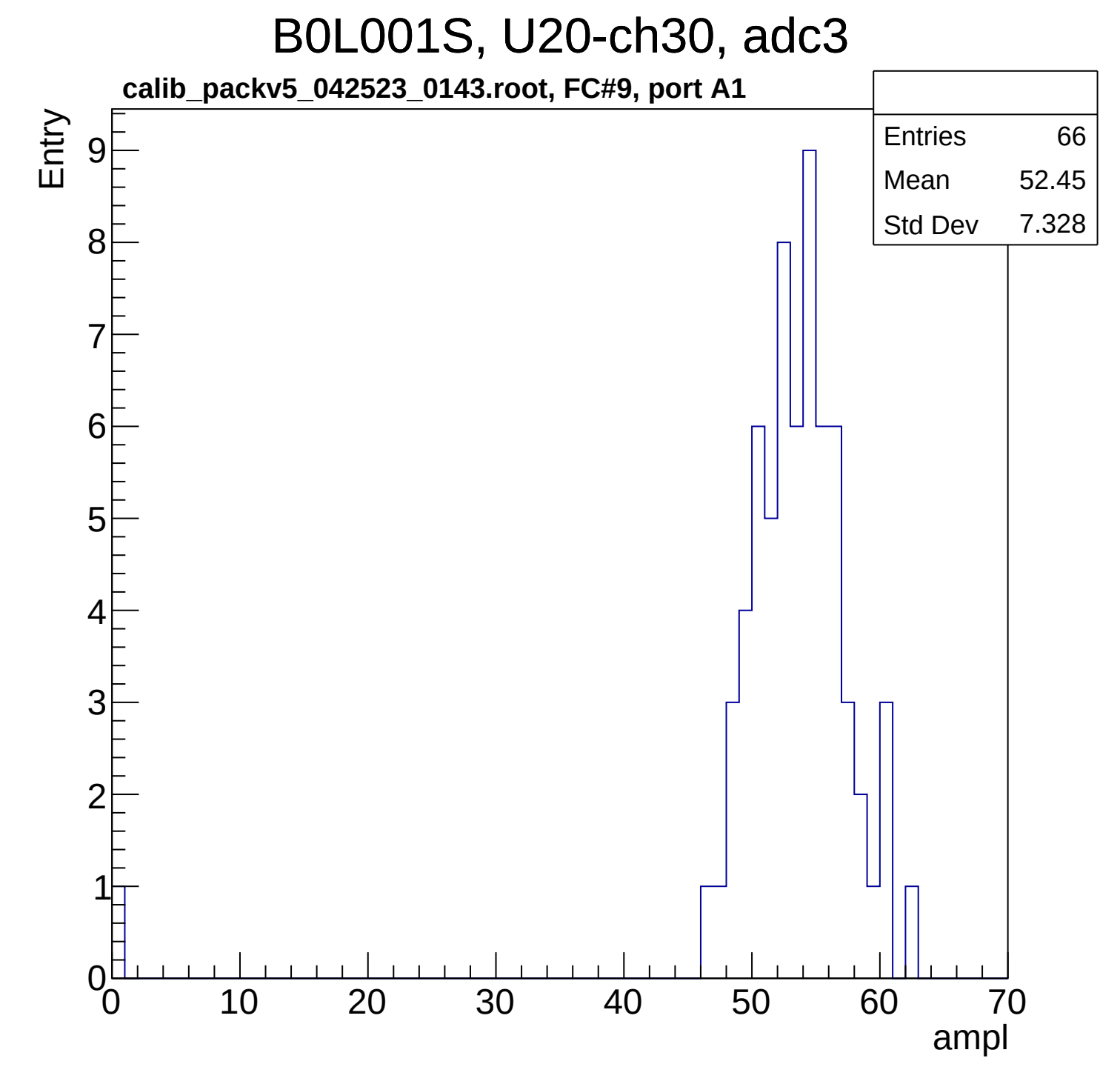
B0L001S, U20-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.45
Std Dev 7.328
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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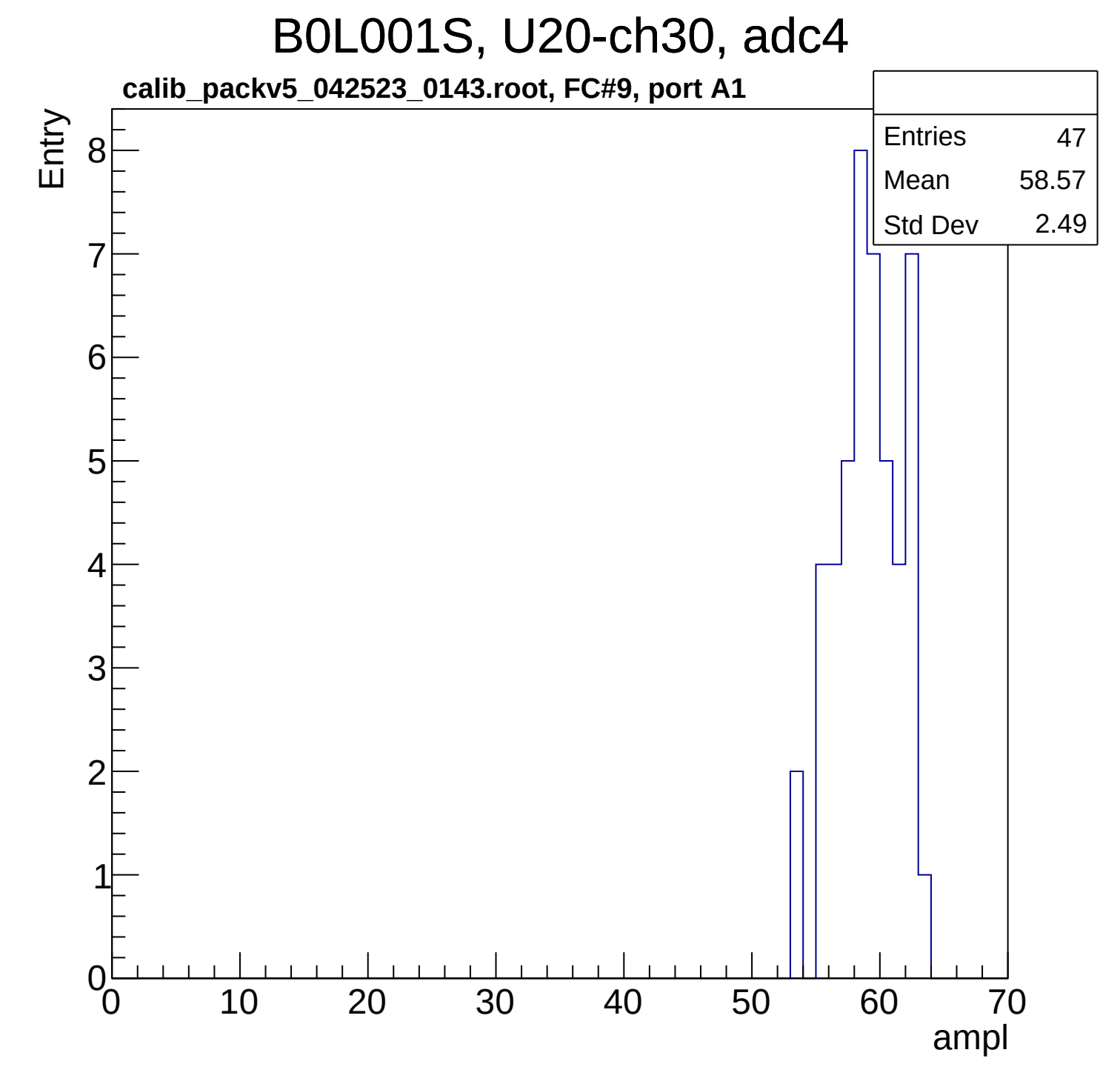
B0L001S, U20-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.57
Std Dev 2.49
Entry
ampl
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2
3
4
5
6
7
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0
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40
50
60
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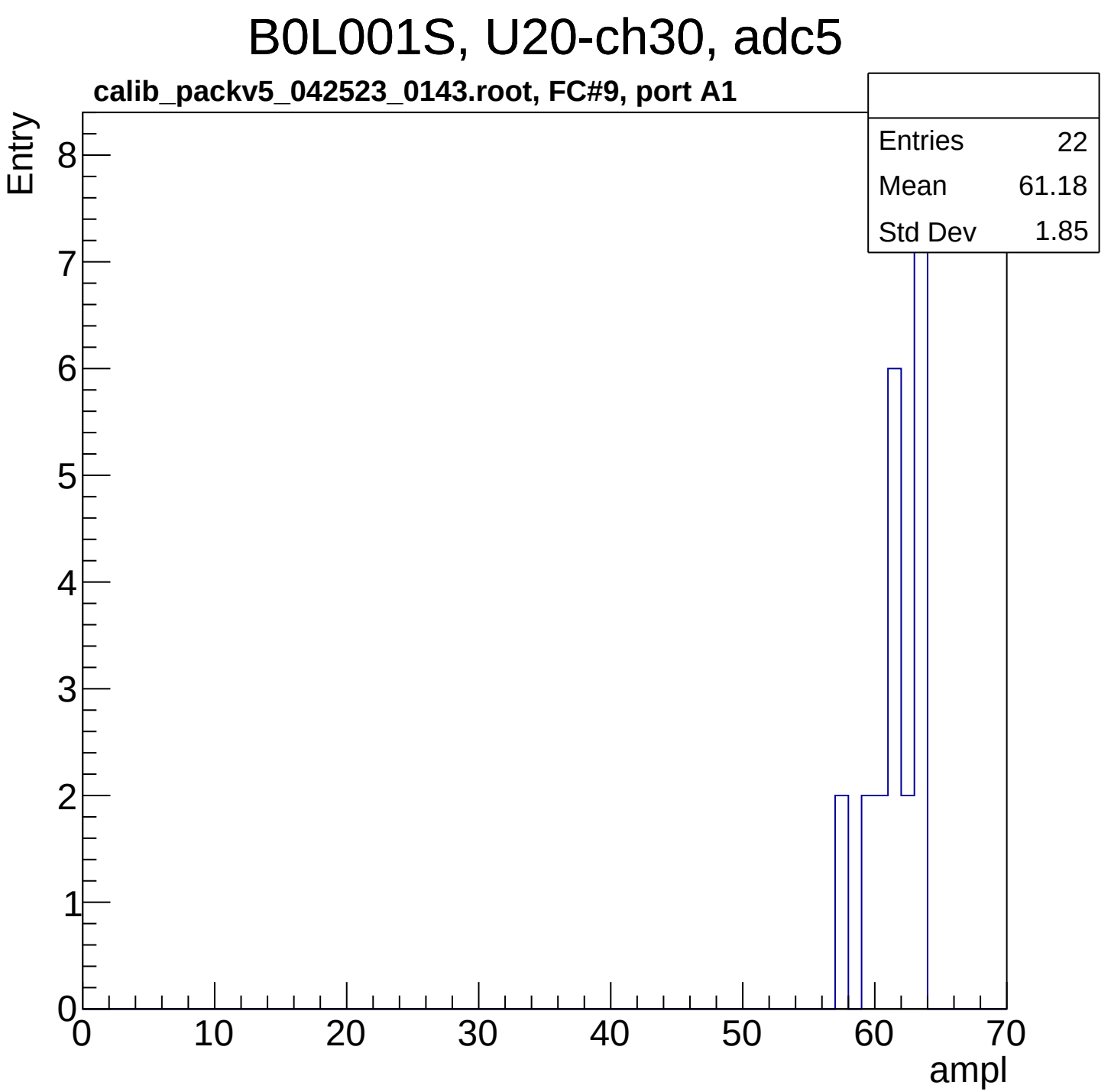
B0L001S, U20-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 61.18
Std Dev 1.85
Entry
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3
4
5
6
7
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10
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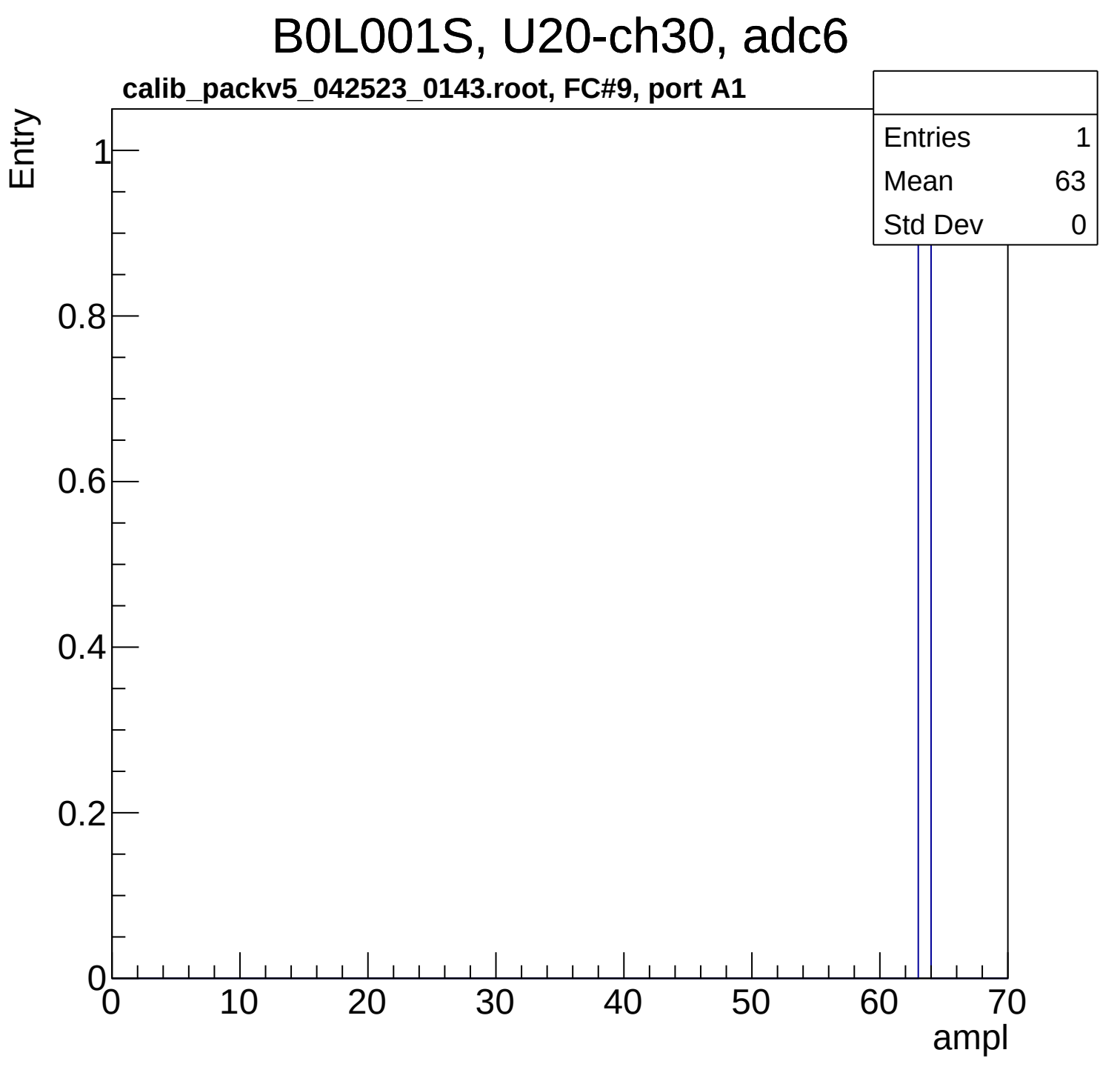
50
60
70
ampl


B0L001S, U20-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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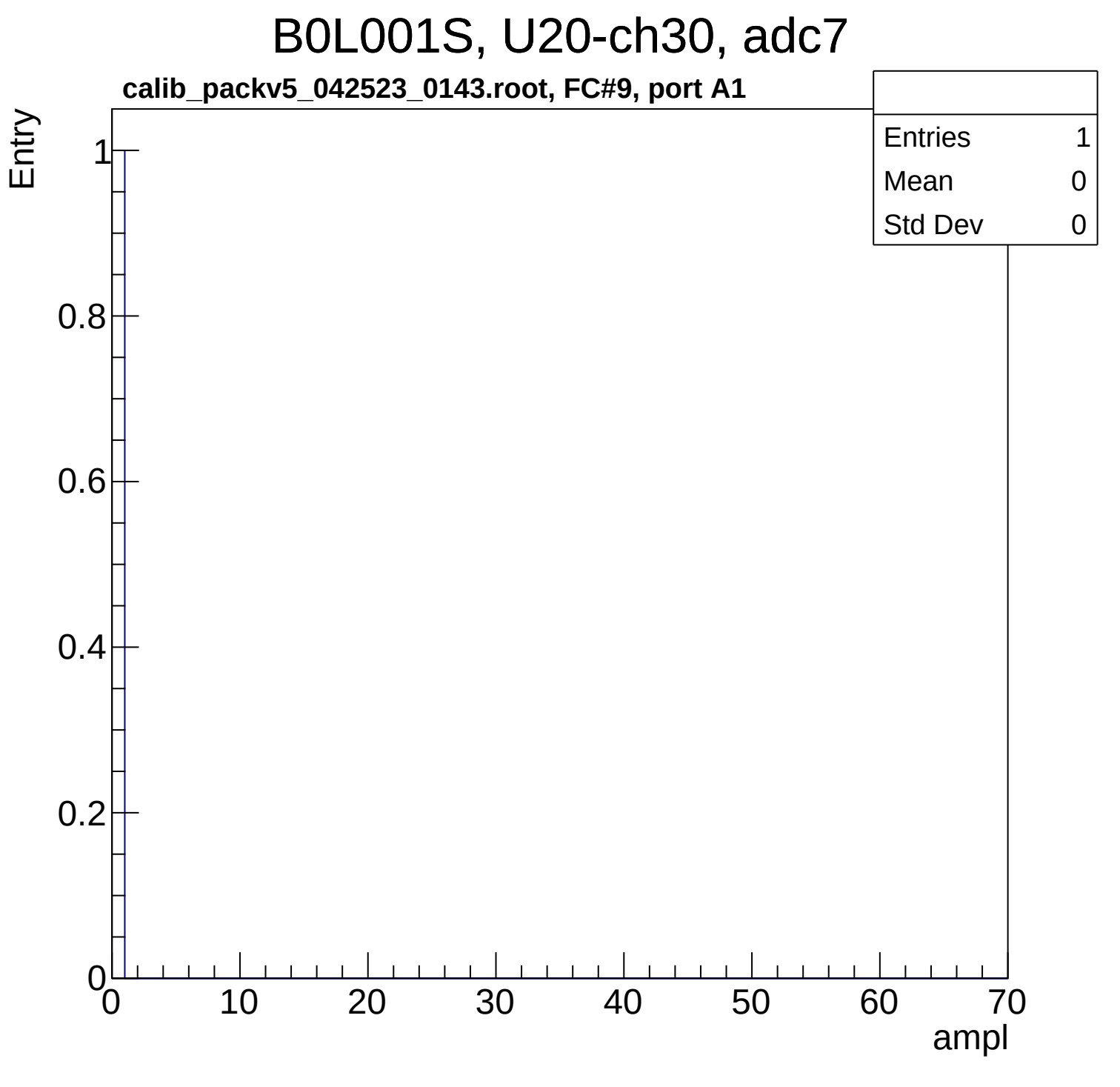
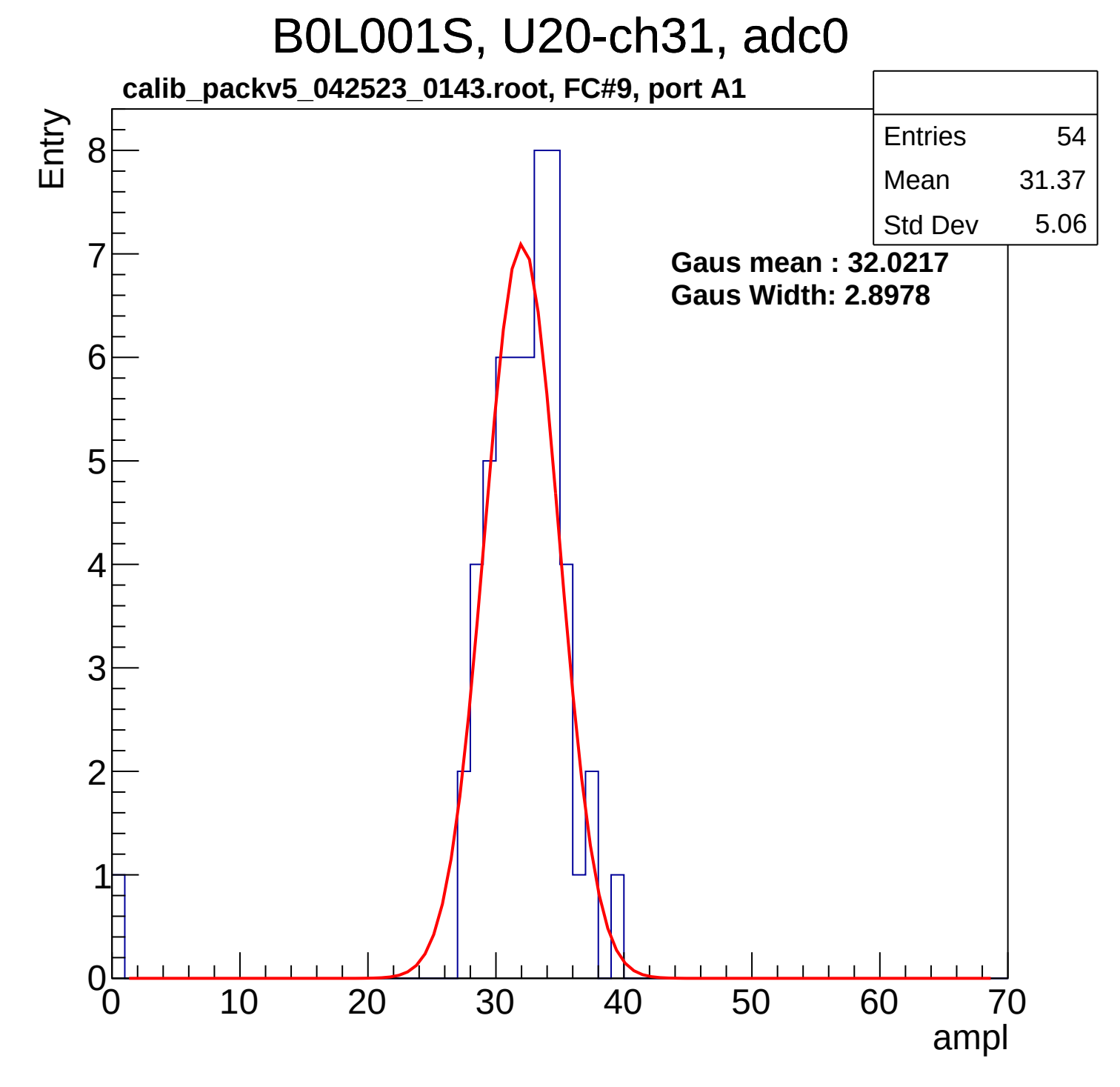
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 31.37
Std Dev 5.06
Gaus mean : 32.0217
Gaus Width: 2.8978
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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40
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60
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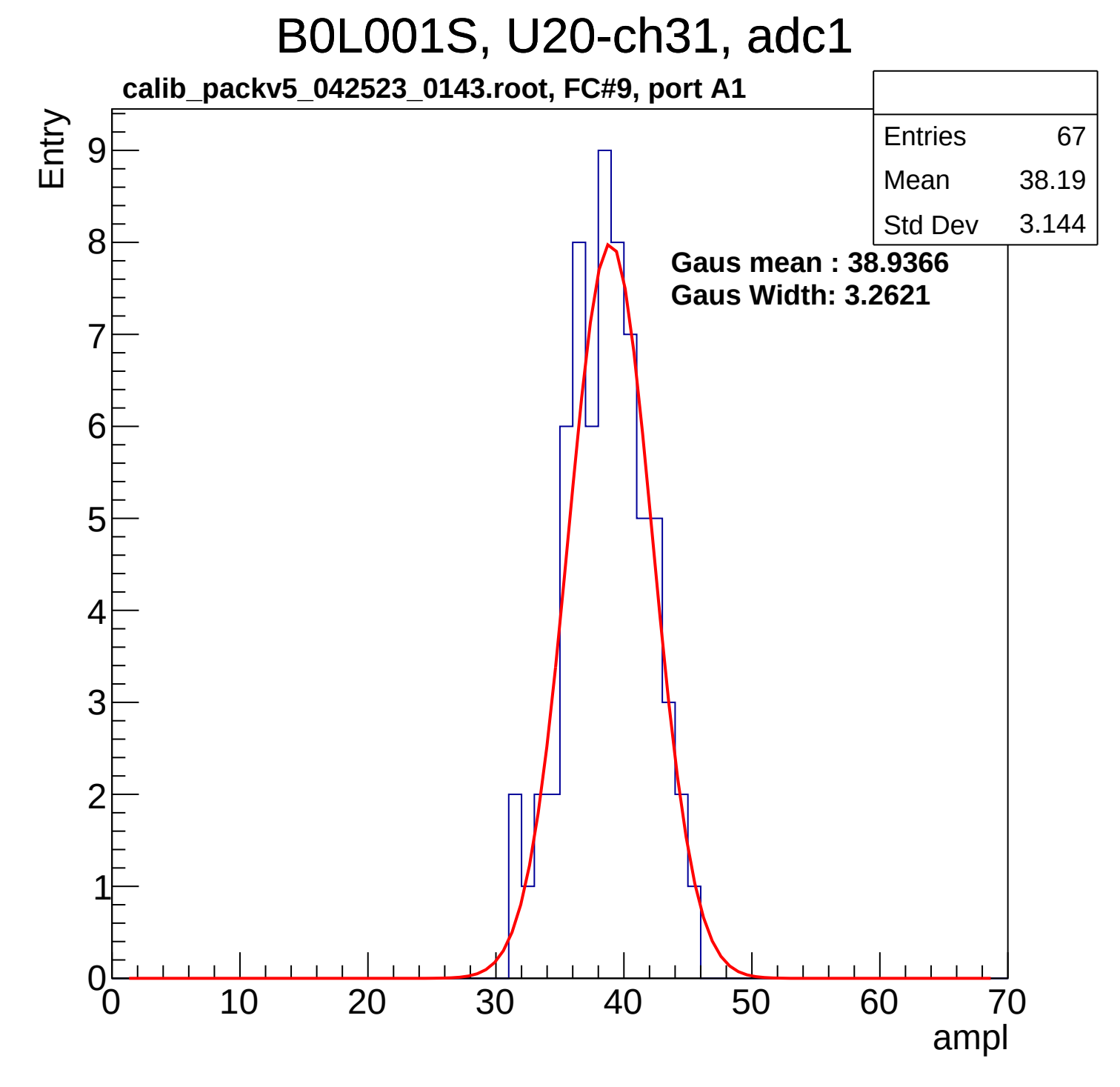
B0L001S, U20-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.19
Std Dev 3.144
Gaus mean : 38.9366
Gaus Width: 3.2621
Entry
ampl
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2
3
4
5
6
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8
9
0
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40
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60
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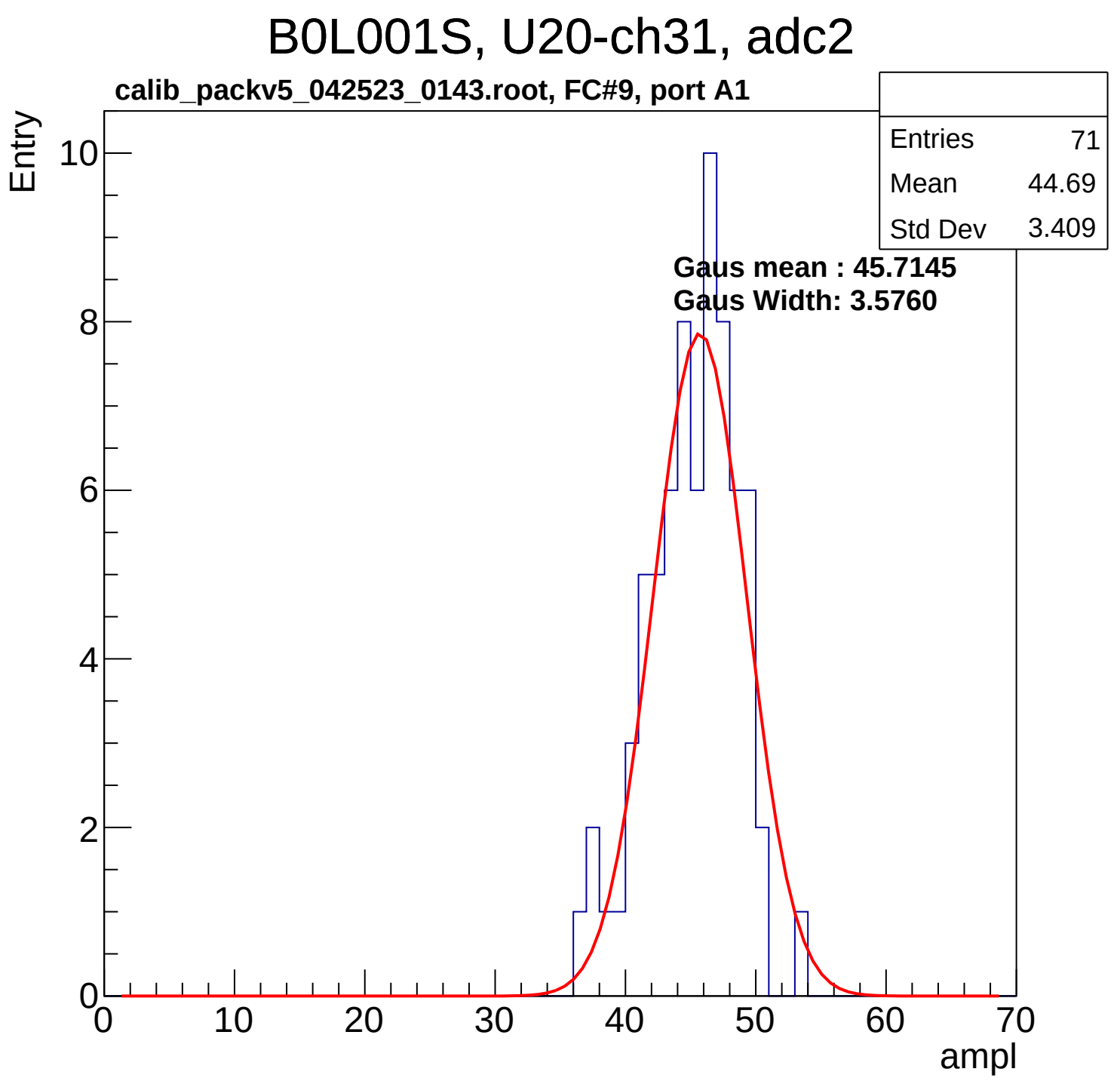
B0L001S, U20-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.69
Std Dev 3.409
Gaus mean : 45.7145
Gaus Width: 3.5760
Entry
ampl
0
2
4
6
8
10
0
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60
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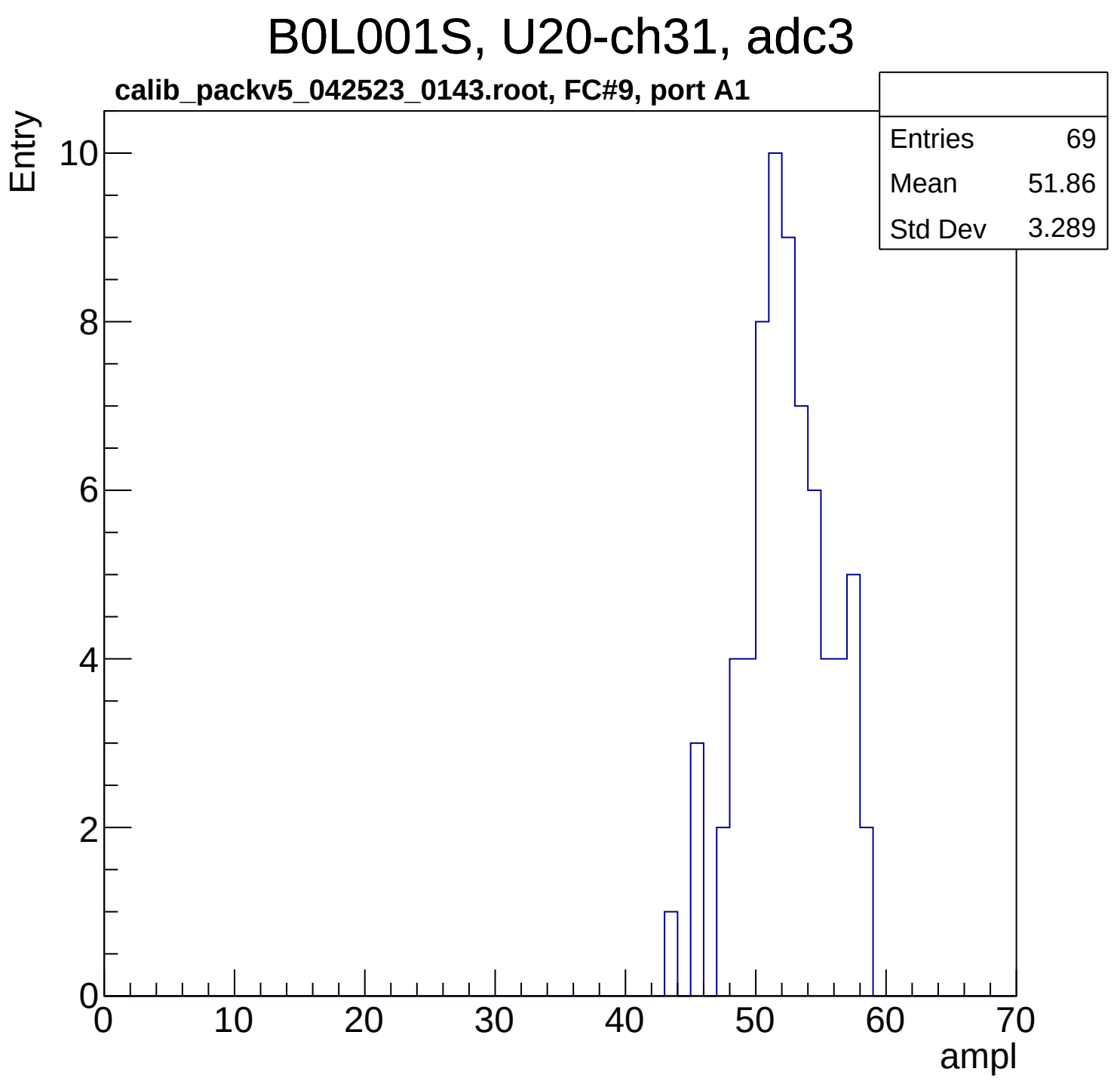
B0L001S, U20-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.86
Std Dev 3.289
Entry
ampl
0
2
4
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8
10
0
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20
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40
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60
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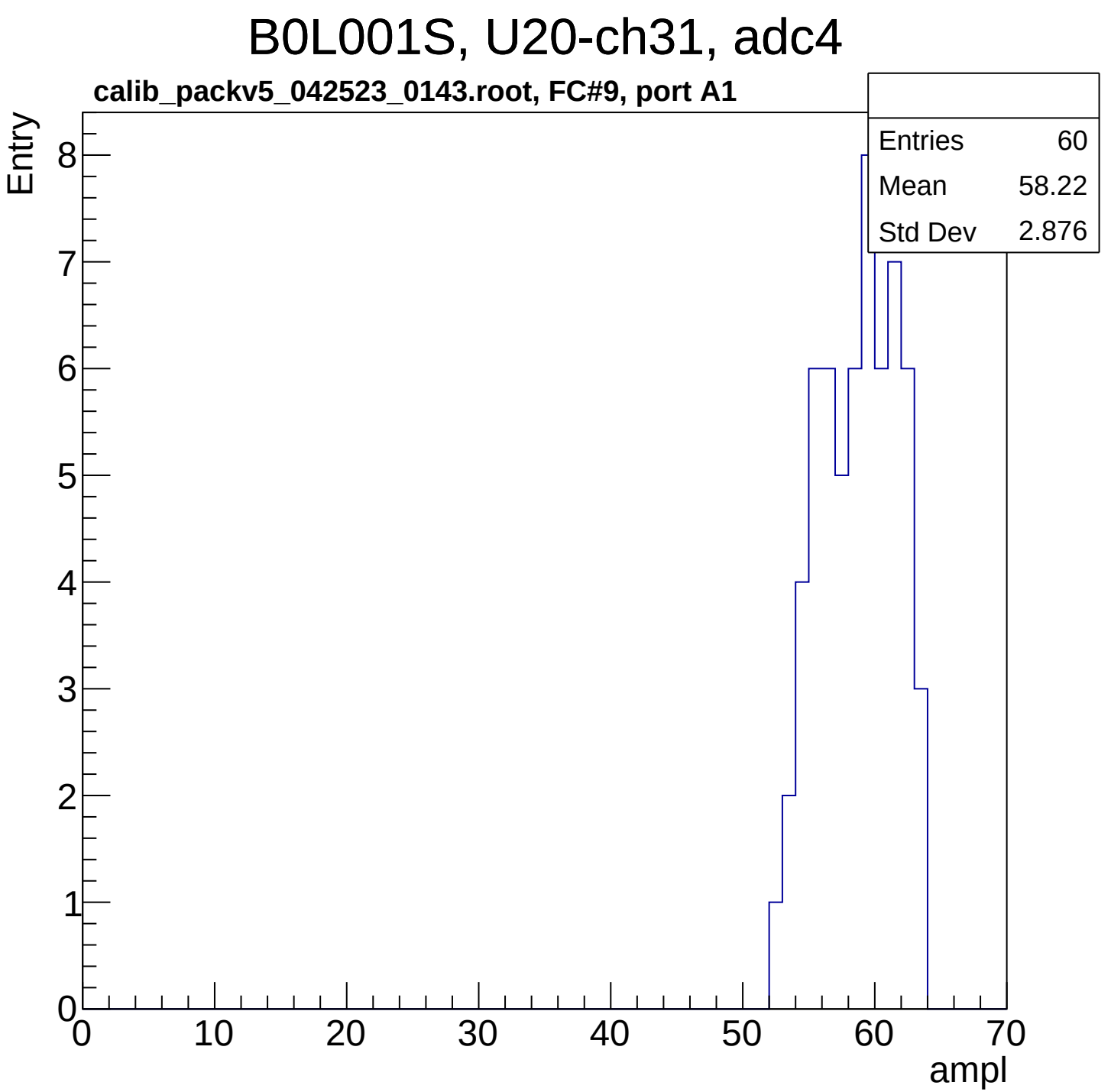
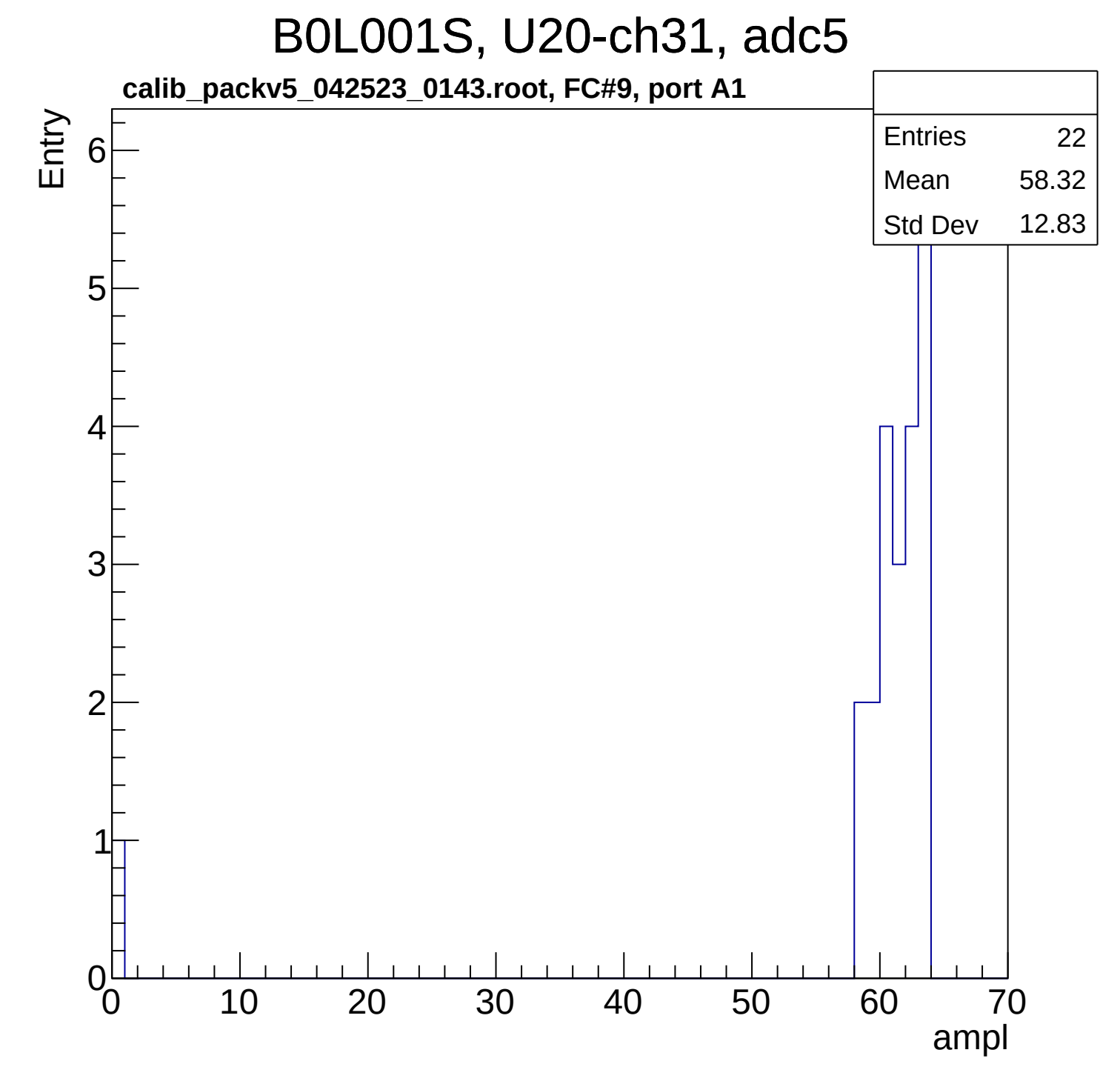
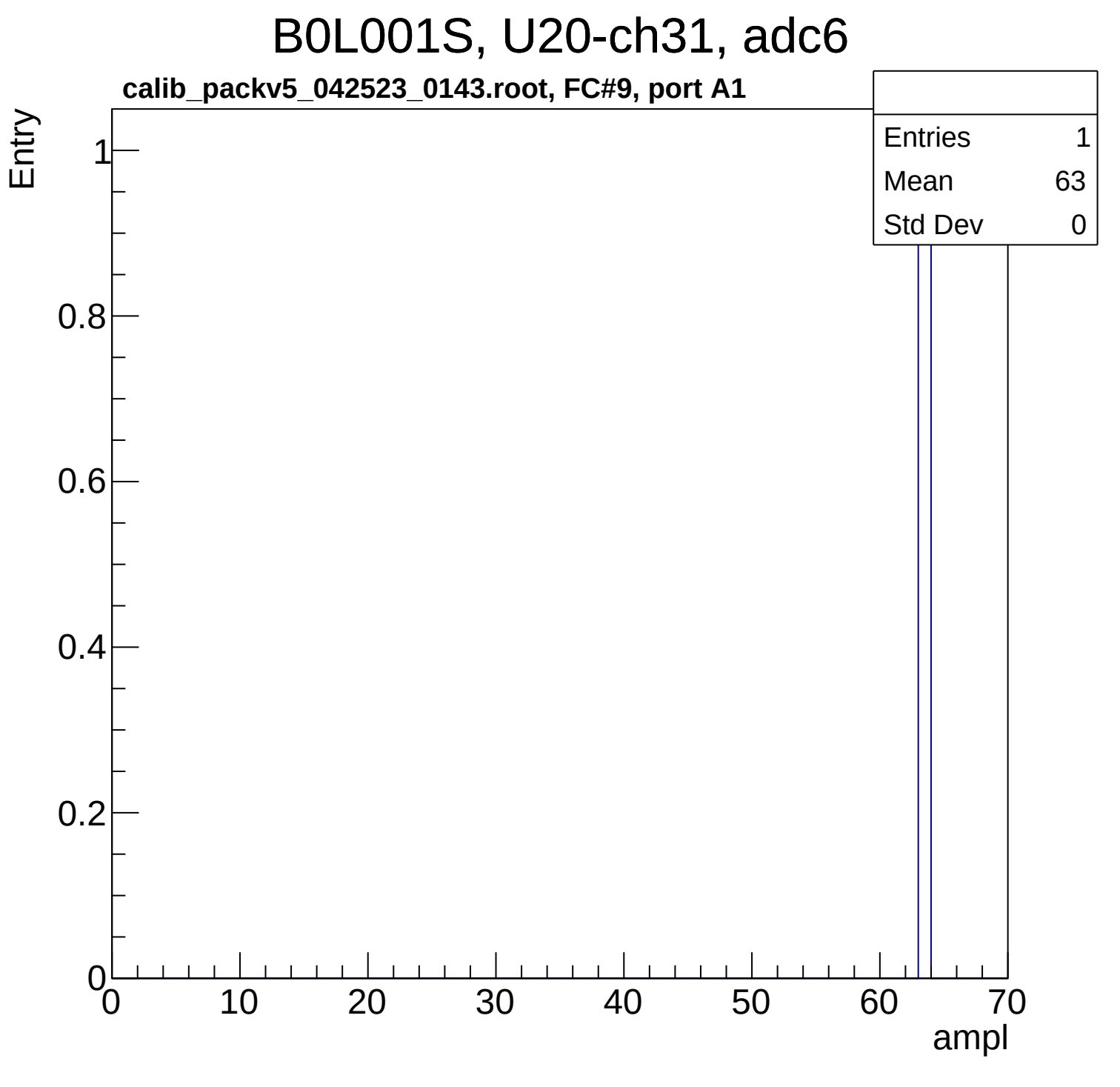
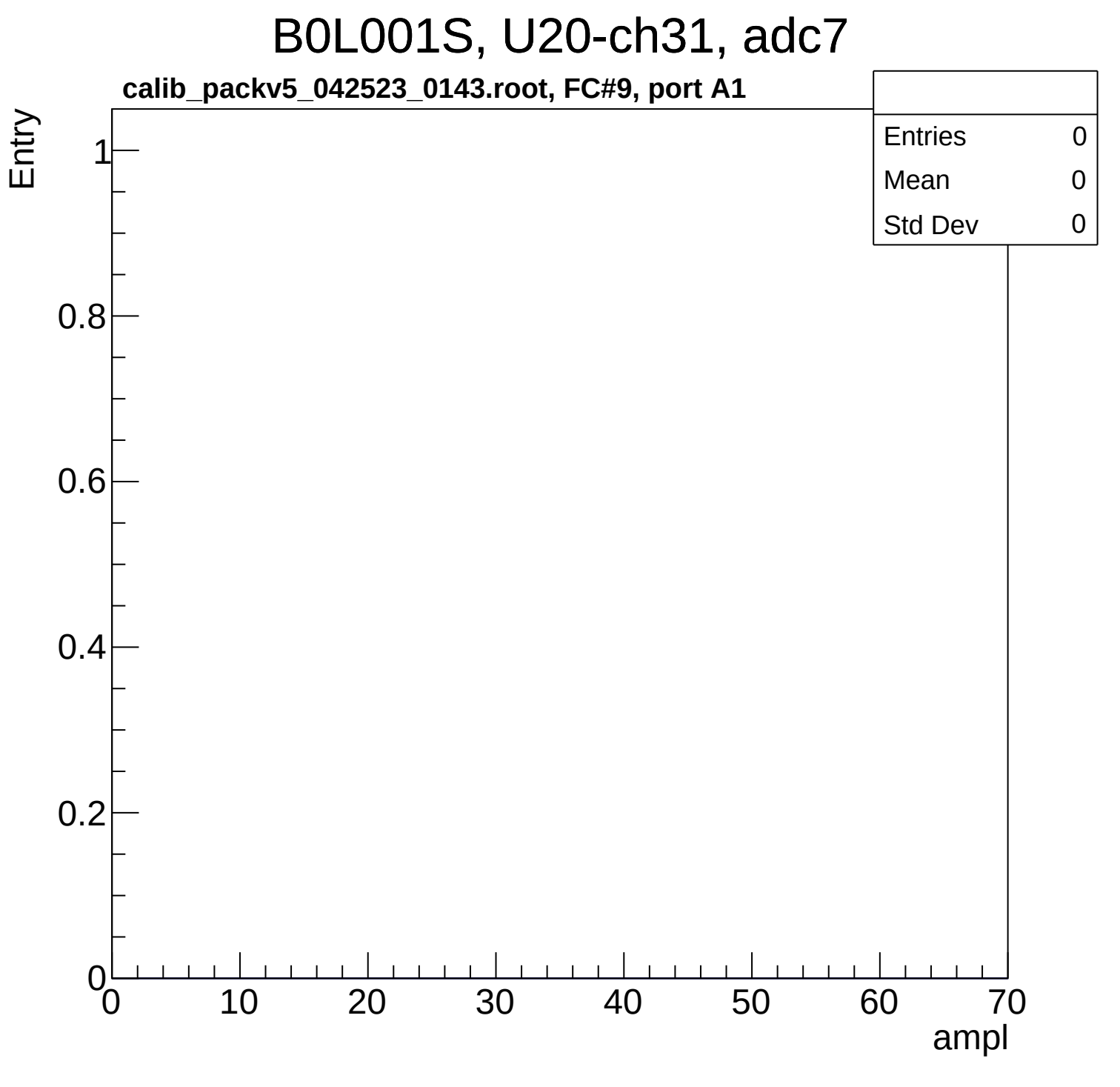
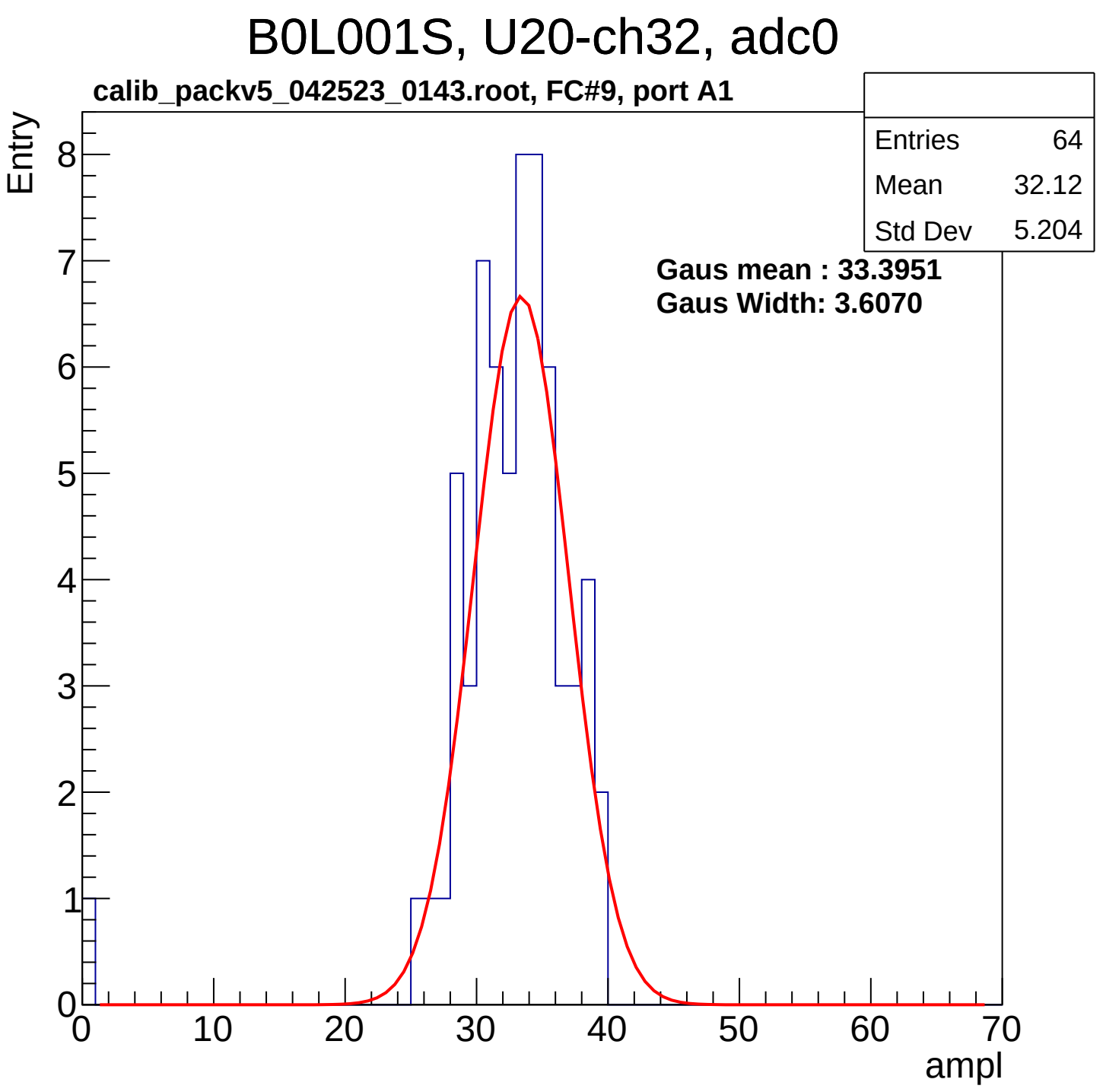
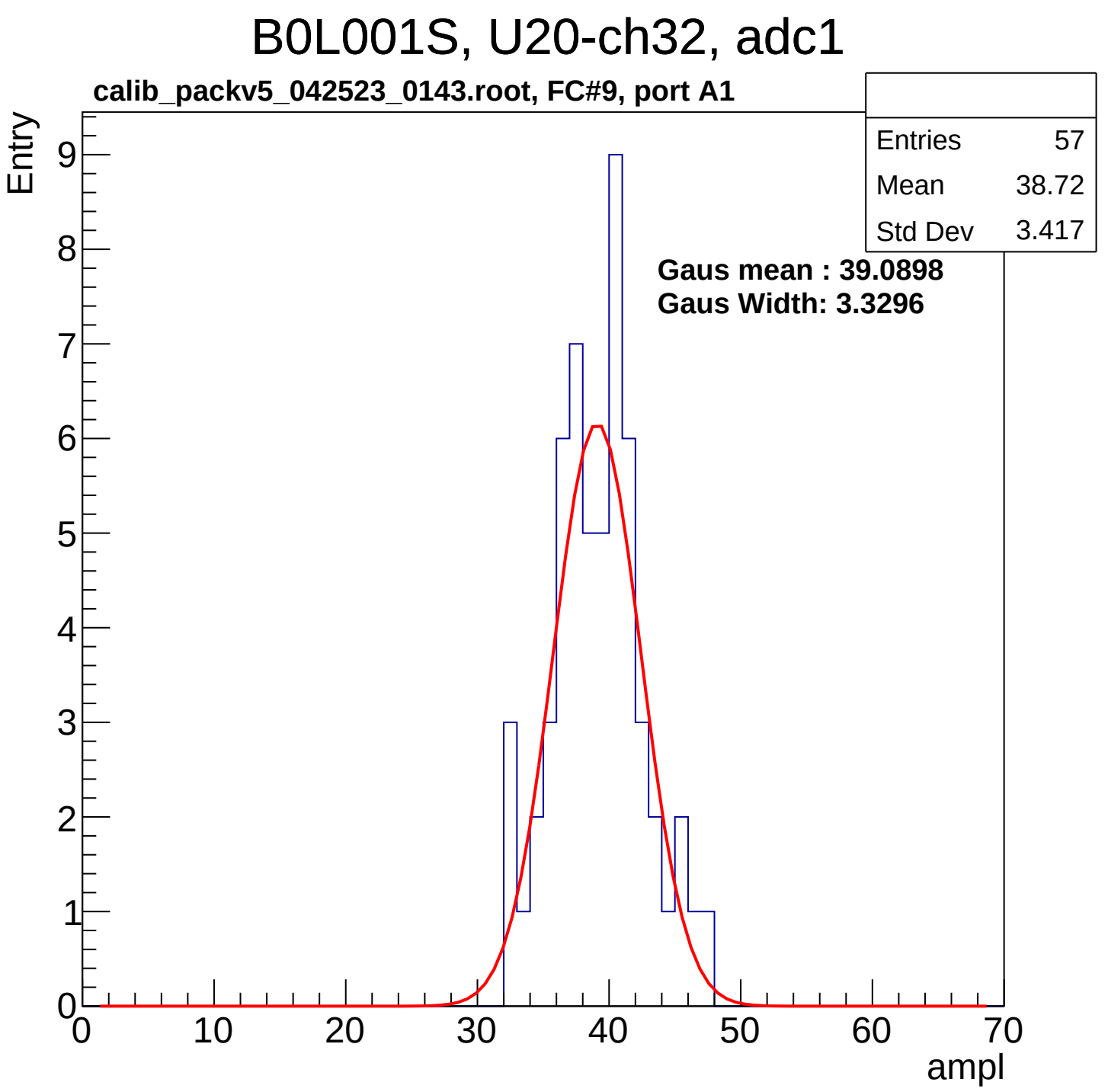

B0L001S, U20-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 58.22
Std Dev 2.876
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

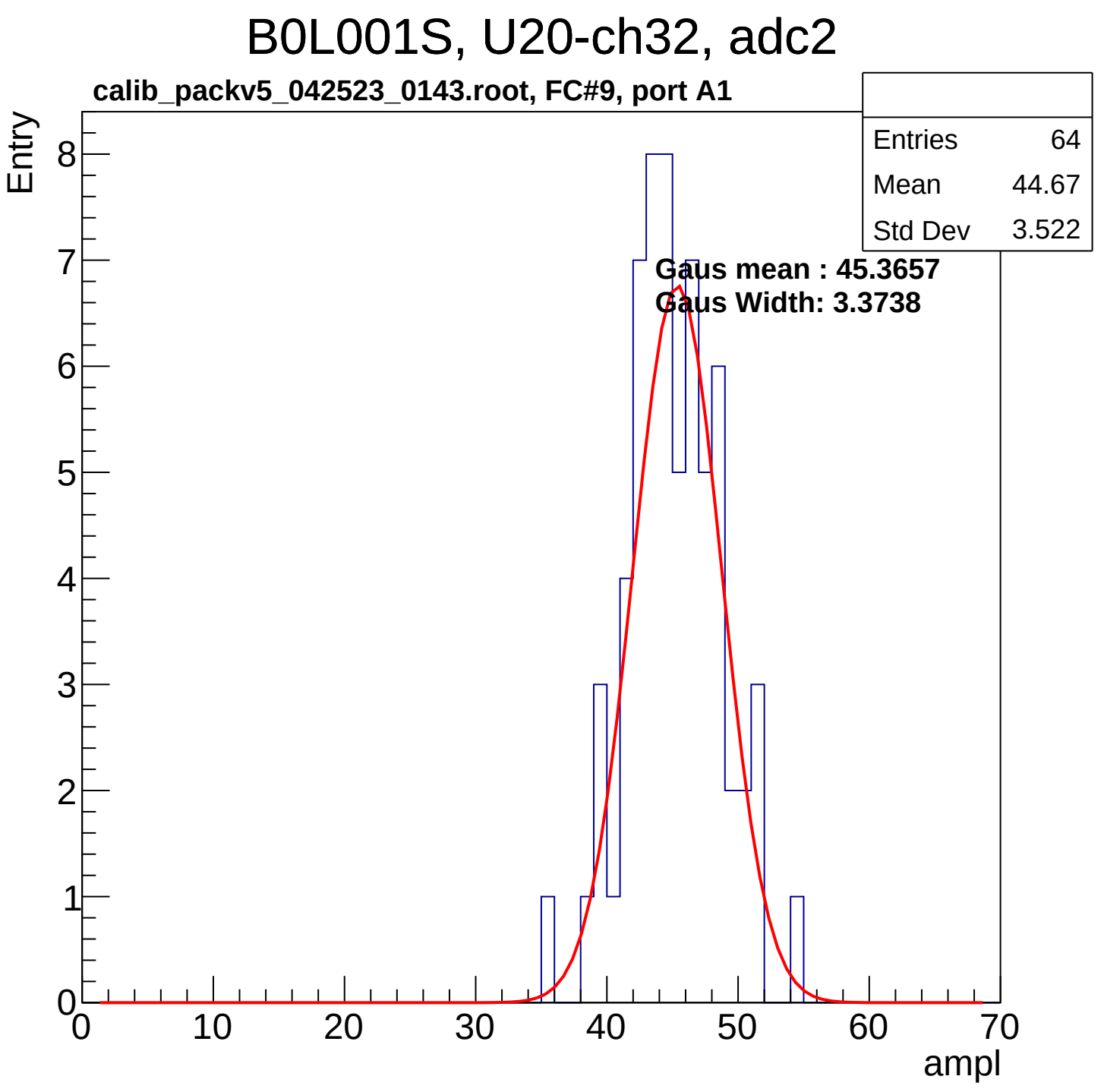

B0L001S, U20-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.32
Std Dev 12.83
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 32.12
Std Dev 5.204
Gaus mean : 33.3951
Gaus Width: 3.6070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 38.72
Std Dev 3.417
Gaus mean : 39.0898
Gaus Width: 3.3296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 44.67
Std Dev 3.522
Gaus mean : 45.3657
Gaus Width: 3.3738
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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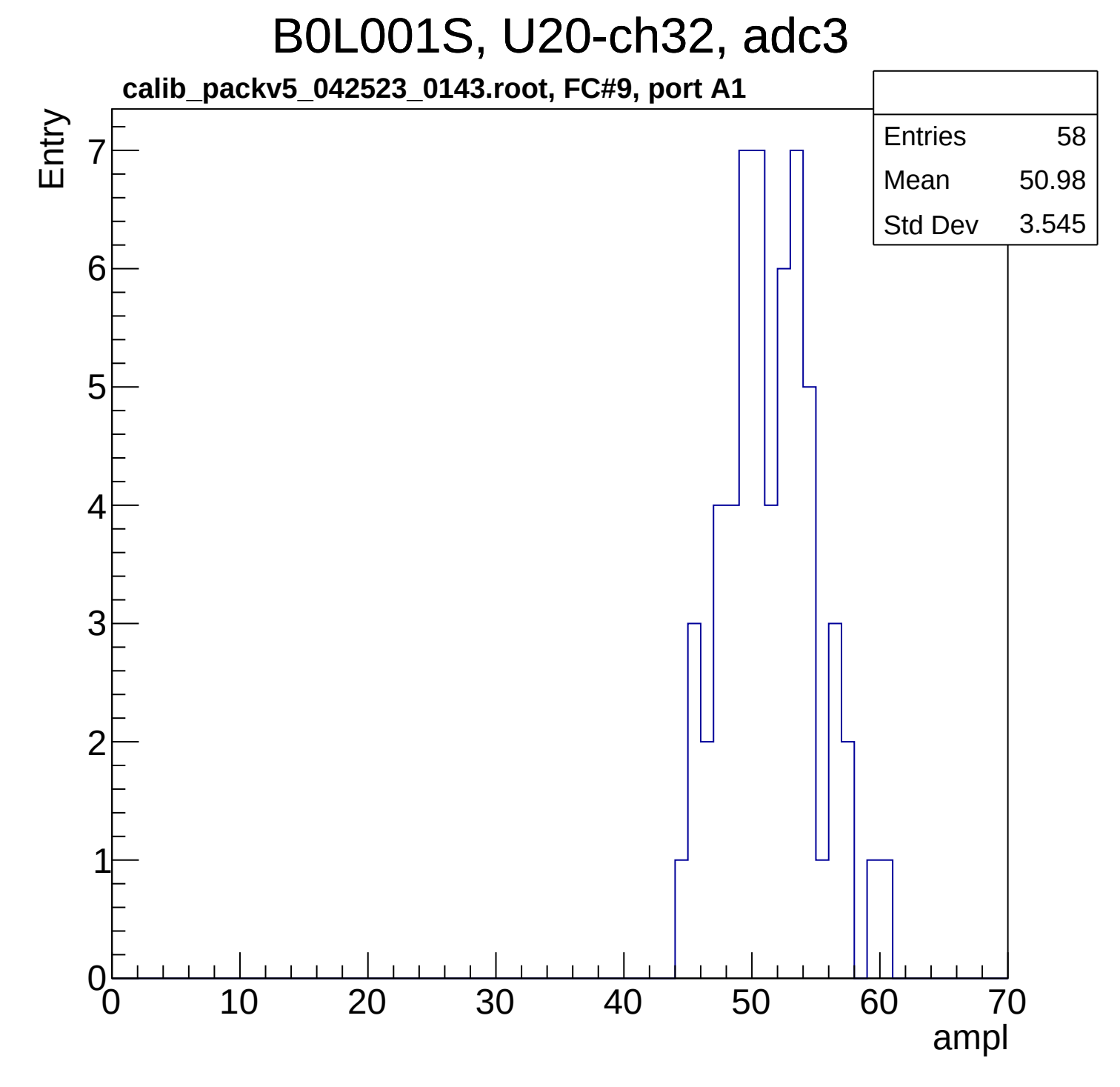
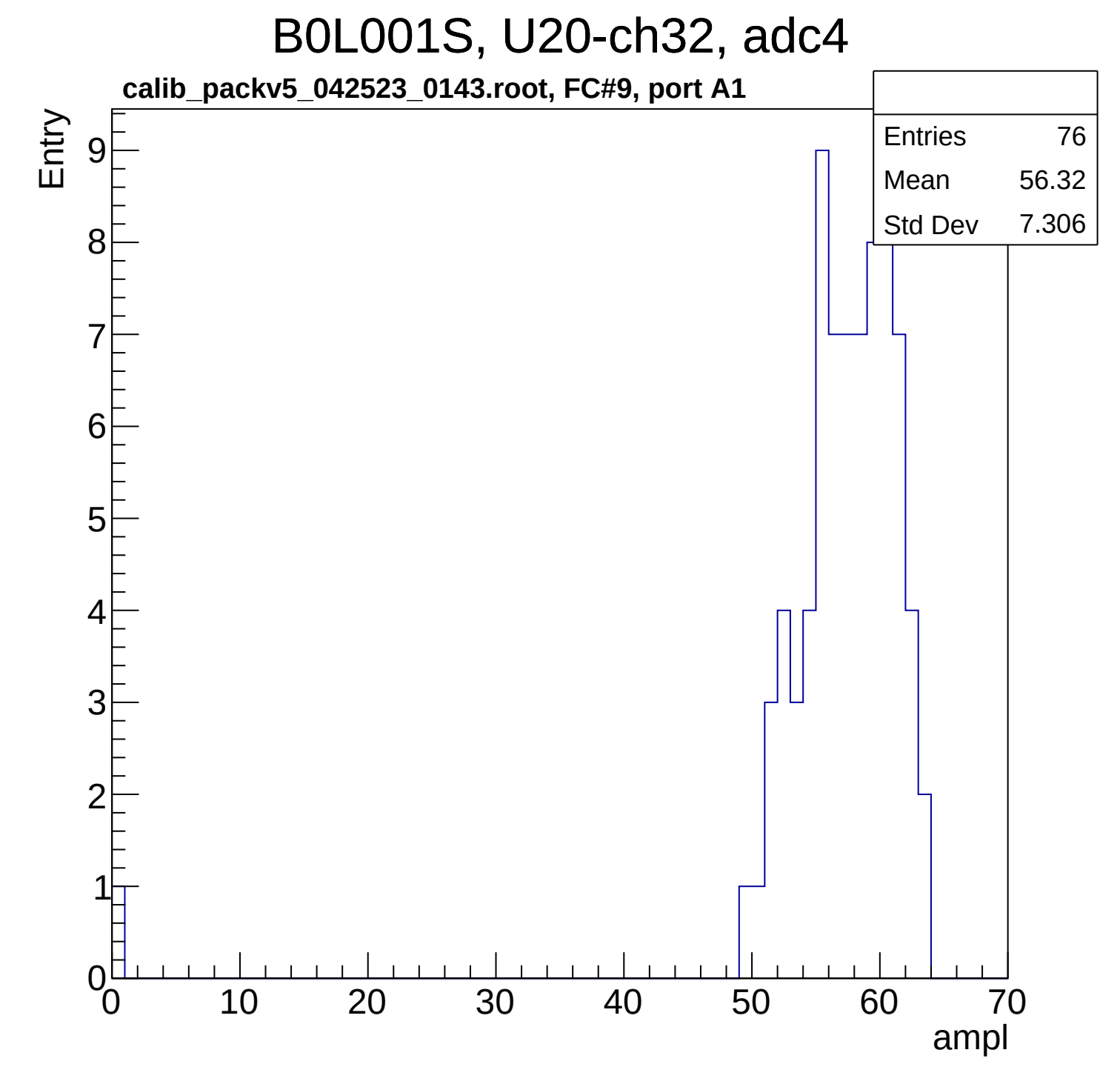

B0L001S, U20-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.98
Std Dev 3.545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 56.32
Std Dev 7.306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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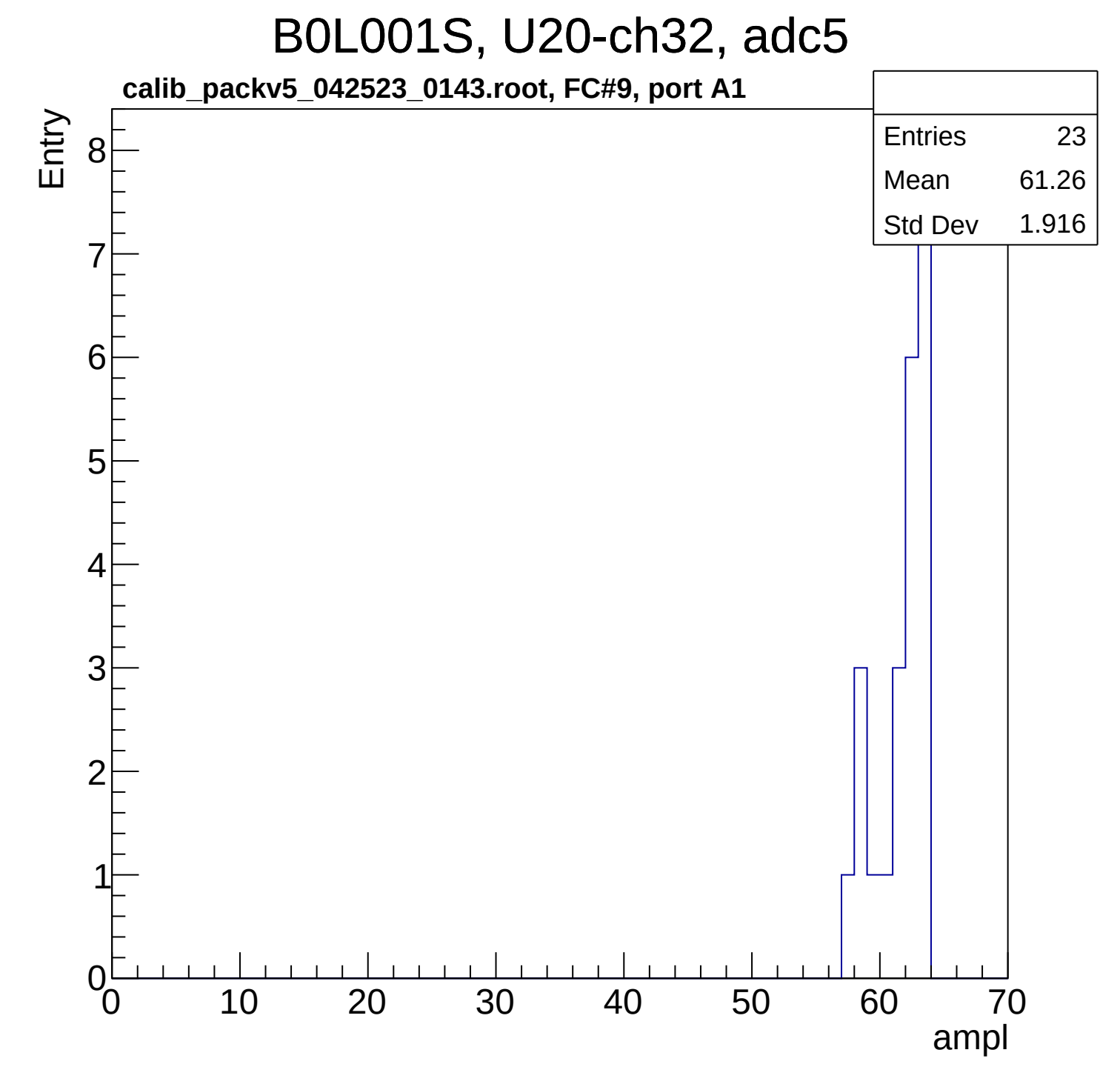
40
50
60
70


B0L001S, U20-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.26
Std Dev 1.916
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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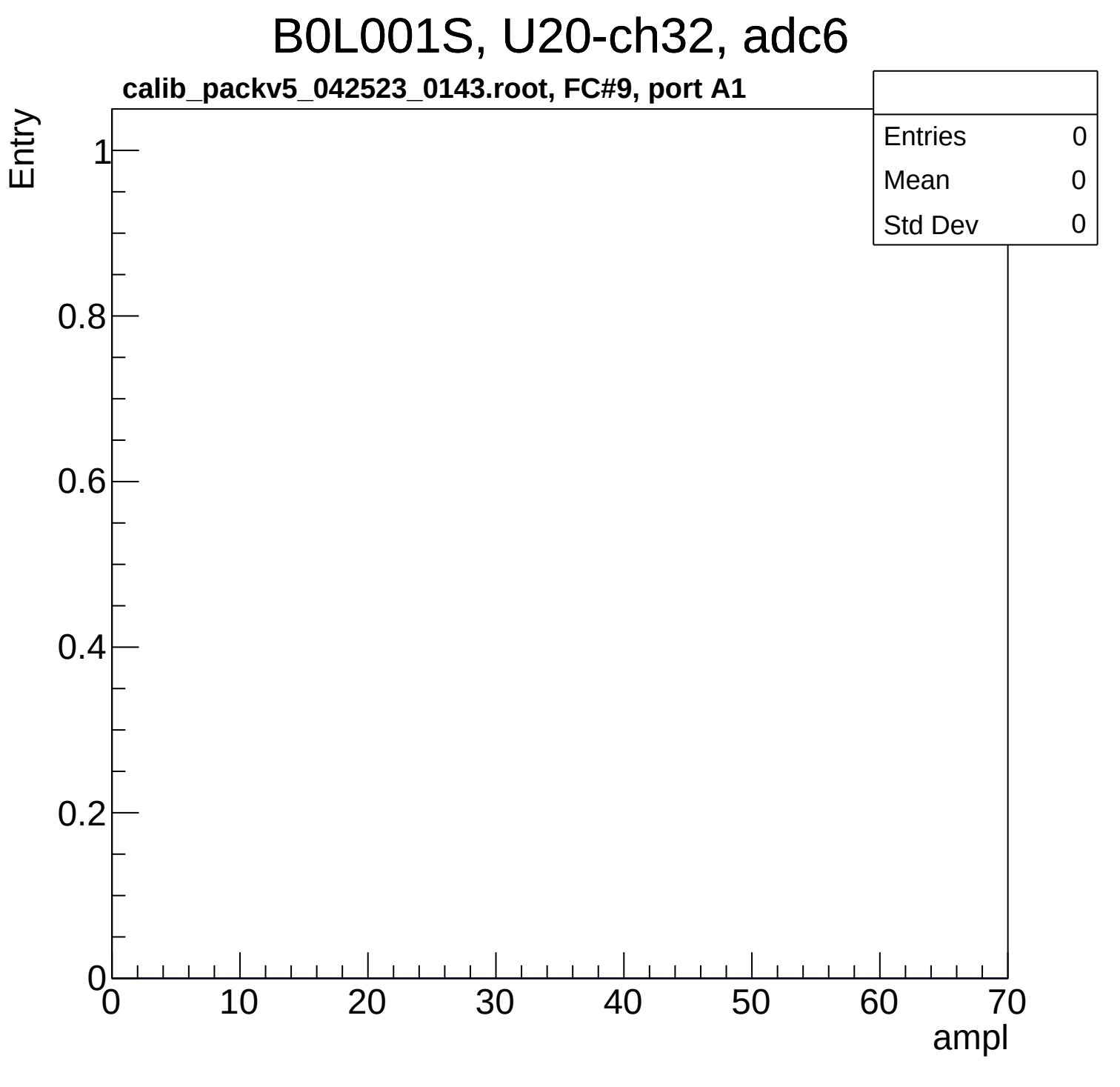
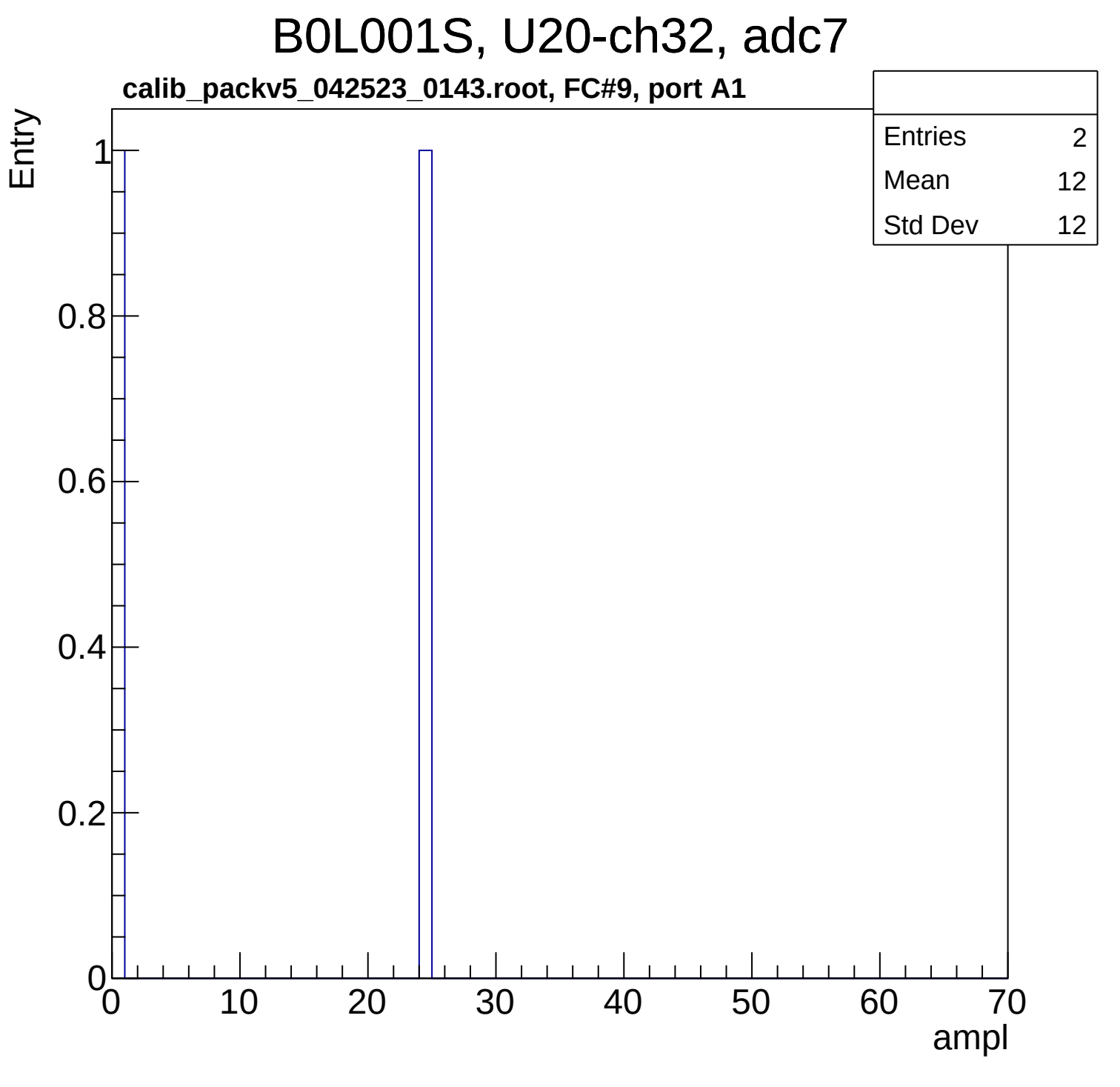

B0L001S, U20-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
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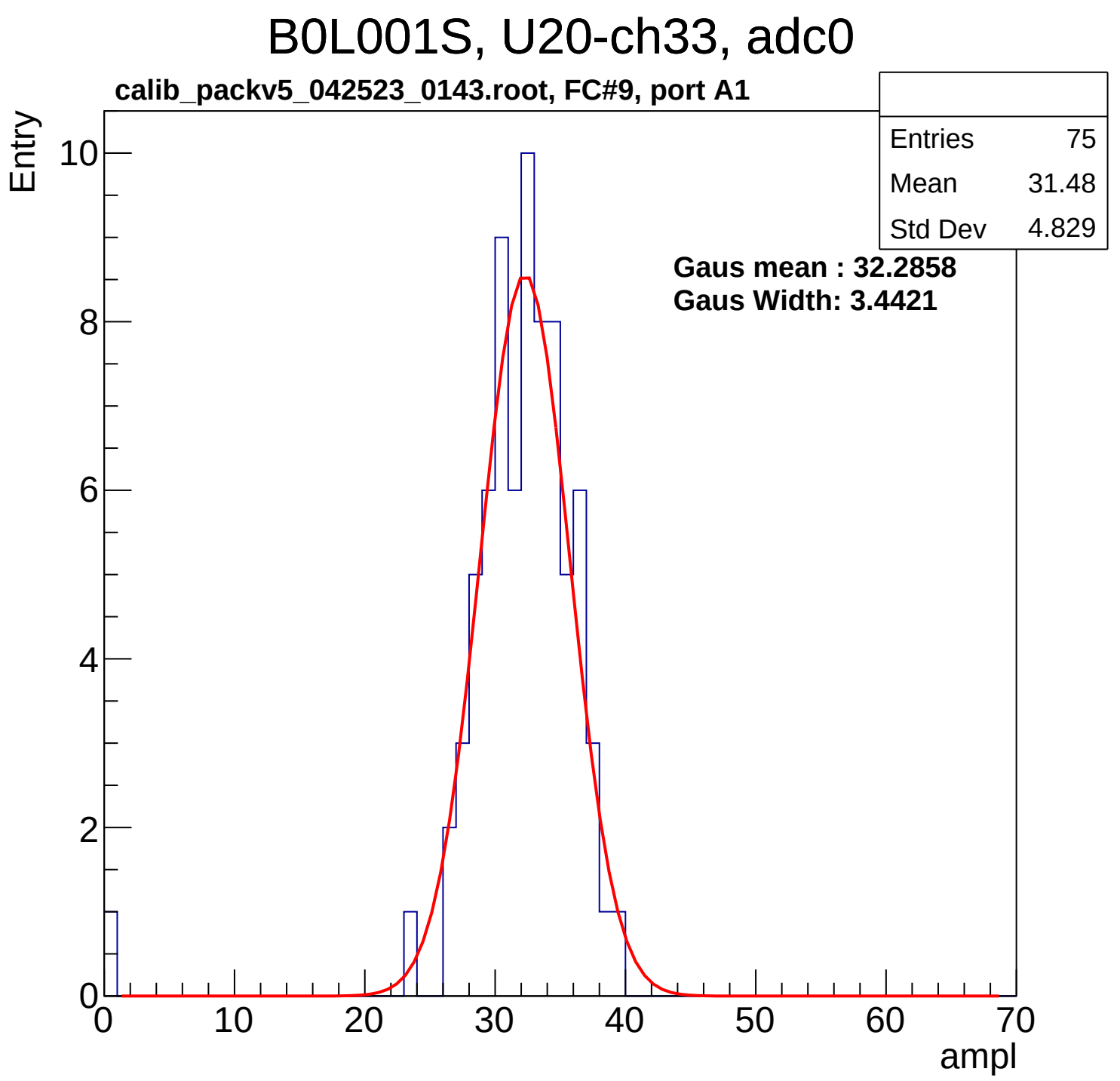
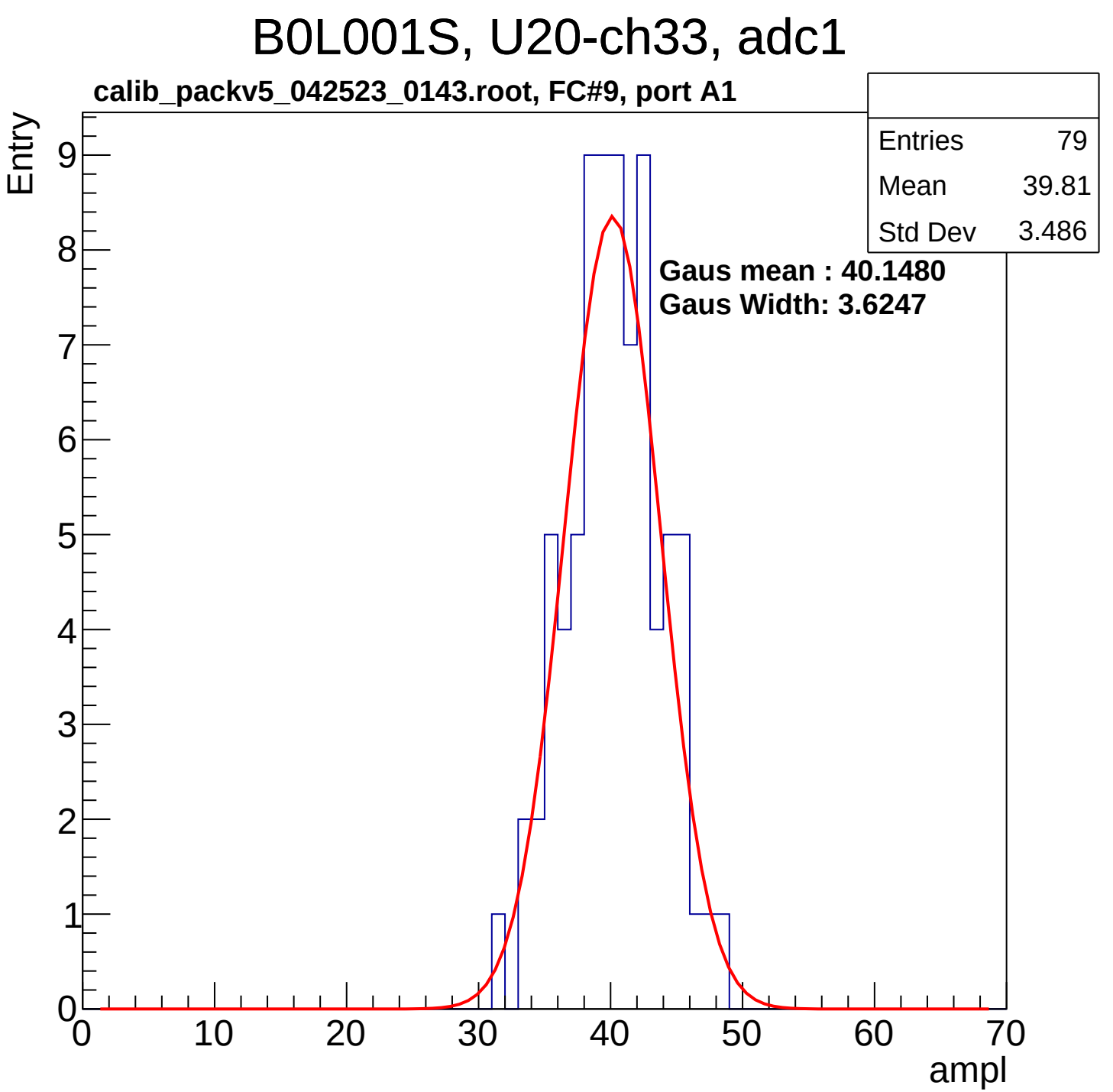
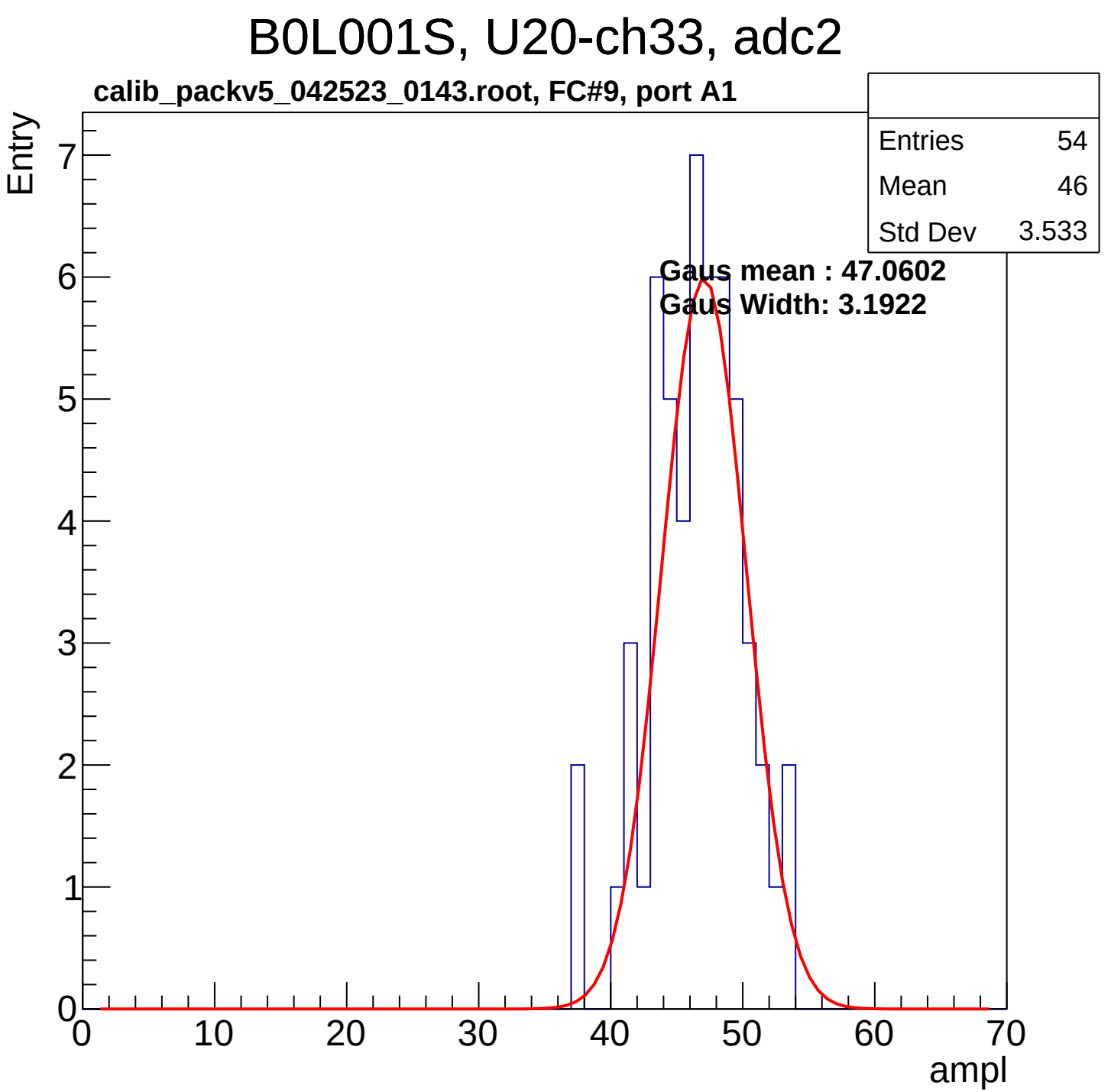
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 31.48
Std Dev 4.829
Gaus mean : 32.2858
Gaus Width: 3.4421
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 39.81
Std Dev 3.486
Gaus mean : 40.1480
Gaus Width: 3.6247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 46
Std Dev 3.533
Gaus mean : 47.0602
Gaus Width: 3.1922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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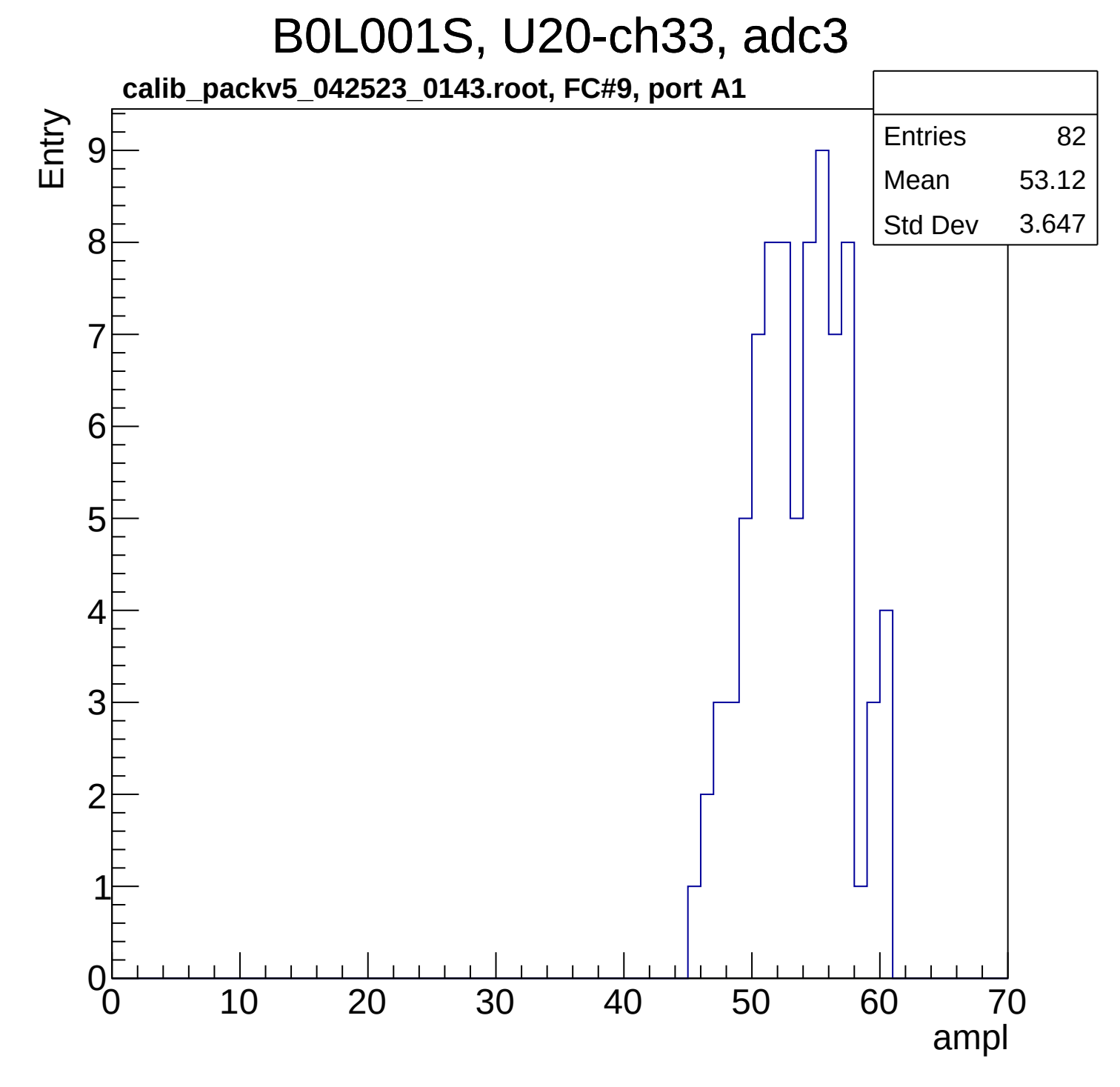
B0L001S, U20-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 53.12
Std Dev 3.647
Entry
ampl
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2
3
4
5
6
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8
9
0
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60
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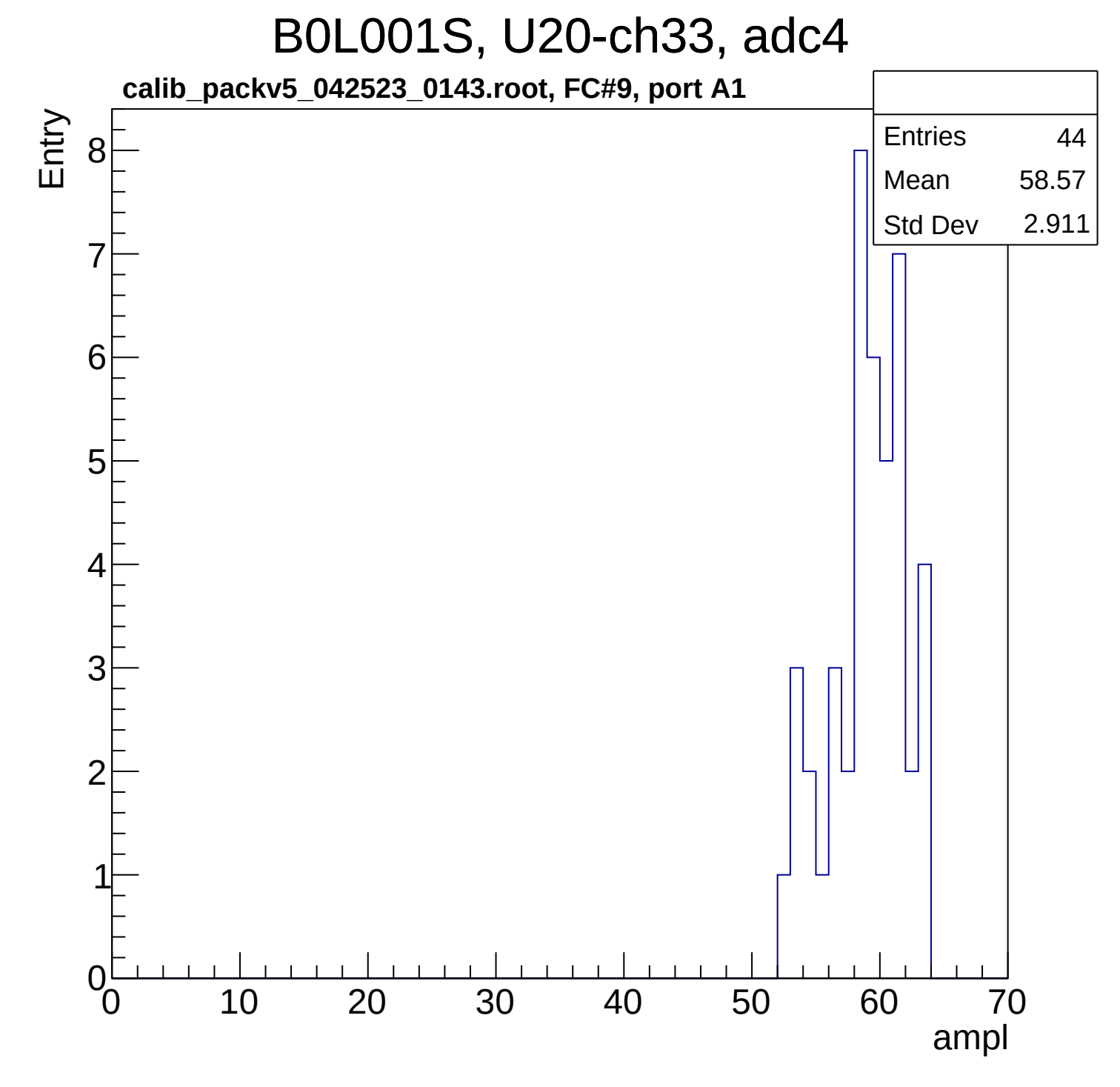
B0L001S, U20-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.57
Std Dev 2.911
Entry
ampl
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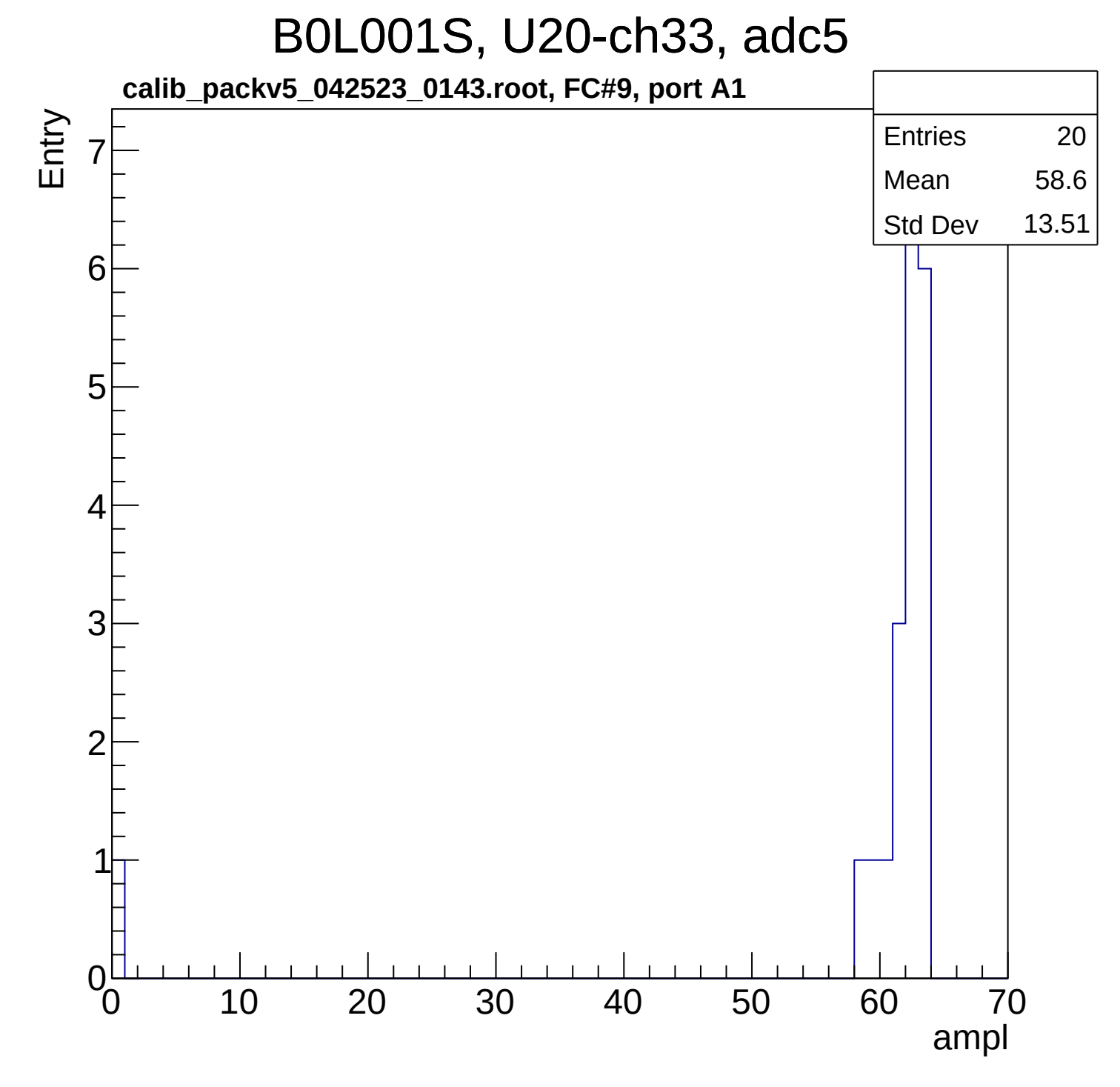
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 58.6
Std Dev 13.51
Entry
ampl
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1
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3
4
5
6
7
0
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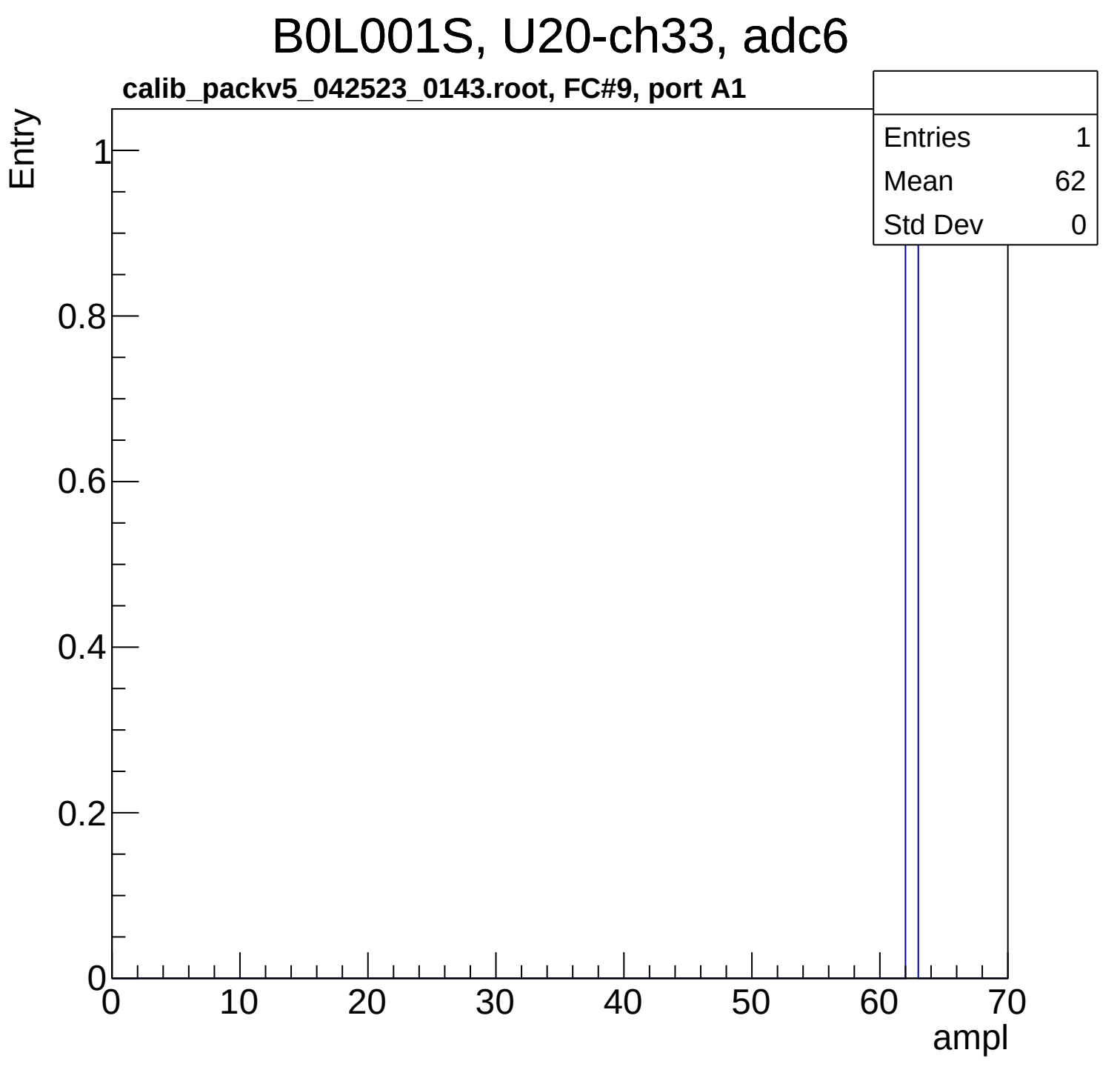
20
30
40
50
60
70


B0L001S, U20-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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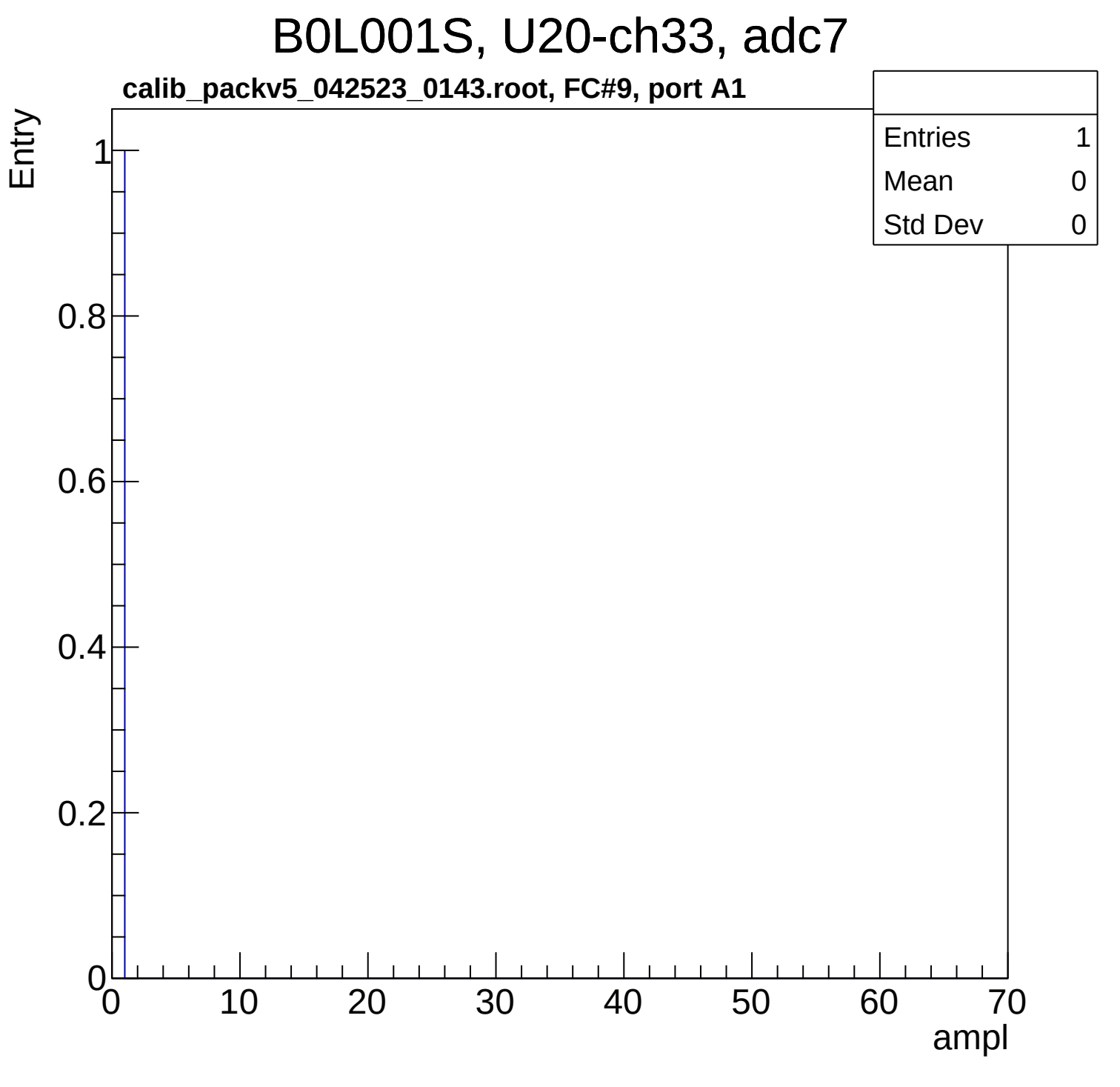
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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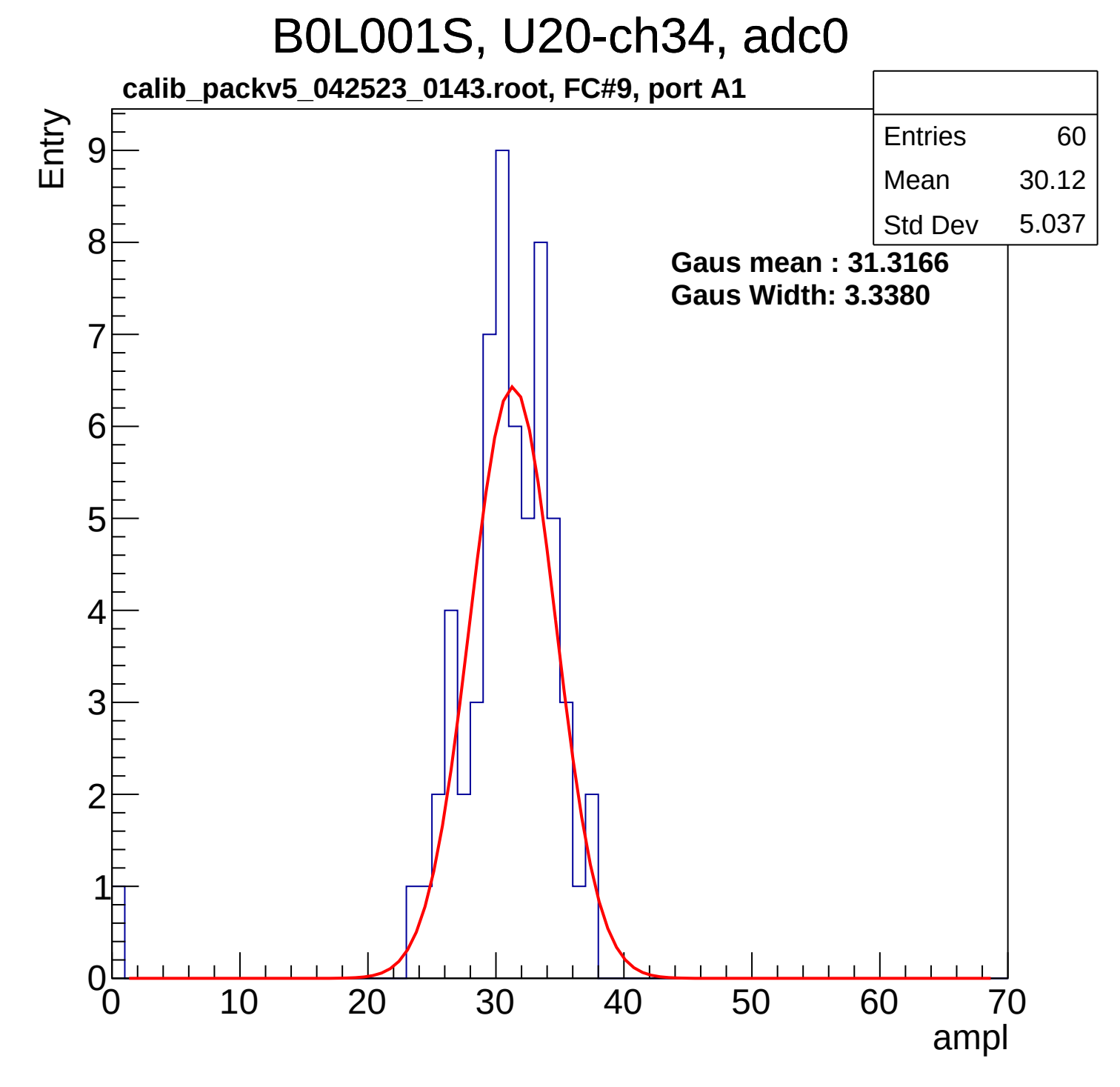
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.12
Std Dev 5.037
Gaus mean : 31.3166
Gaus Width: 3.3380
Entry
ampl
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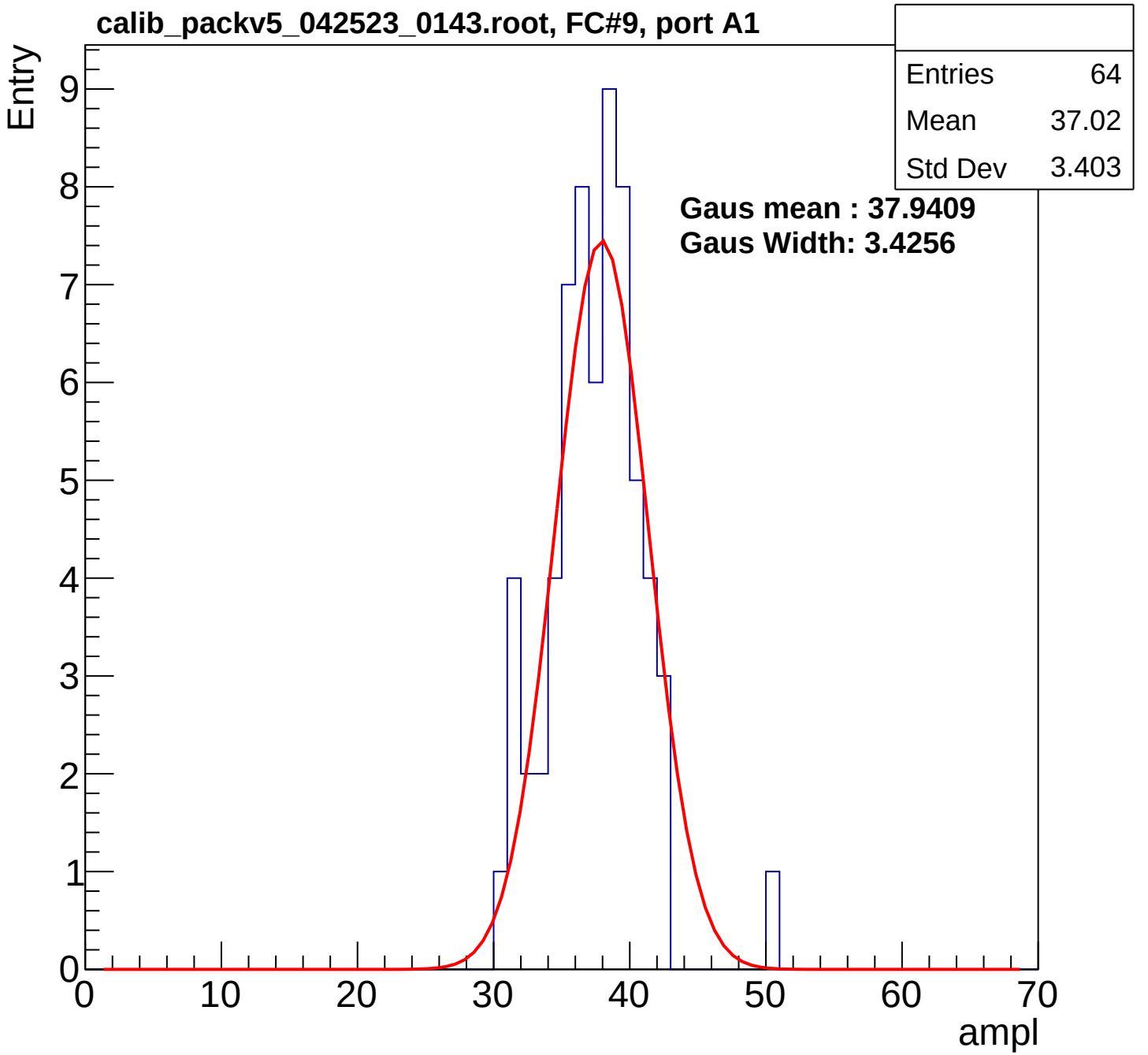
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.02
Std Dev 3.403
Gaus mean : 37.9409
Gaus Width: 3.4256
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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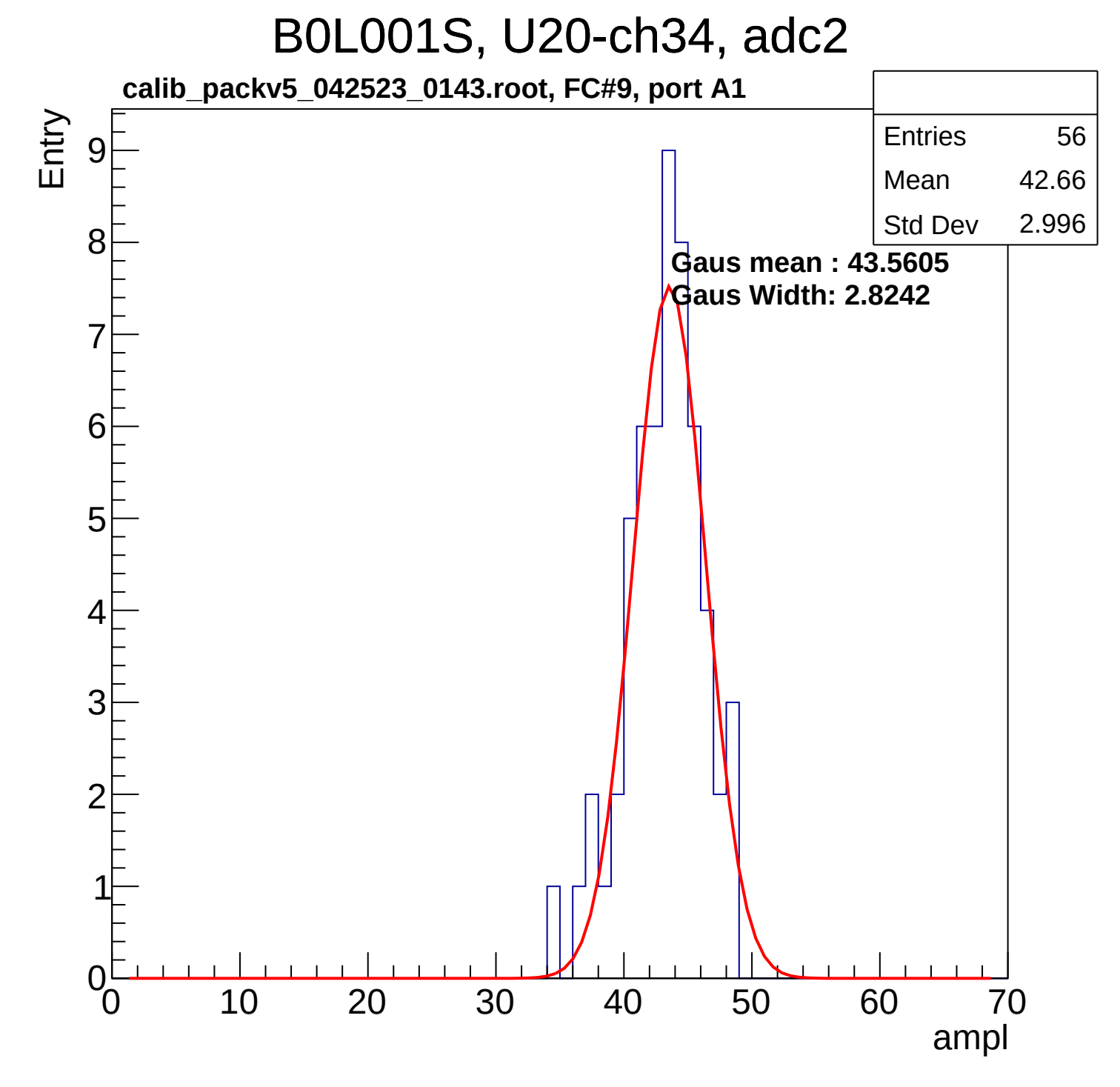
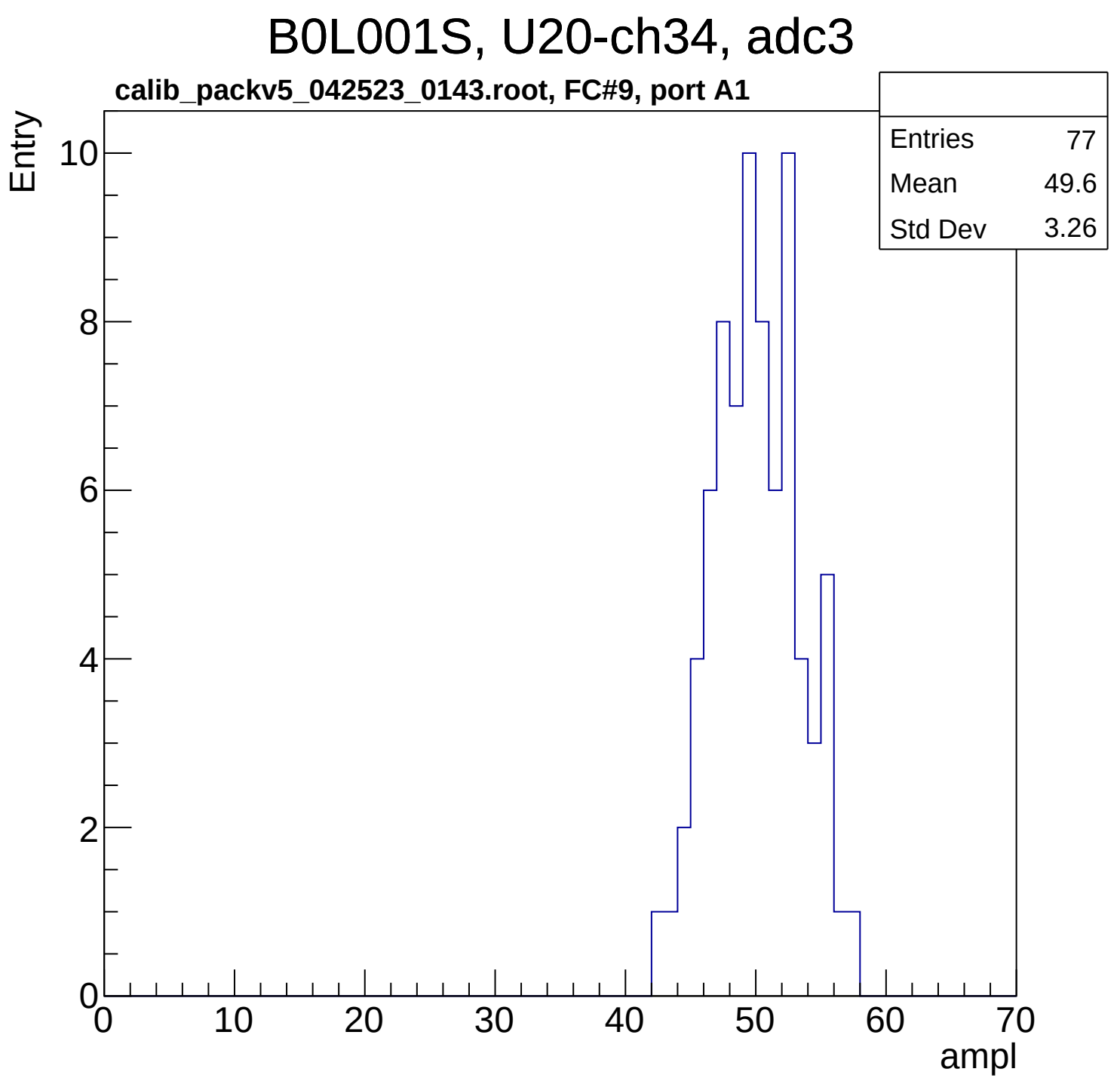

B0L001S, U20-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.66
Std Dev 2.996
Gaus mean : 43.5605
Gaus Width: 2.8242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.6
Std Dev 3.26
Entry
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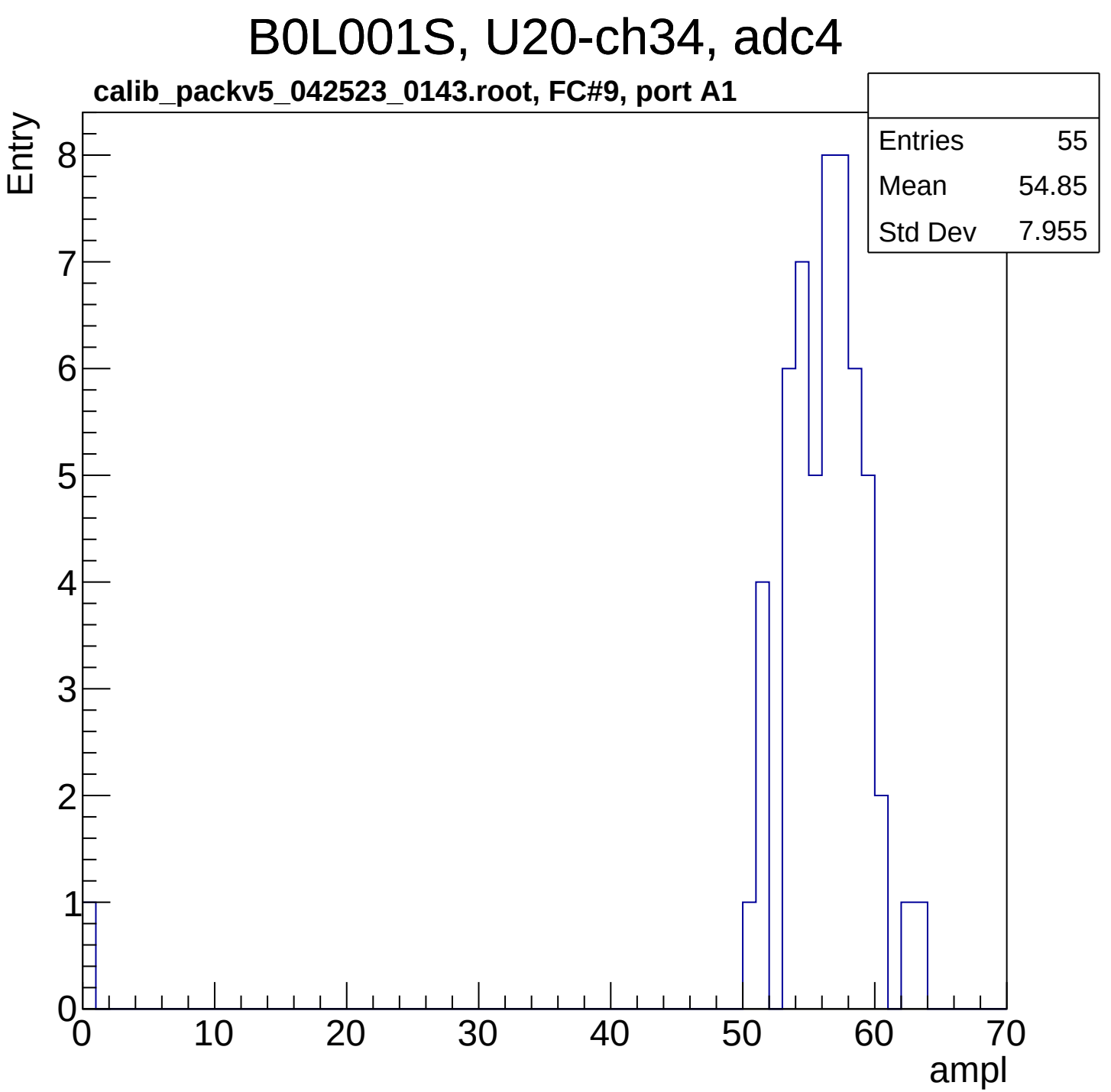
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.85
Std Dev 7.955
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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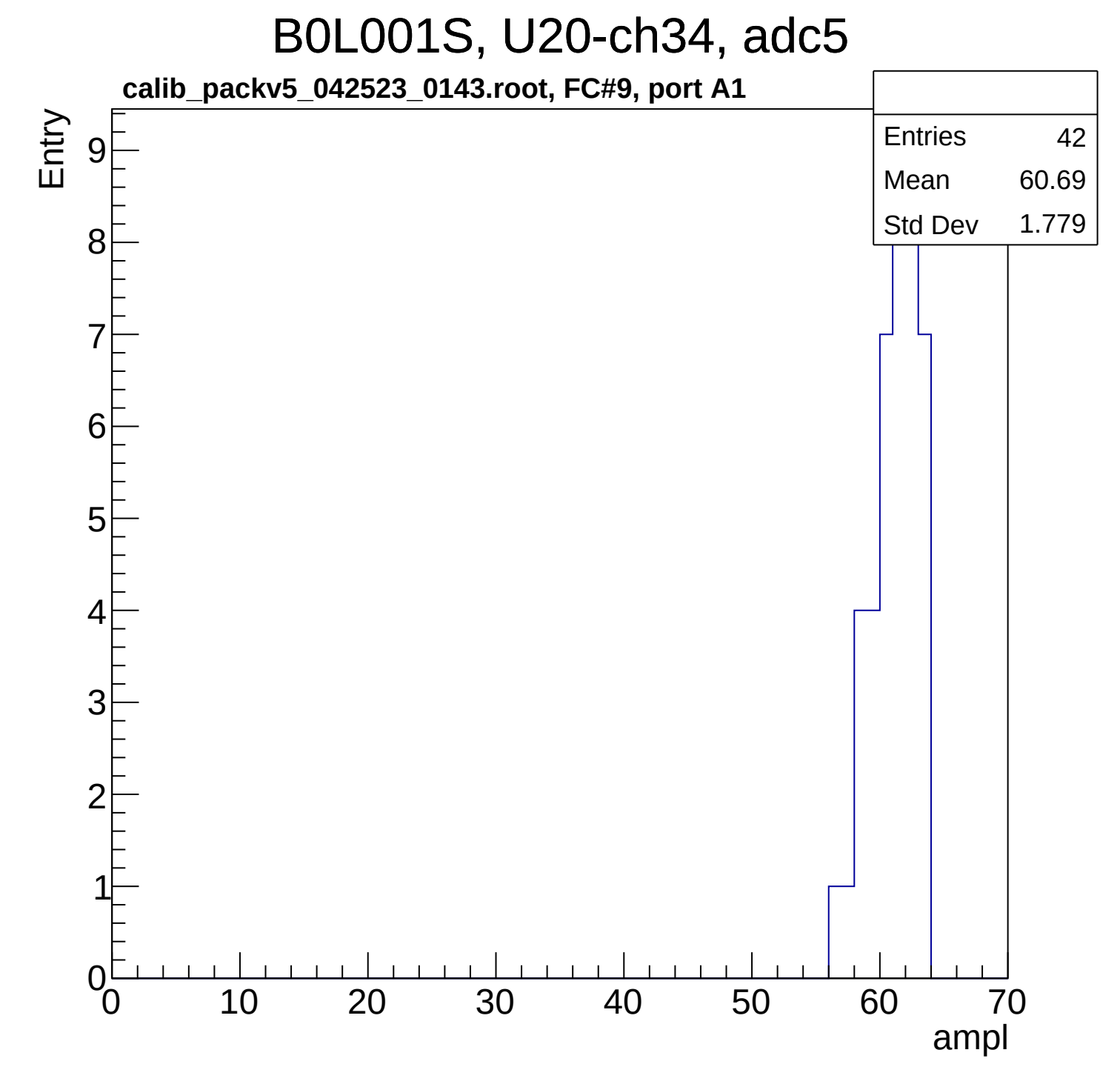
B0L001S, U20-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.69
Std Dev 1.779
Entry
ampl
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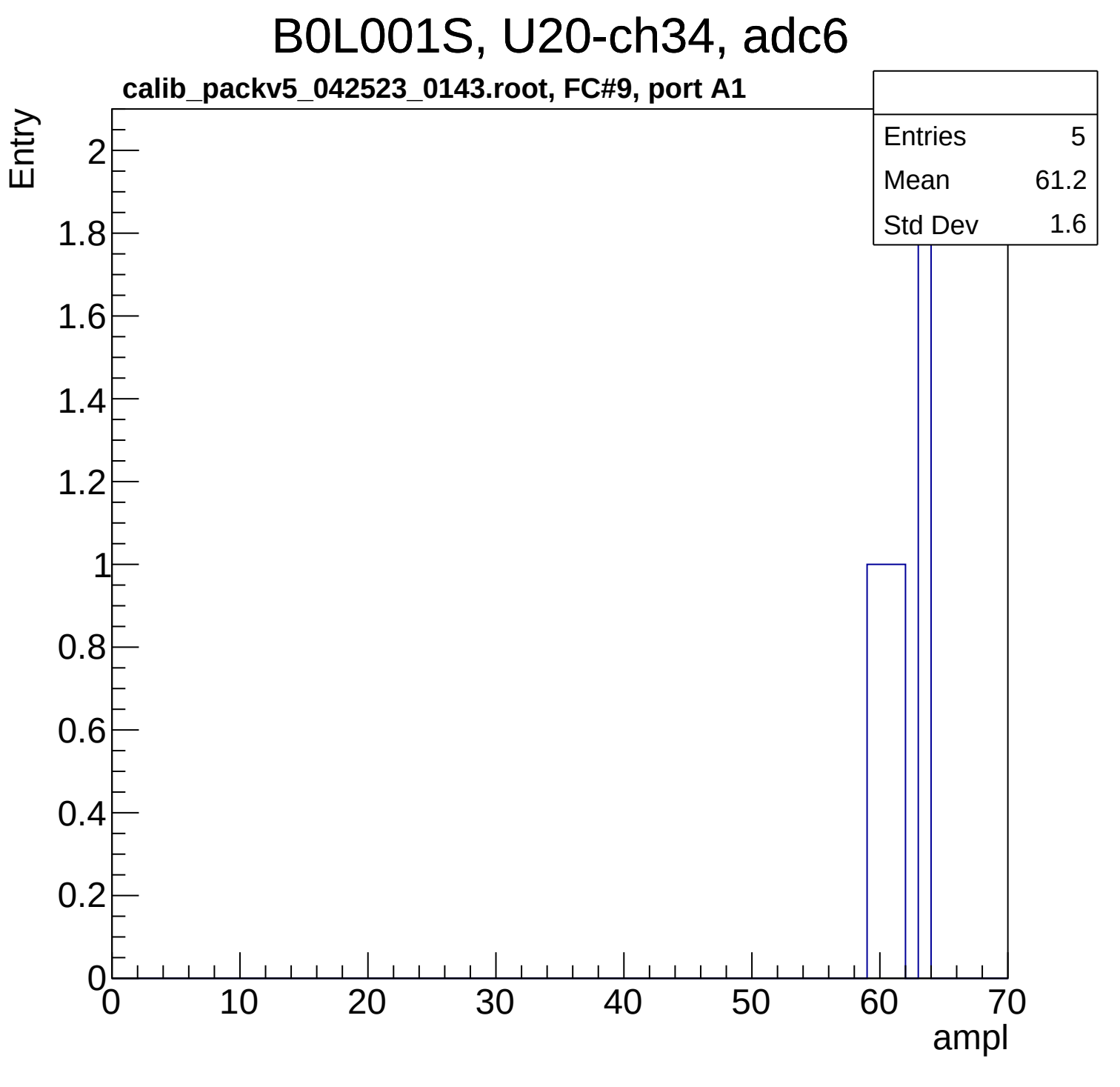
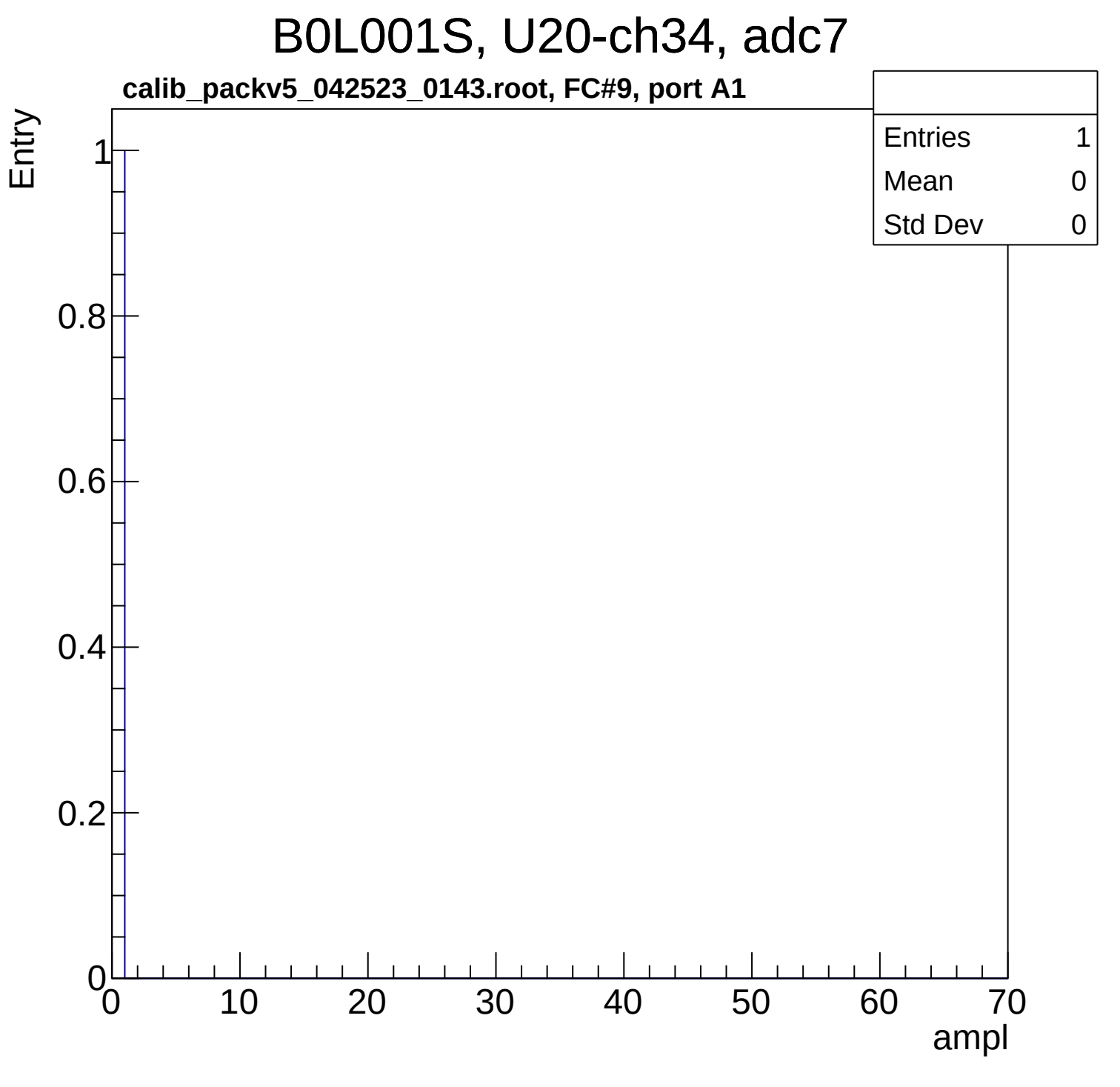
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.2
Std Dev 1.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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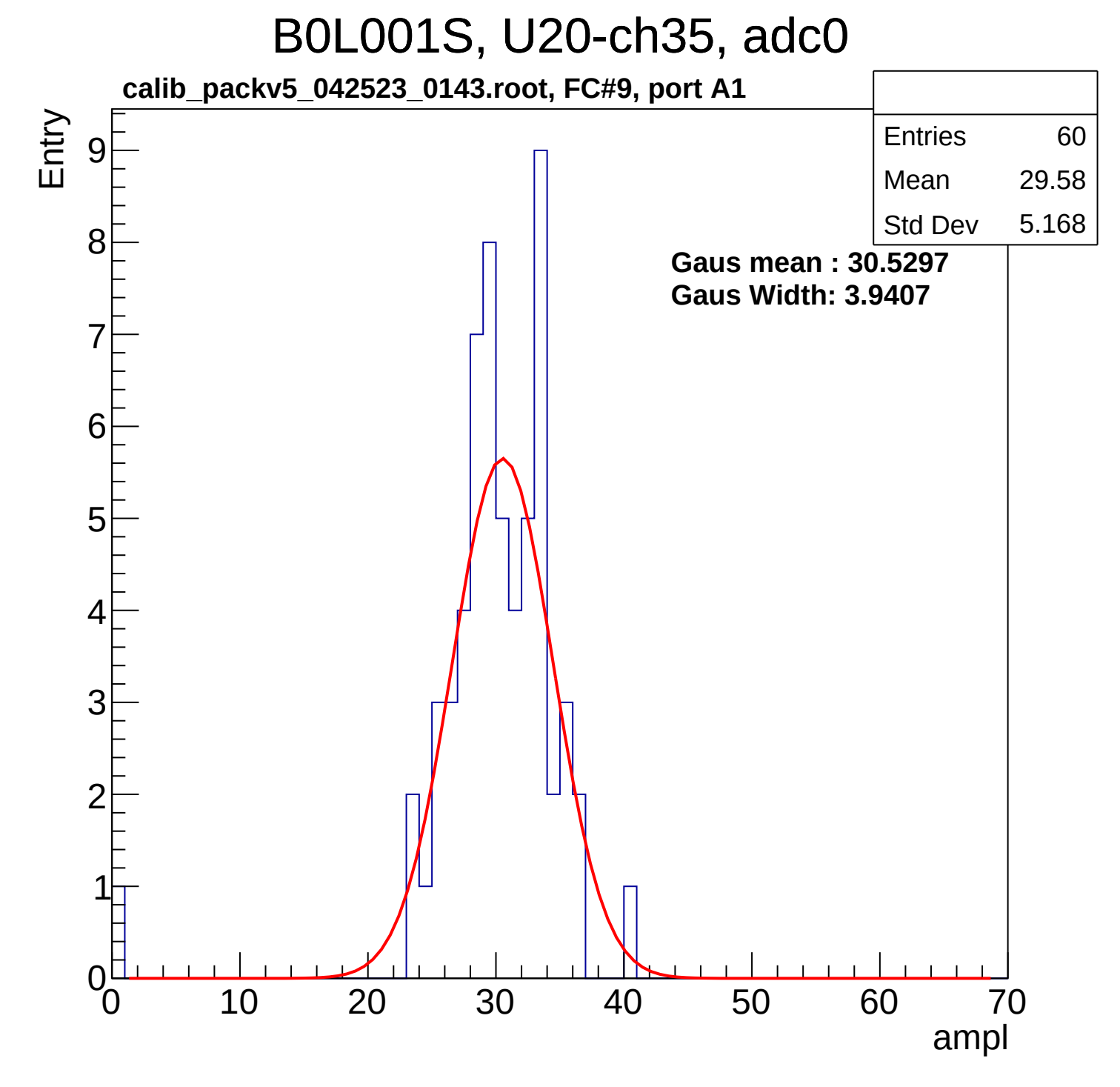
60
70
ampl


B0L001S, U20-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.58
Std Dev 5.168
Gaus mean : 30.5297
Gaus Width: 3.9407
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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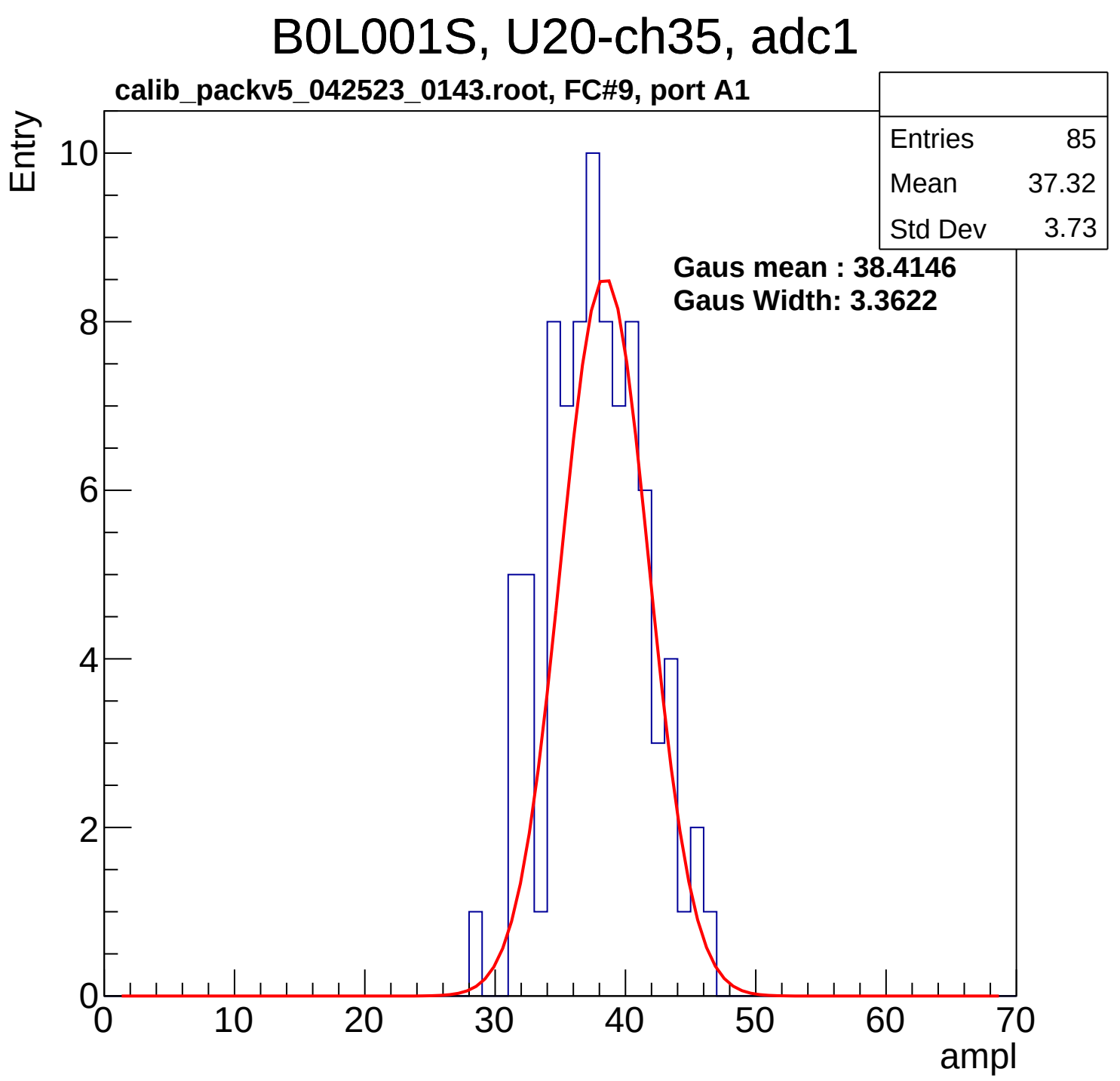
B0L001S, U20-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 37.32
Std Dev 3.73
Gaus mean : 38.4146
Gaus Width: 3.3622
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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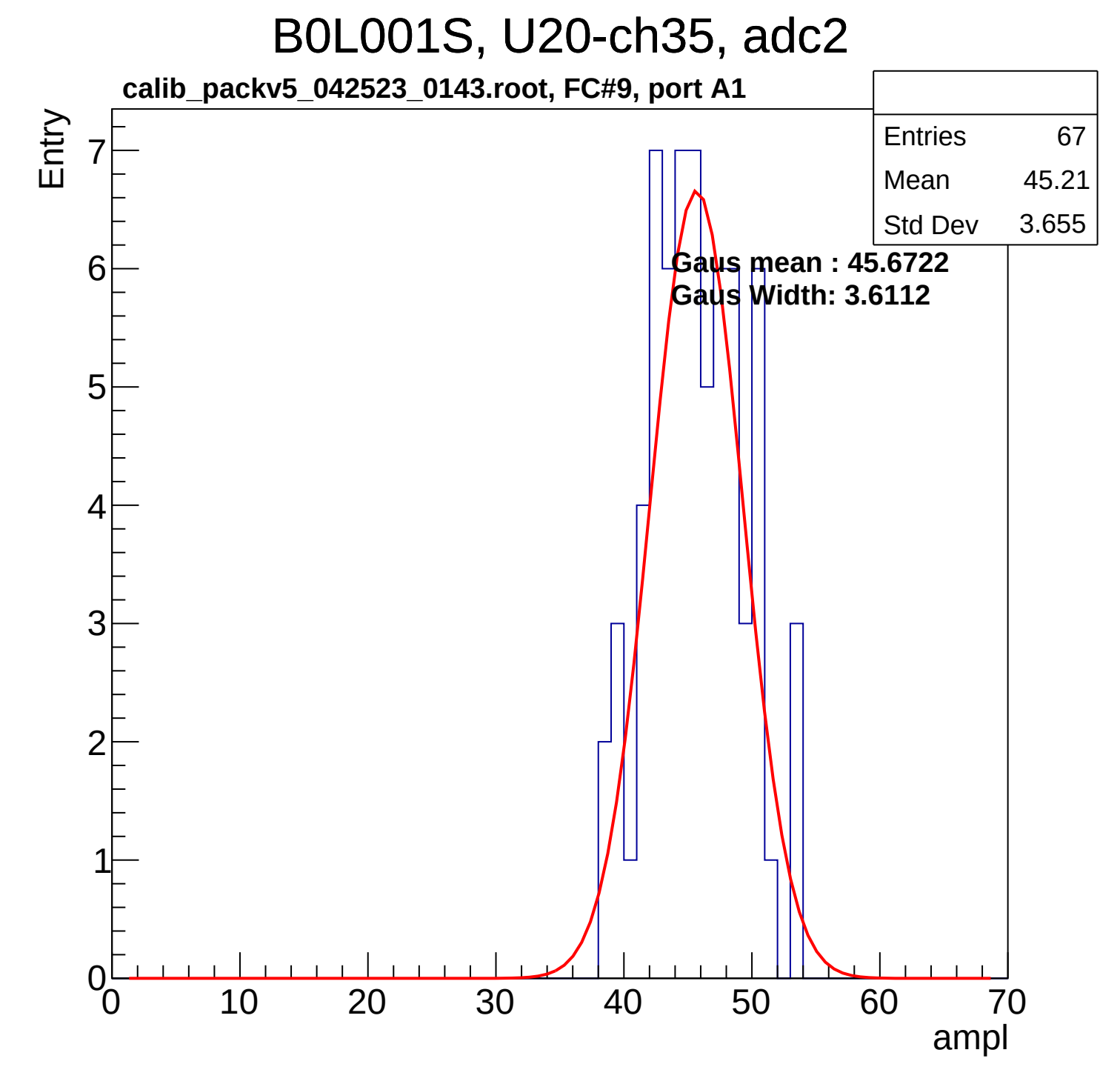
B0L001S, U20-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.21
Std Dev 3.655
Gaus mean : 45.6722
Gaus Width: 3.6112
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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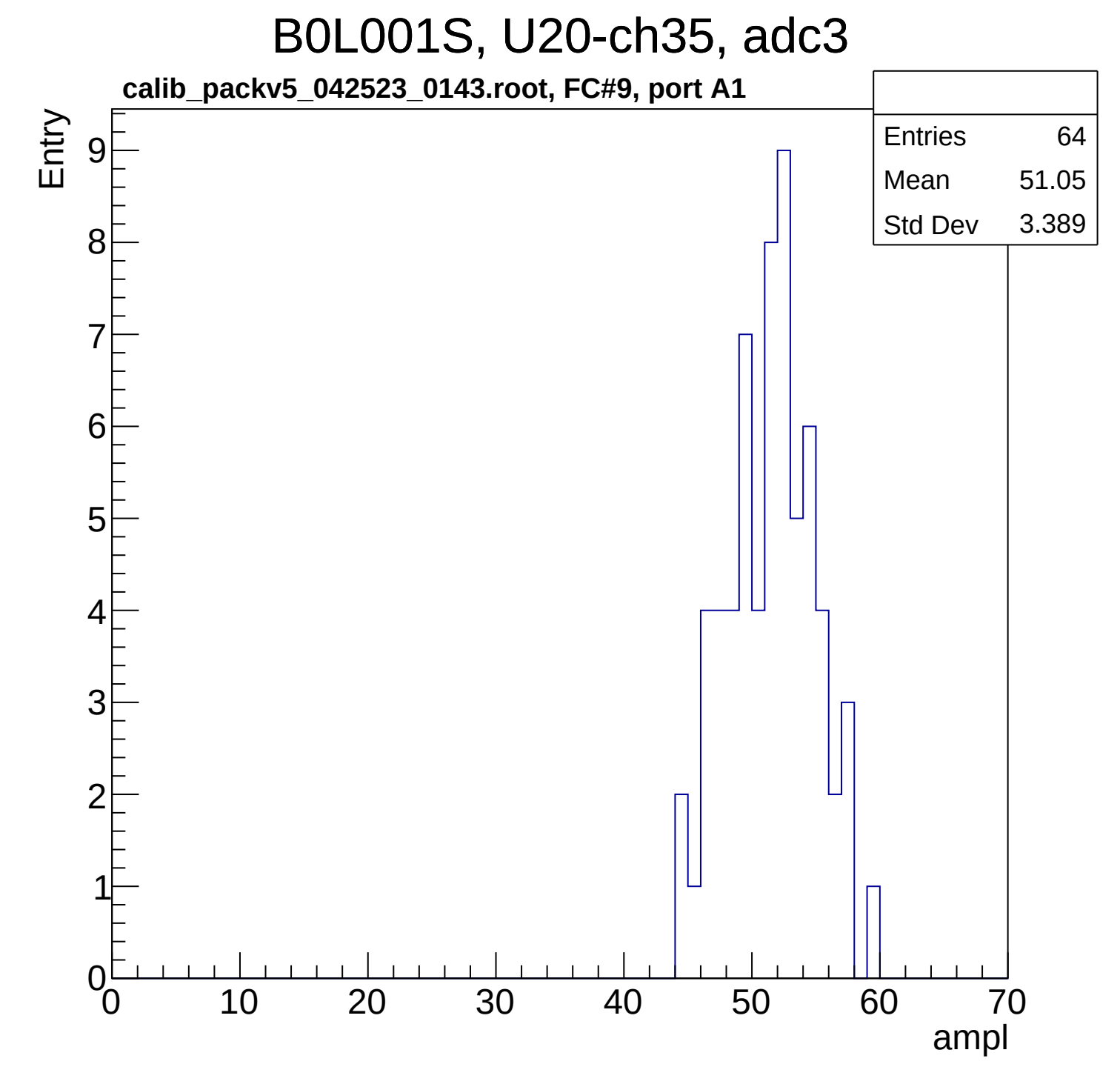
B0L001S, U20-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.05
Std Dev 3.389
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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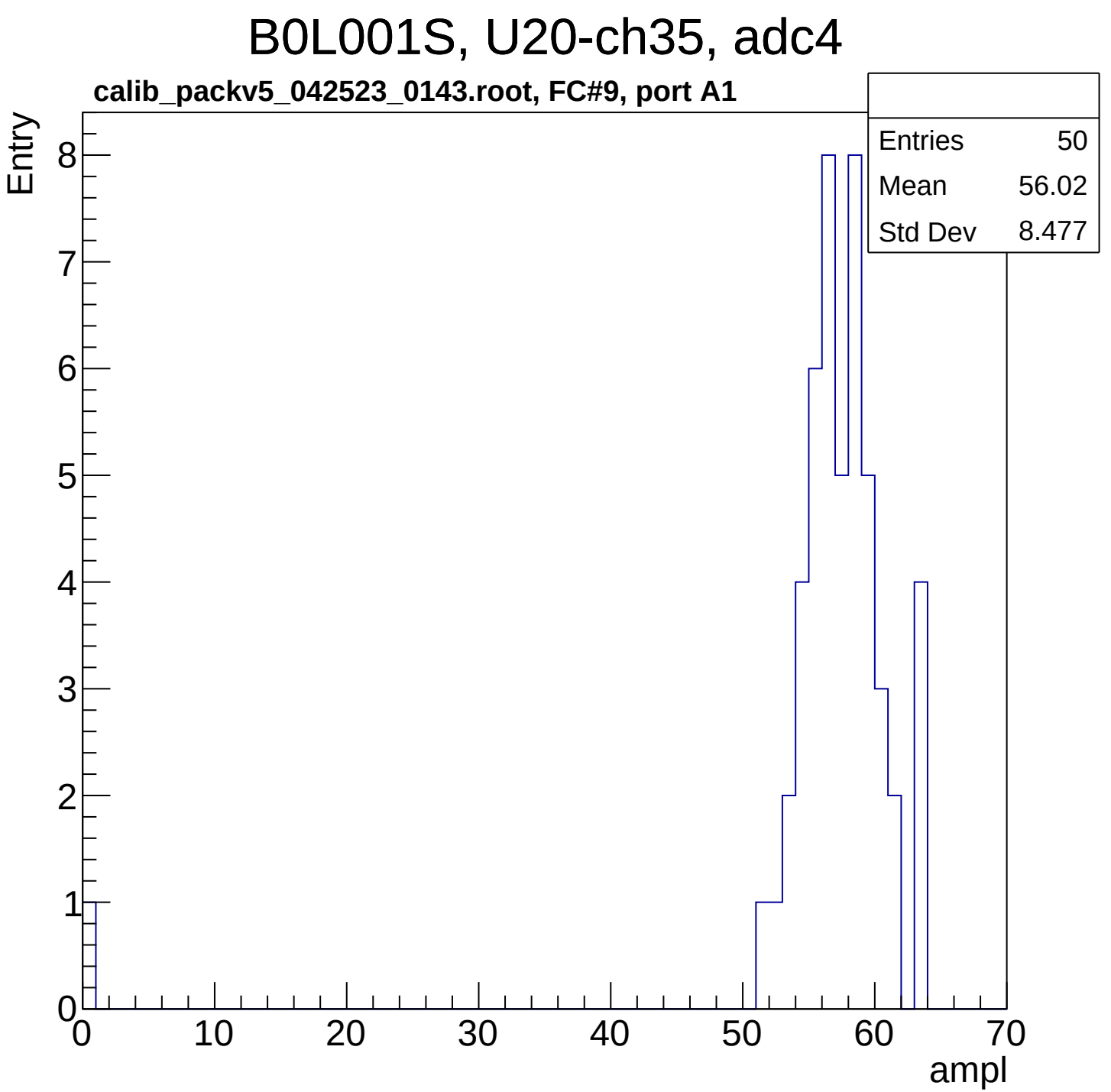
B0L001S, U20-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.02
Std Dev 8.477
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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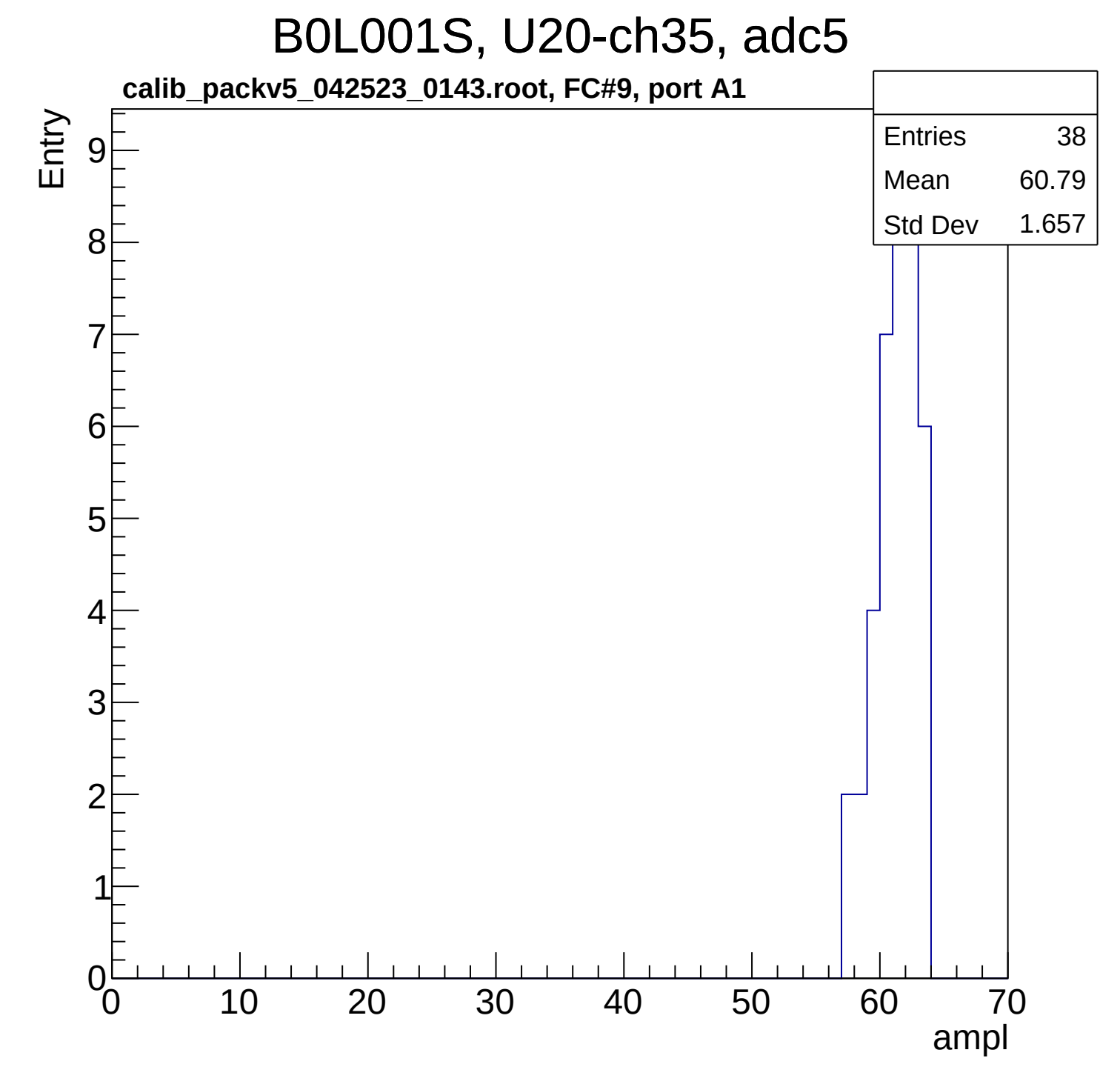
B0L001S, U20-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.79
Std Dev 1.657
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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50
60
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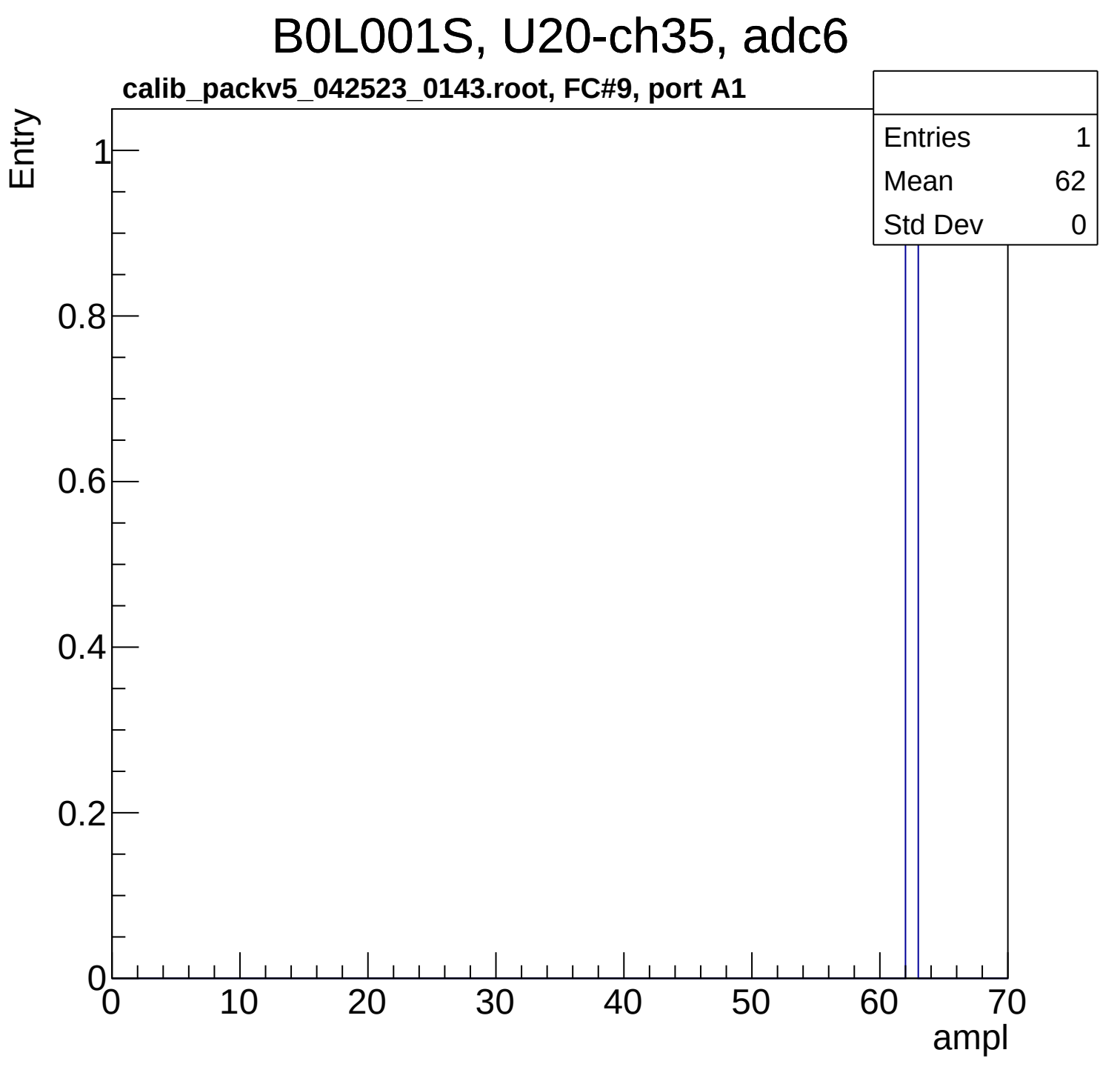
B0L001S, U20-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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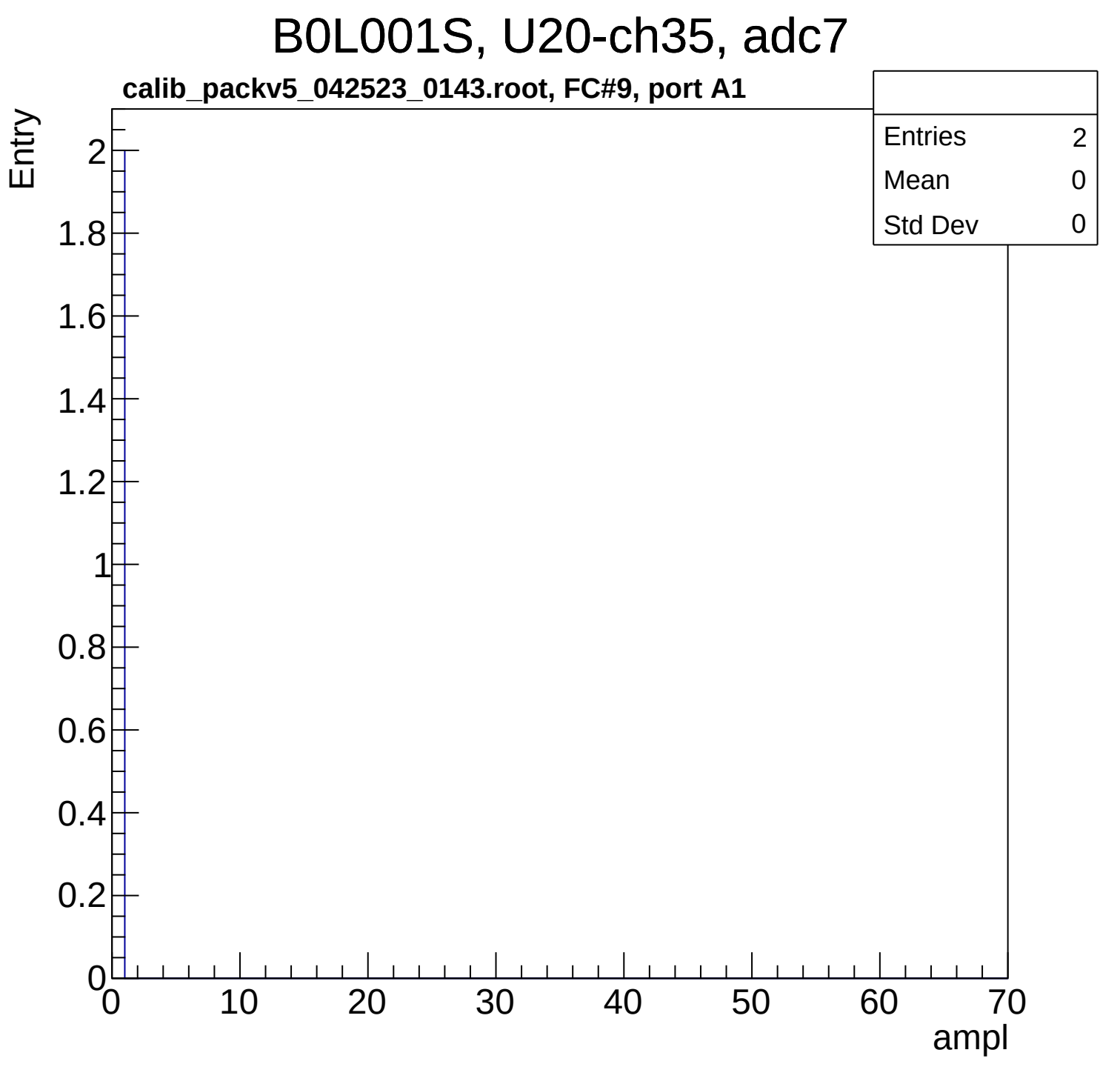
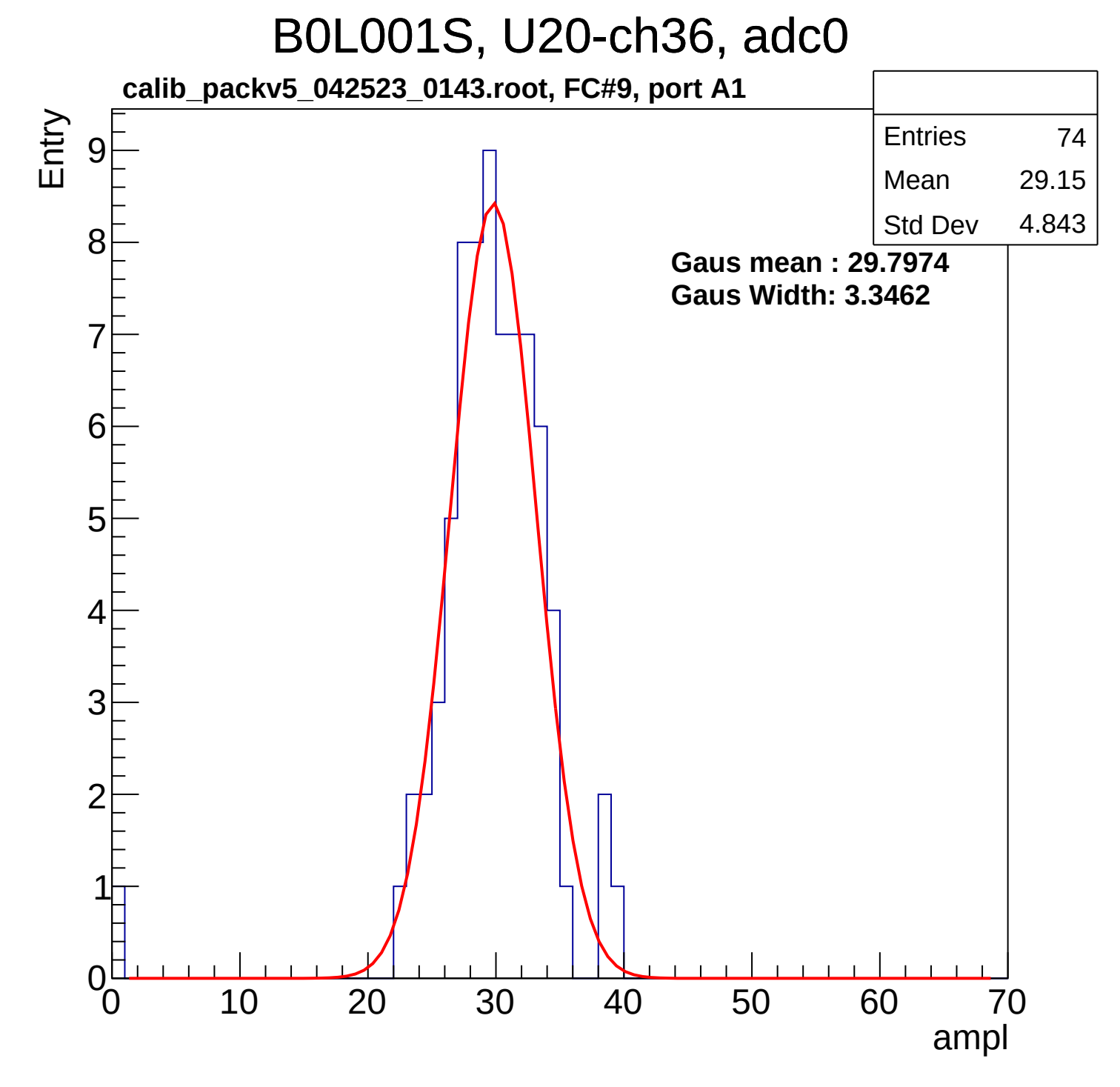
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.15
Std Dev 4.843
Gaus mean : 29.7974
Gaus Width: 3.3462
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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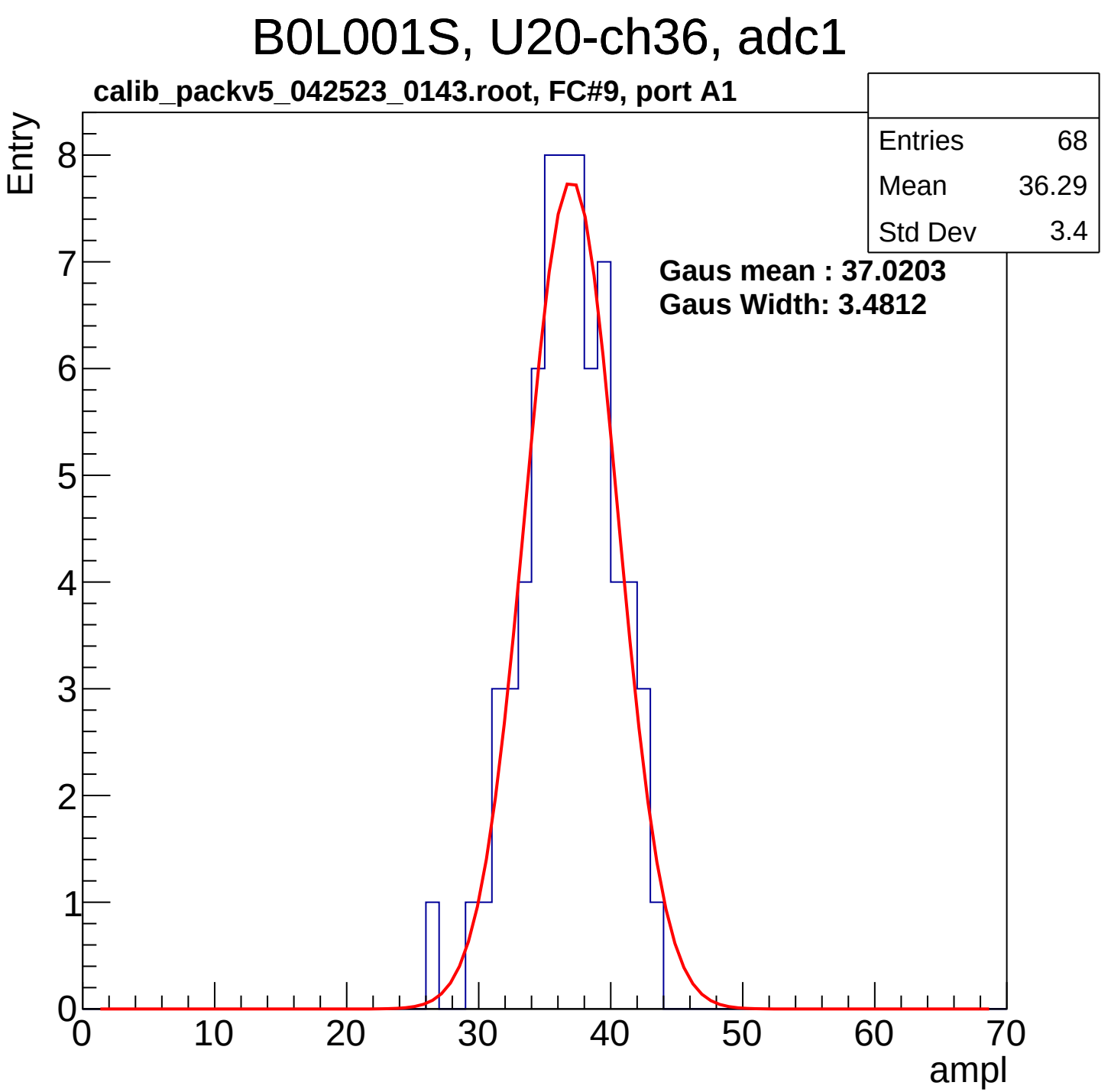
B0L001S, U20-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.29
Std Dev 3.4
Gaus mean : 37.0203
Gaus Width: 3.4812
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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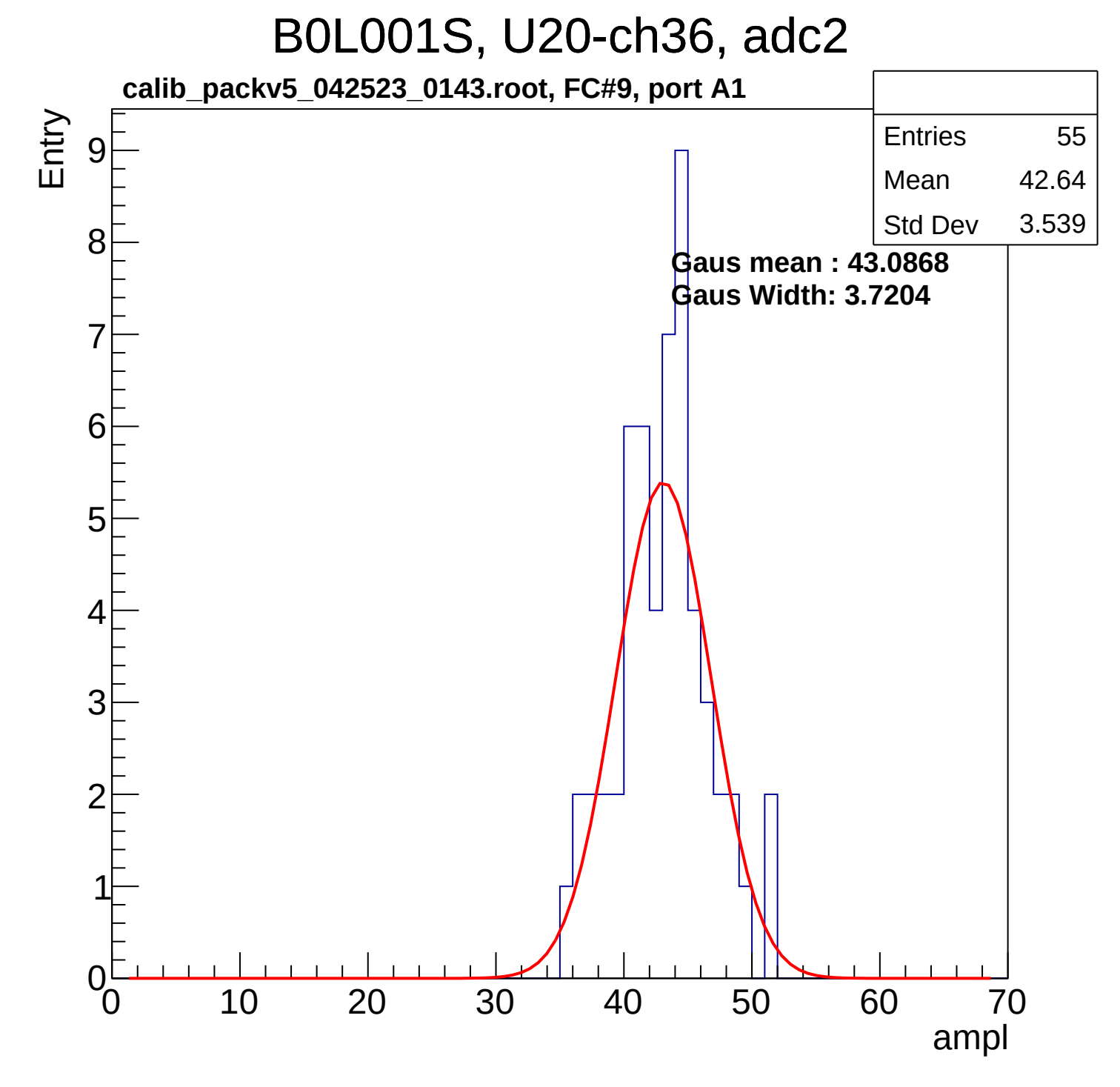
B0L001S, U20-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.64
Std Dev 3.539
Gaus mean : 43.0868
Gaus Width: 3.7204
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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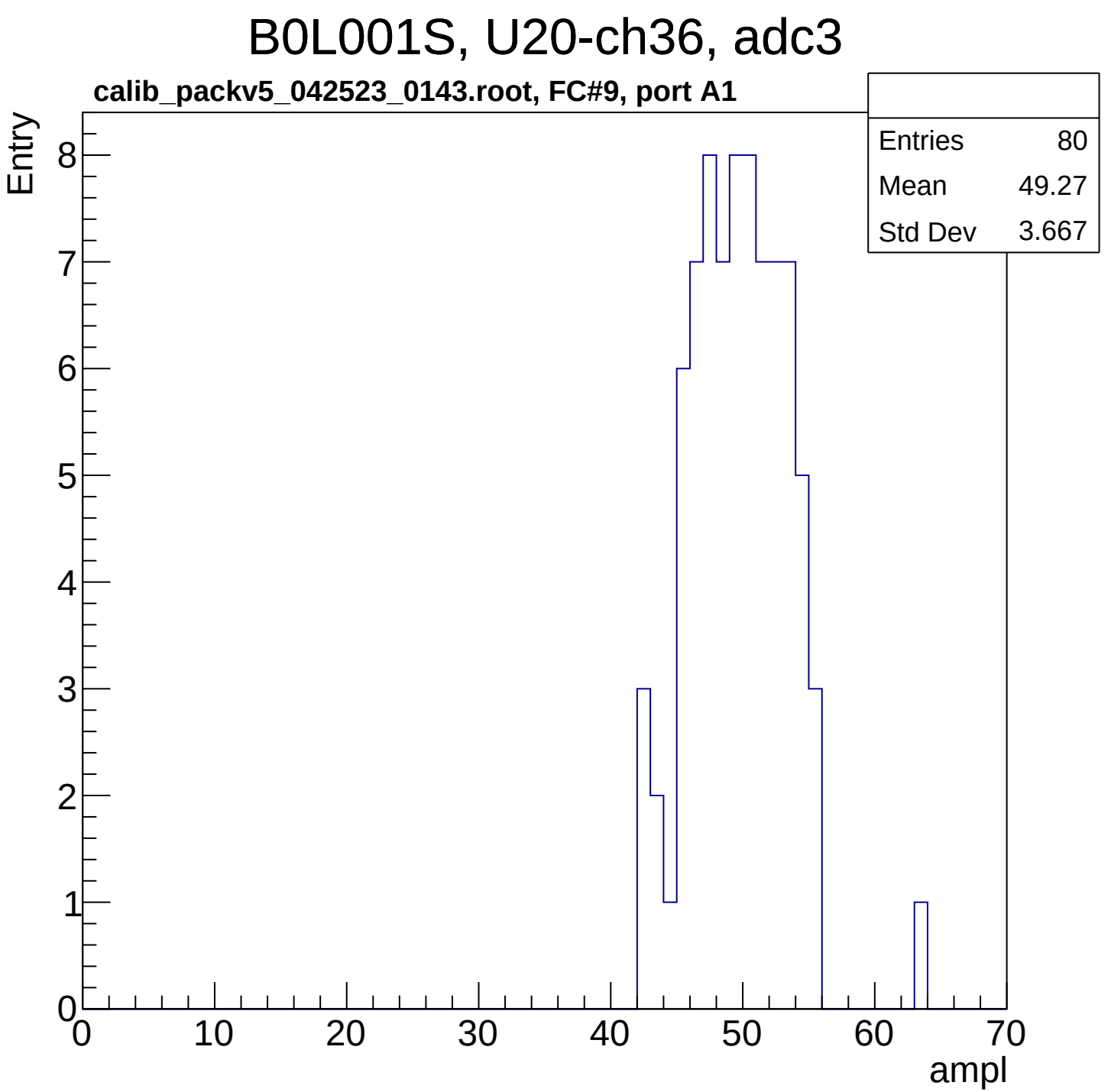
B0L001S, U20-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.27
Std Dev 3.667
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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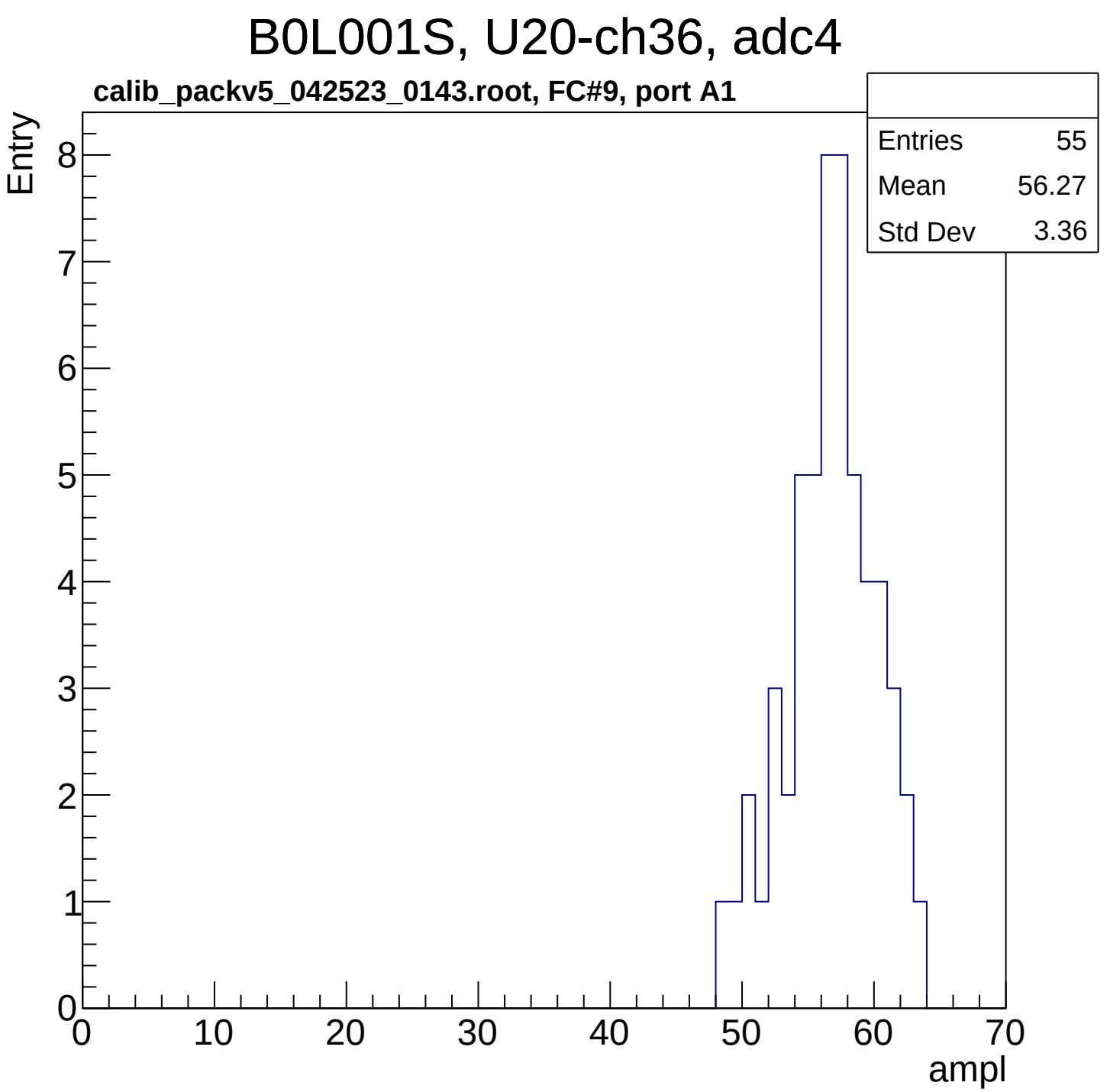
B0L001S, U20-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.27
Std Dev 3.36
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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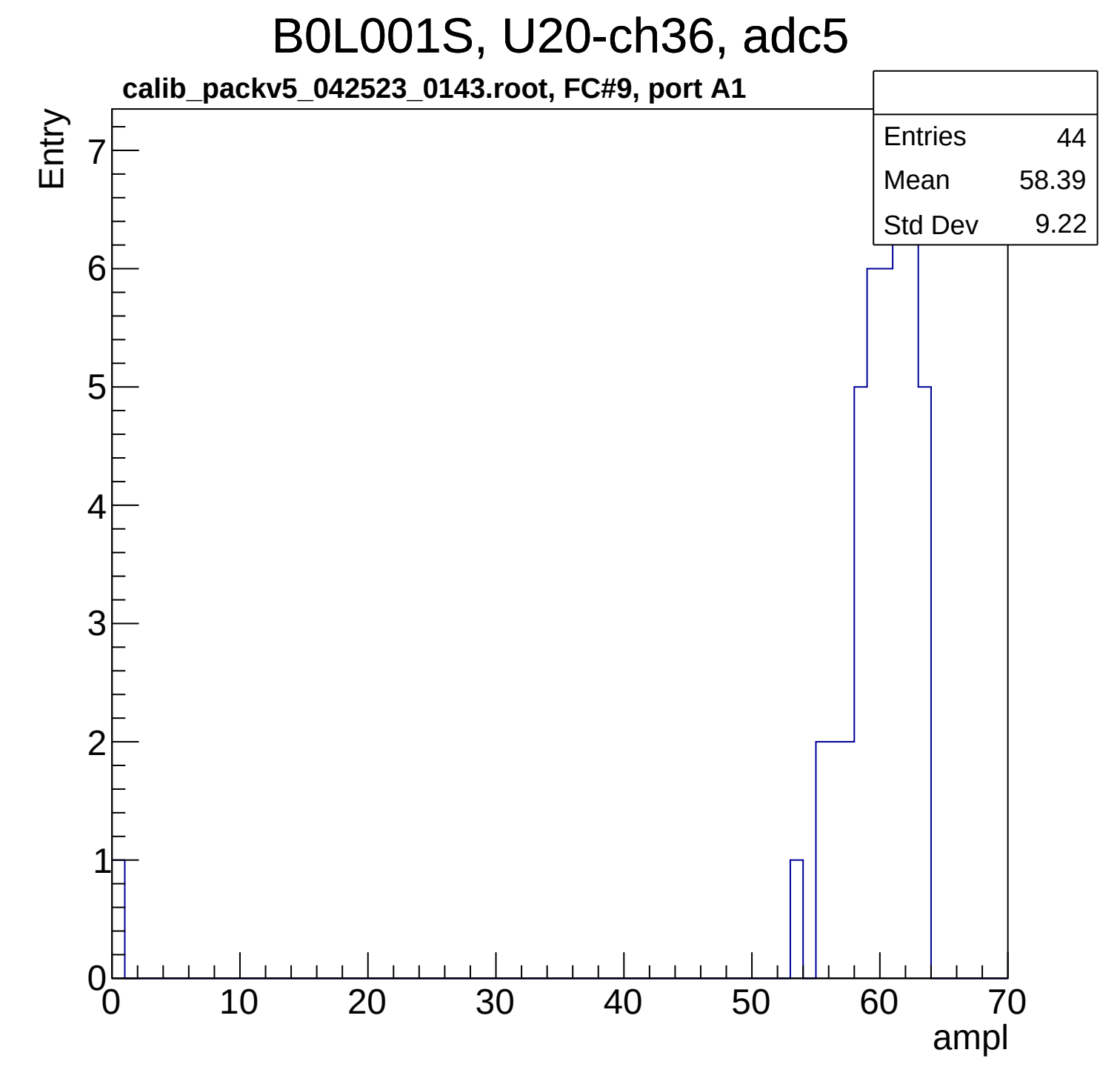
B0L001S, U20-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.39
Std Dev 9.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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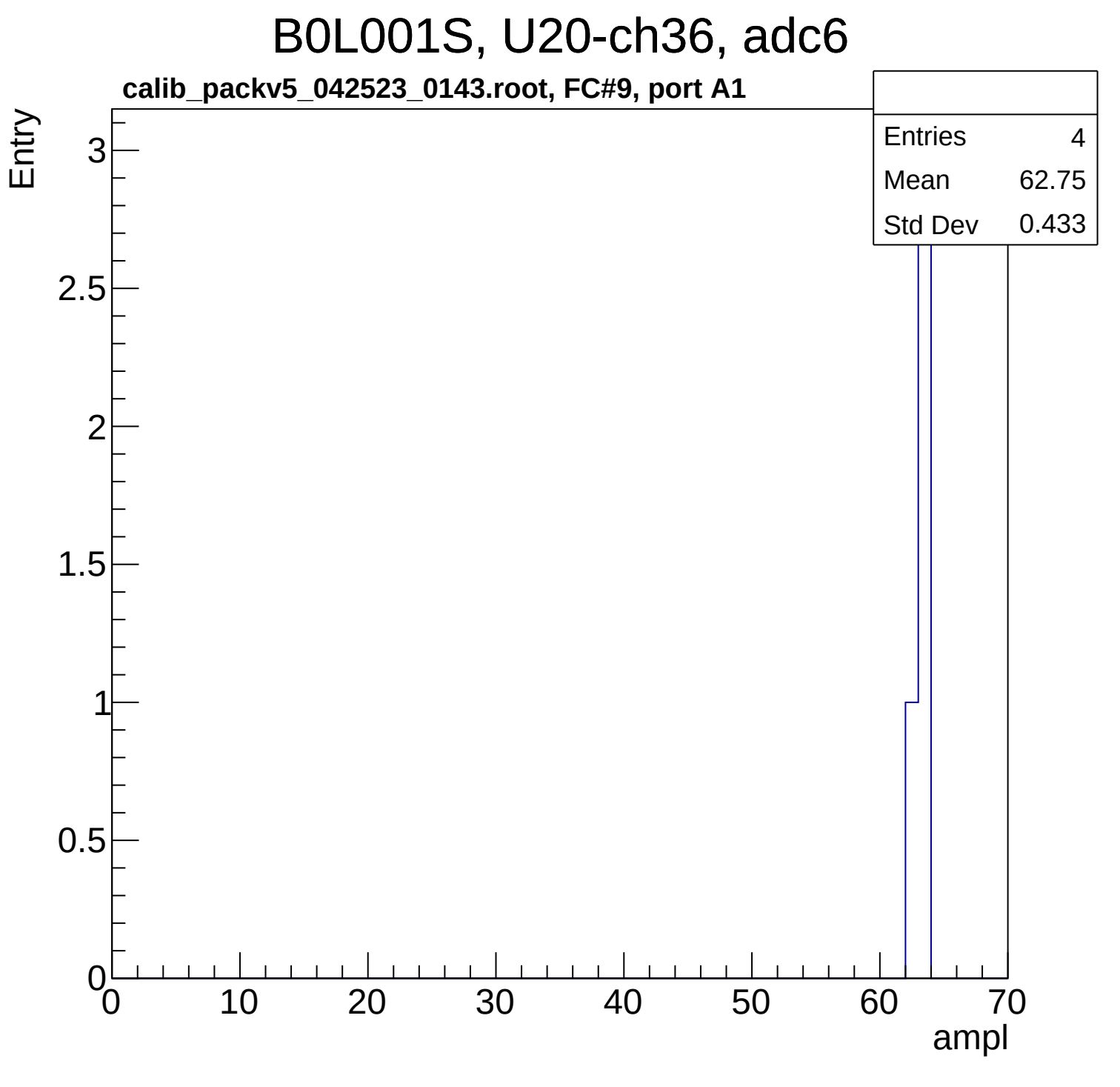
B0L001S, U20-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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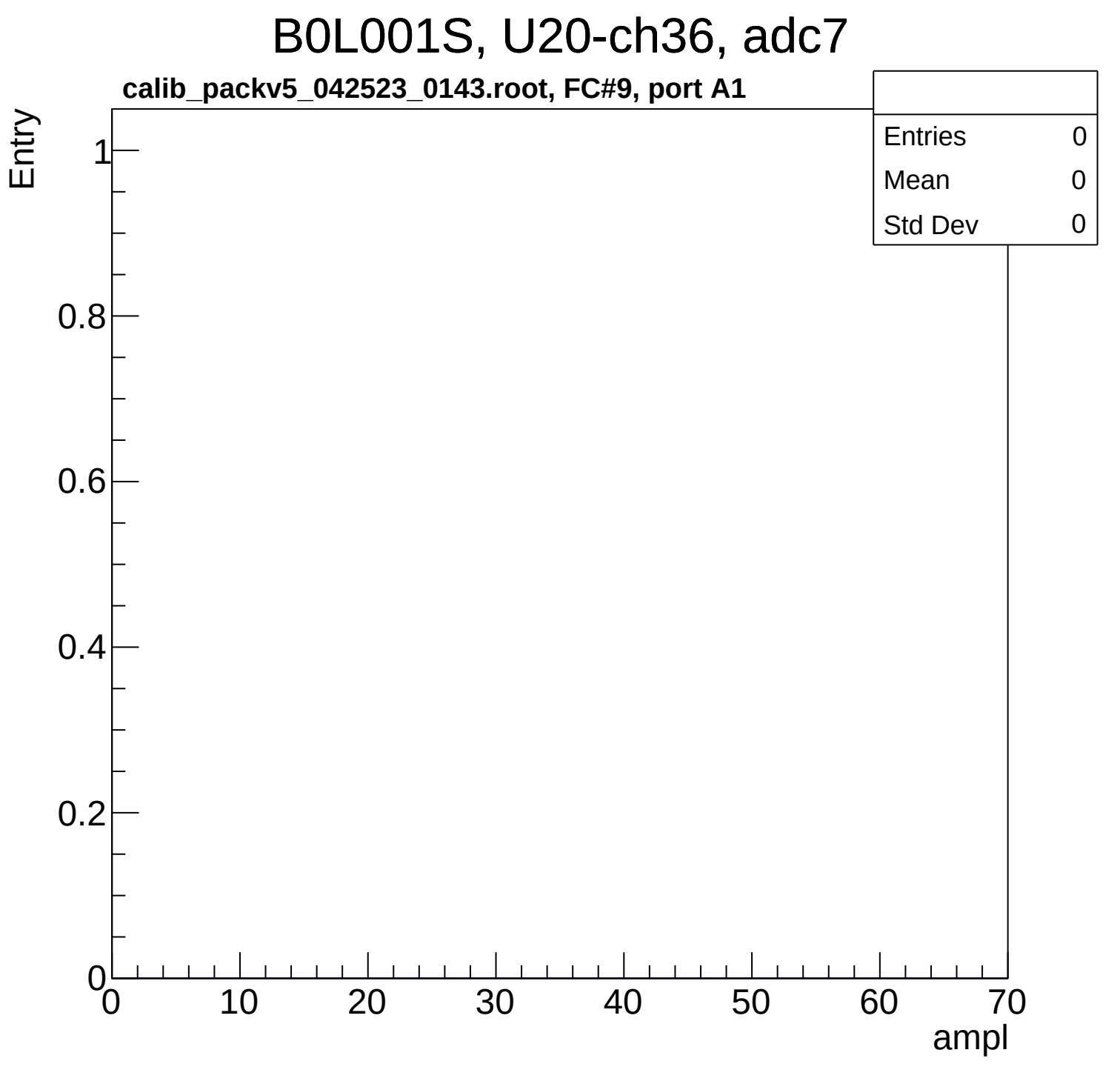
40
50
60
70


B0L001S, U20-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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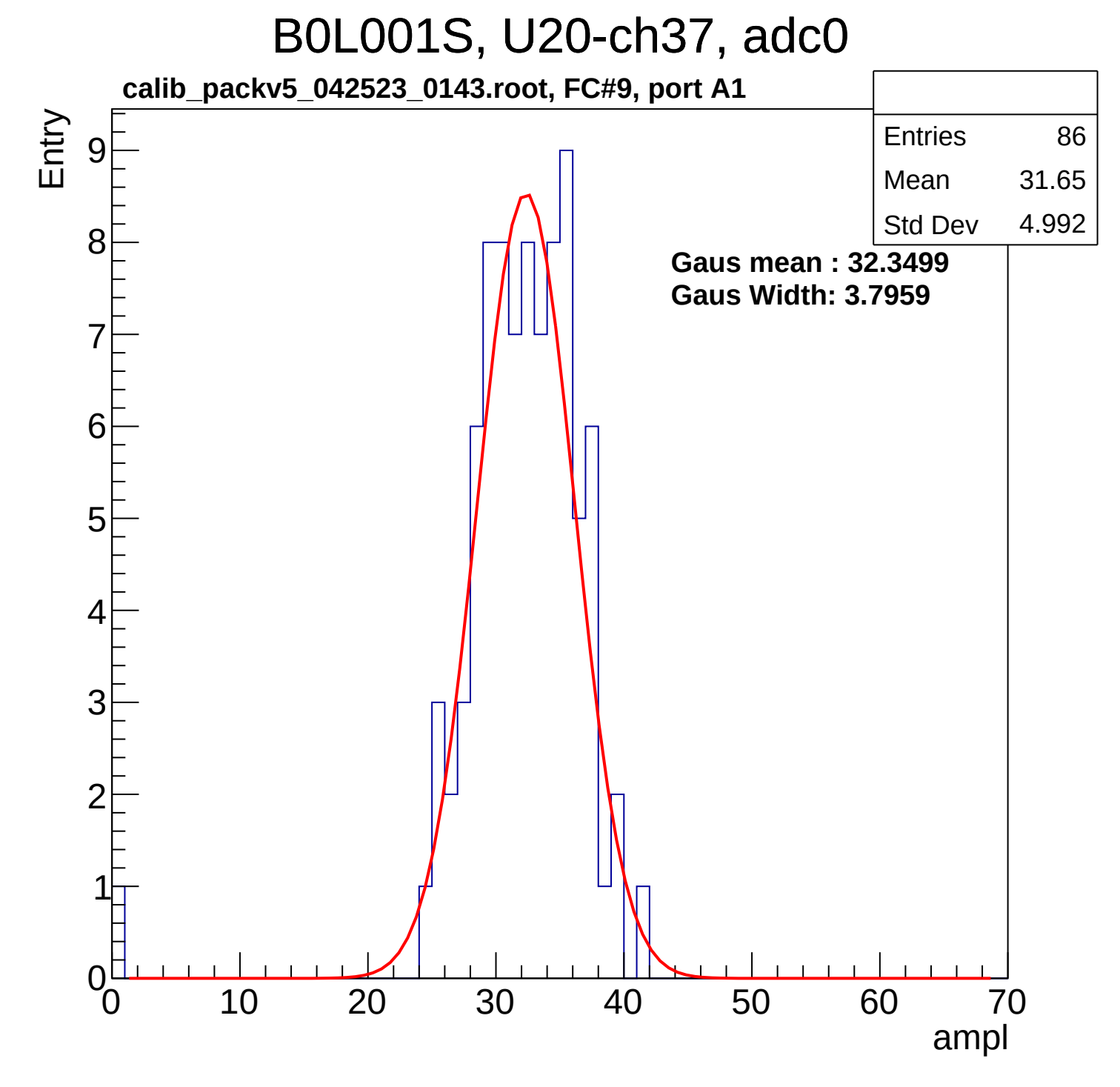
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 31.65
Std Dev 4.992
Gaus mean : 32.3499
Gaus Width: 3.7959
Entry
ampl
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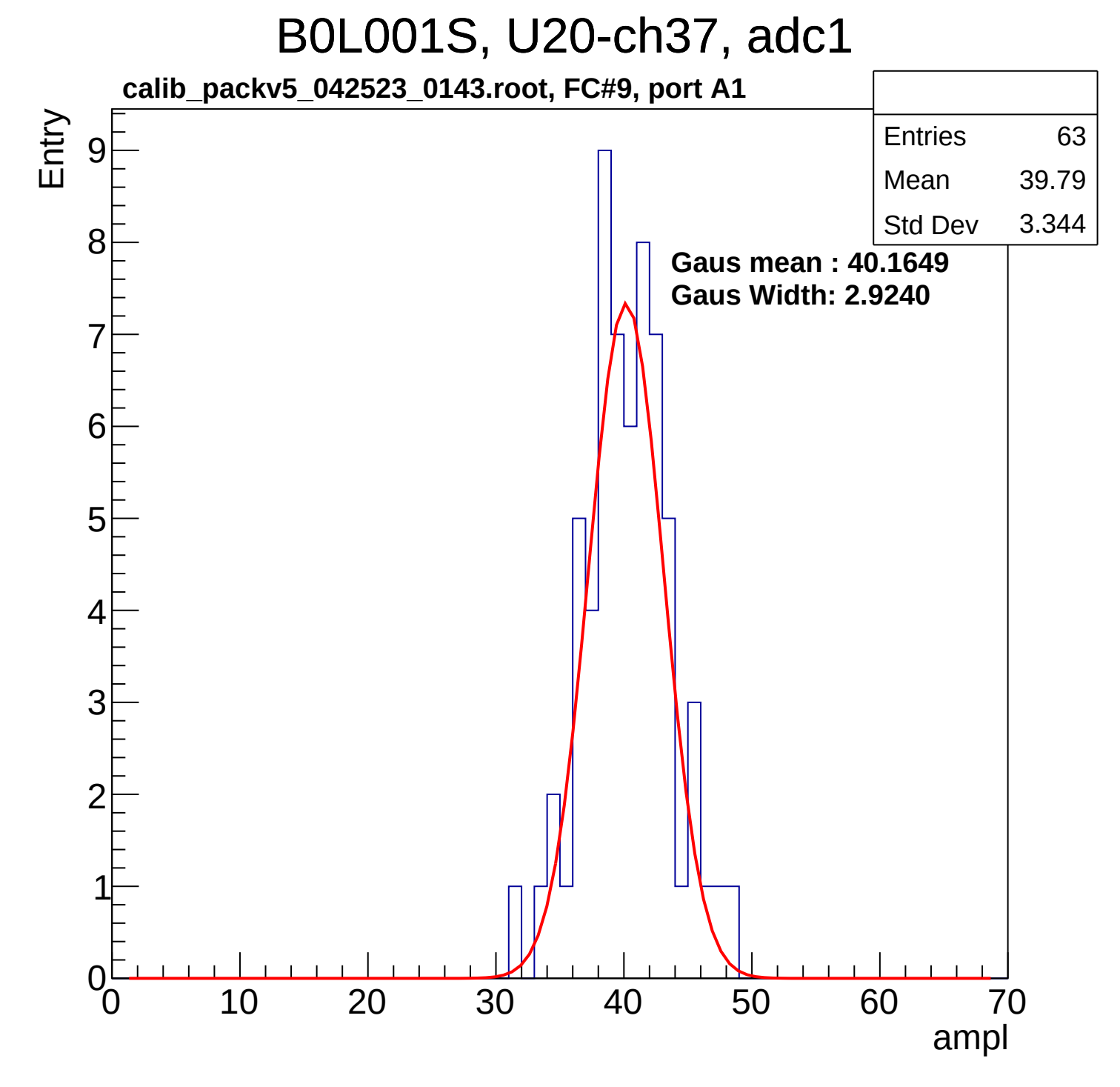
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 39.79
Std Dev 3.344
Gaus mean : 40.1649
Gaus Width: 2.9240
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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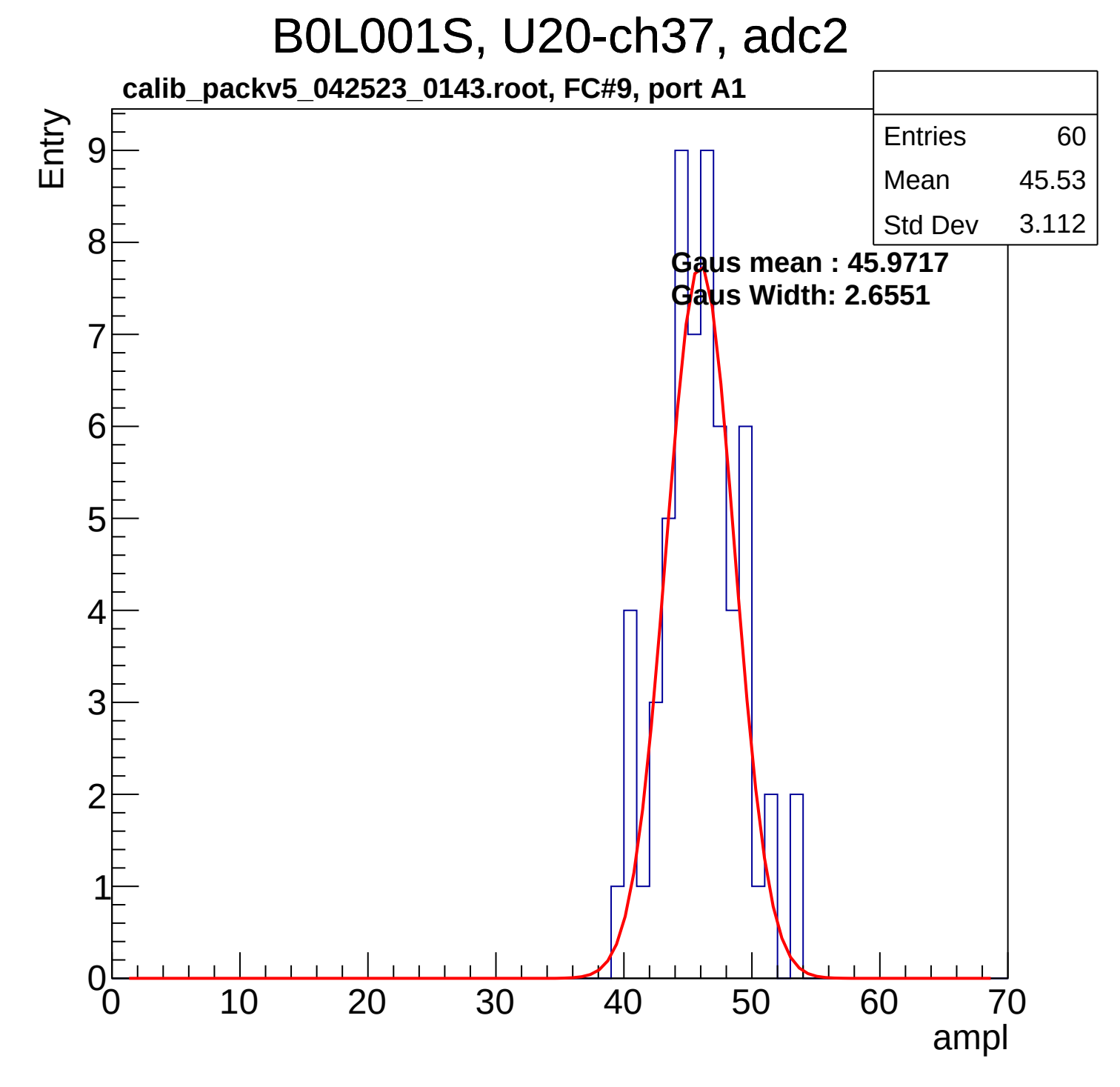
B0L001S, U20-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.53
Std Dev 3.112
Gaus mean : 45.9717
Gaus Width: 2.6551
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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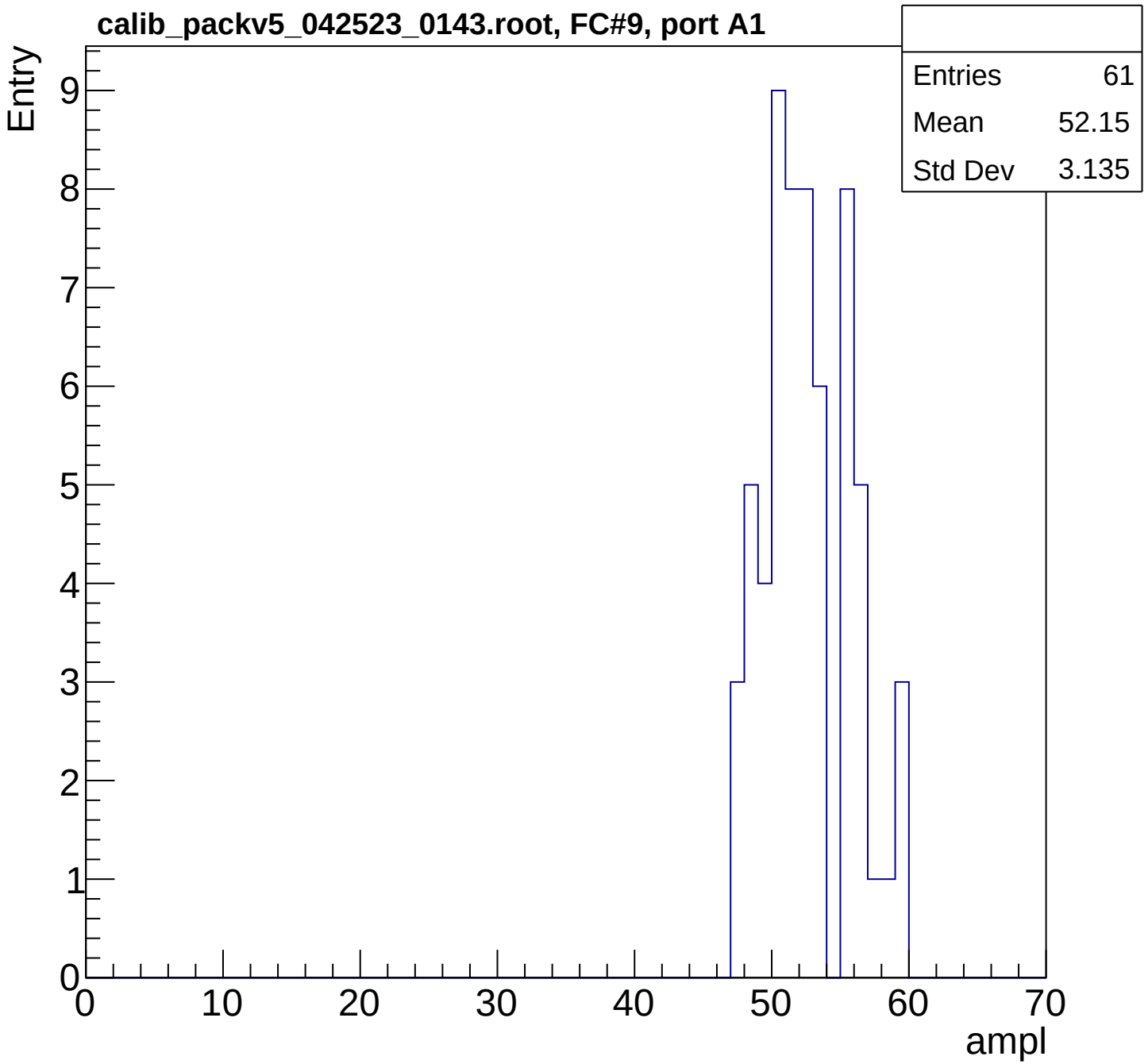
B0L001S, U20-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 52.15
Std Dev 3.135
Entry
ampl
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0
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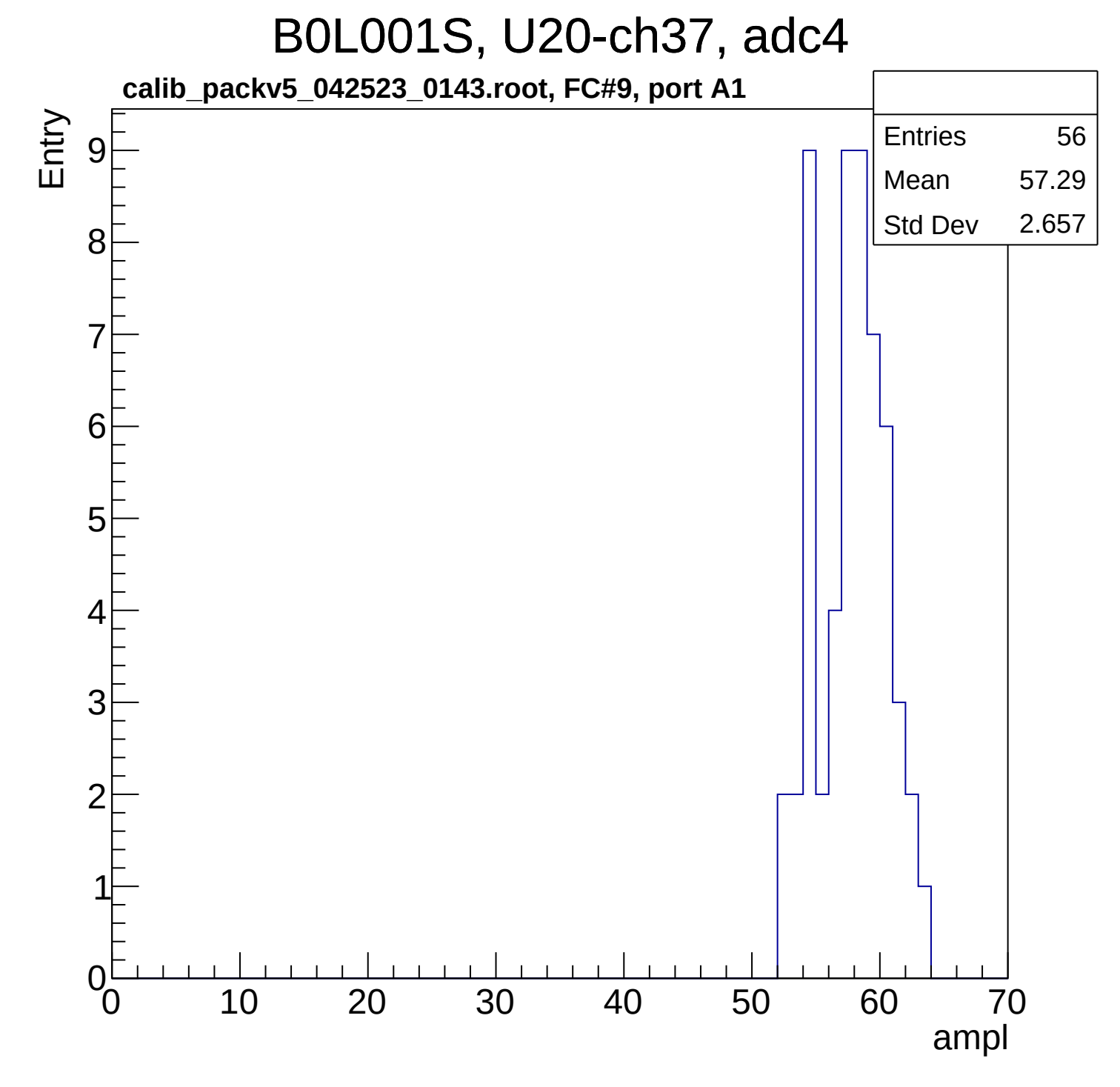
B0L001S, U20-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.29
Std Dev 2.657
Entry
ampl
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2
3
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5
6
7
8
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0
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60
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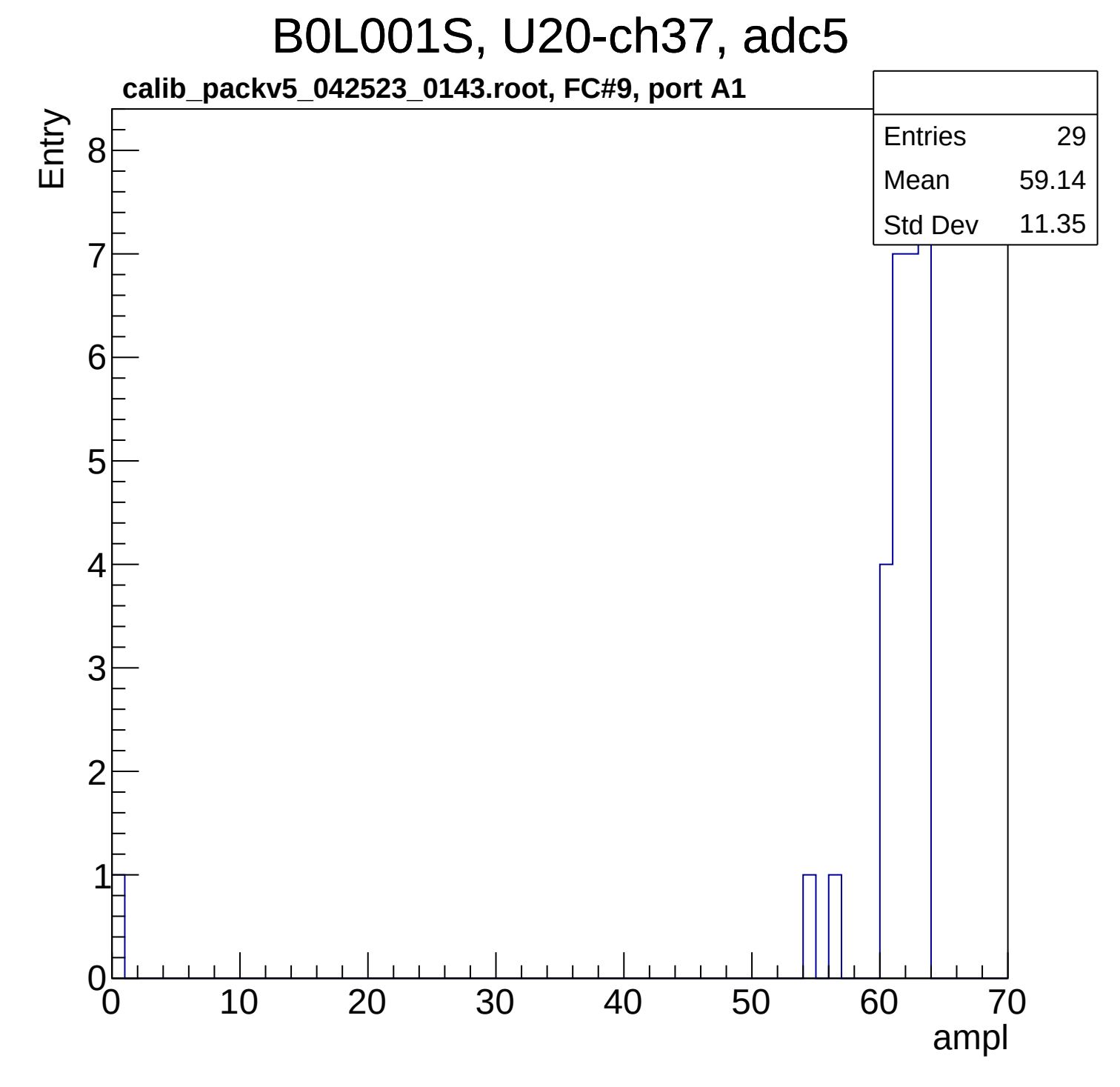
B0L001S, U20-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59.14
Std Dev 11.35
Entry
ampl
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0
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60
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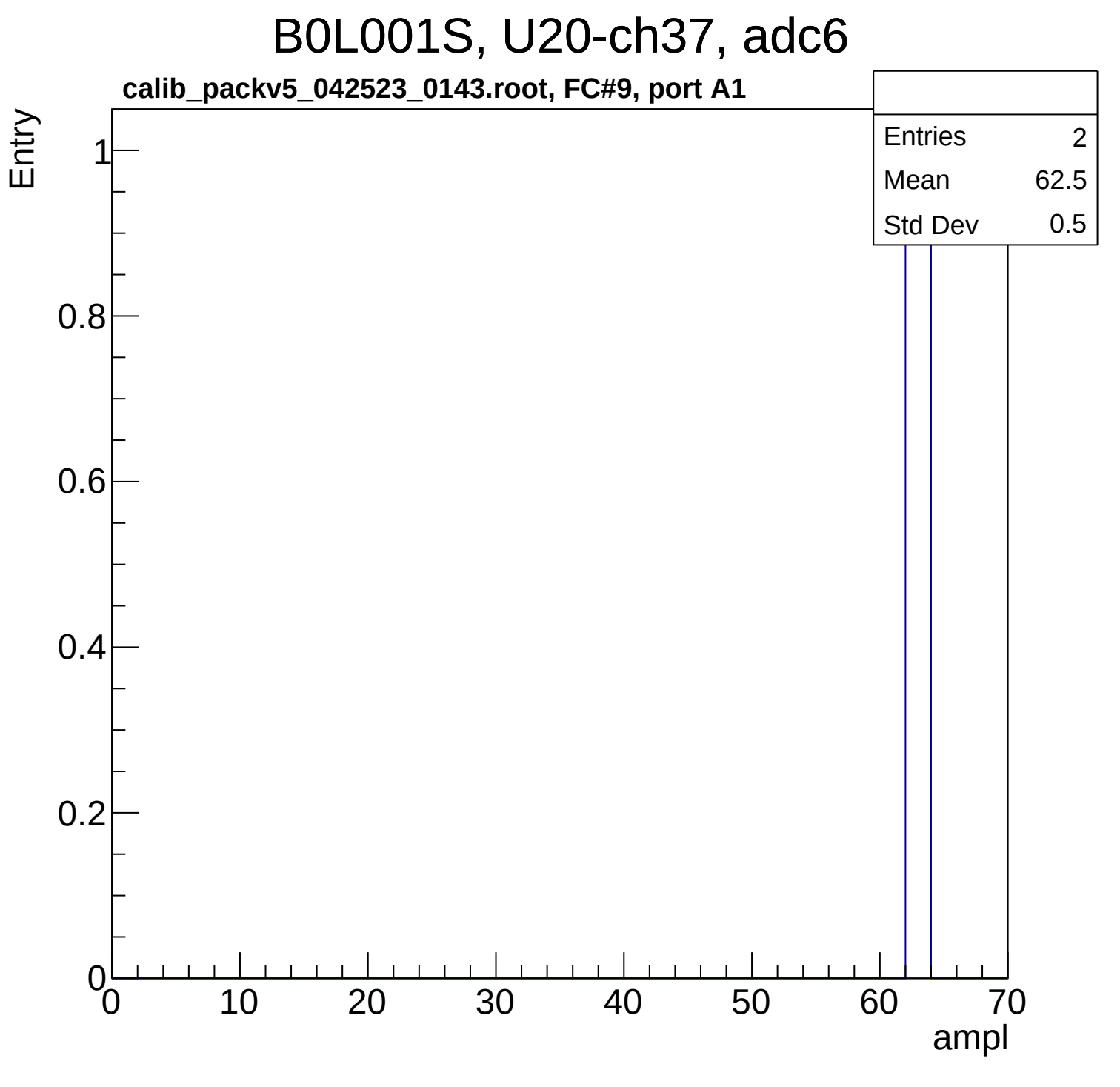
B0L001S, U20-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.6
0.4
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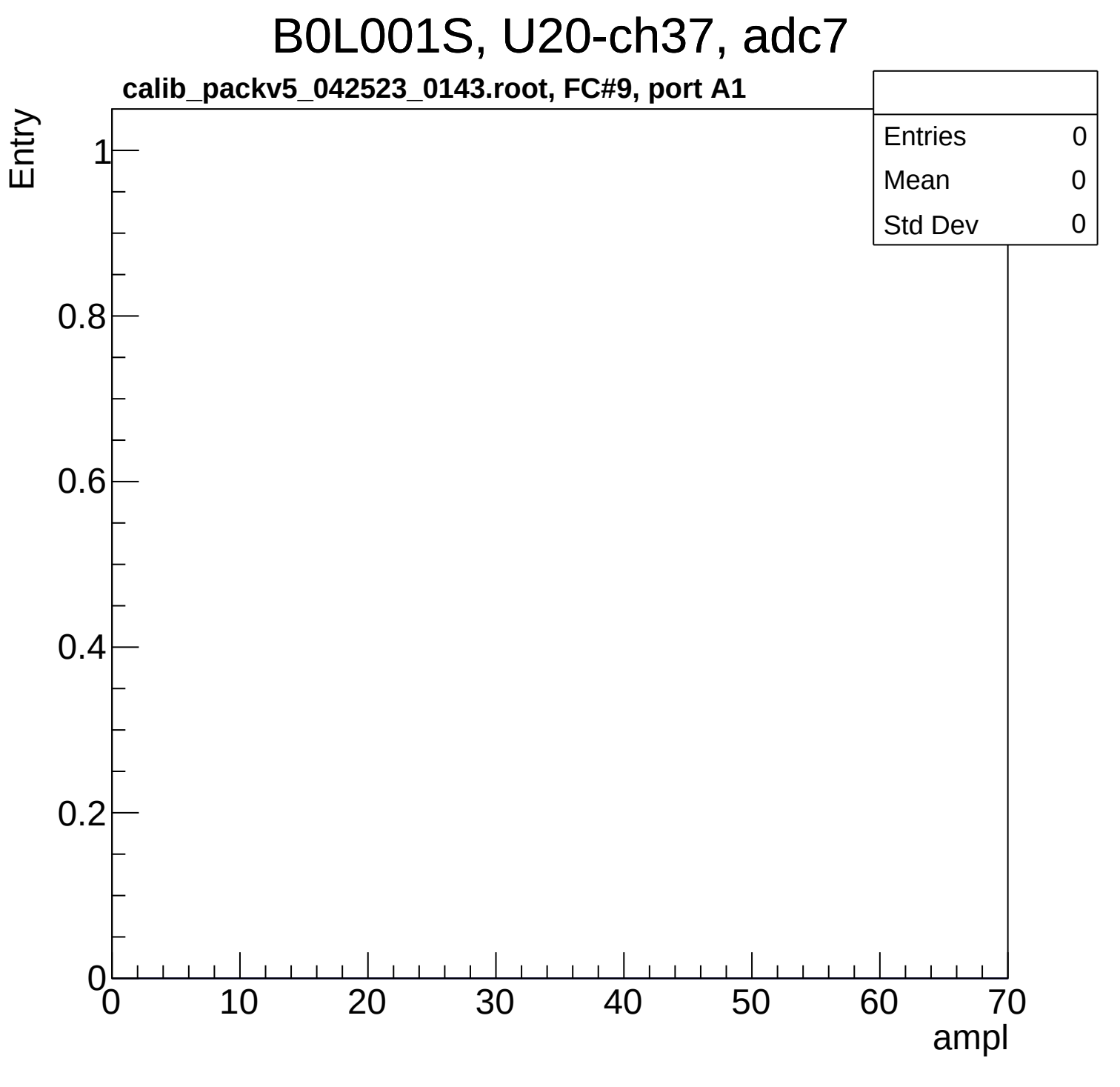
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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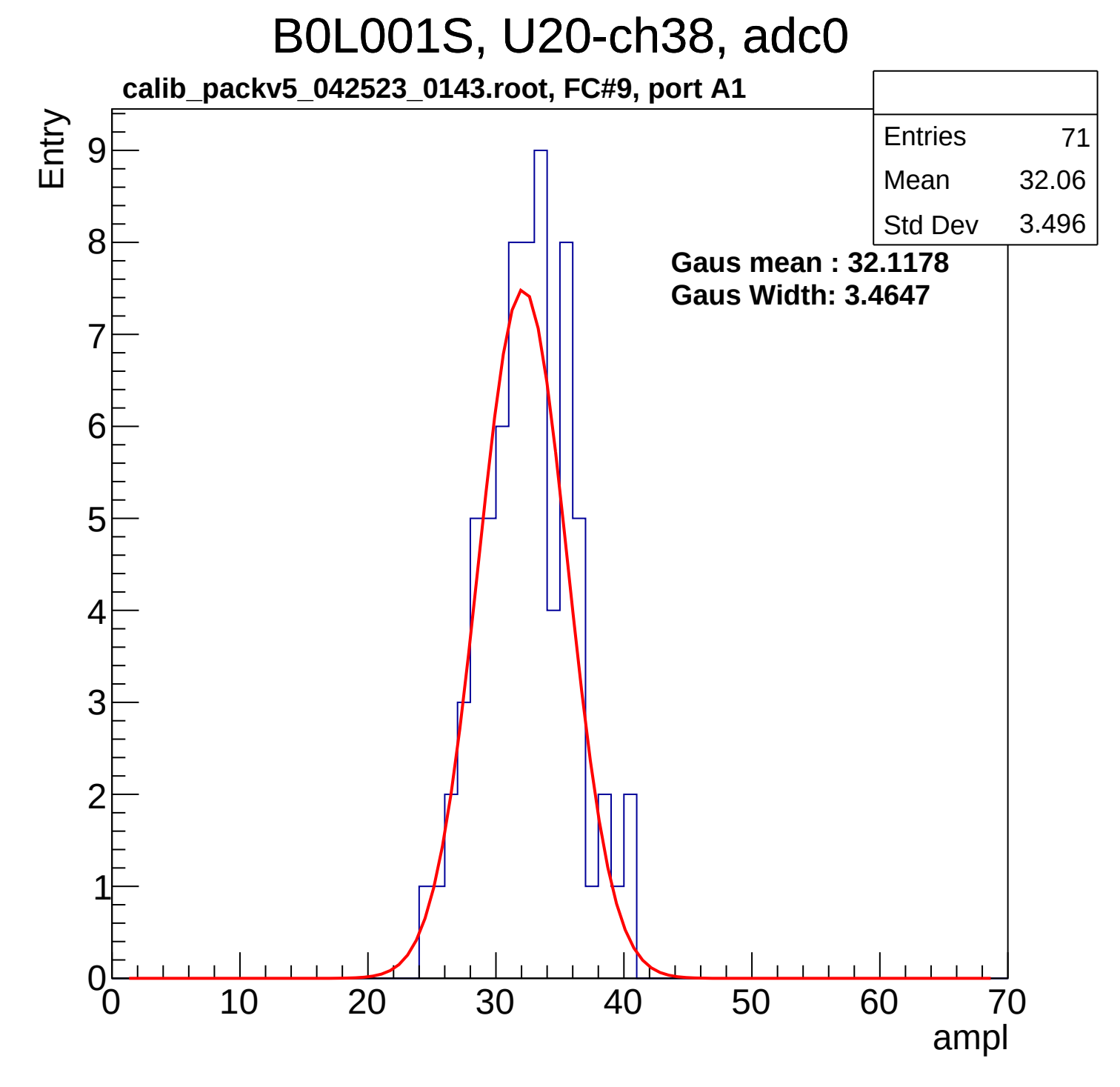
50
60
70
ampl


B0L001S, U20-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.06
Std Dev 3.496
Gaus mean : 32.1178
Gaus Width: 3.4647
Entry
ampl
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7
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0
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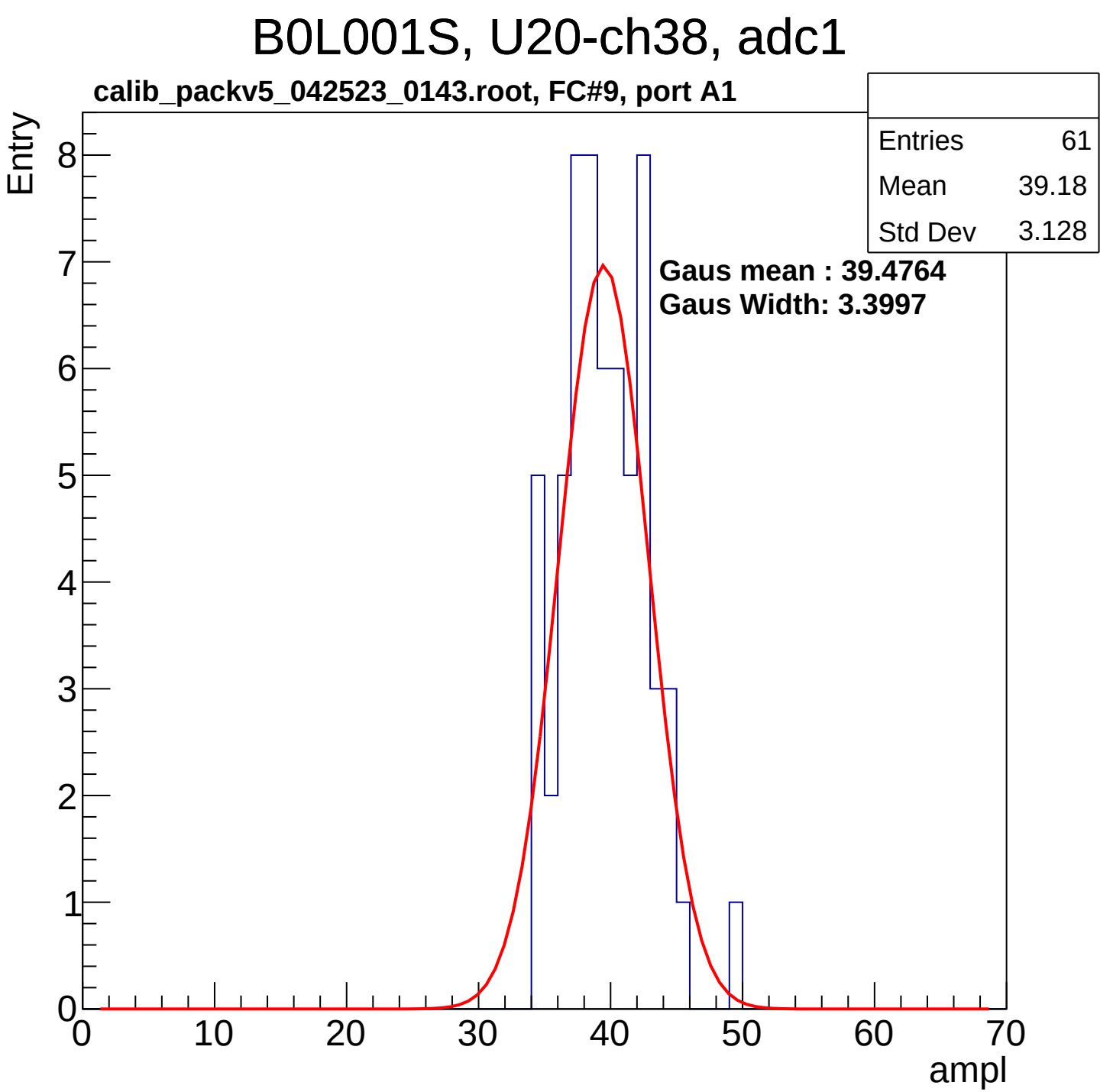
B0L001S, U20-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 39.18
Std Dev 3.128
Gaus mean : 39.4764
Gaus Width: 3.3997
Entry
ampl
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2
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4
5
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0
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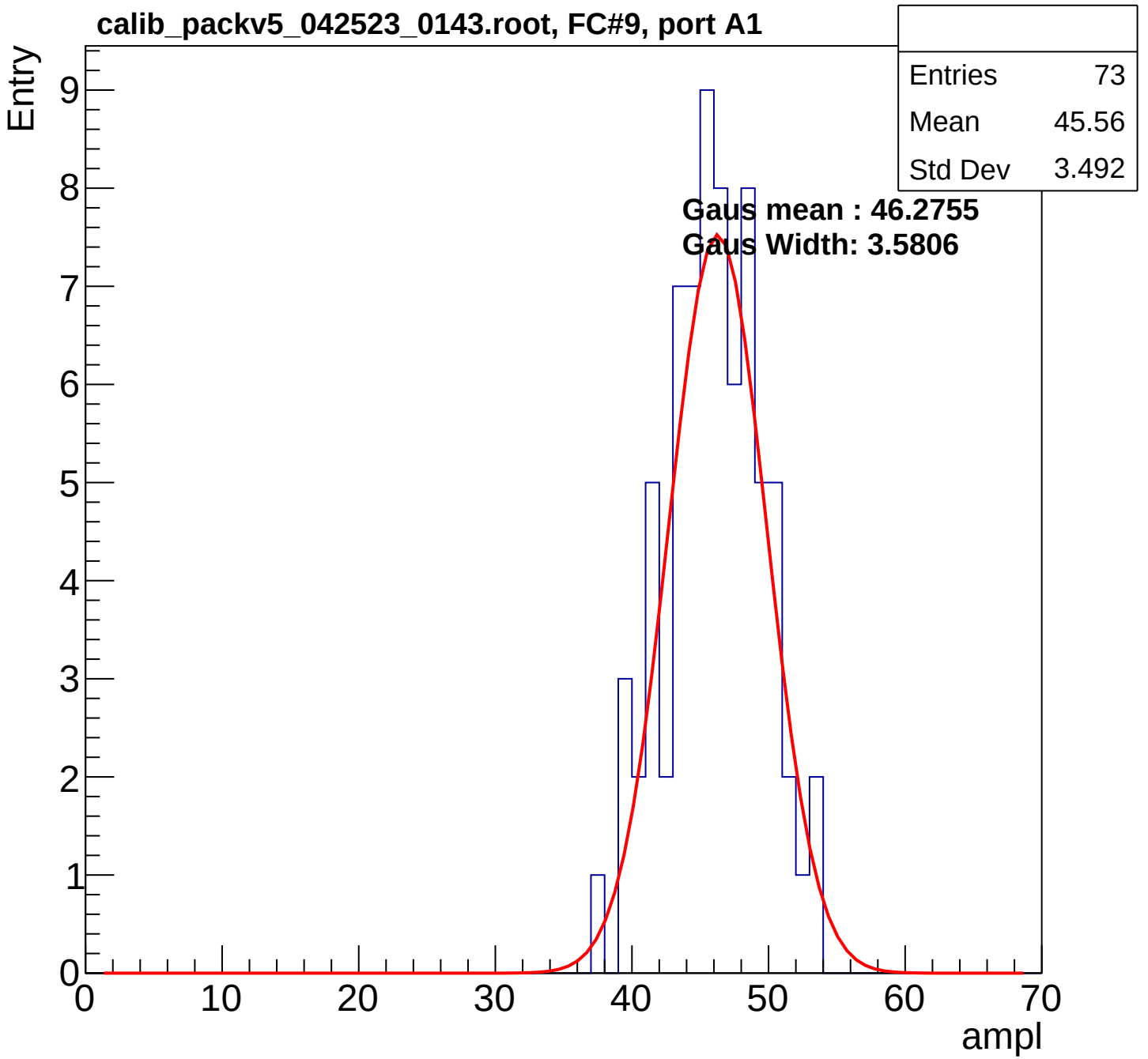
B0L001S, U20-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.56
Std Dev 3.492
Gaus mean : 46.2755
Gaus Width: 3.5806
Entry
ampl
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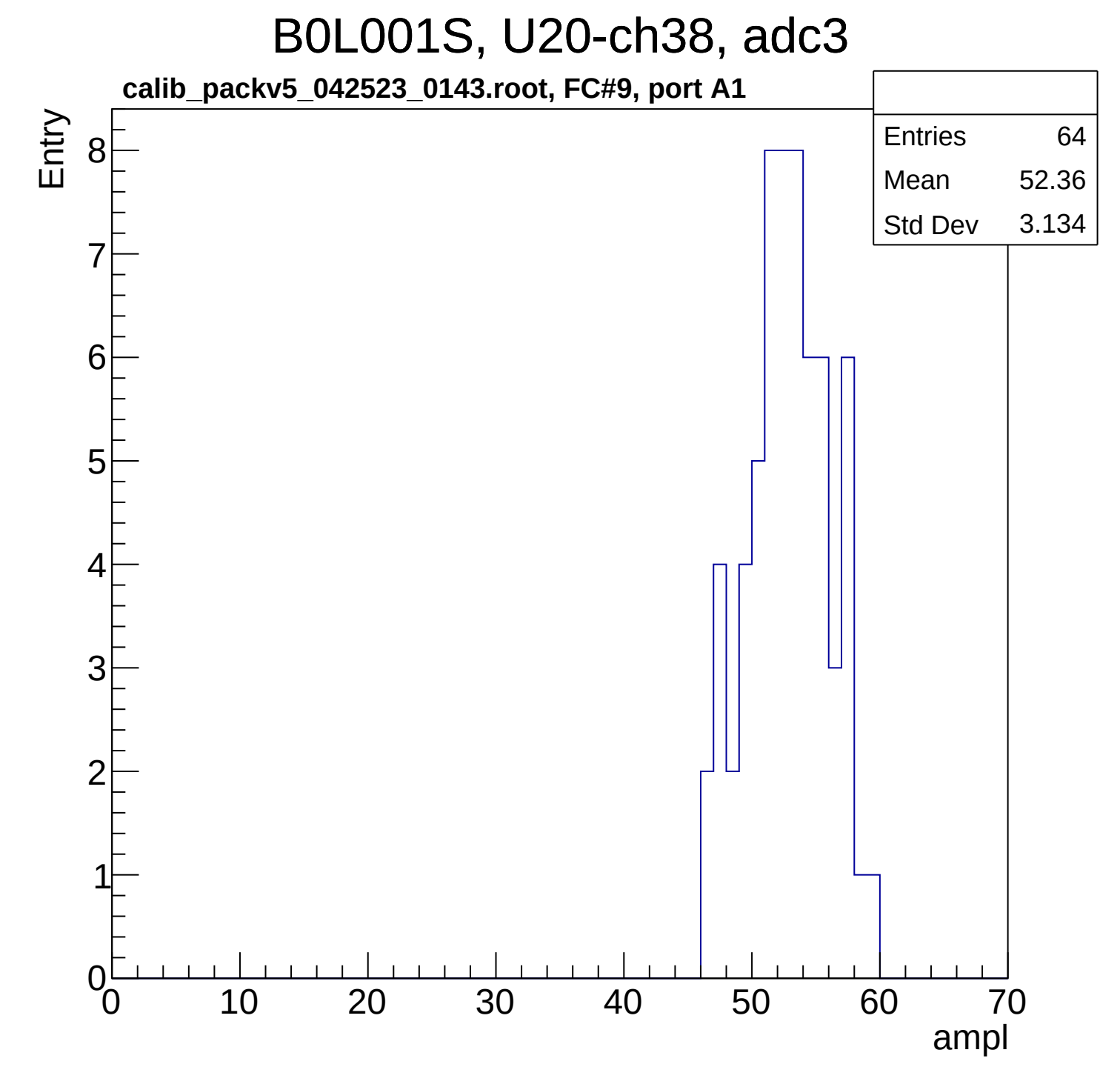
B0L001S, U20-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.36
Std Dev 3.134
Entry
ampl
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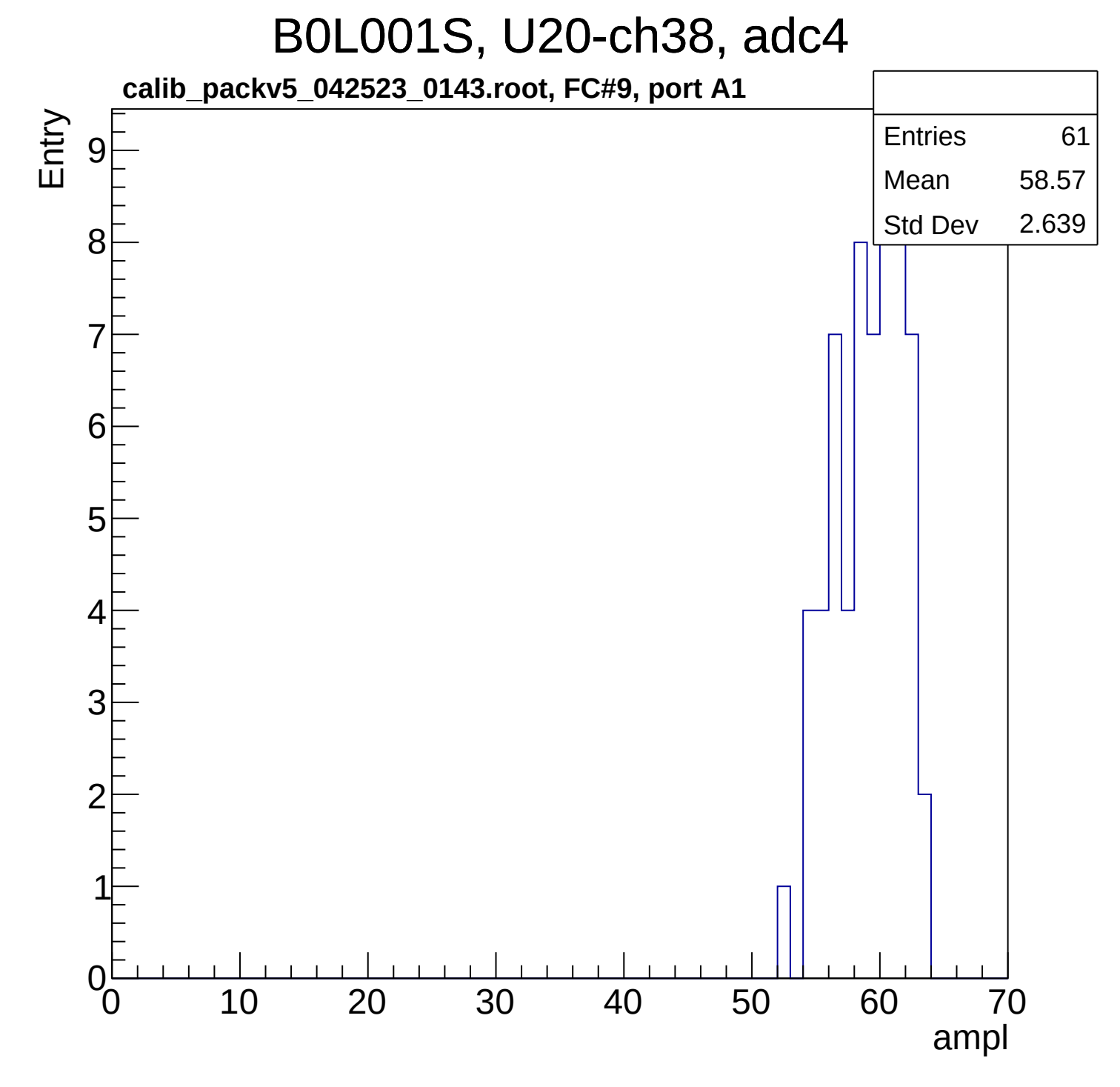
B0L001S, U20-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 58.57
Std Dev 2.639
Entry
ampl
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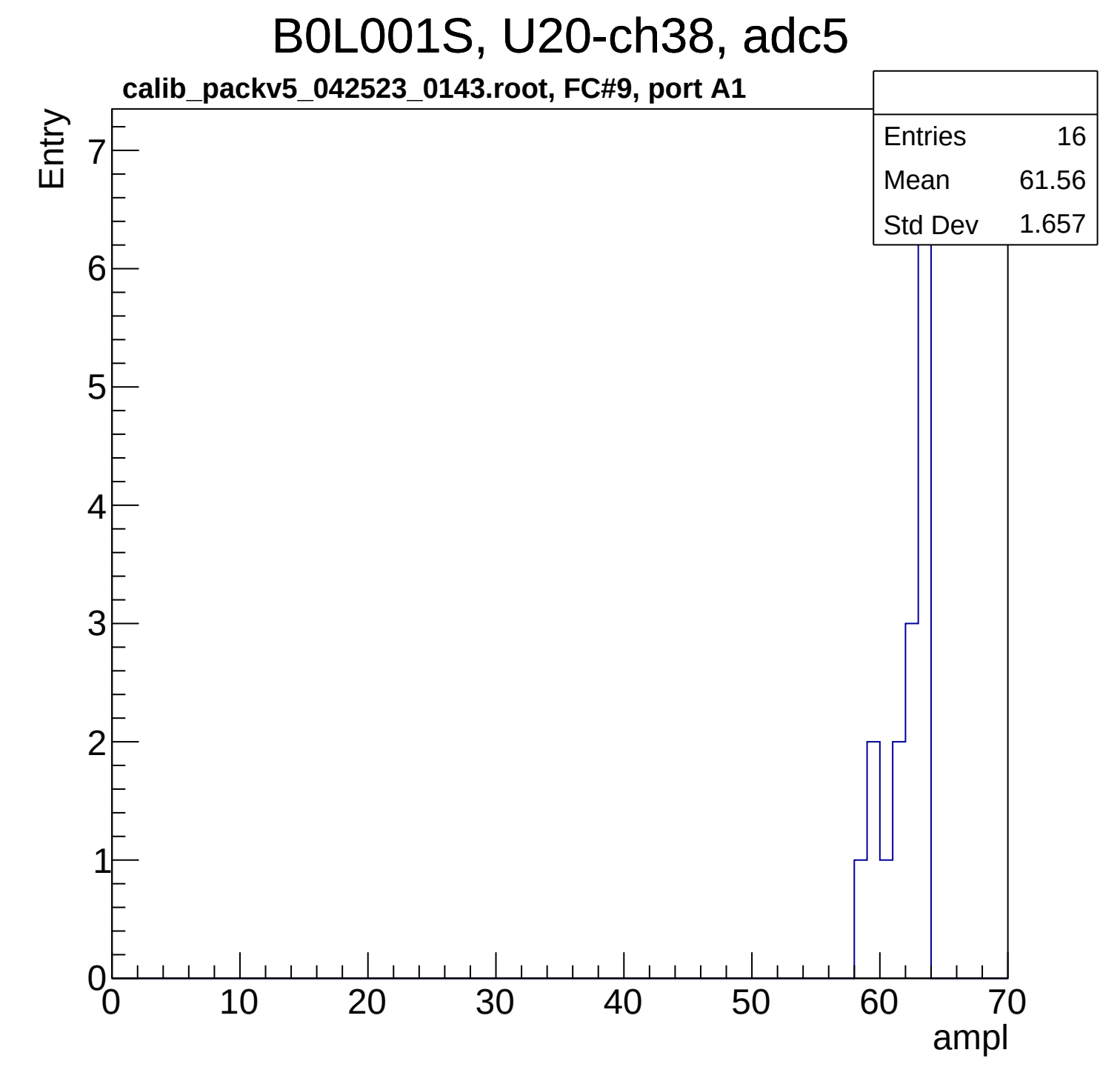
B0L001S, U20-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 61.56
Std Dev 1.657
Entry
ampl
0
1
2
3
4
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0
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60
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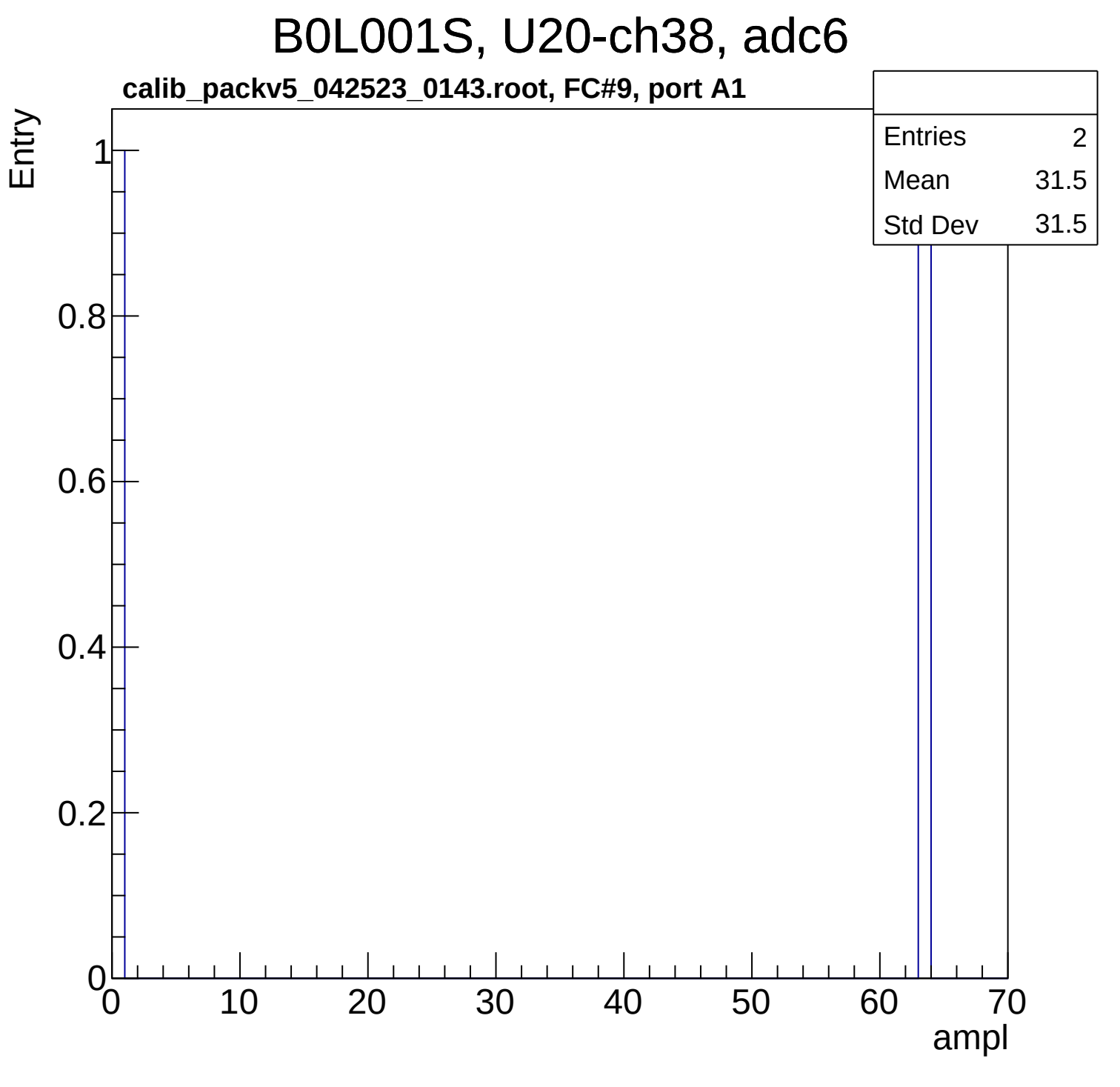
B0L001S, U20-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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10
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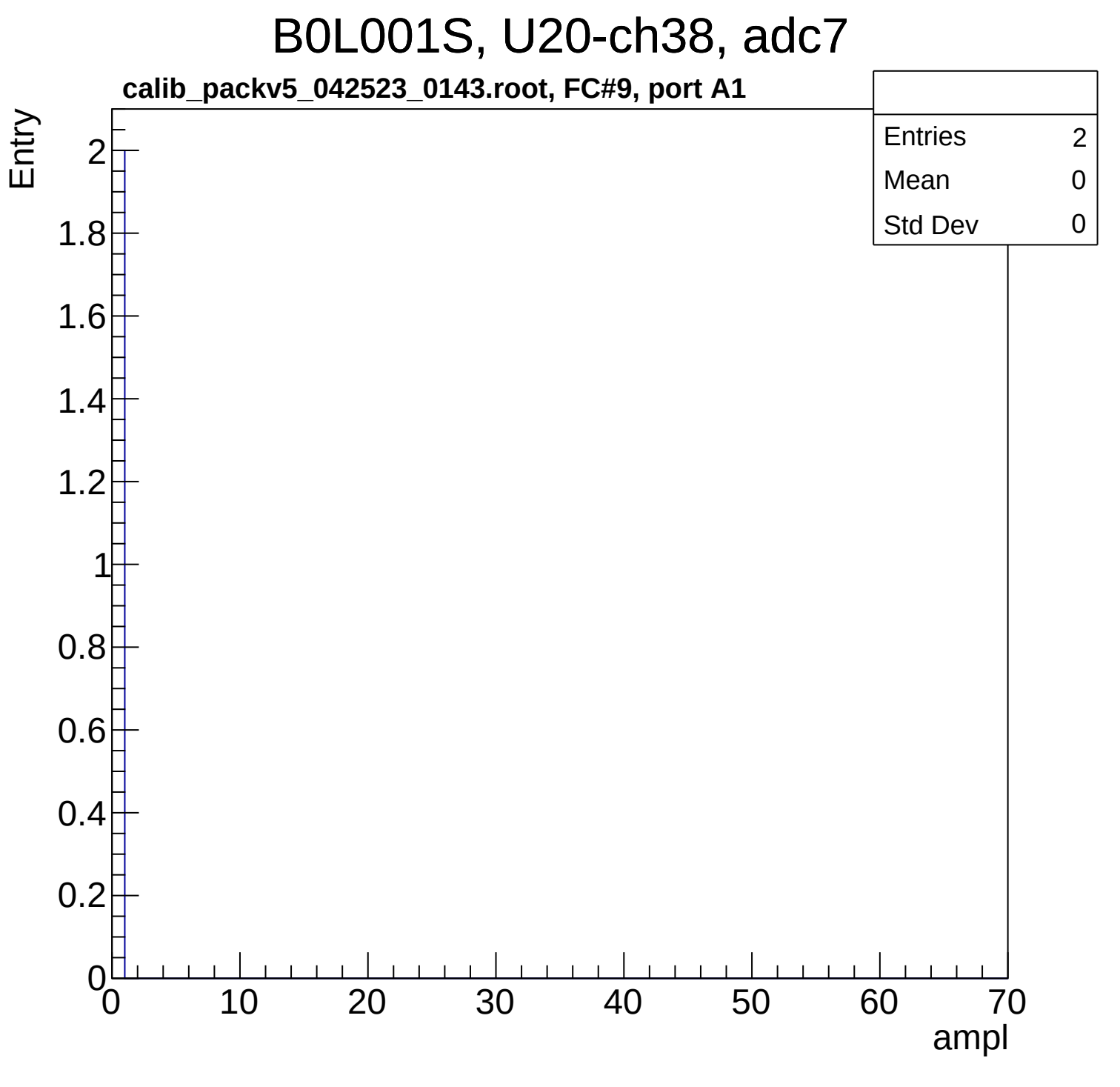
70
ampl


B0L001S, U20-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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0.6
0.4
0.2
0
0
10
20
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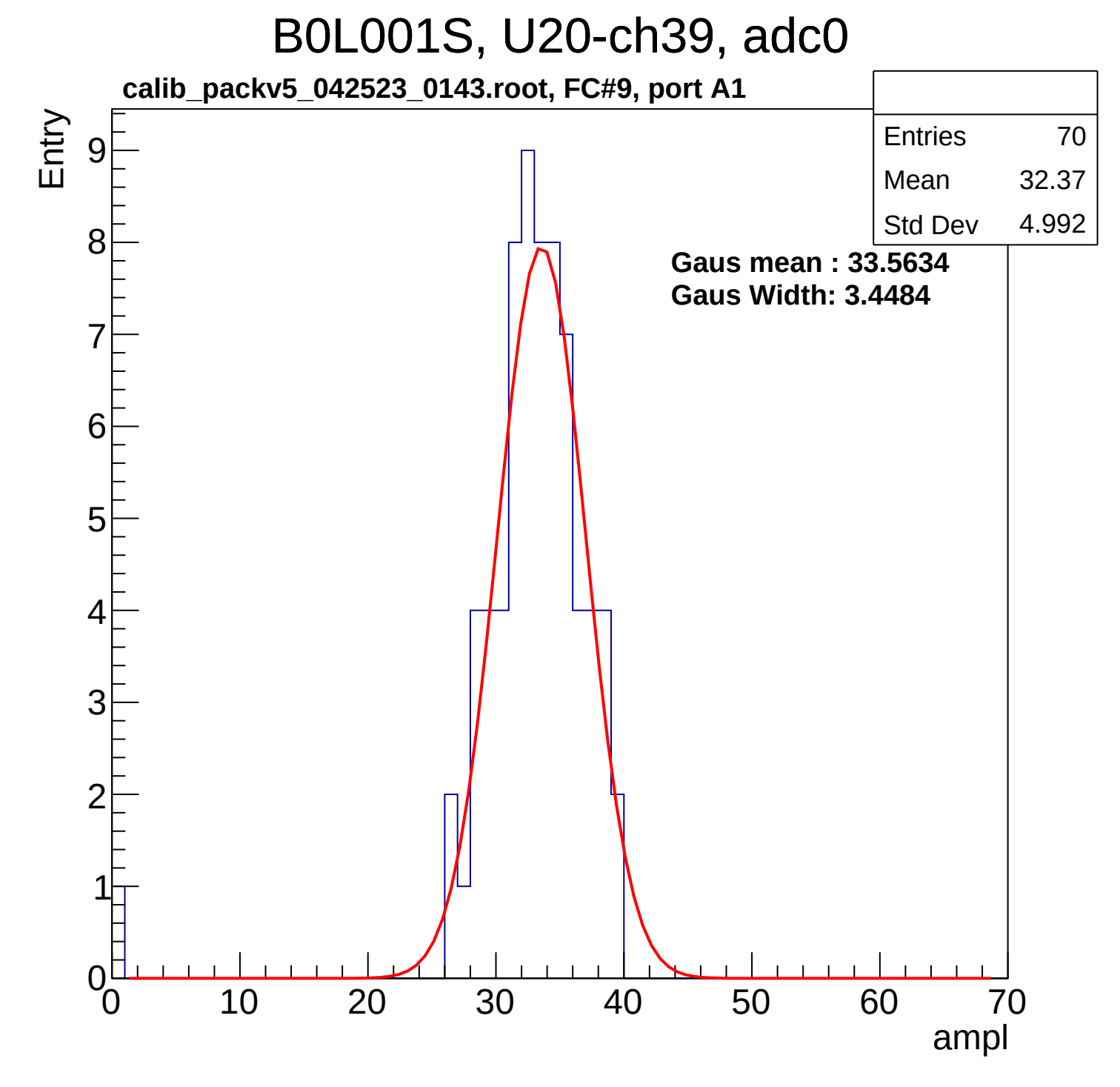
50
60
70
ampl


B0L001S, U20-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 32.37
Std Dev 4.992
Gaus mean : 33.5634
Gaus Width: 3.4484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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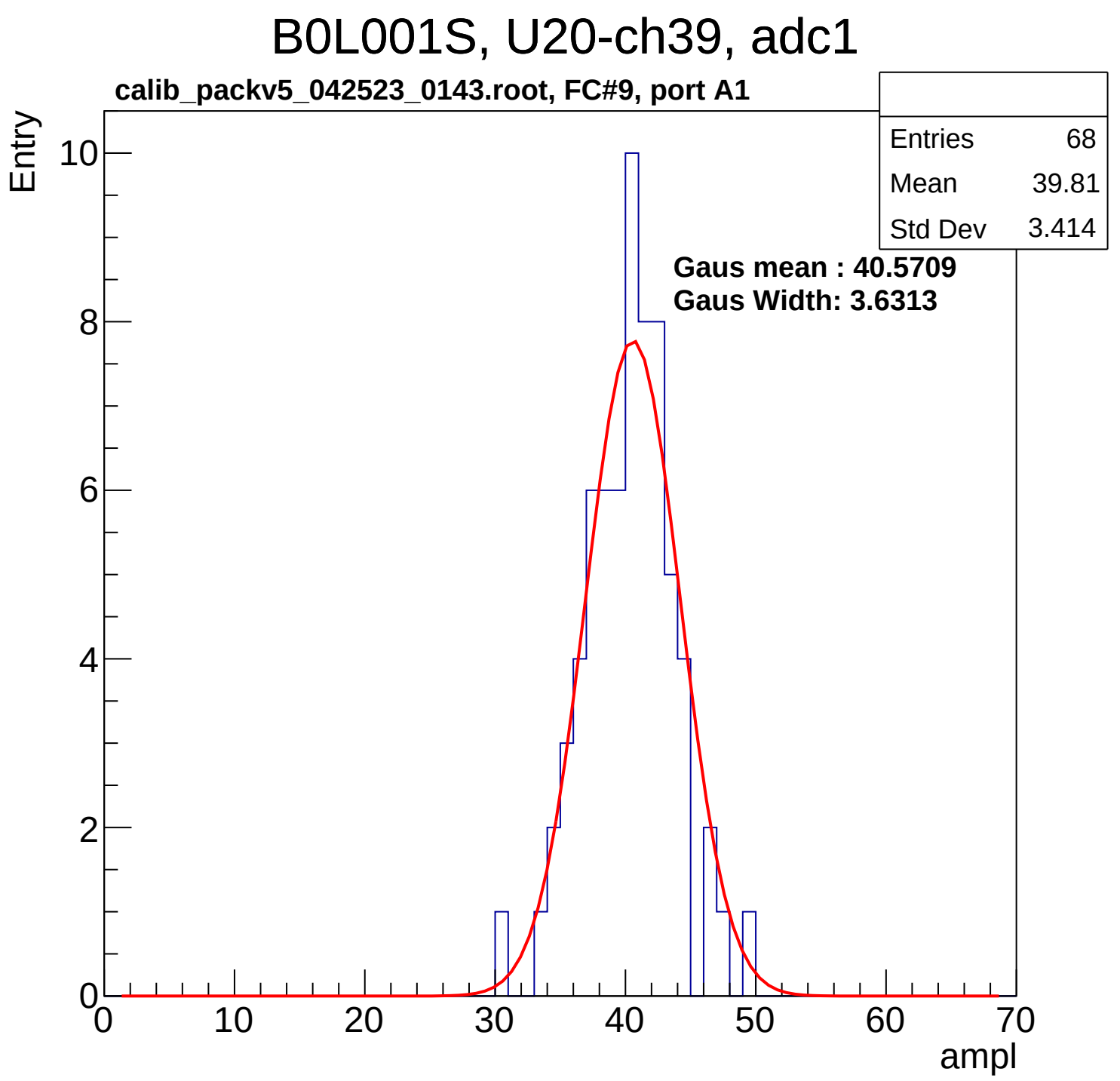
B0L001S, U20-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 39.81
Std Dev 3.414
Gaus mean : 40.5709
Gaus Width: 3.6313
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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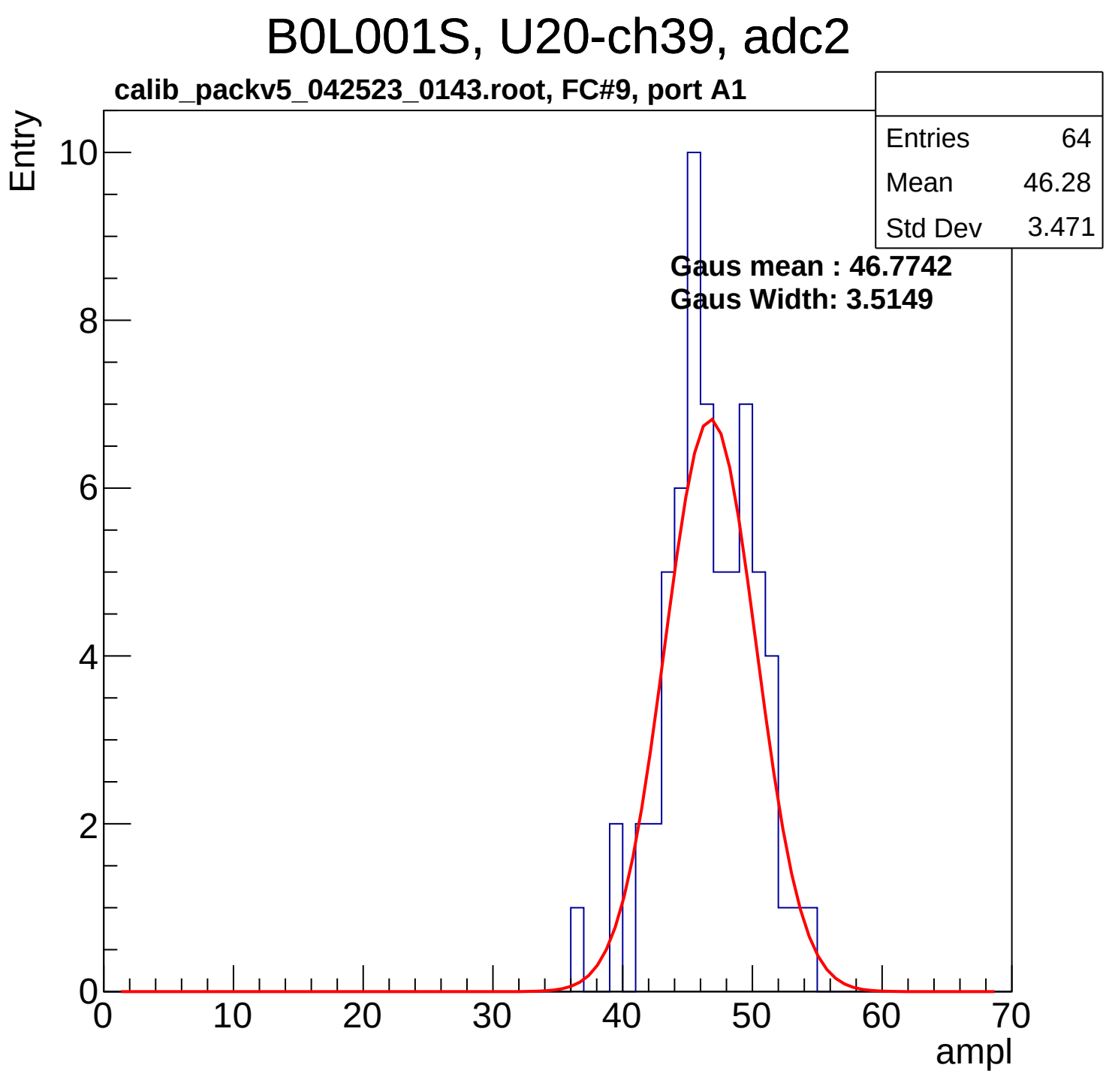
B0L001S, U20-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 46.28
Std Dev 3.471
Gaus mean : 46.7742
Gaus Width: 3.5149
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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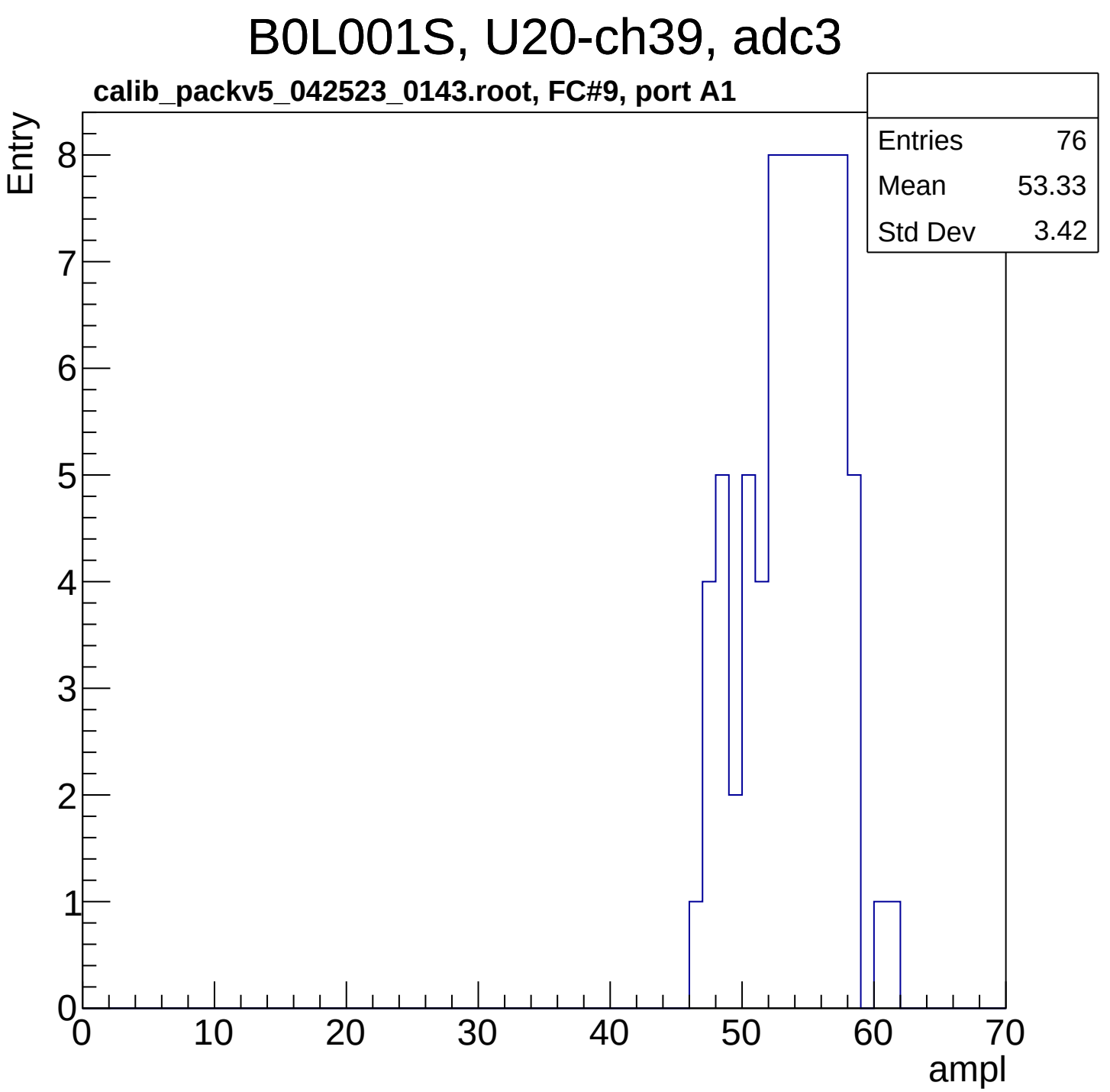
B0L001S, U20-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 53.33
Std Dev 3.42
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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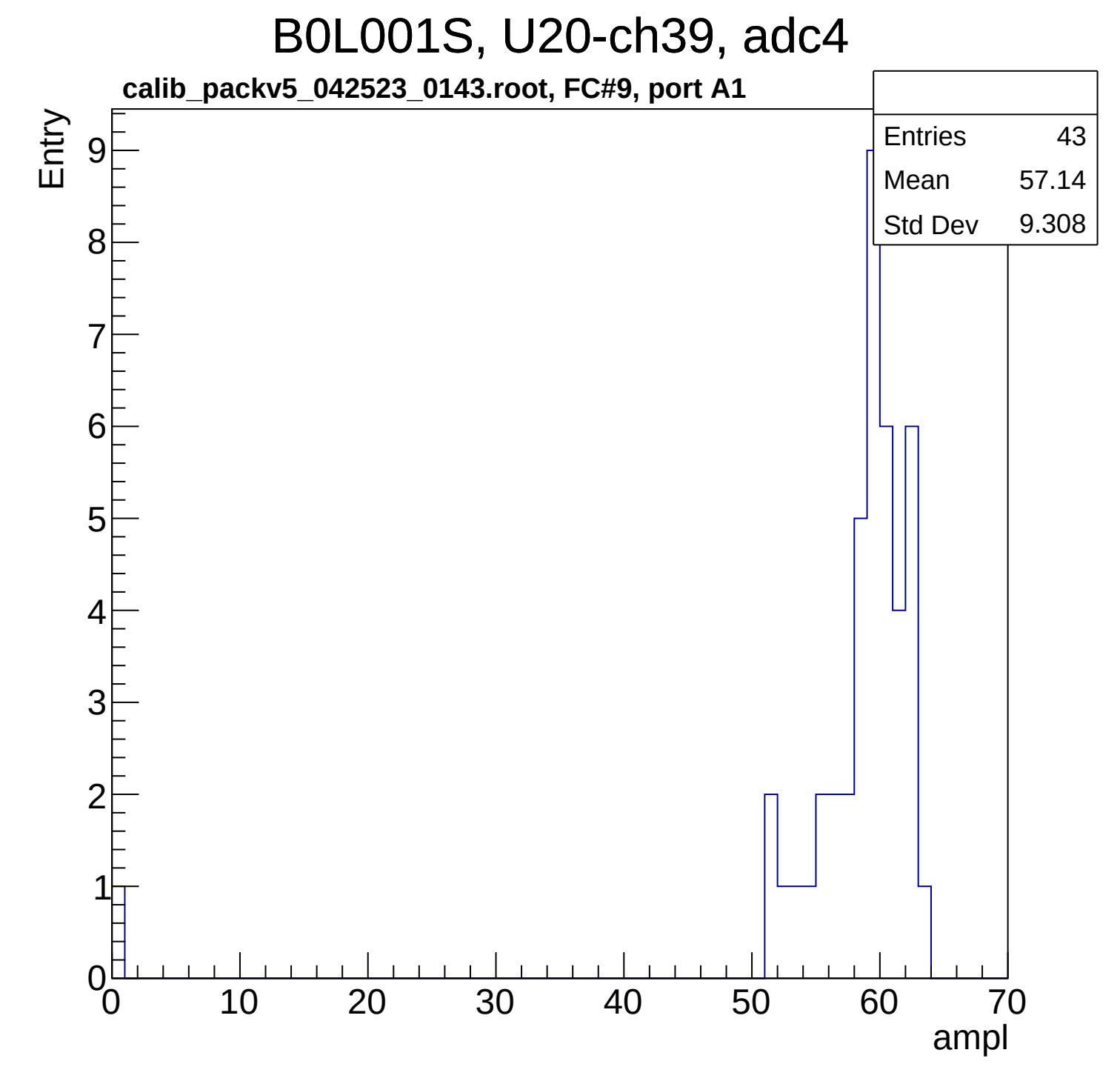
B0L001S, U20-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.14
Std Dev 9.308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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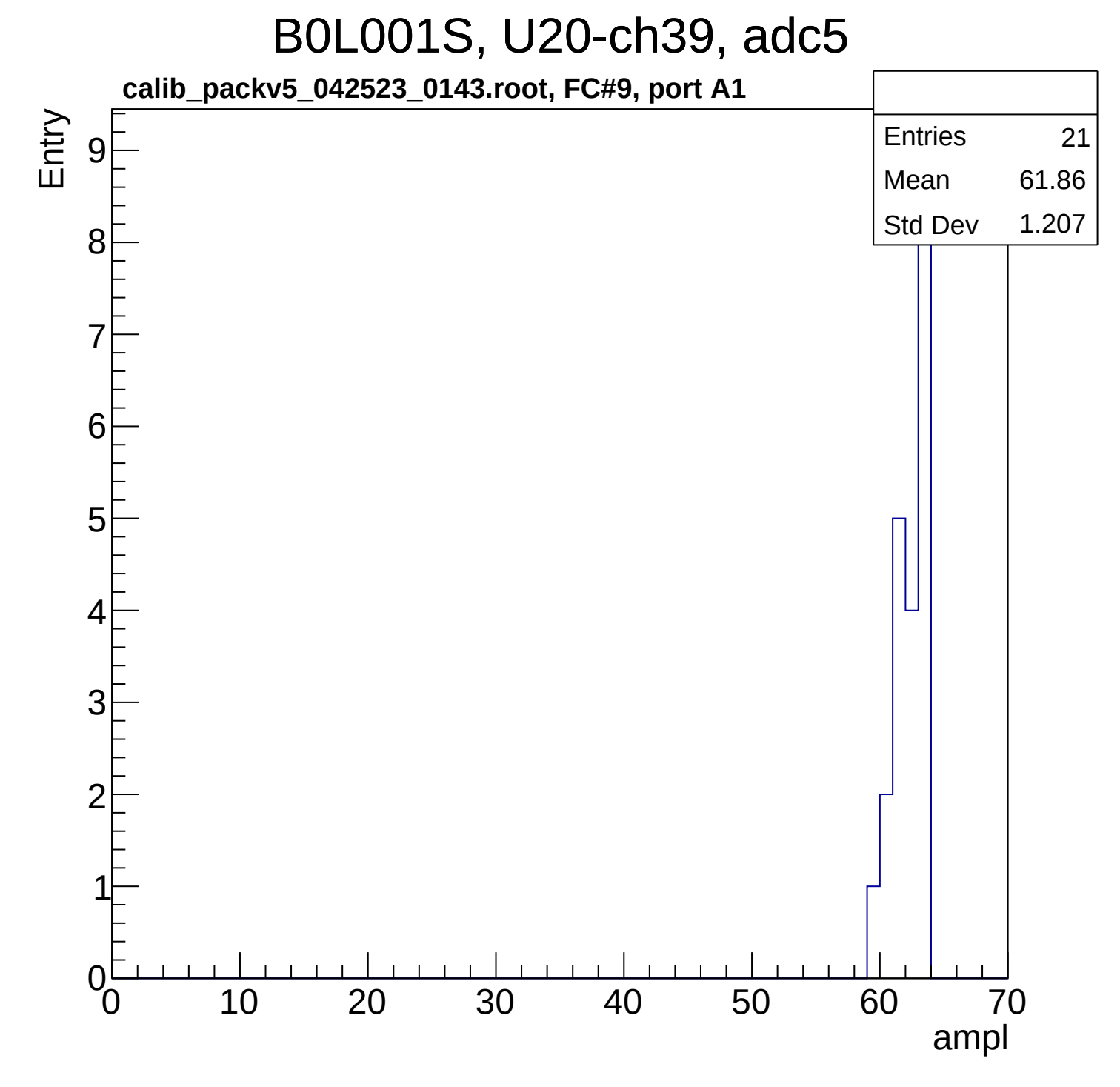
B0L001S, U20-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 61.86
Std Dev 1.207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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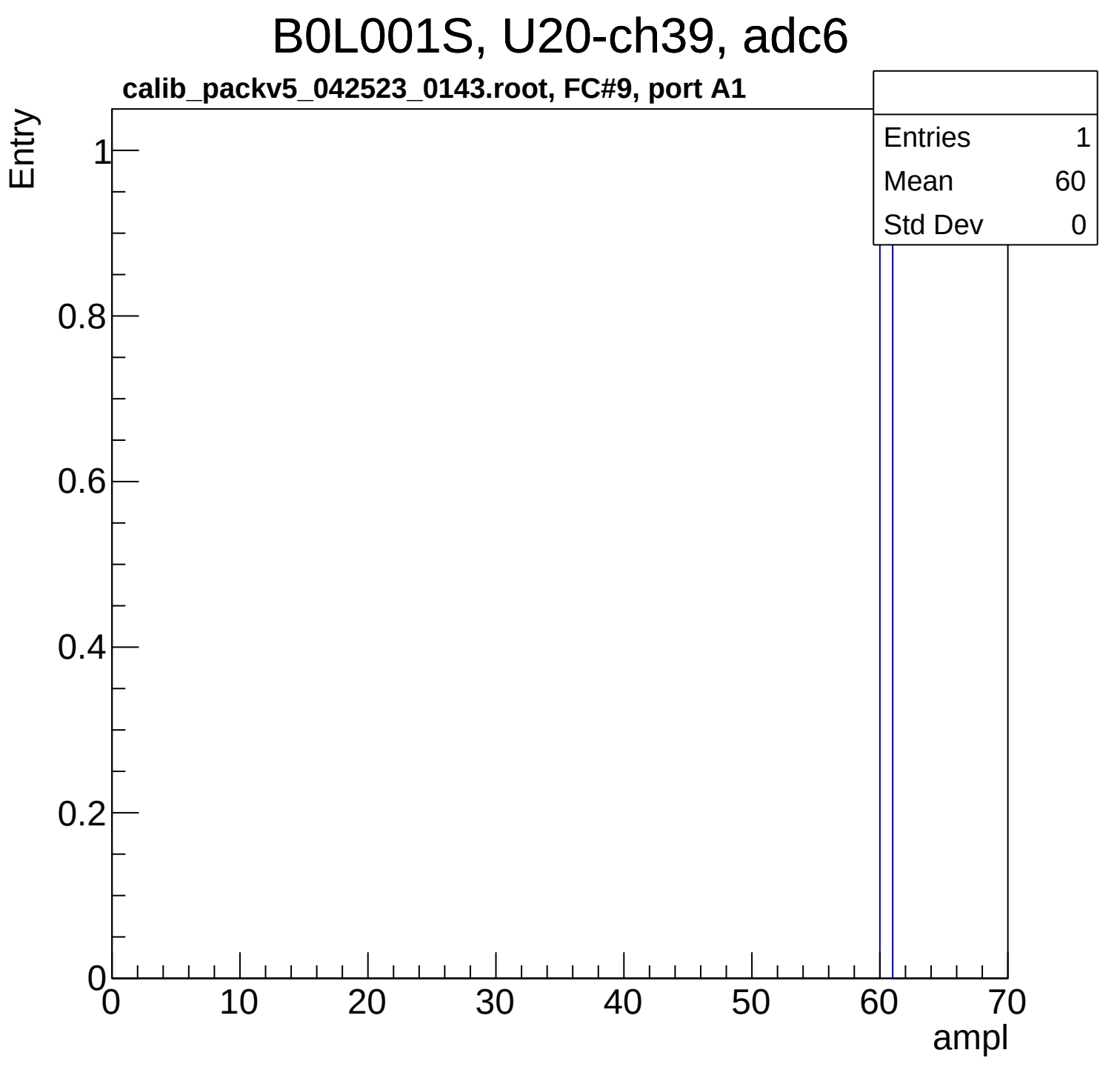
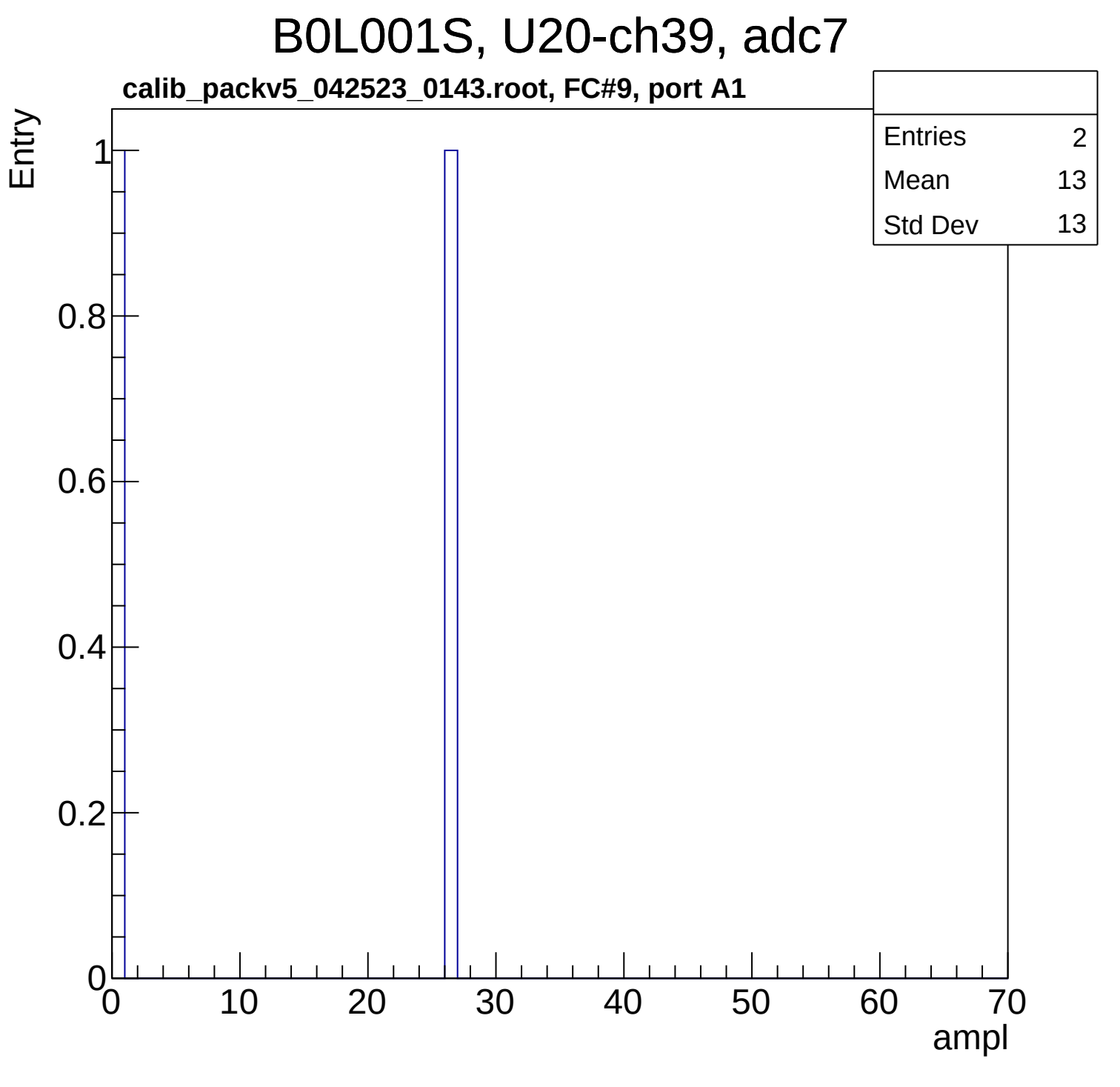

B0L001S, U20-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 13
Std Dev 13
Entry
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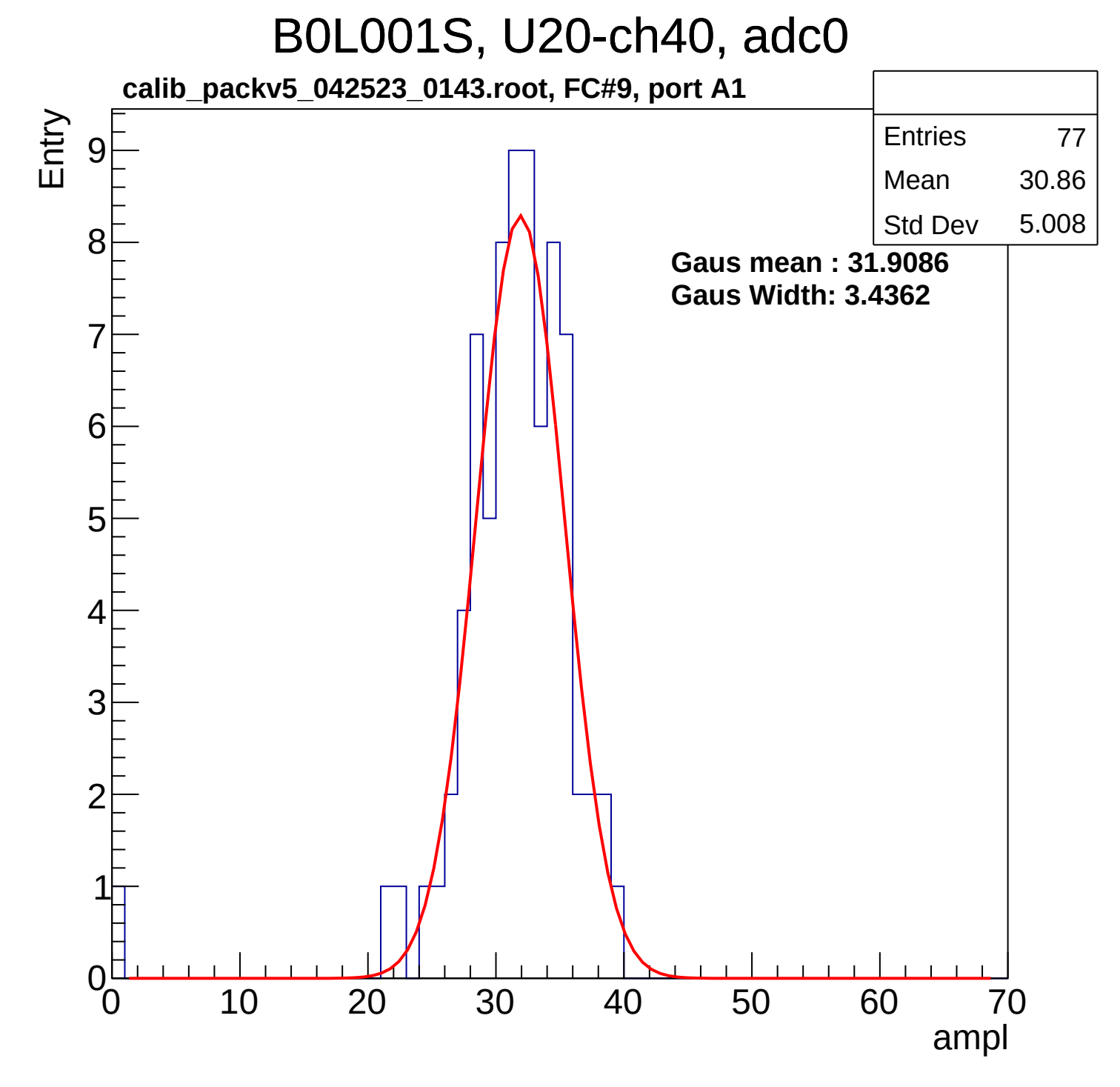
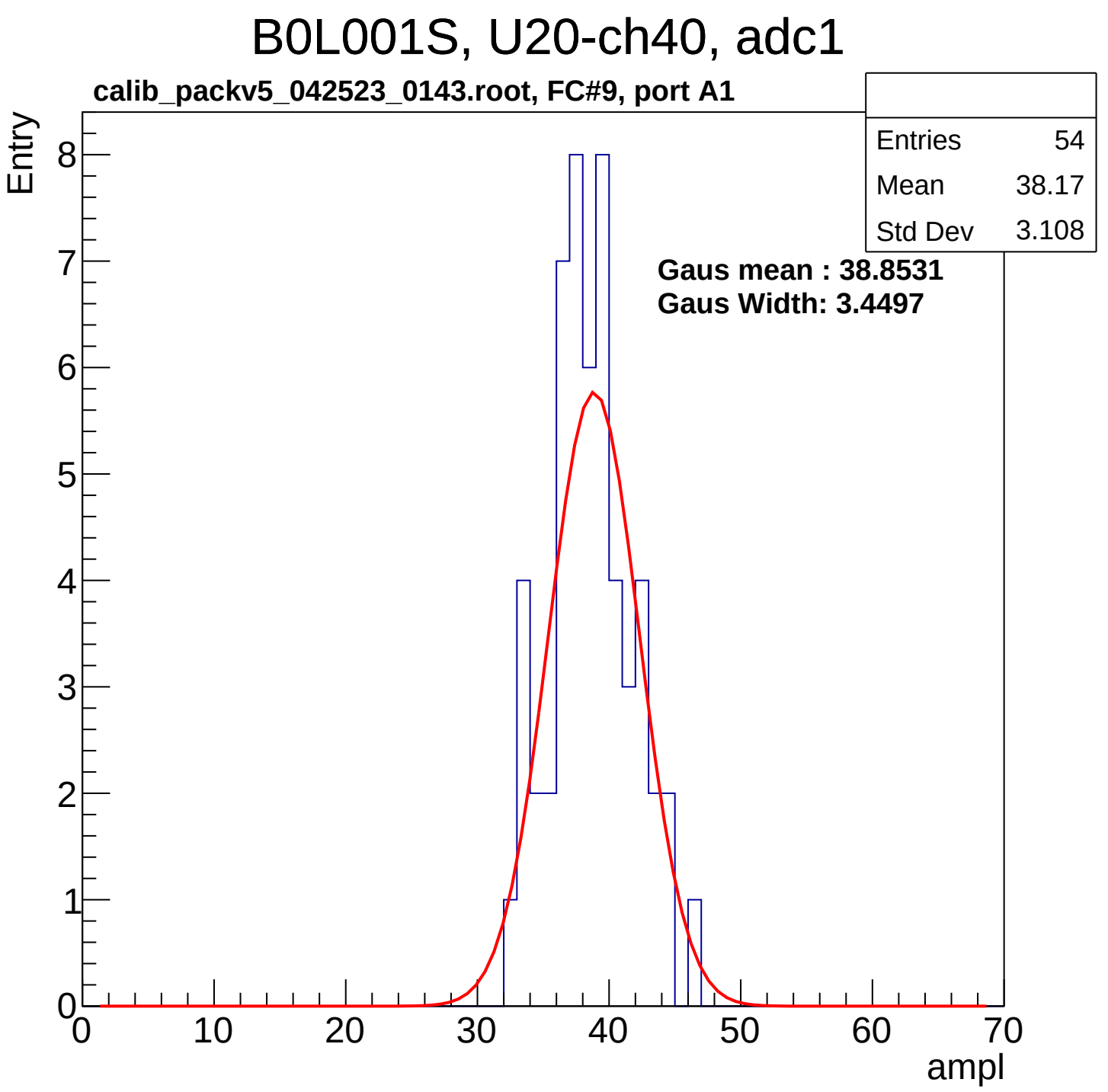
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.86
Std Dev 5.008
Gaus mean : 31.9086
Gaus Width: 3.4362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.17
Std Dev 3.108
Gaus mean : 38.8531
Gaus Width: 3.4497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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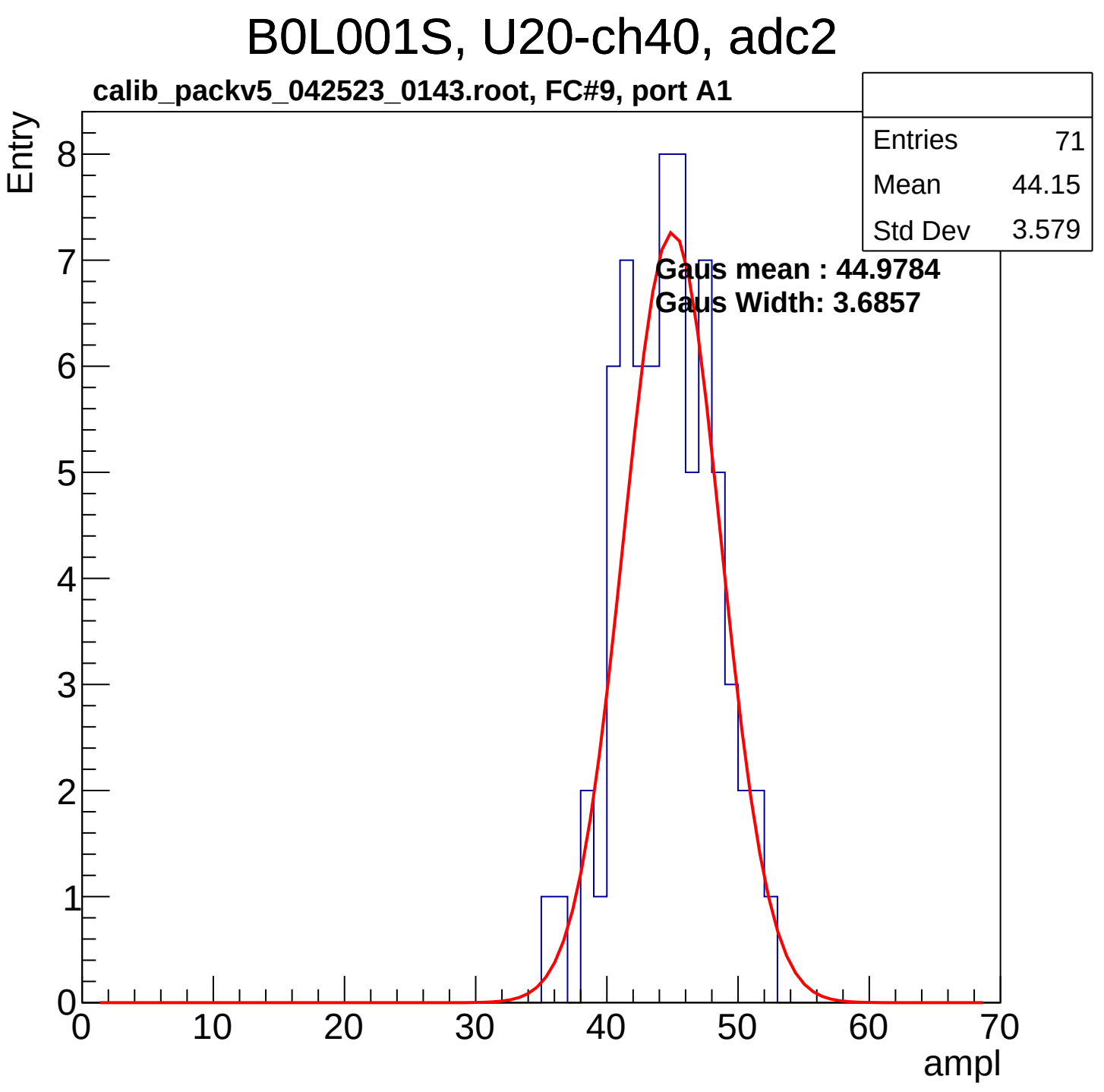
B0L001S, U20-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.15
Std Dev 3.579
Gaus mean : 44.9784
Gaus Width: 3.6857
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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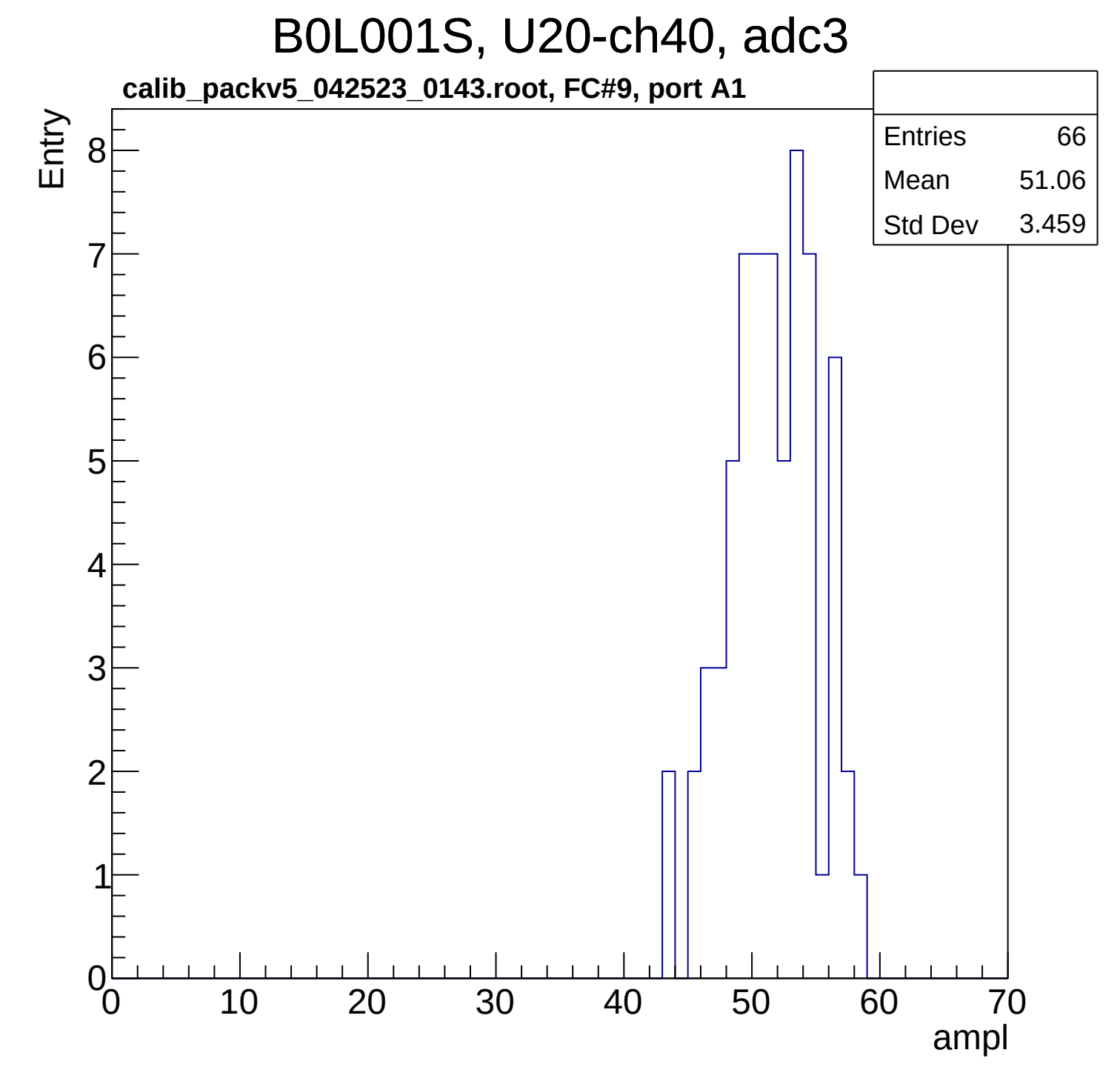
B0L001S, U20-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.06
Std Dev 3.459
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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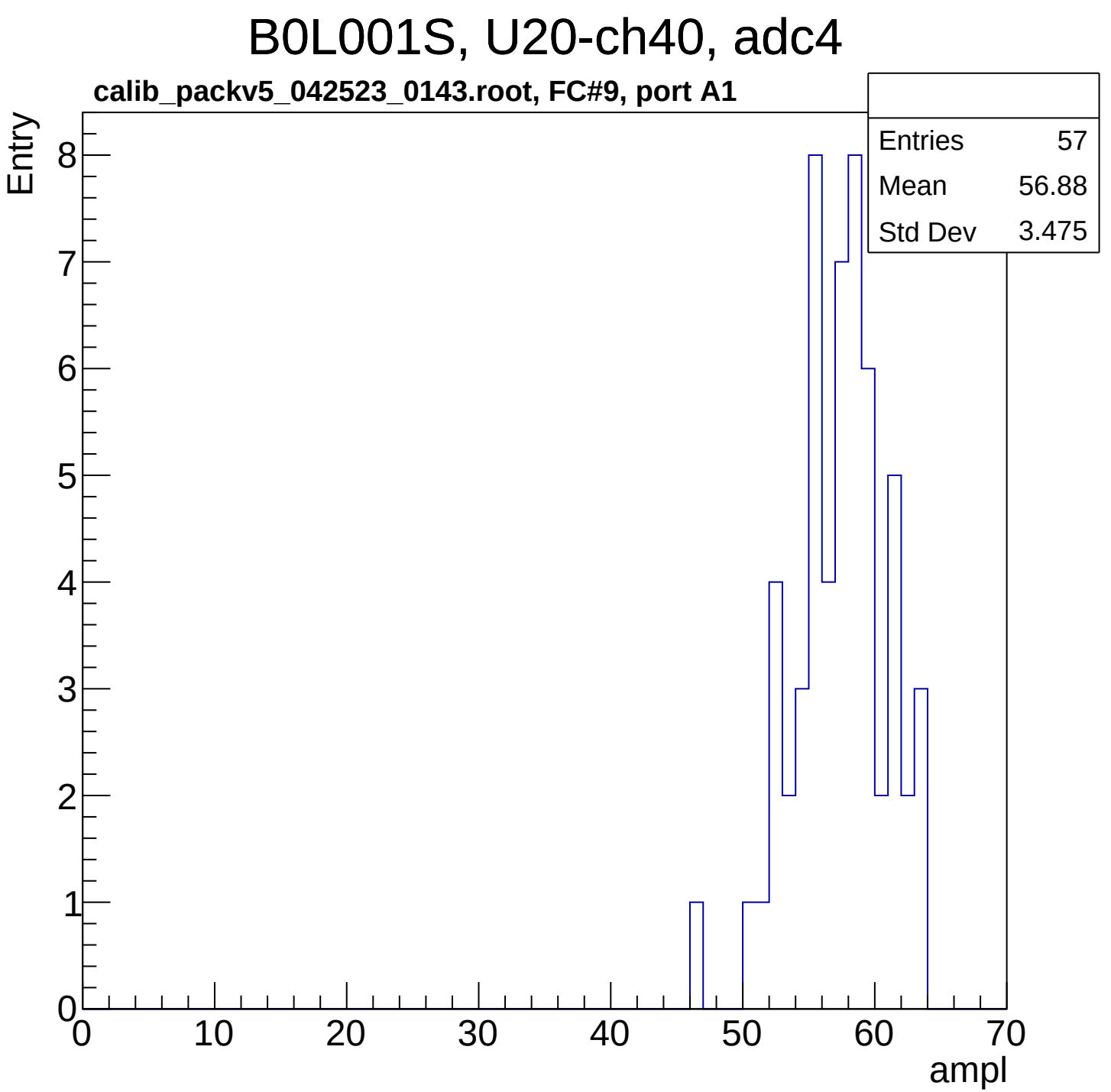
B0L001S, U20-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.88
Std Dev 3.475
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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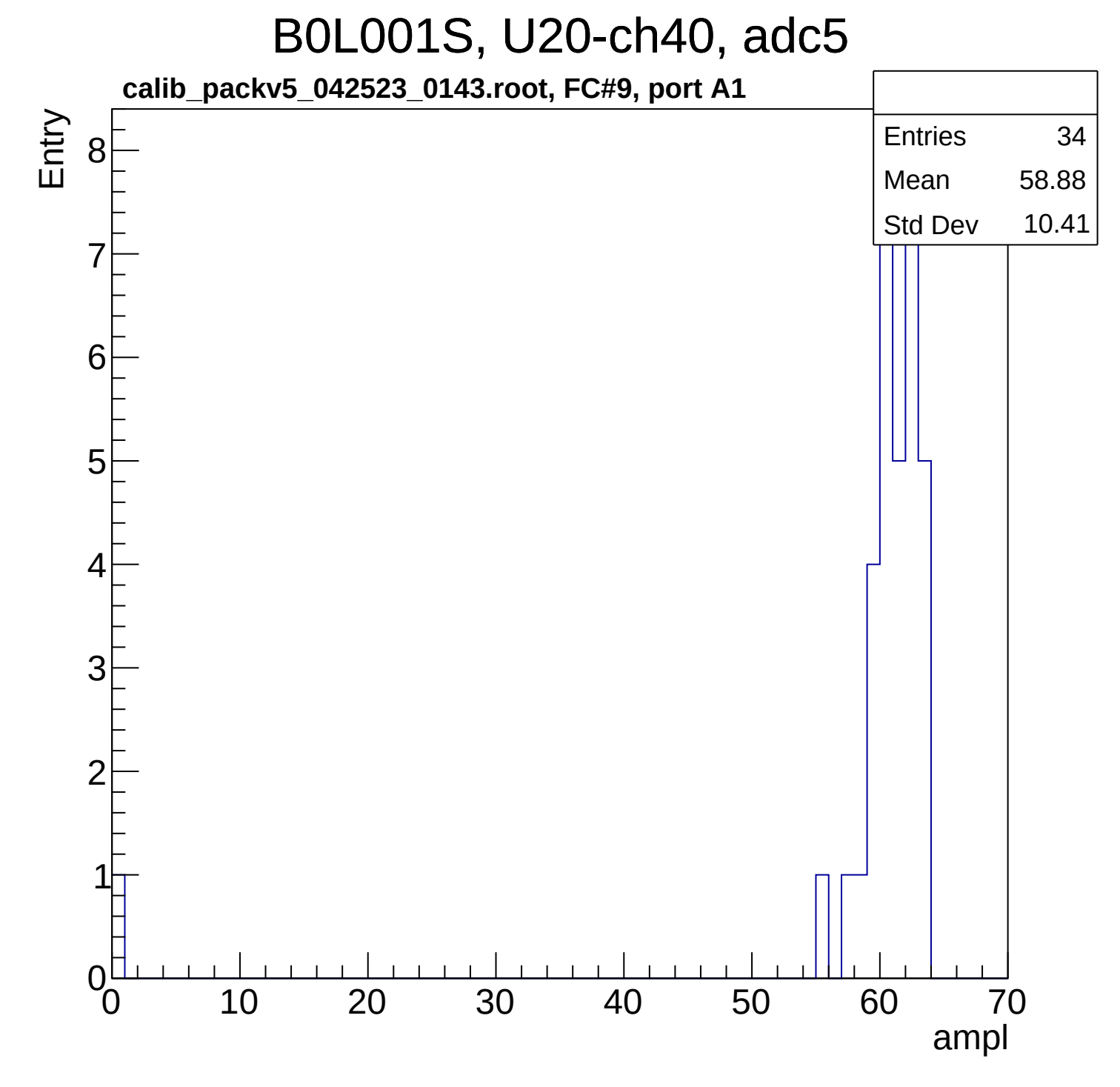
B0L001S, U20-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.88
Std Dev 10.41
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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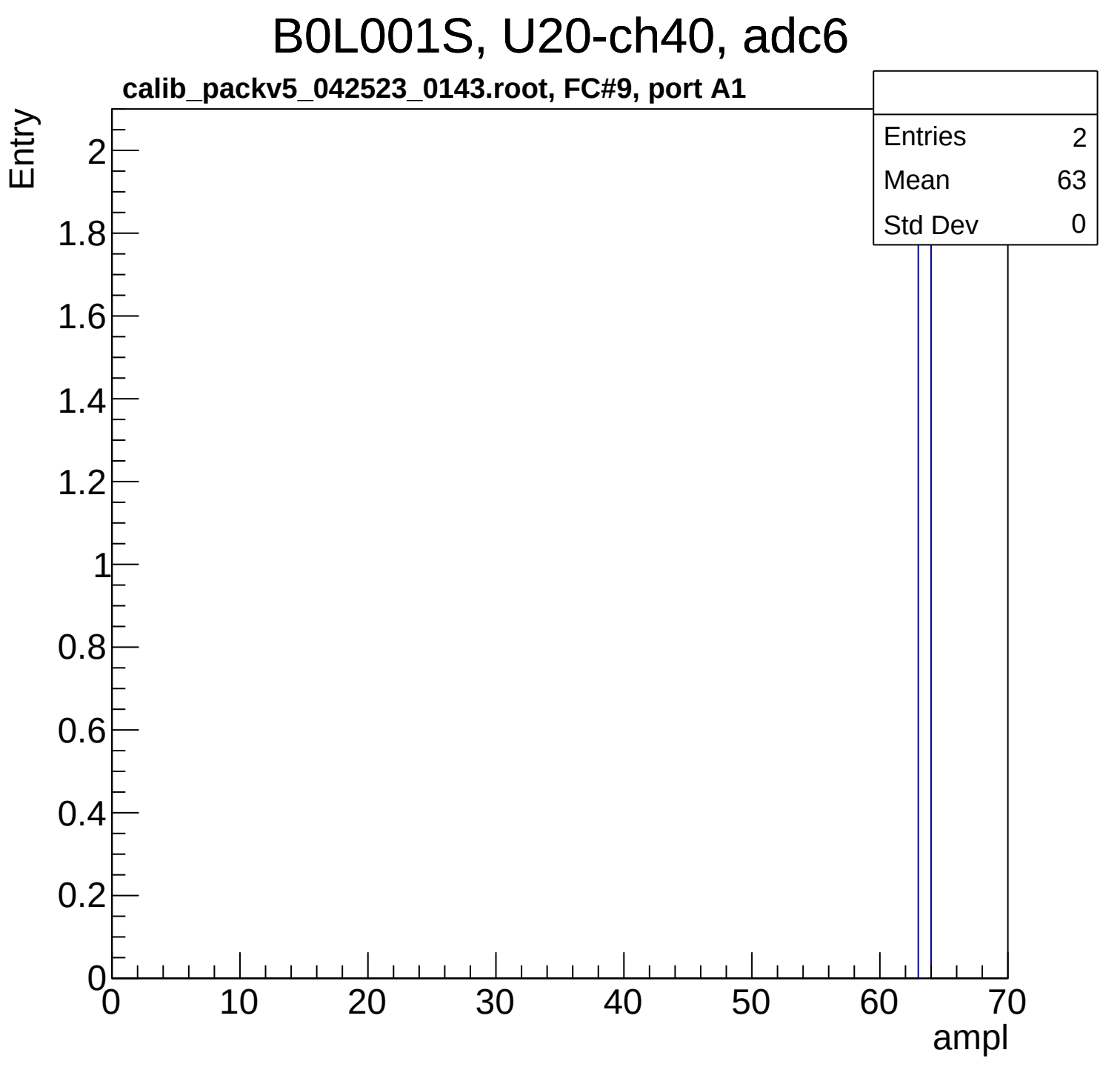
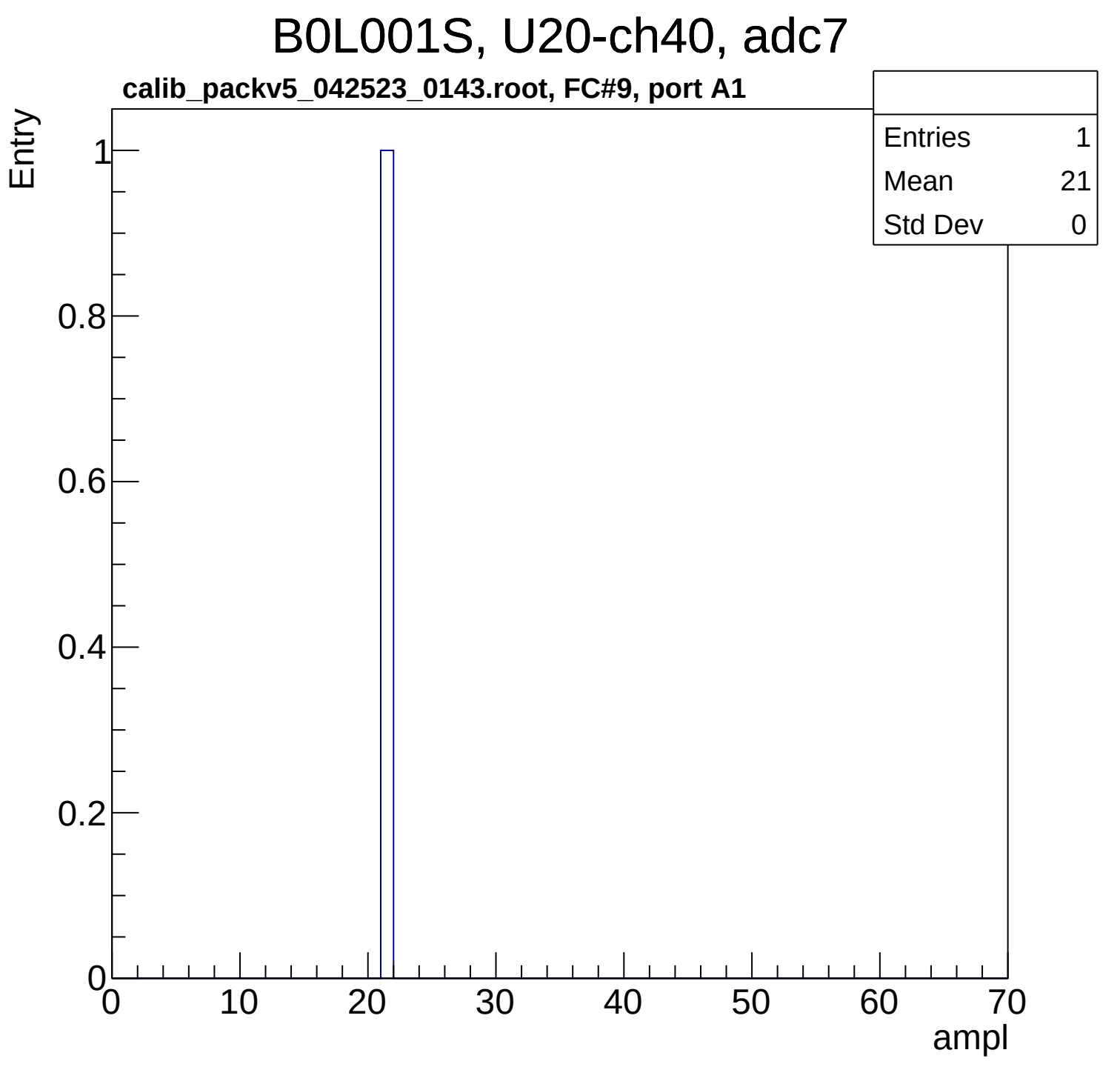
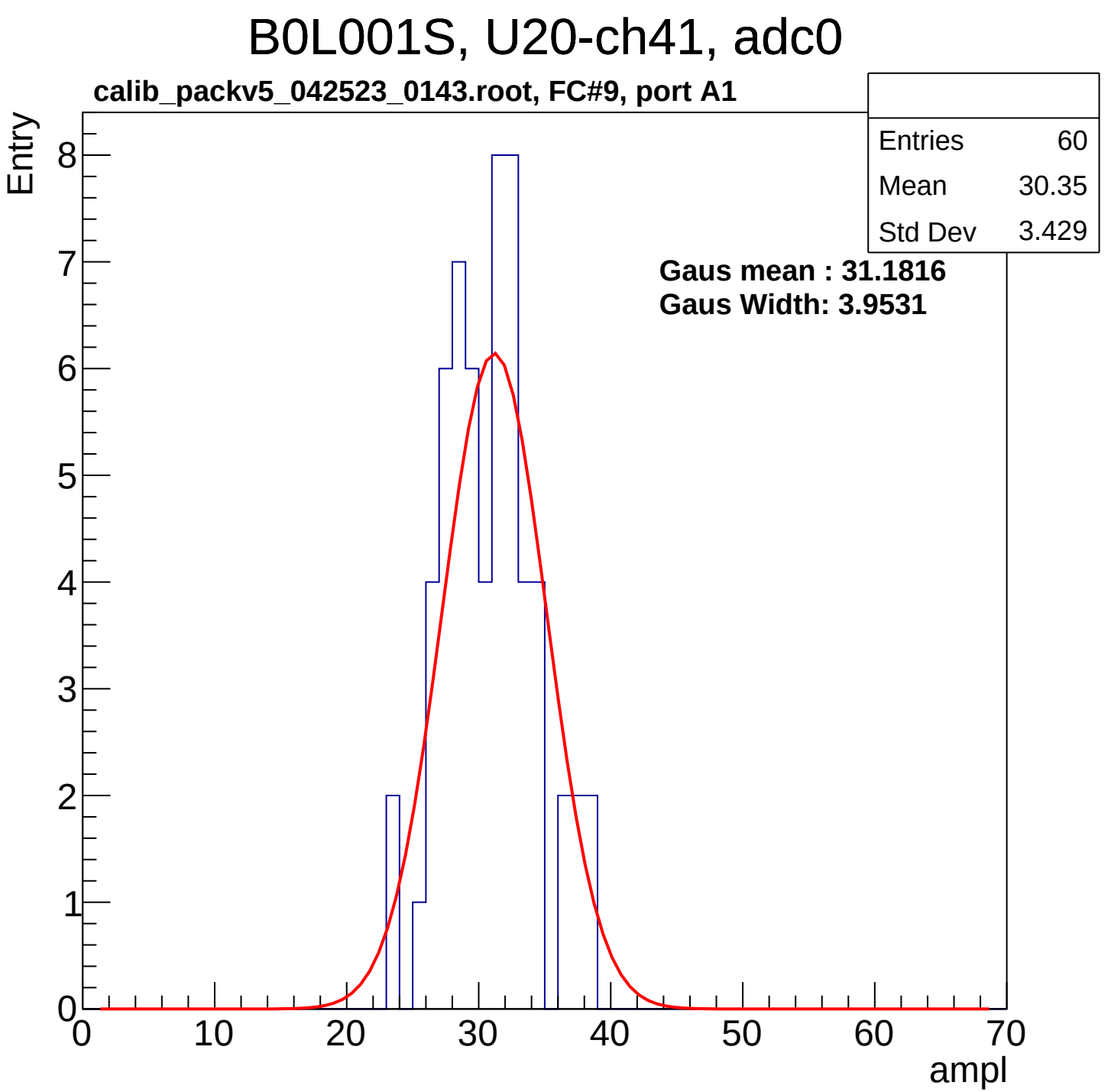
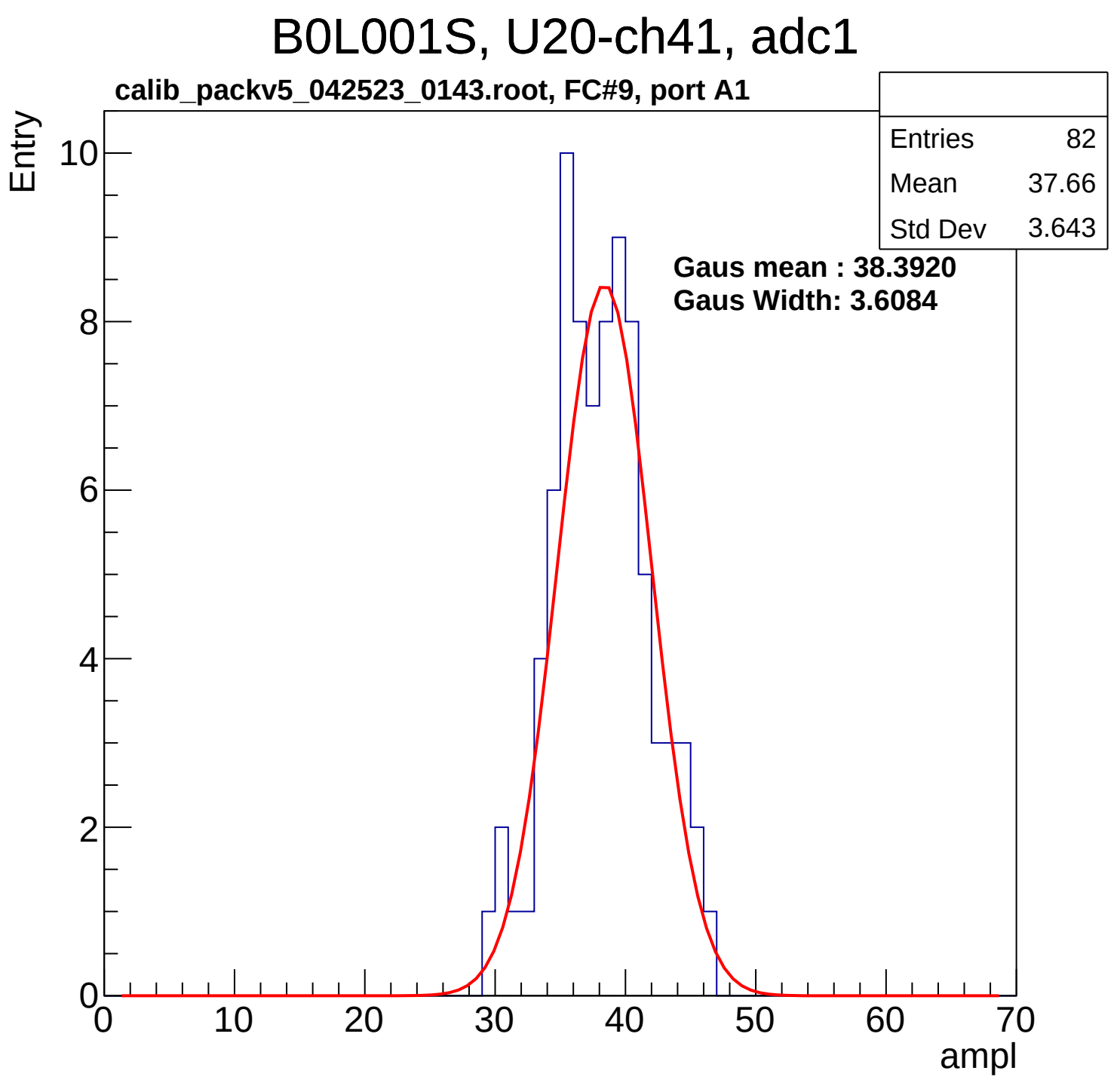
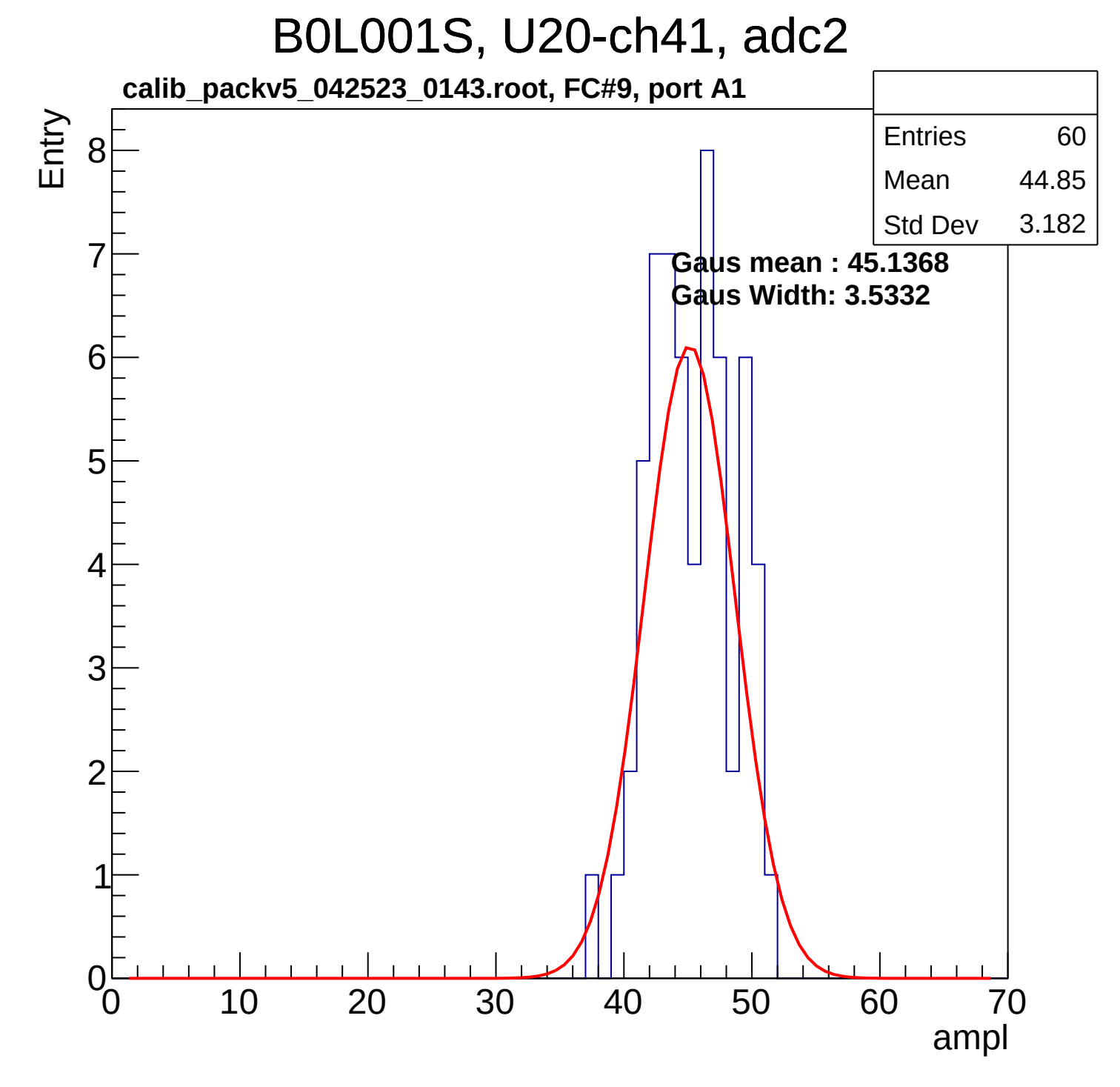

B0L001S, U20-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.35
Std Dev 3.429
Gaus mean : 31.1816
Gaus Width: 3.9531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 37.66
Std Dev 3.643
Gaus mean : 38.3920
Gaus Width: 3.6084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.85
Std Dev 3.182
Gaus mean : 45.1368
Gaus Width: 3.5332
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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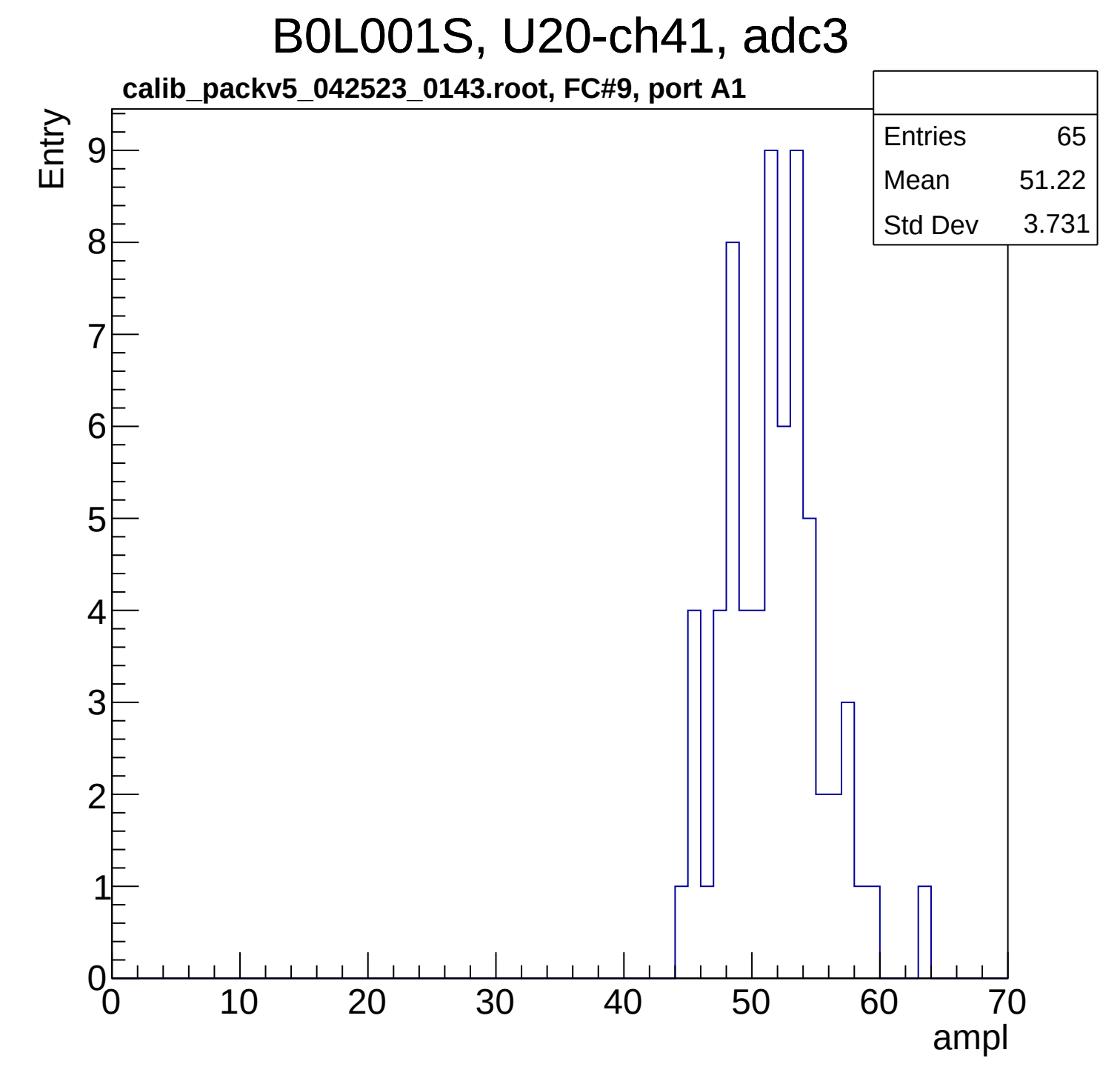
B0L001S, U20-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.22
Std Dev 3.731
Entry
ampl
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7
8
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0
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20
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60
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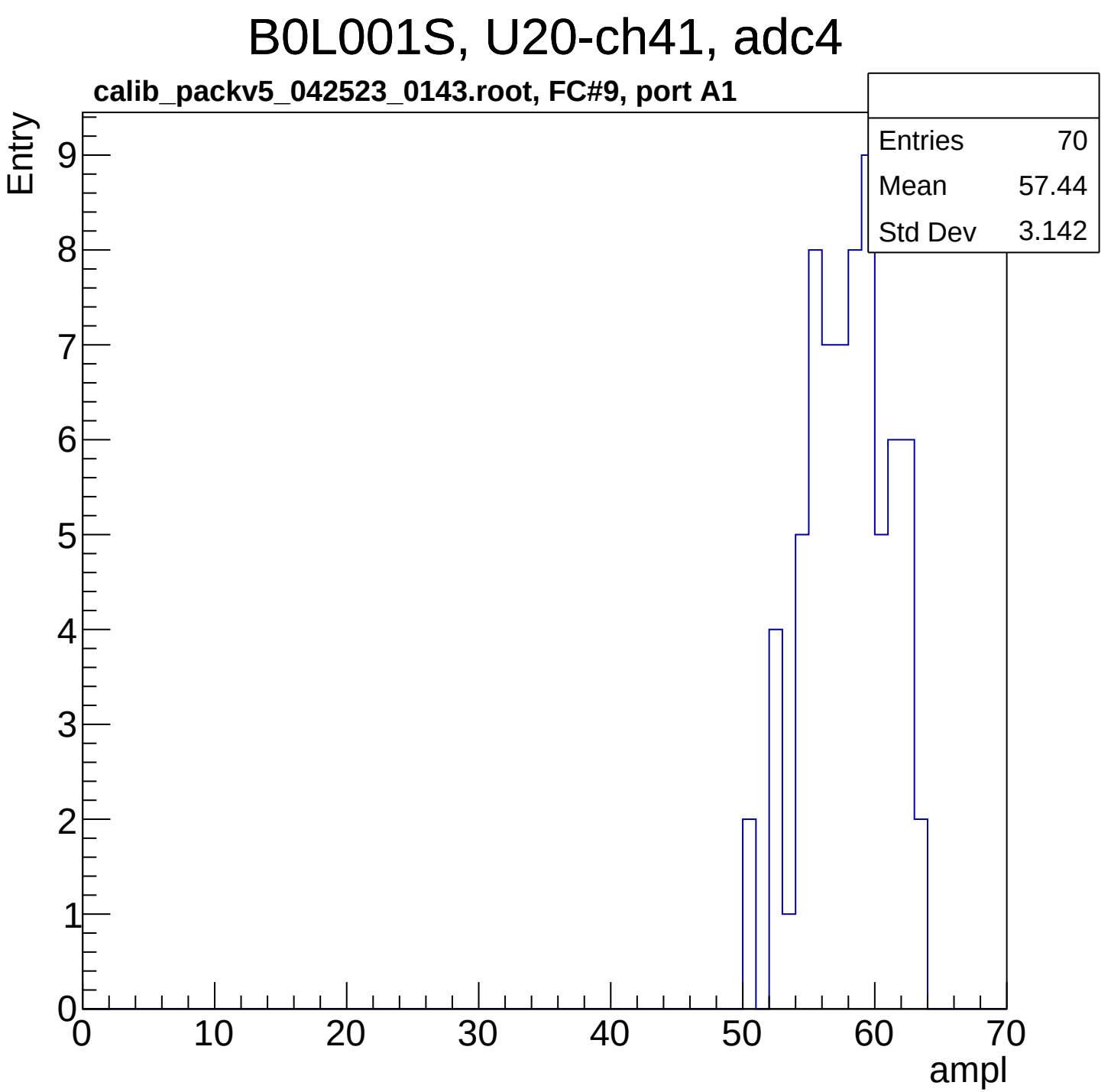
B0L001S, U20-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 57.44
Std Dev 3.142
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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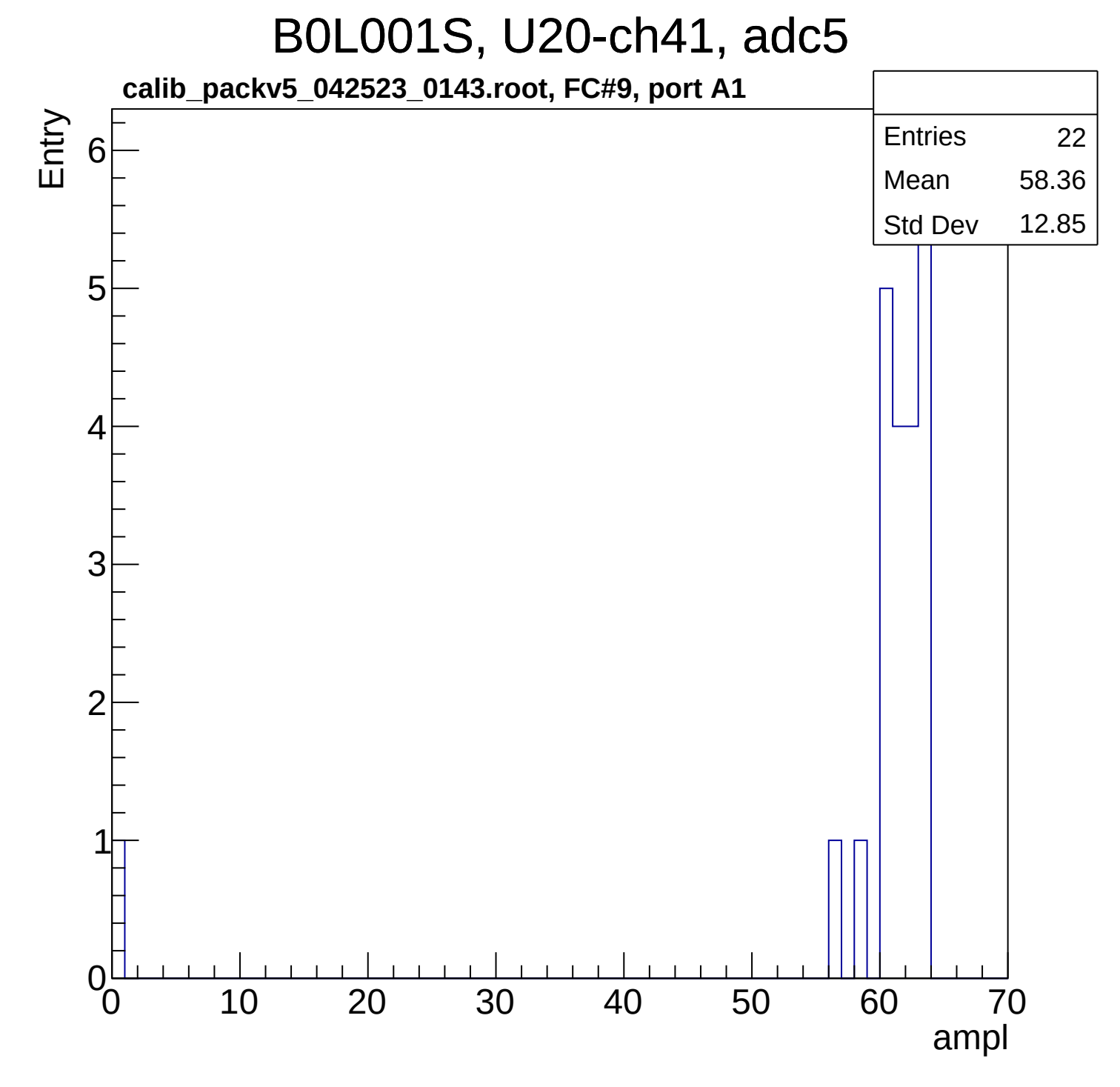
B0L001S, U20-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.36
Std Dev 12.85
Entry
ampl
0
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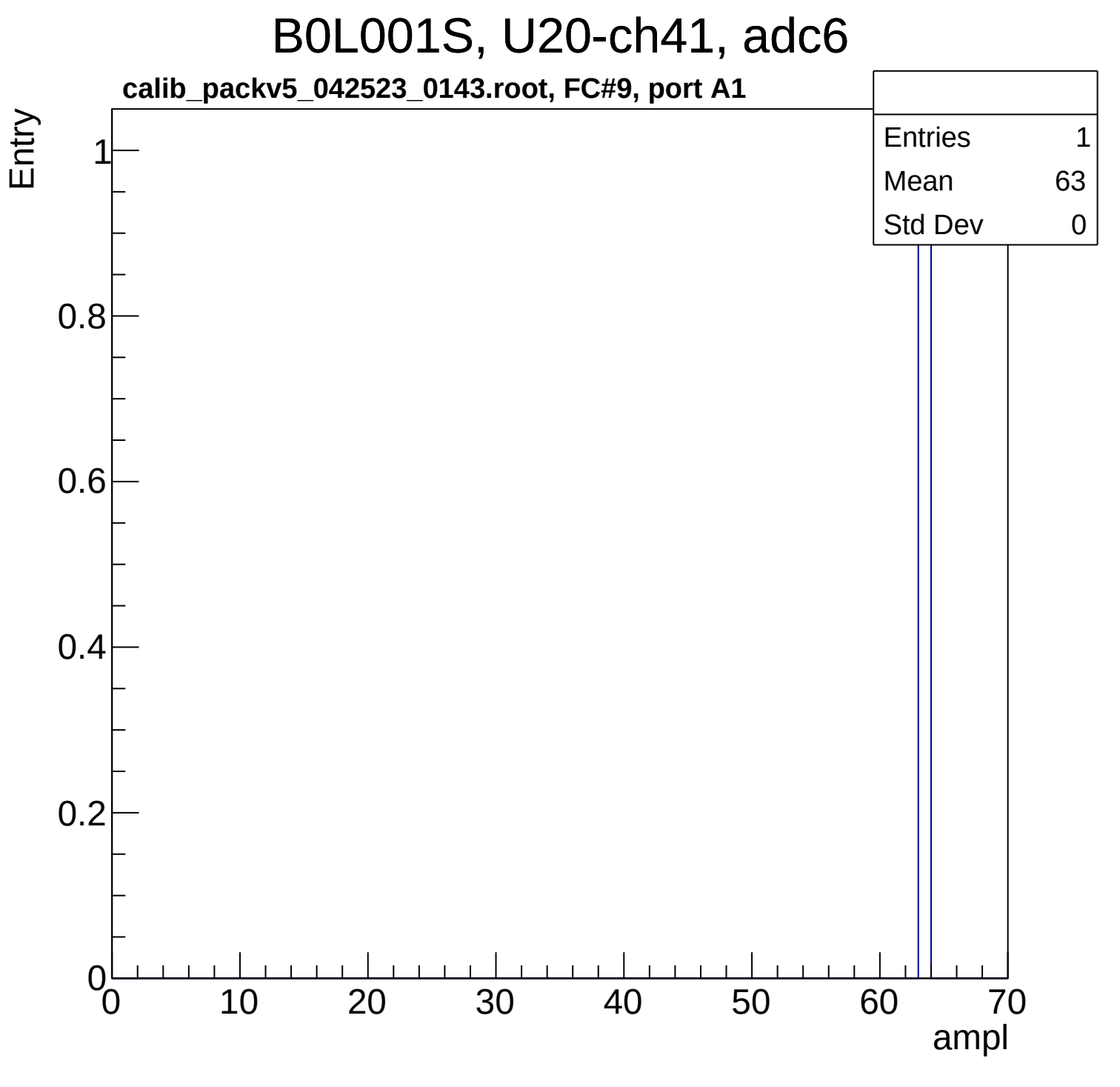
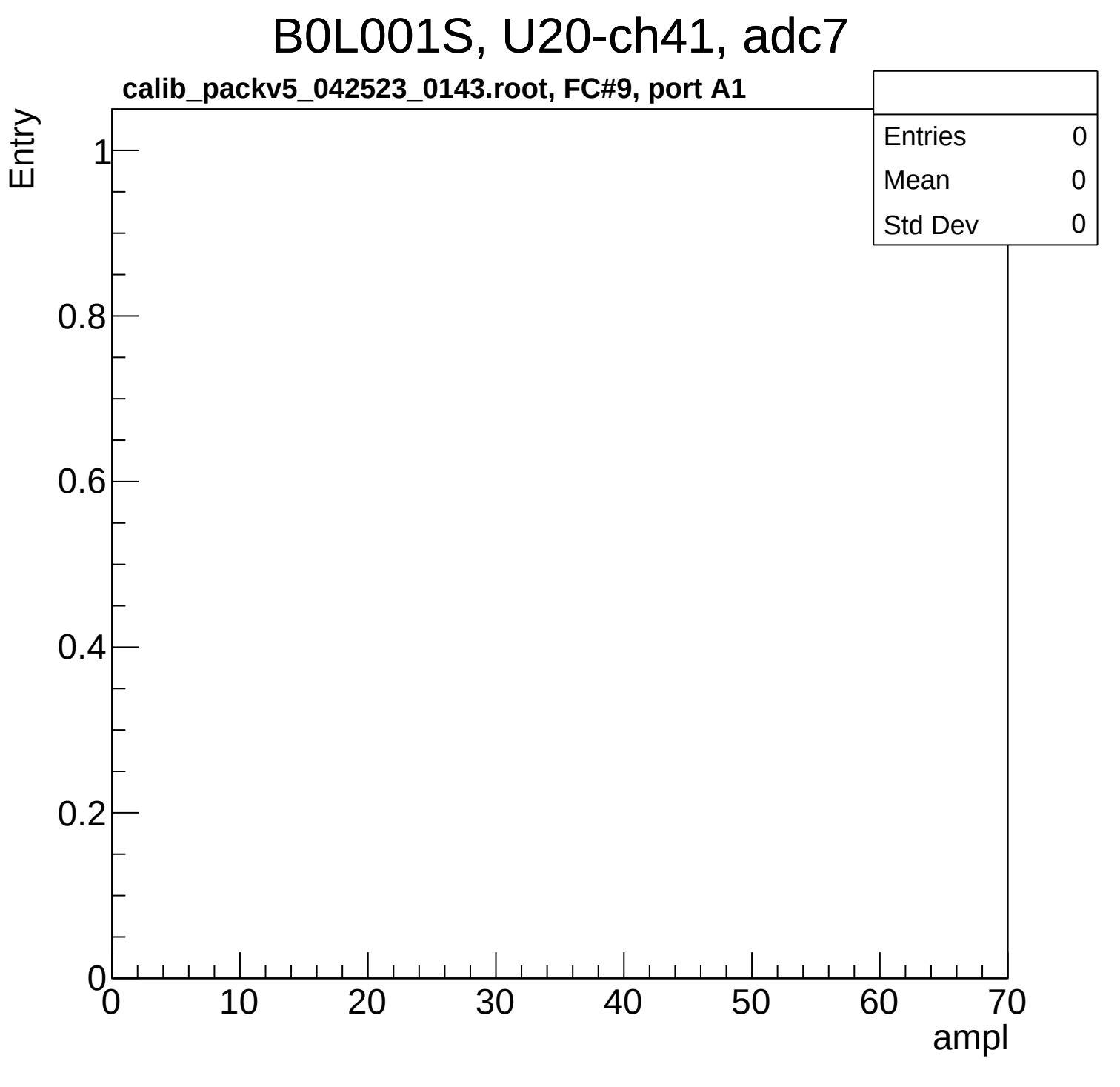
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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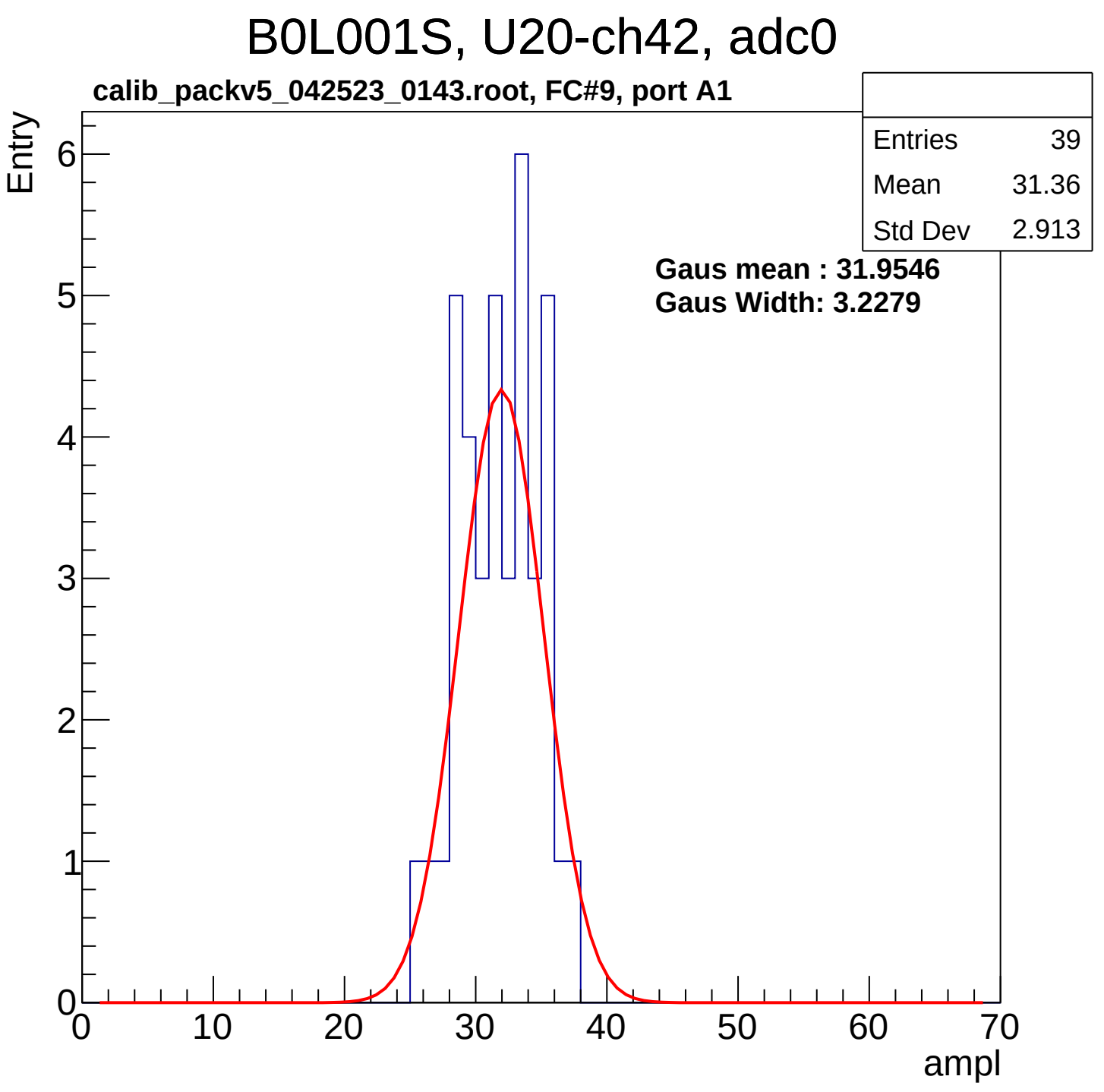
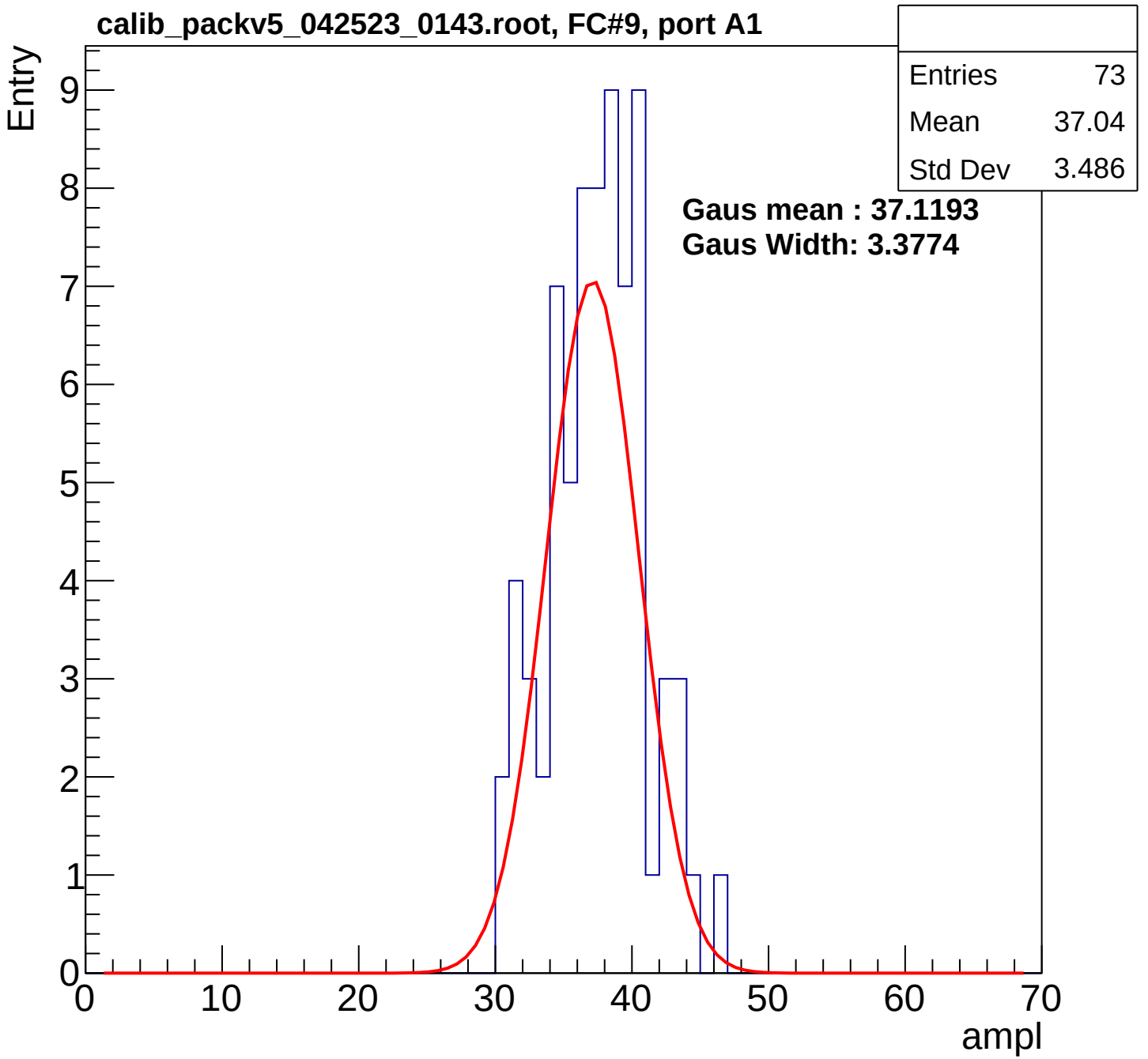
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 31.36
Std Dev 2.913
Gaus mean : 31.9546
Gaus Width: 3.2279
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.04
Std Dev 3.486
Gaus mean : 37.1193
Gaus Width: 3.3774
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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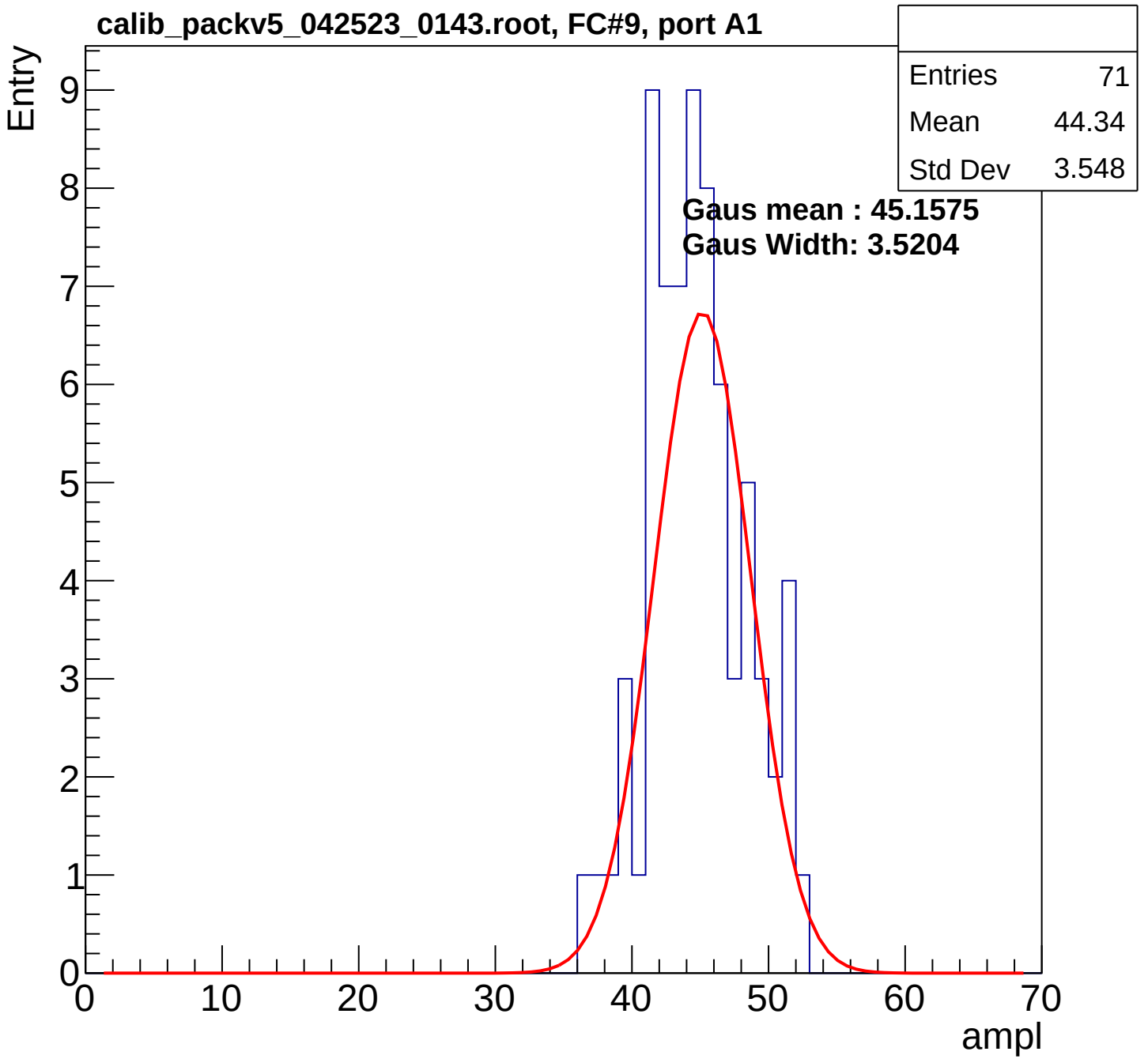
B0L001S, U20-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.34
Std Dev 3.548
Gaus mean : 45.1575
Gaus Width: 3.5204
Entry
ampl
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2
3
4
5
6
7
8
9
0
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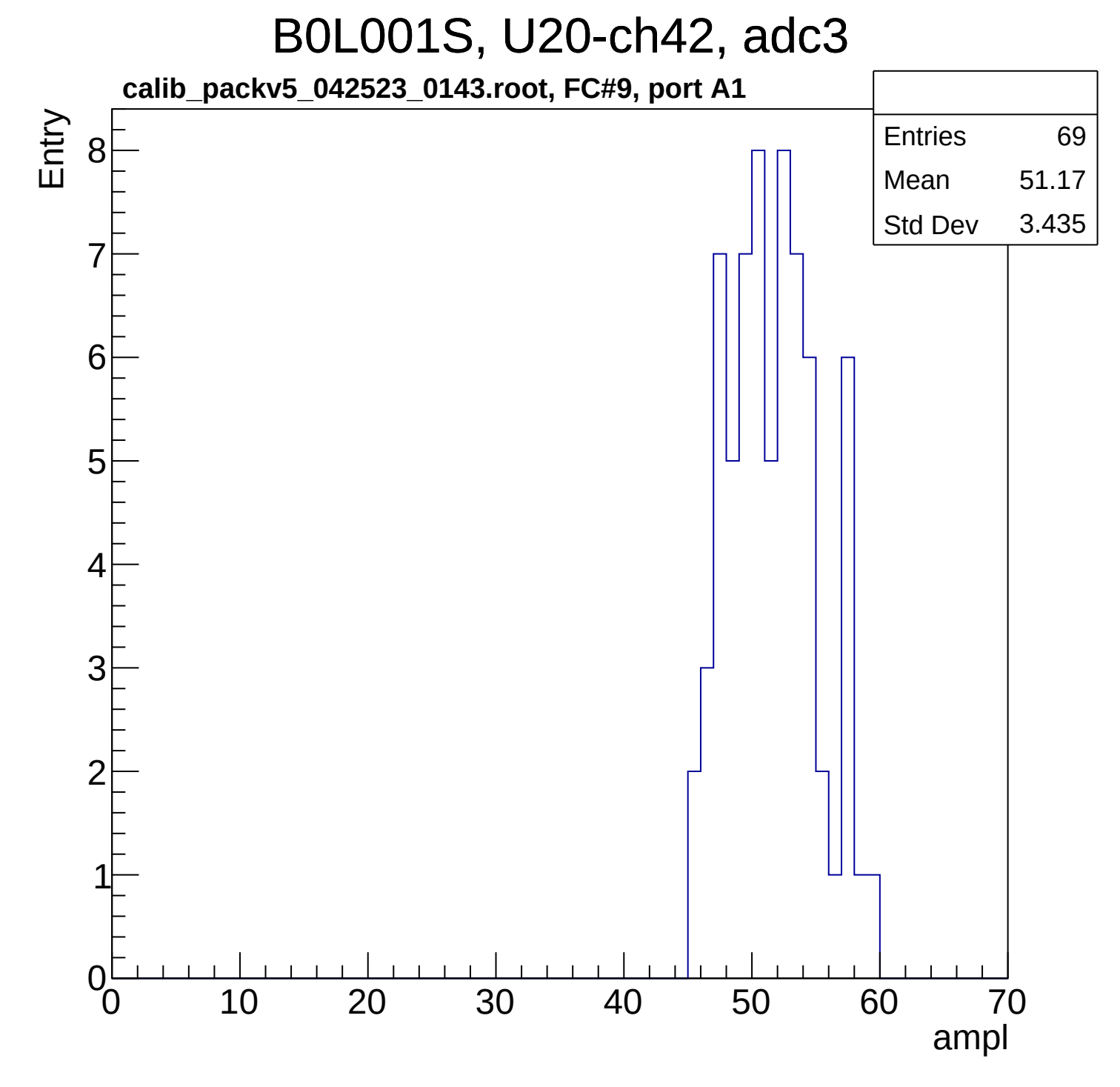
B0L001S, U20-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.17
Std Dev 3.435
Entry
ampl
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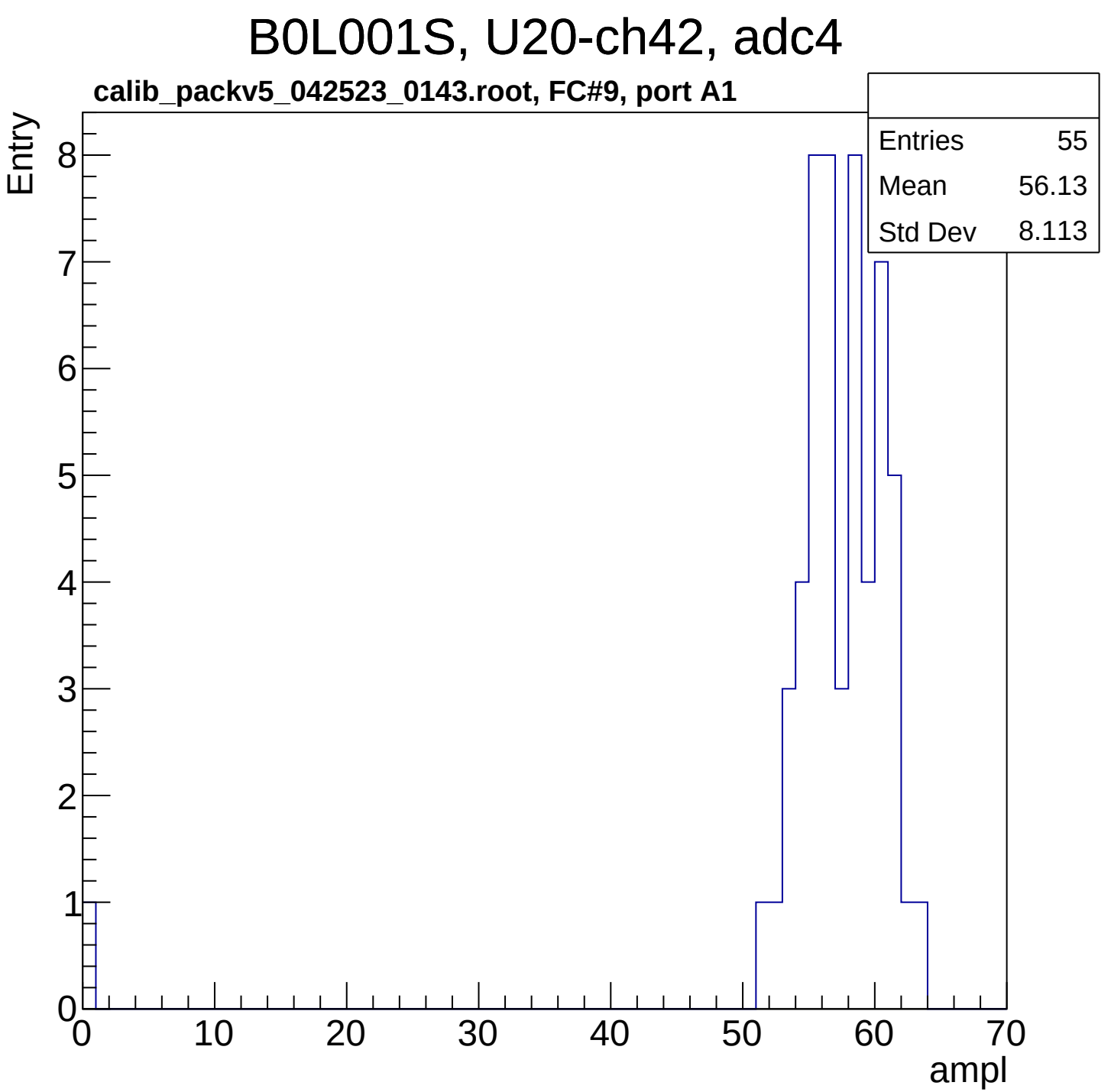
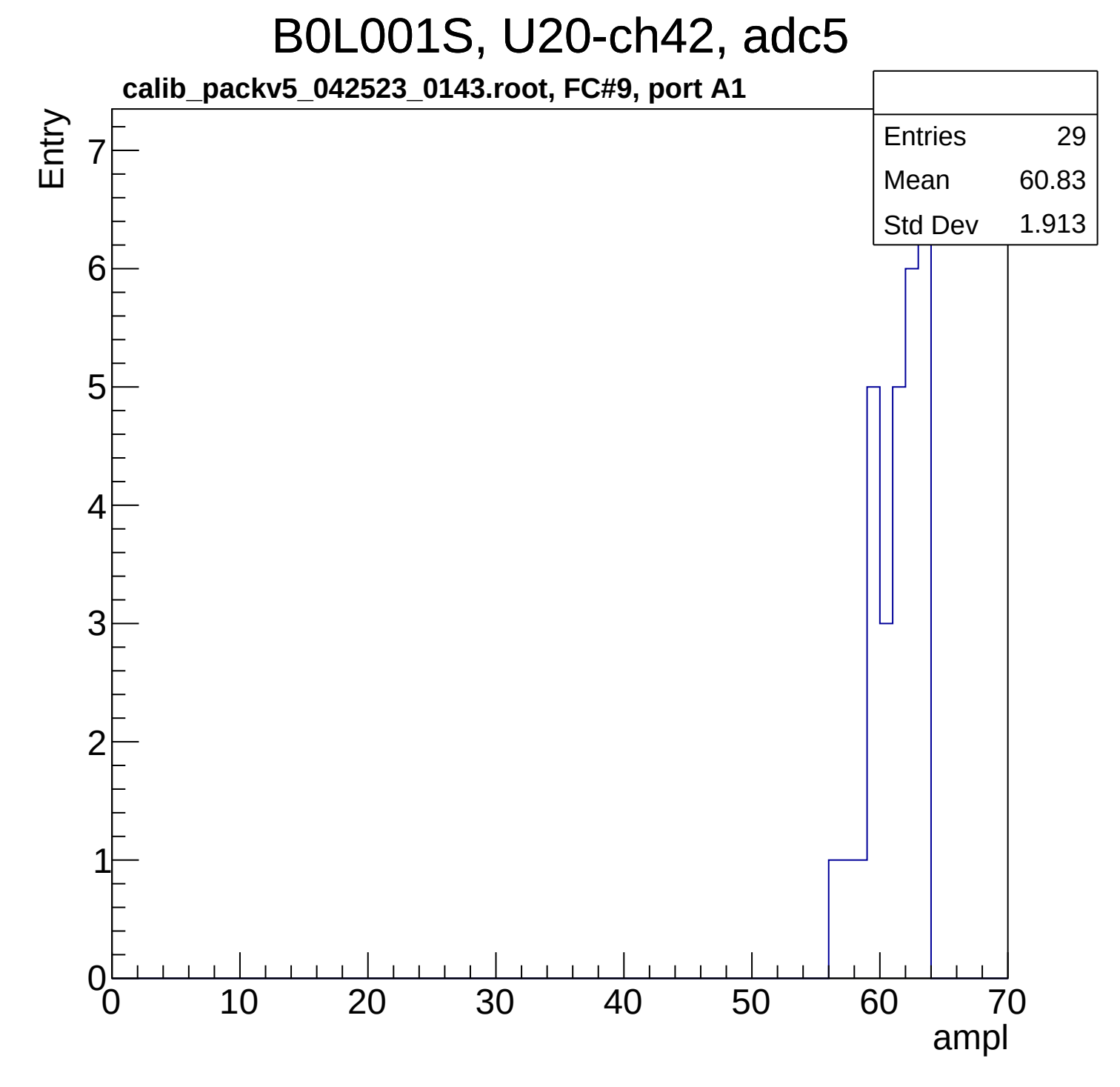
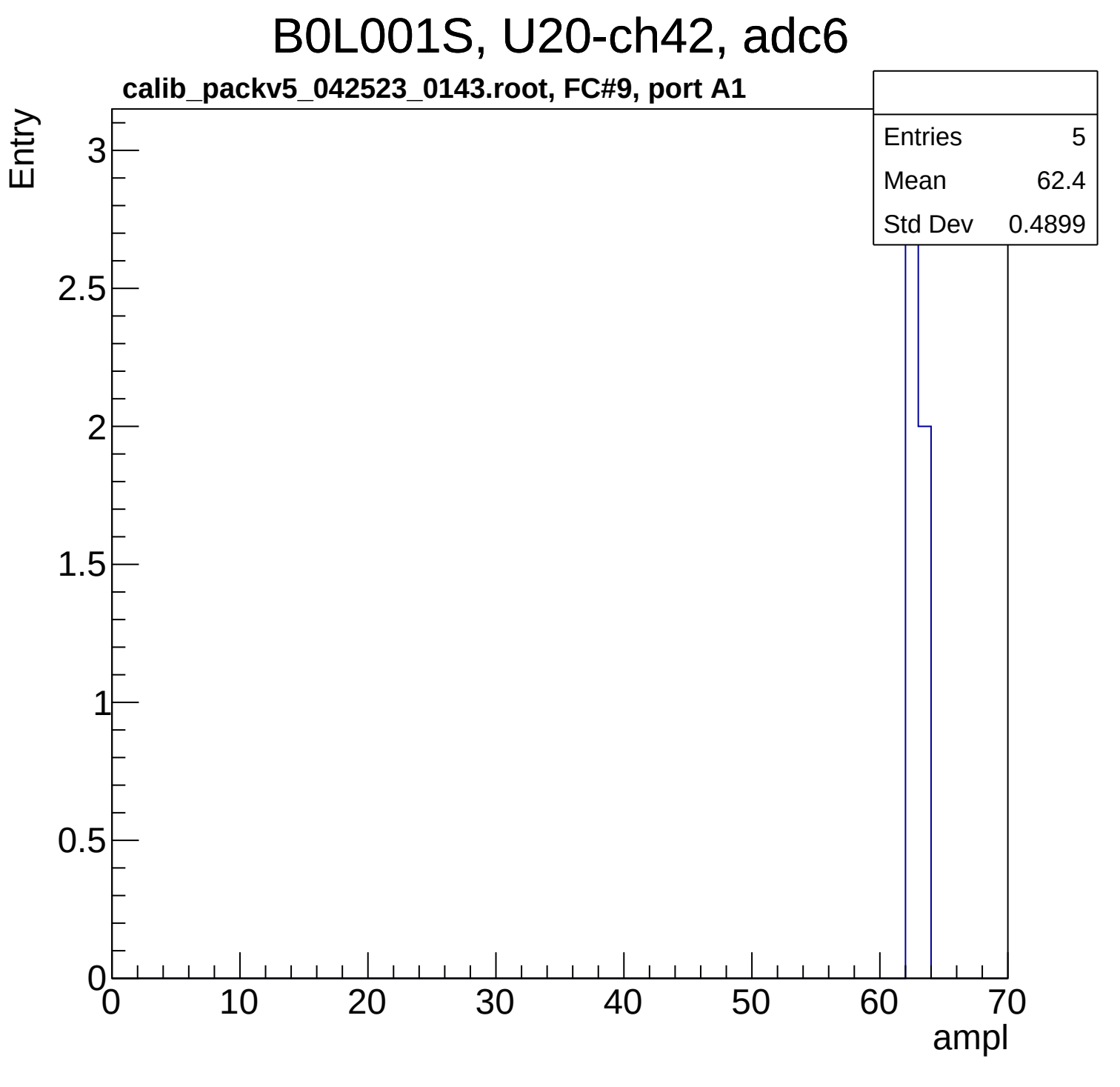
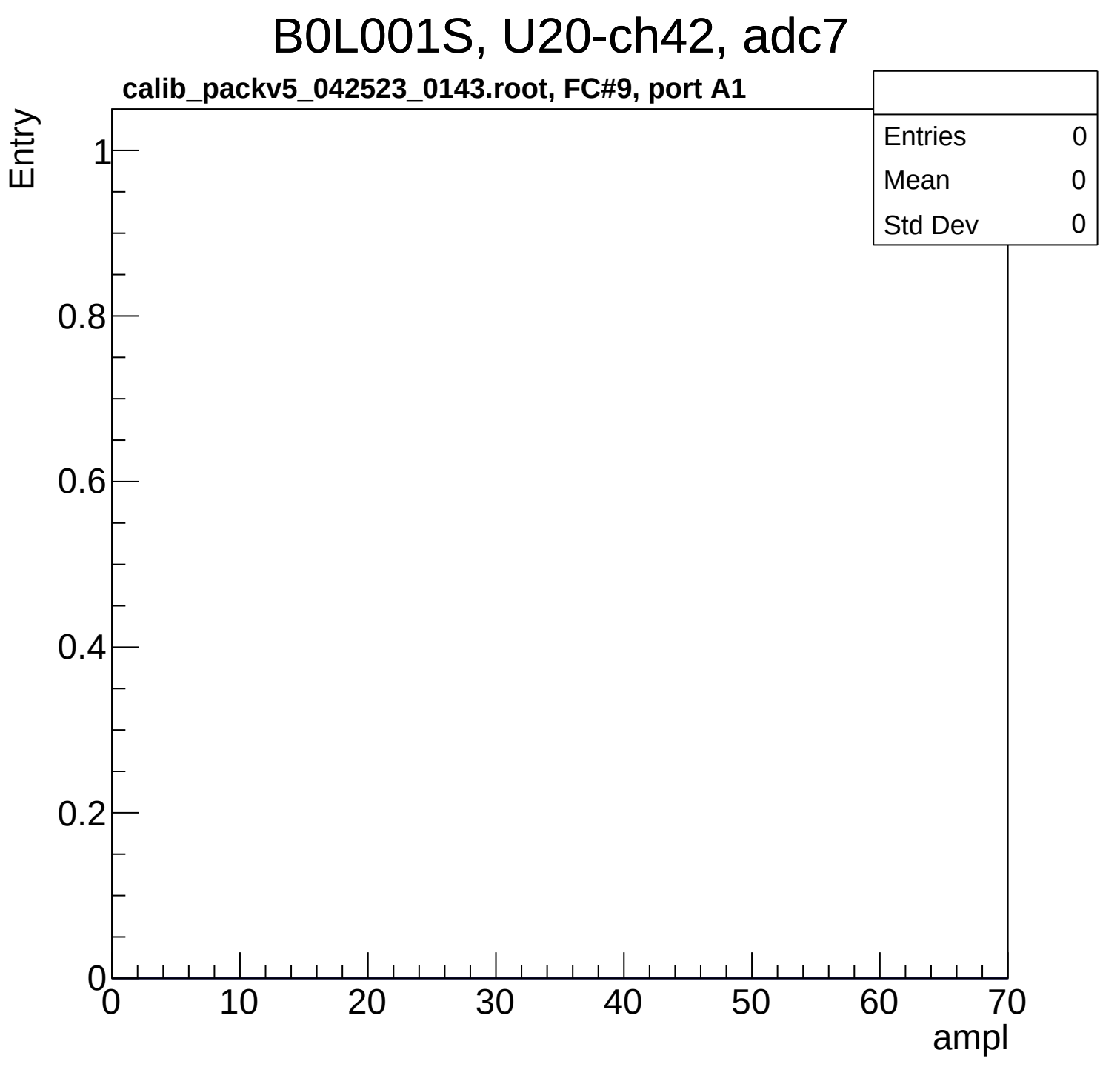
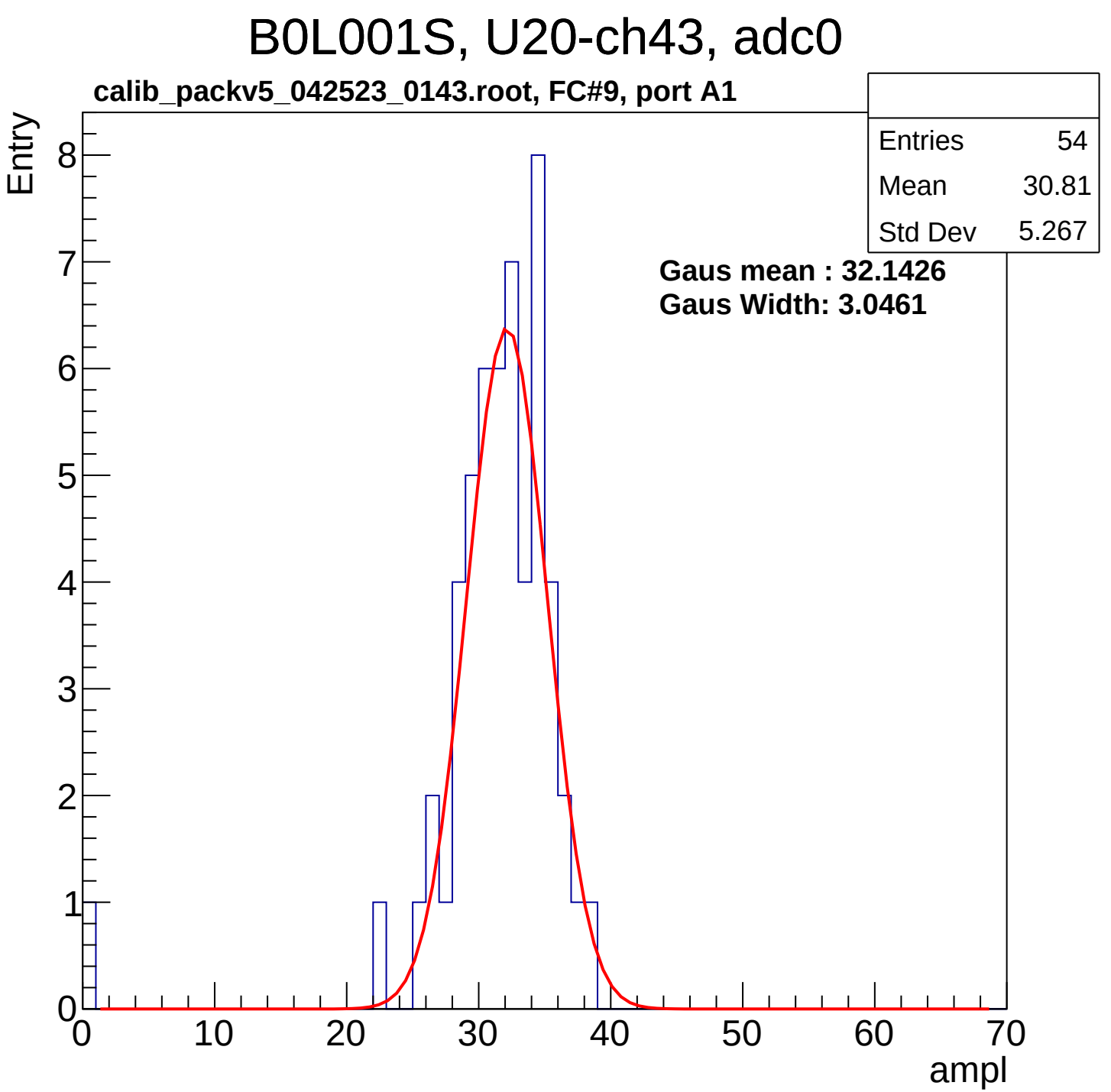
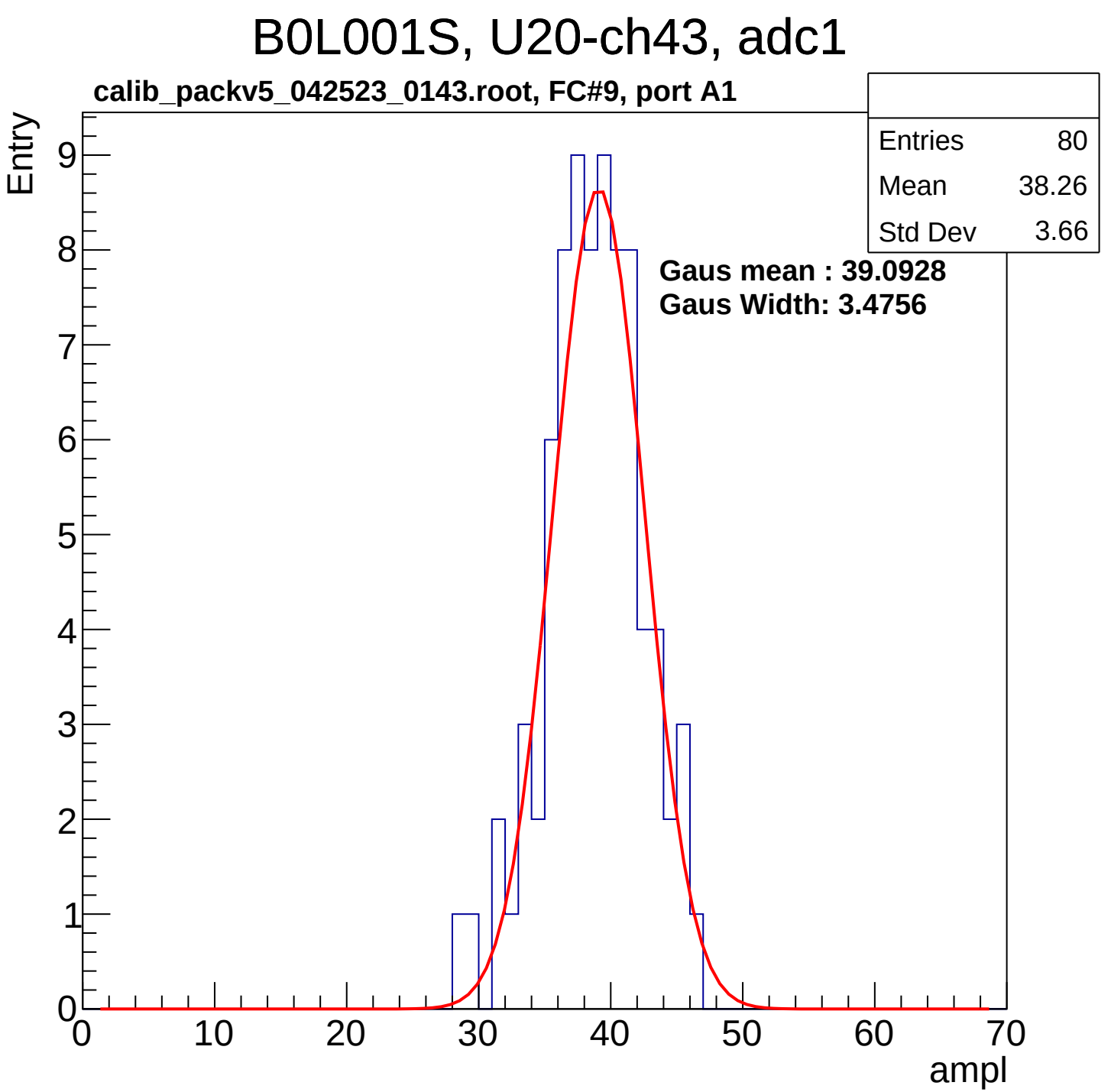

B0L001S, U20-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.13
Std Dev 8.113
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.83
Std Dev 1.913
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 30.81
Std Dev 5.267
Gaus mean : 32.1426
Gaus Width: 3.0461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 38.26
Std Dev 3.66
Gaus mean : 39.0928
Gaus Width: 3.4756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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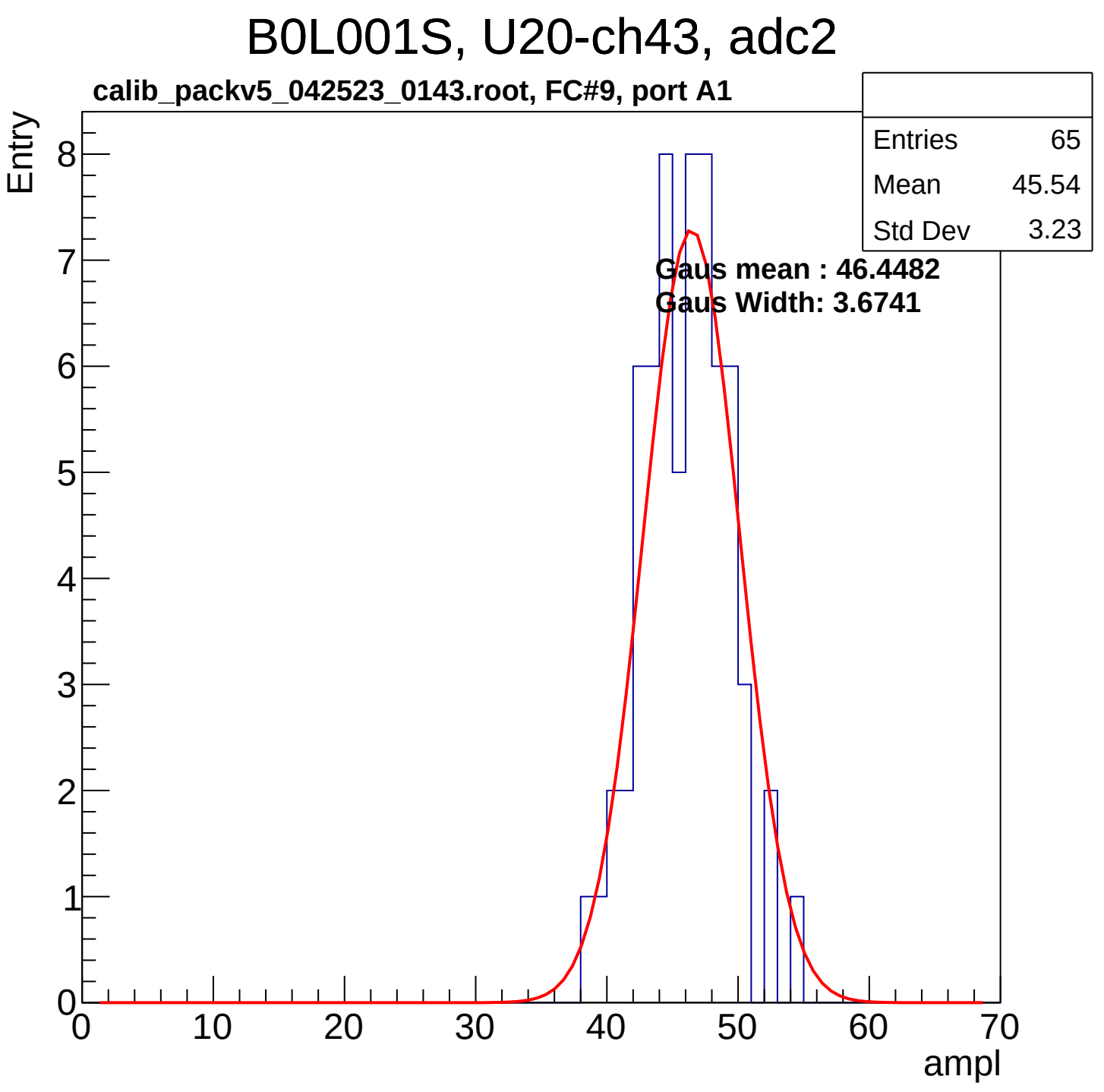
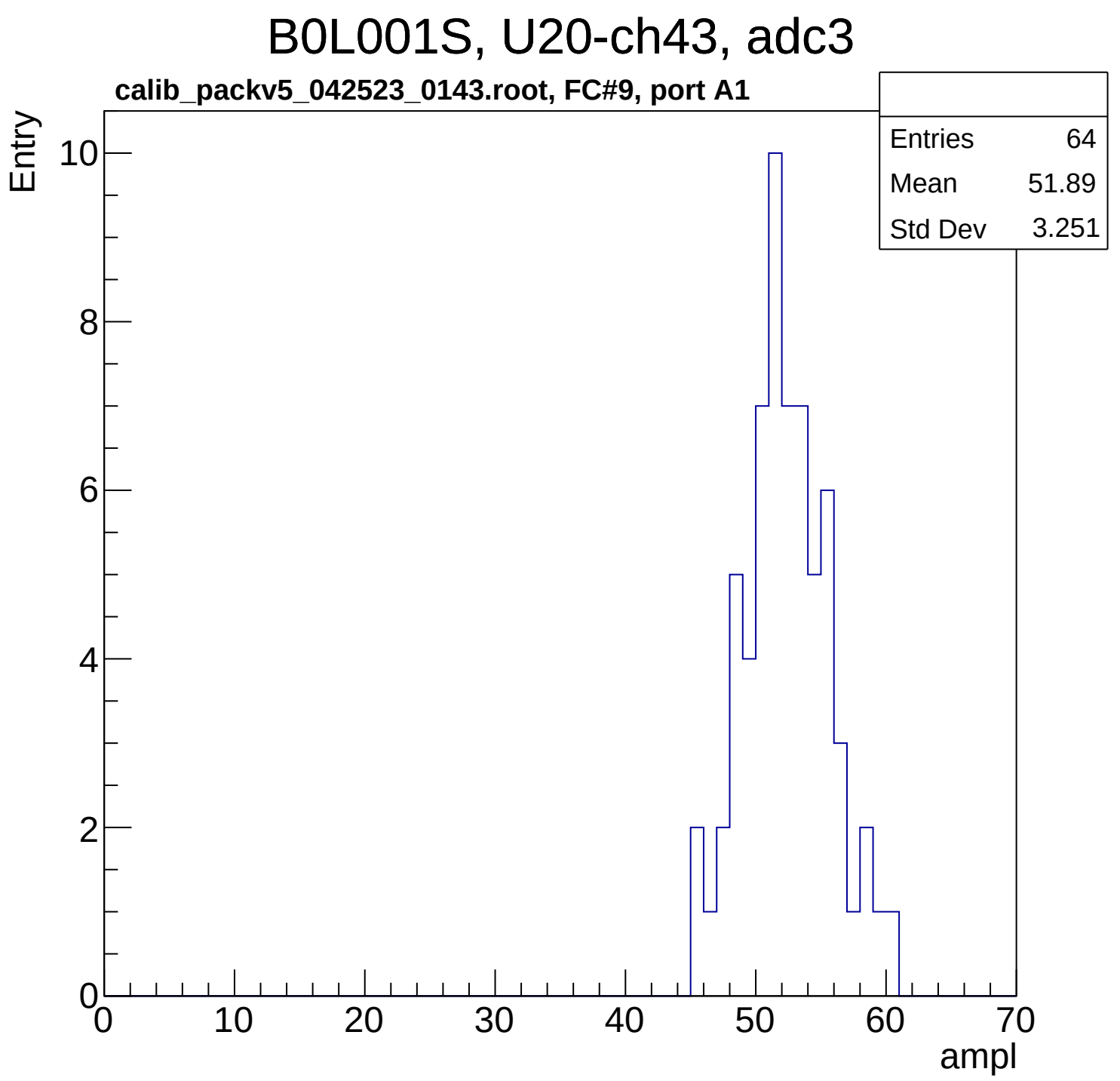
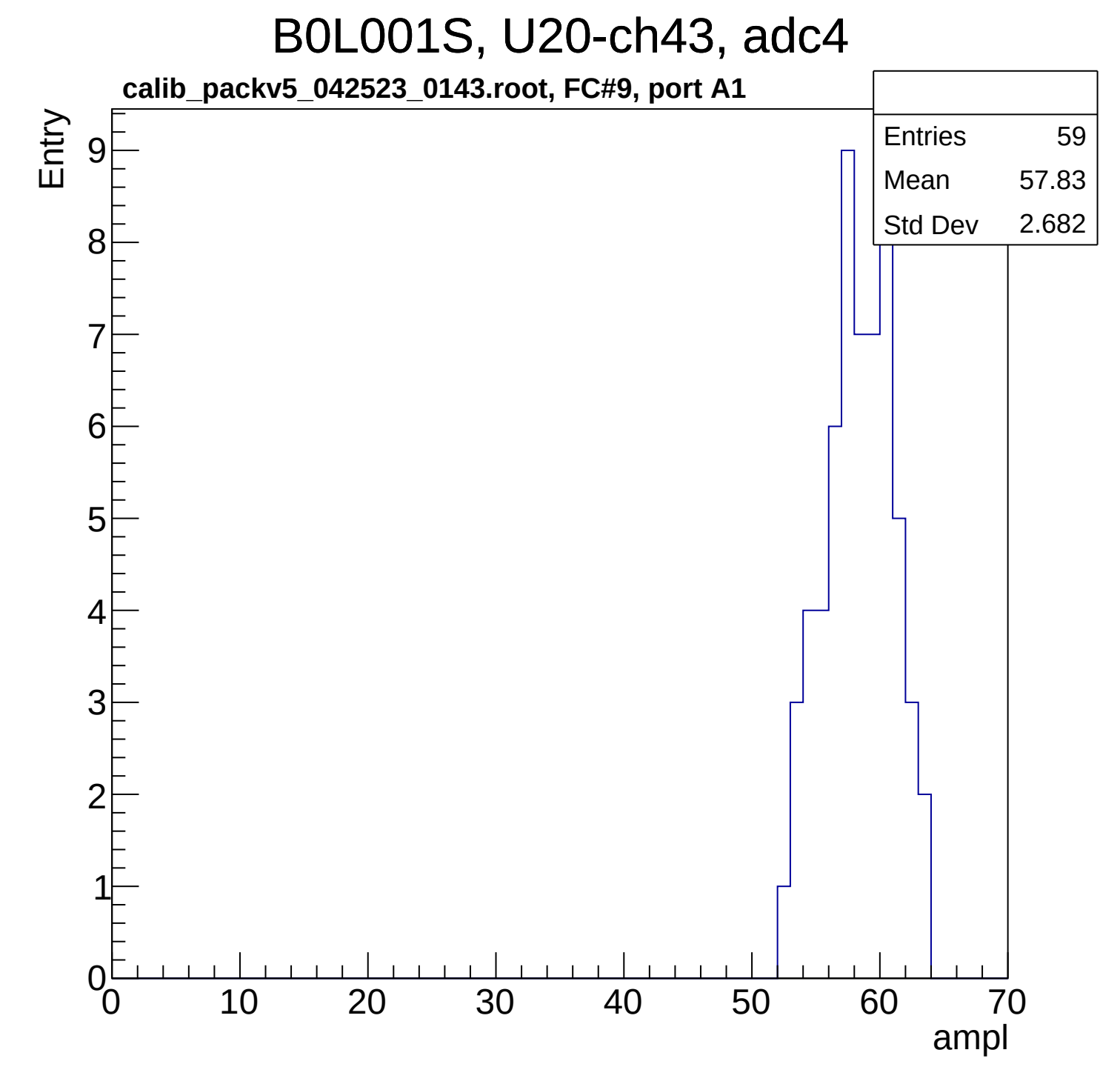
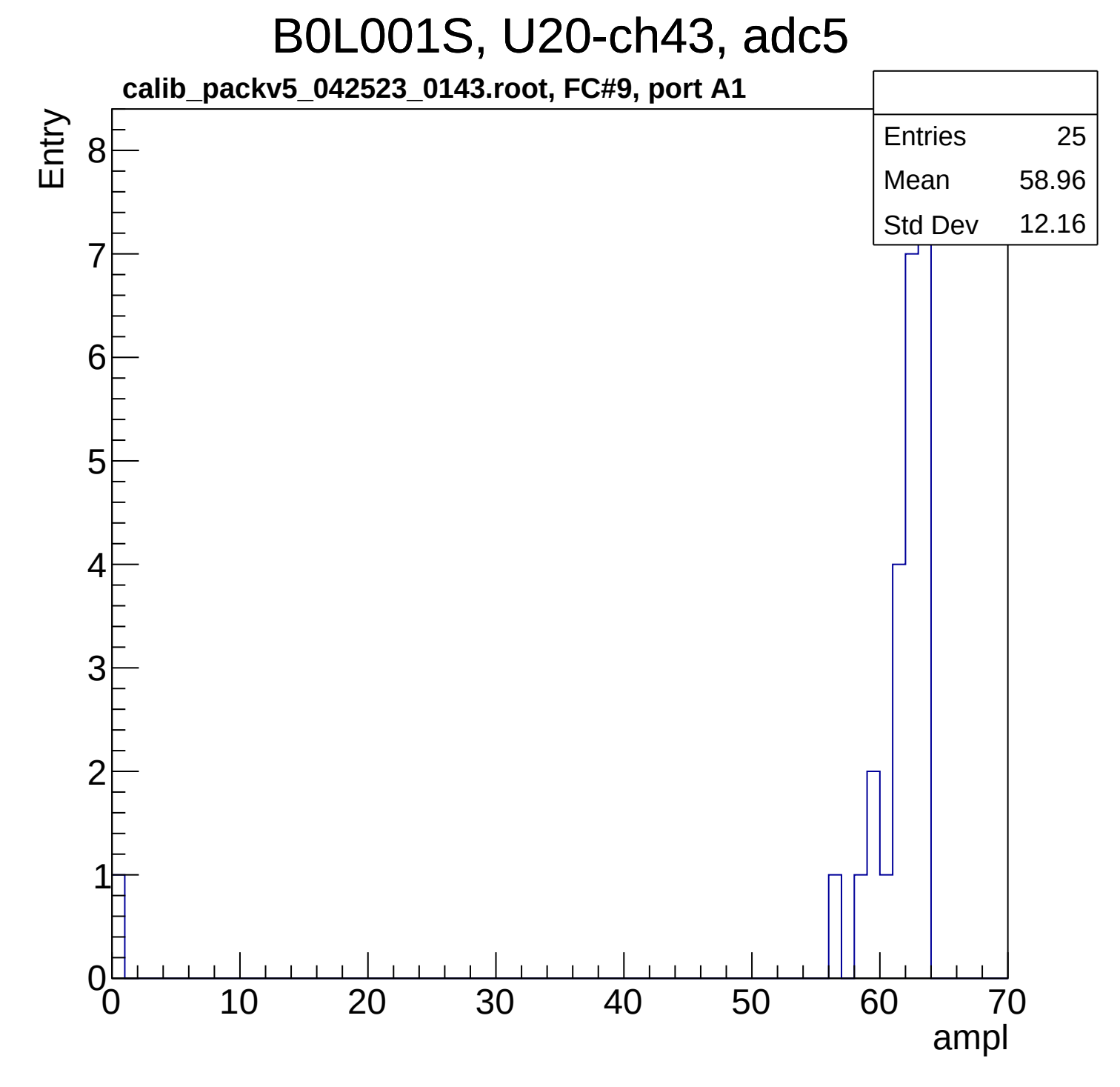
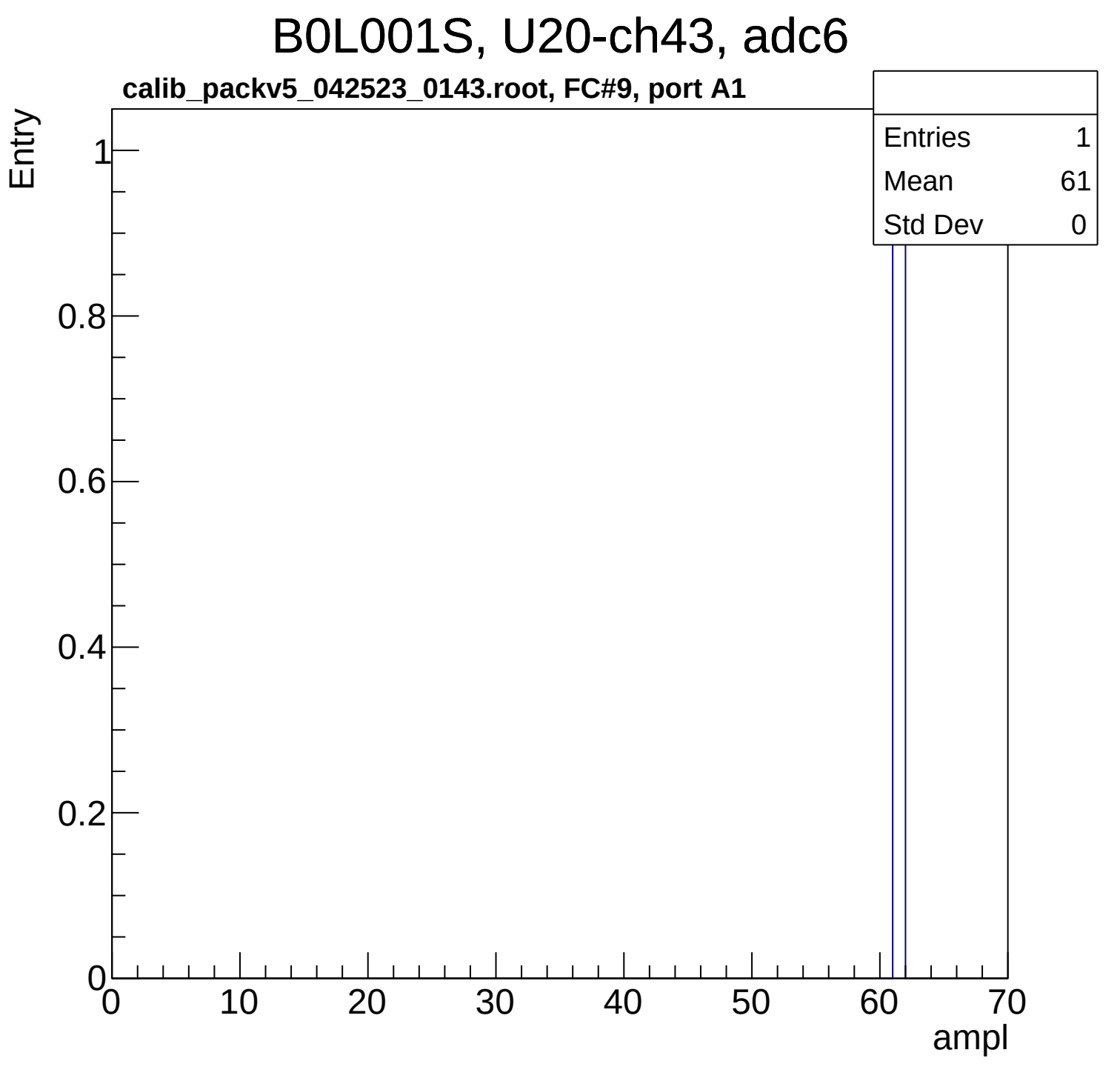
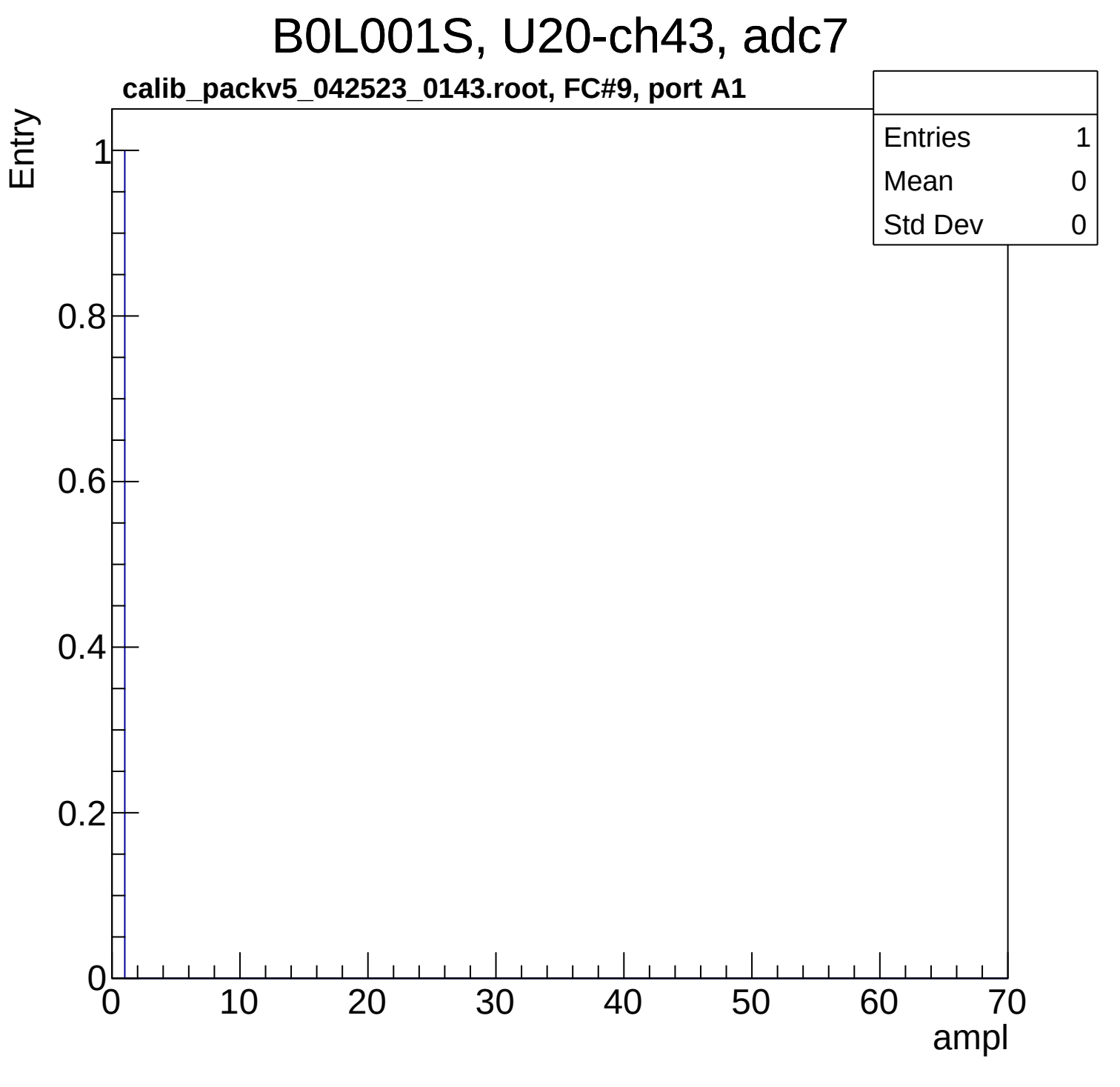

B0L001S, U20-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.54
Std Dev 3.23
Gaus mean : 46.4482
Gaus Width: 3.6741
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

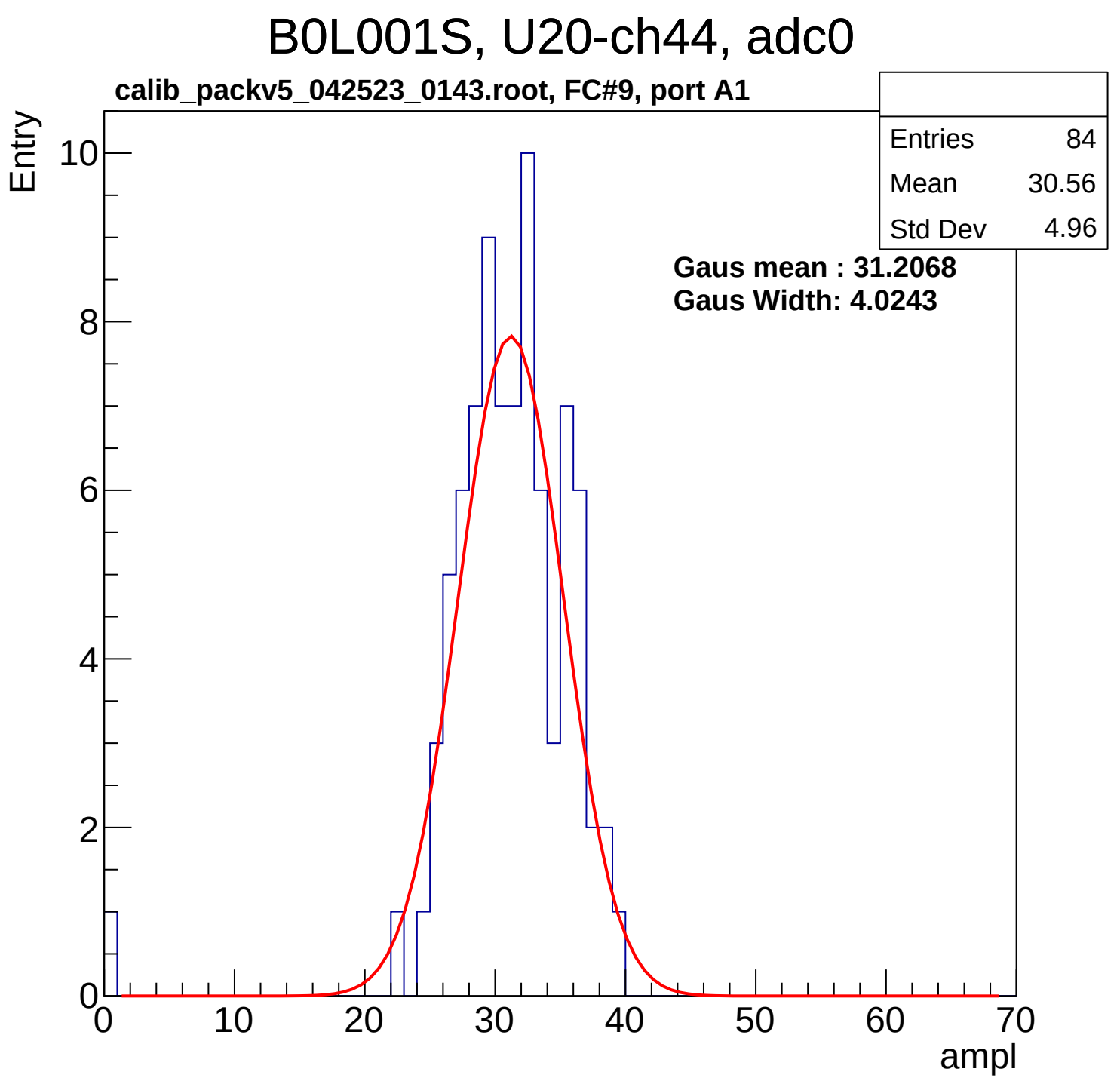

B0L001S, U20-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.89
Std Dev 3.251
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

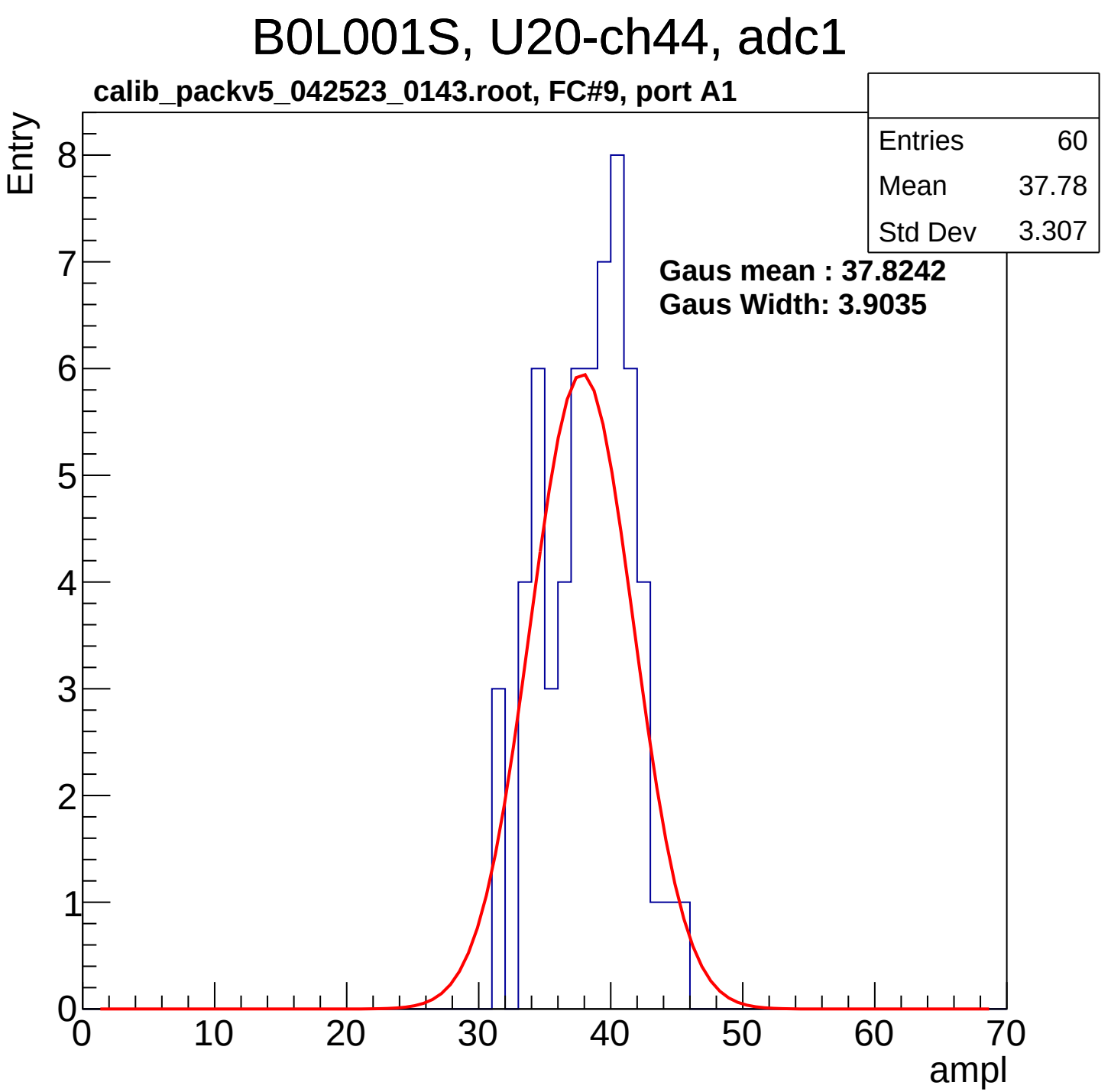

B0L001S, U20-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.83
Std Dev 2.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

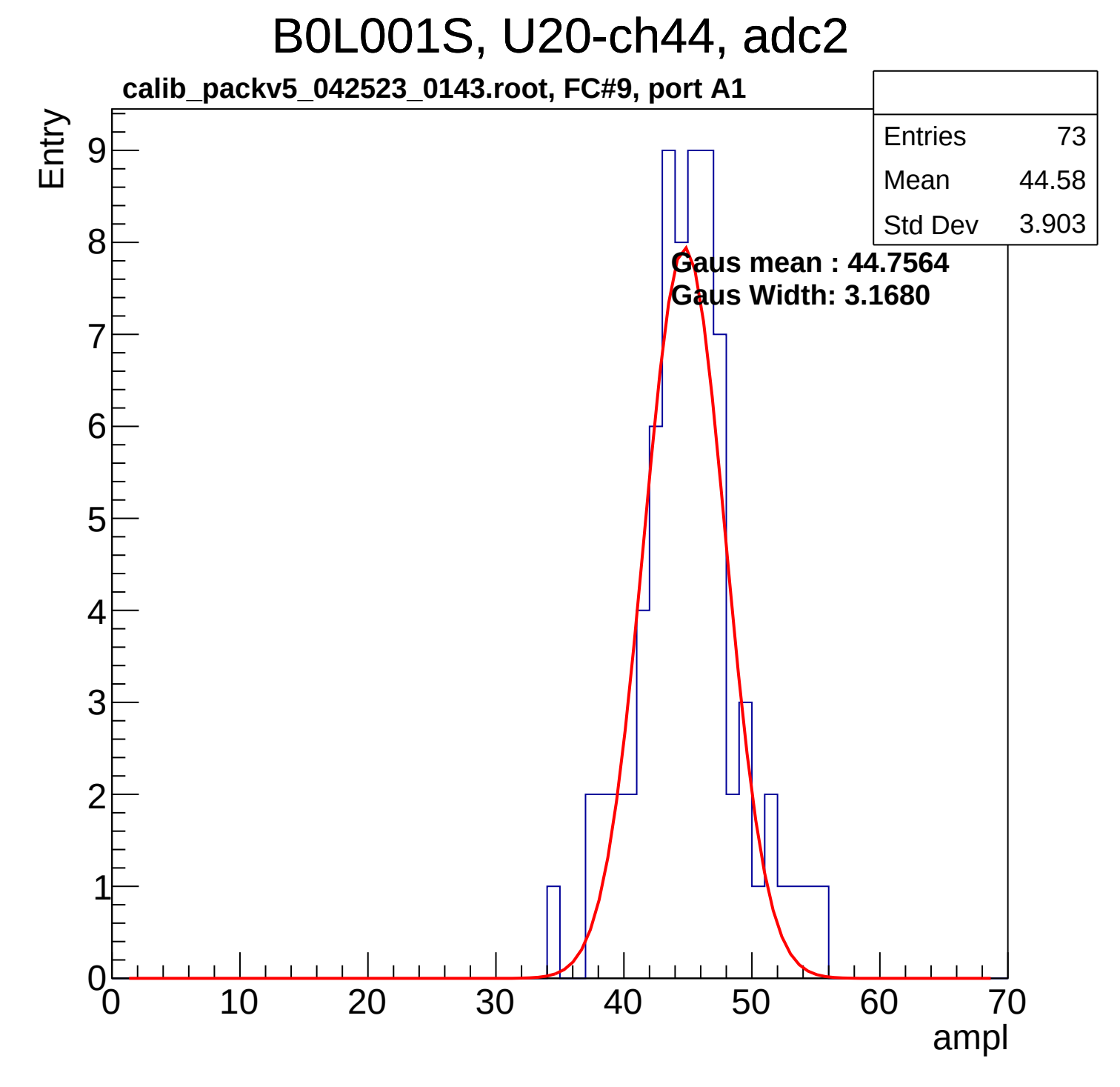

B0L001S, U20-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.96
Std Dev 12.16
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

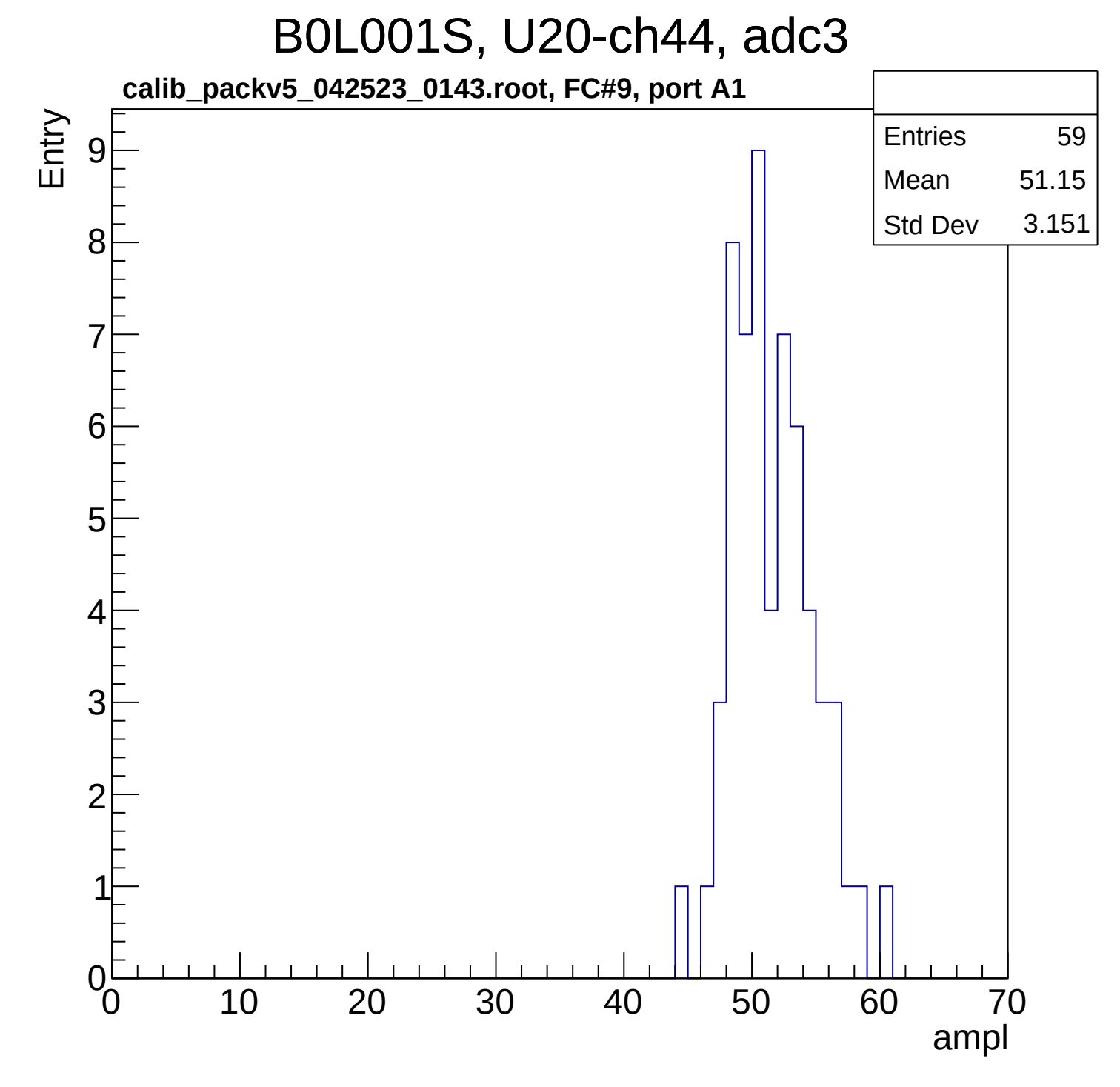

B0L001S, U20-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 30.56
Std Dev 4.96
Gaus mean : 31.2068
Gaus Width: 4.0243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.78
Std Dev 3.307
Gaus mean : 37.8242
Gaus Width: 3.9035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.58
Std Dev 3.903
Gaus mean : 44.7564
Gaus Width: 3.1680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.15
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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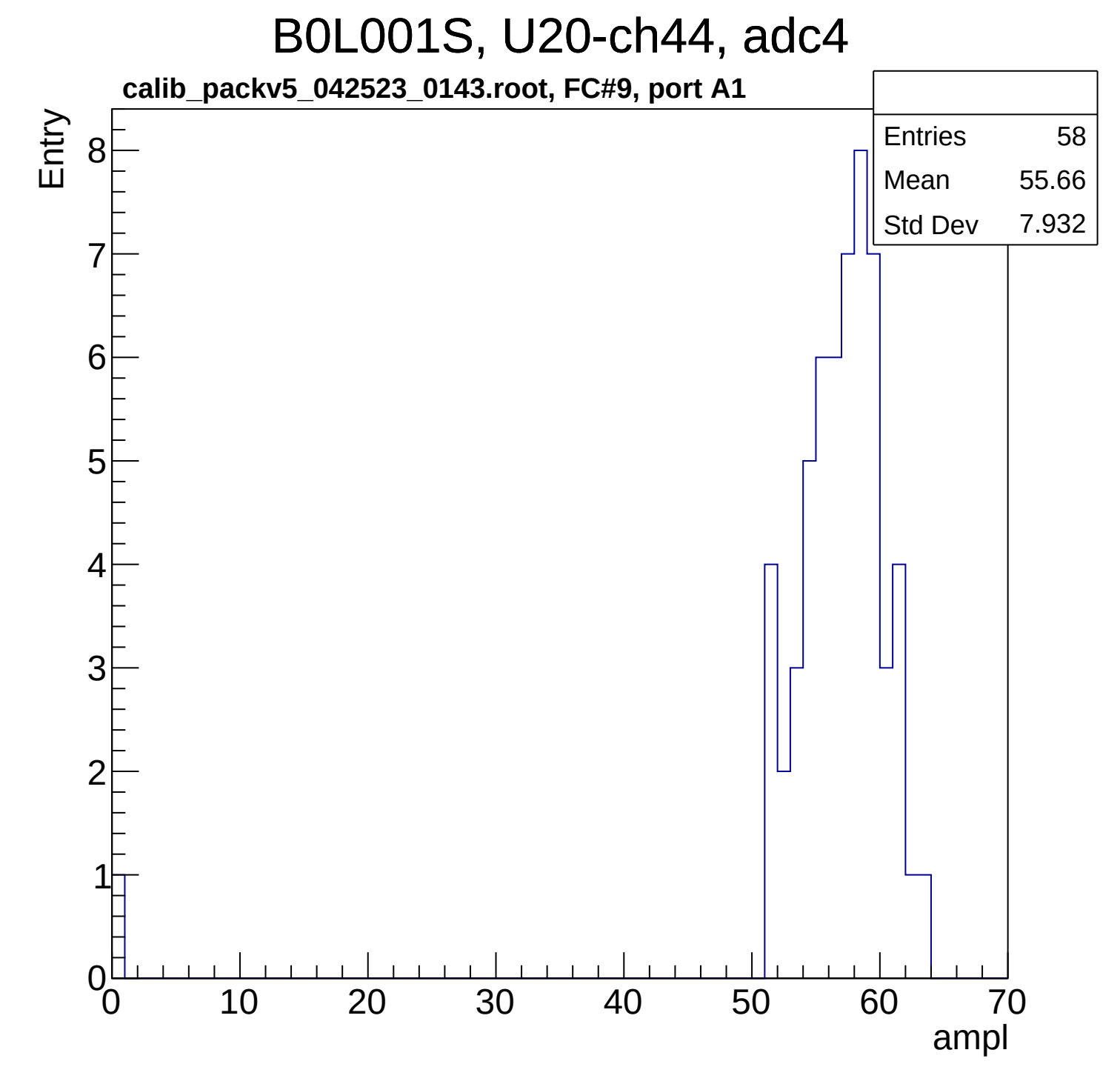
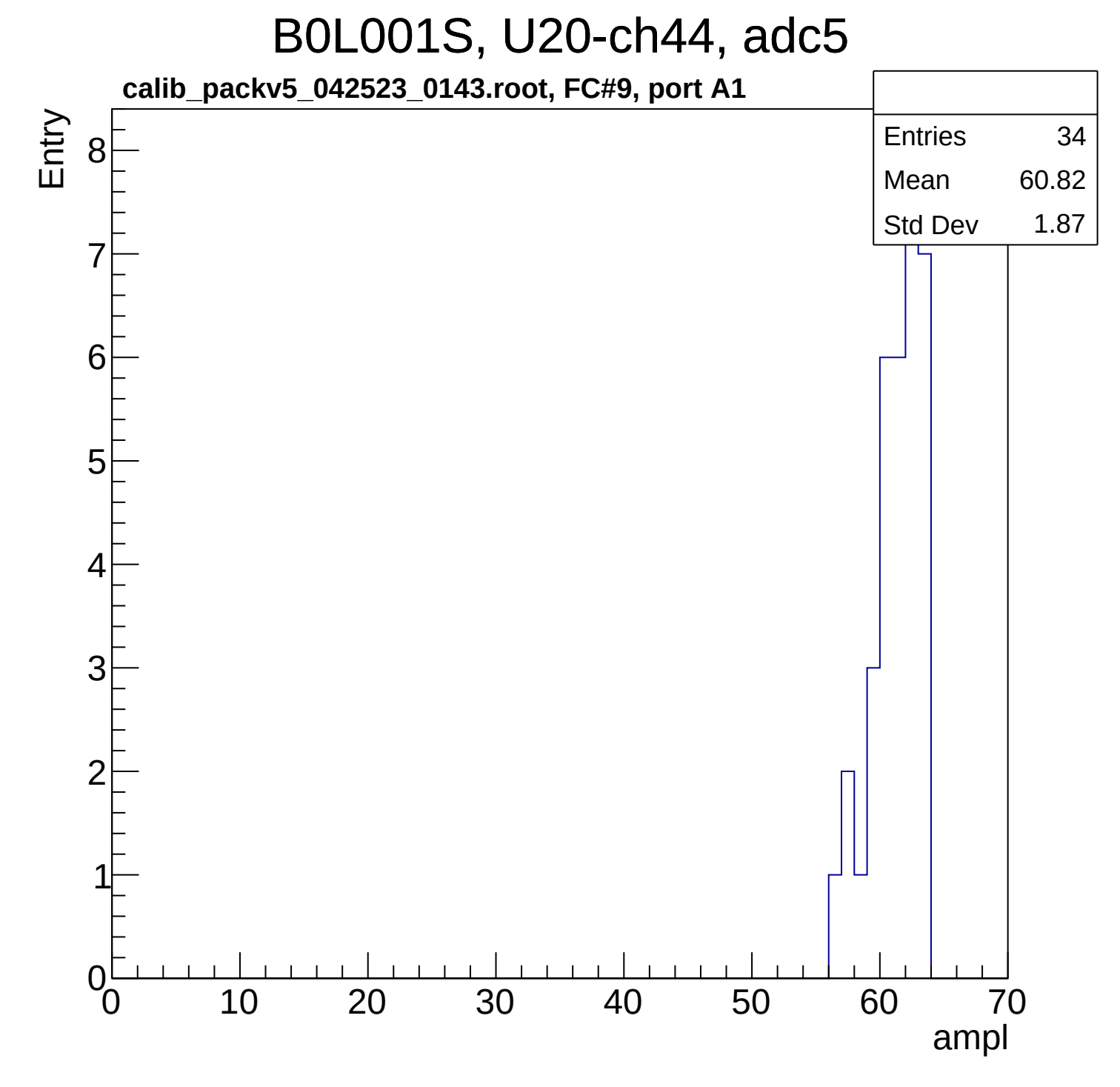
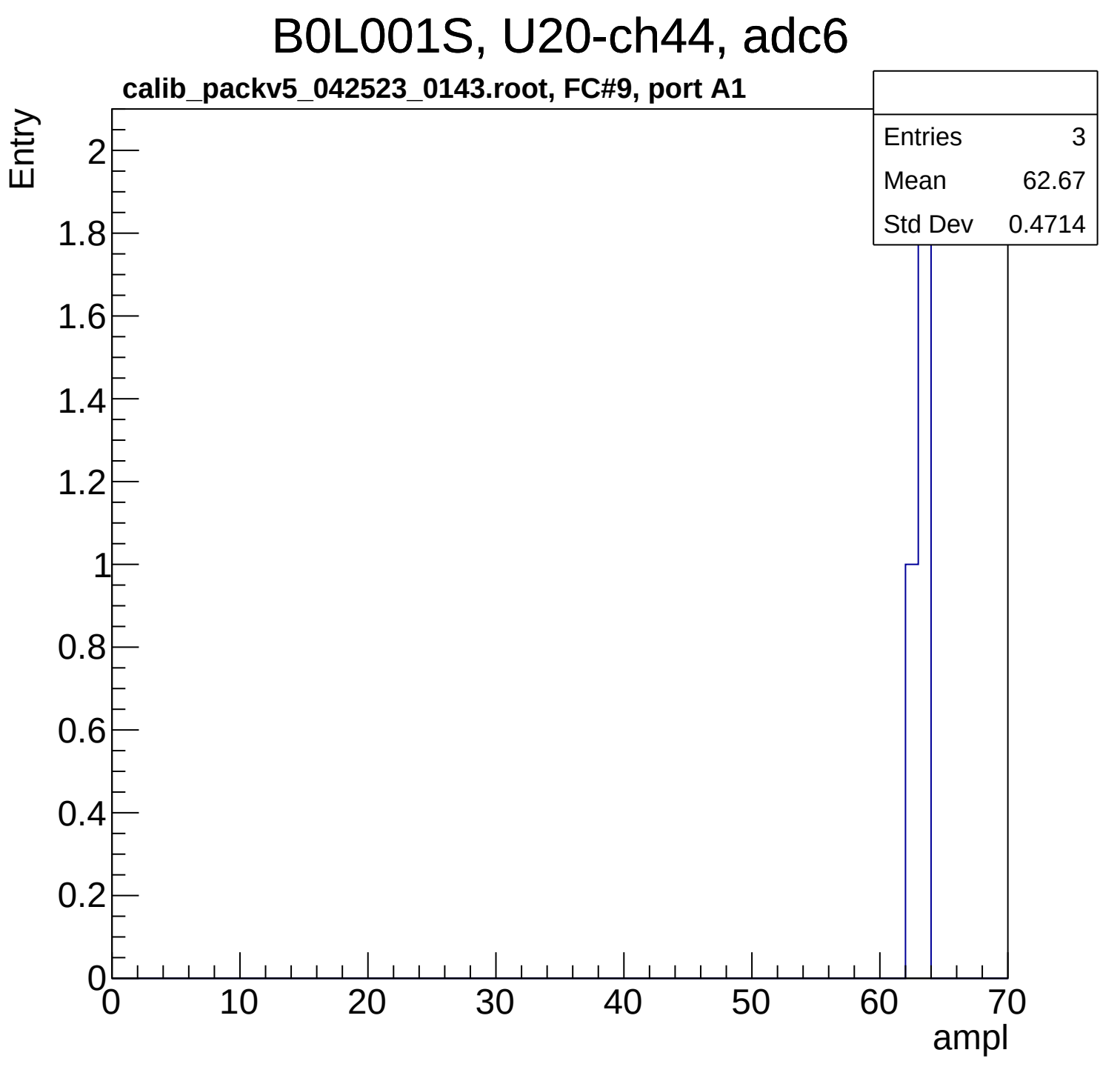
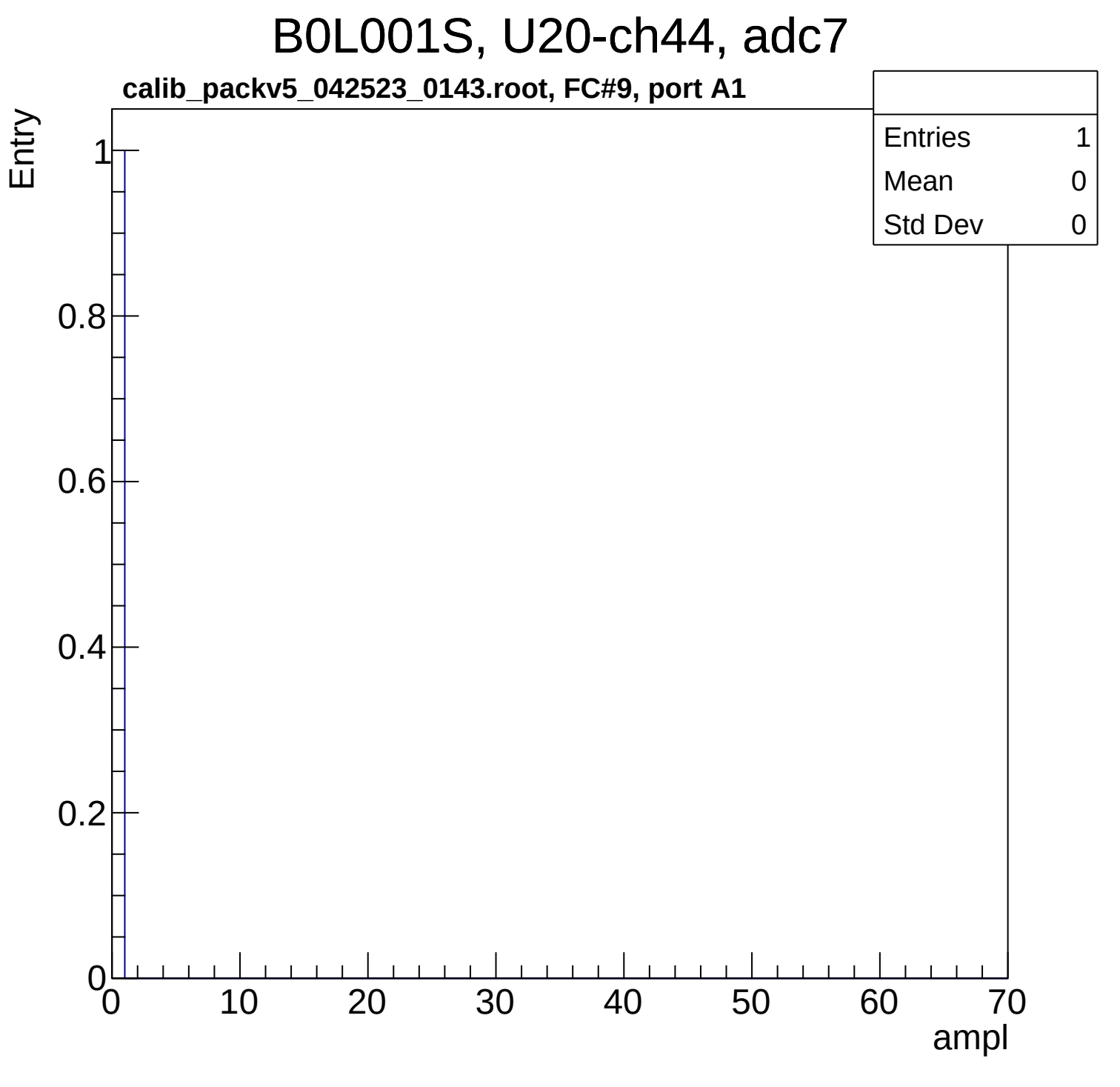

B0L001S, U20-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.66
Std Dev 7.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.82
Std Dev 1.87
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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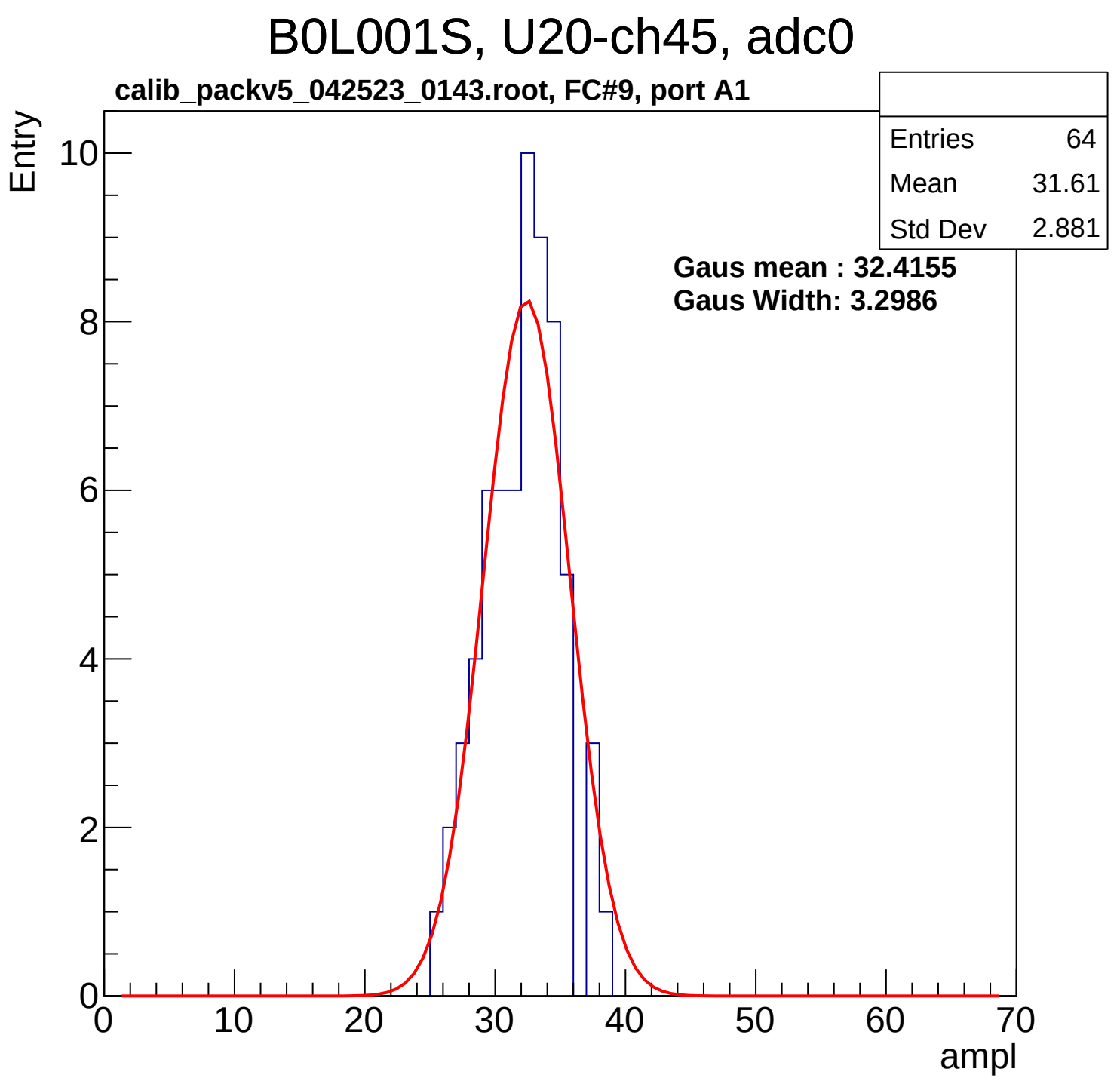
50
60
70
ampl


B0L001S, U20-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 31.61
Std Dev 2.881
Gaus mean : 32.4155
Gaus Width: 3.2986
Entry
ampl
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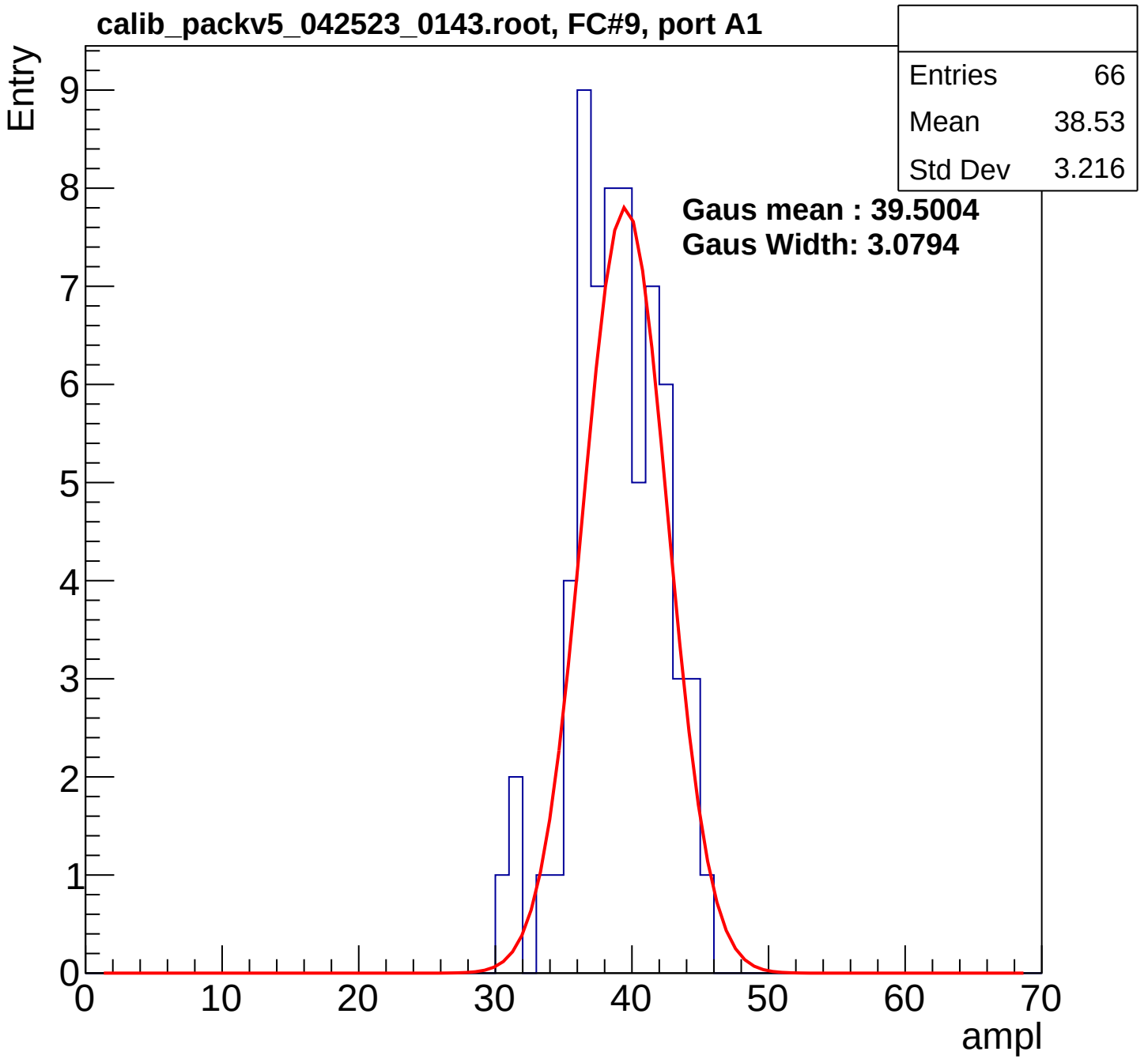
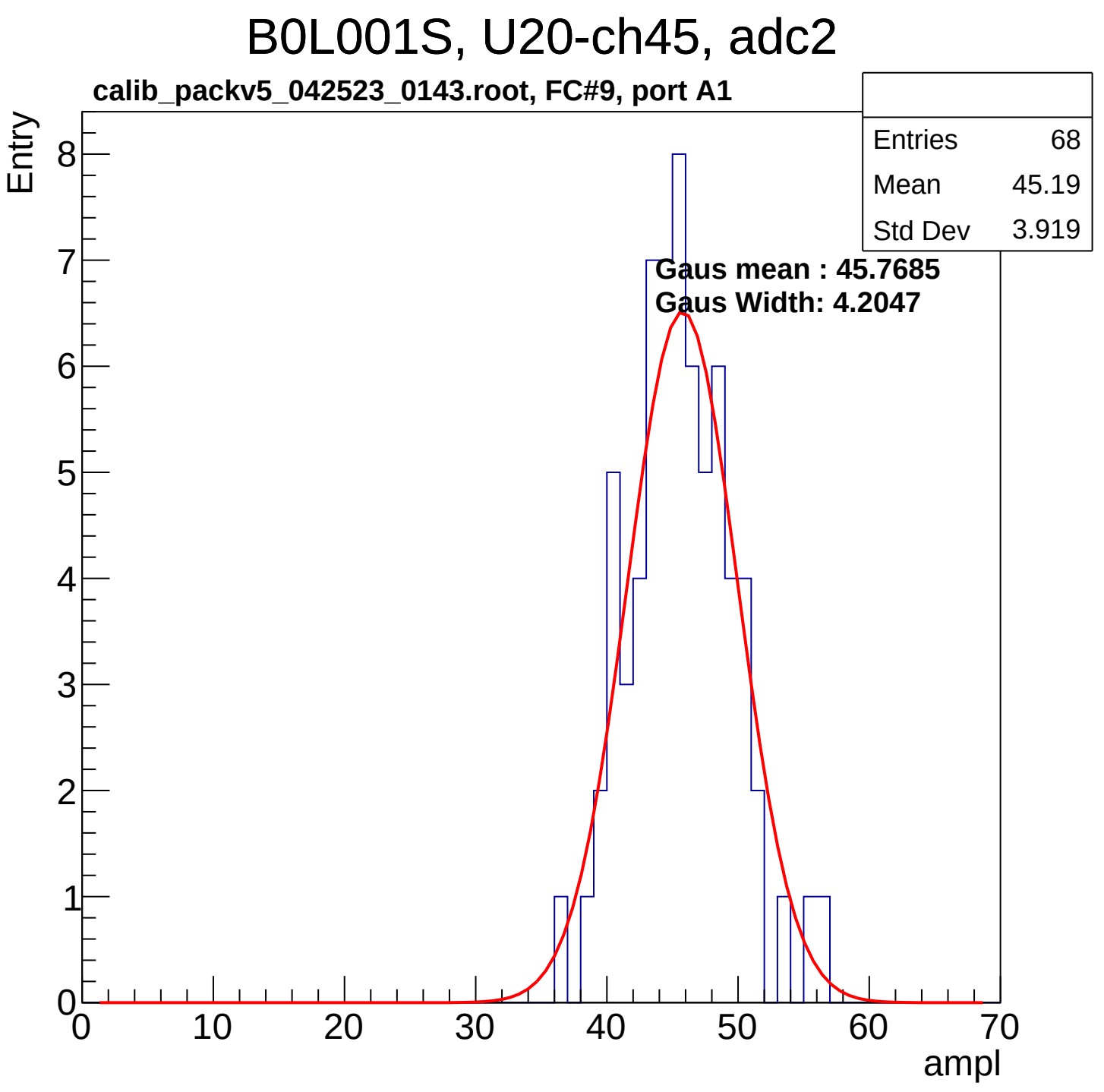
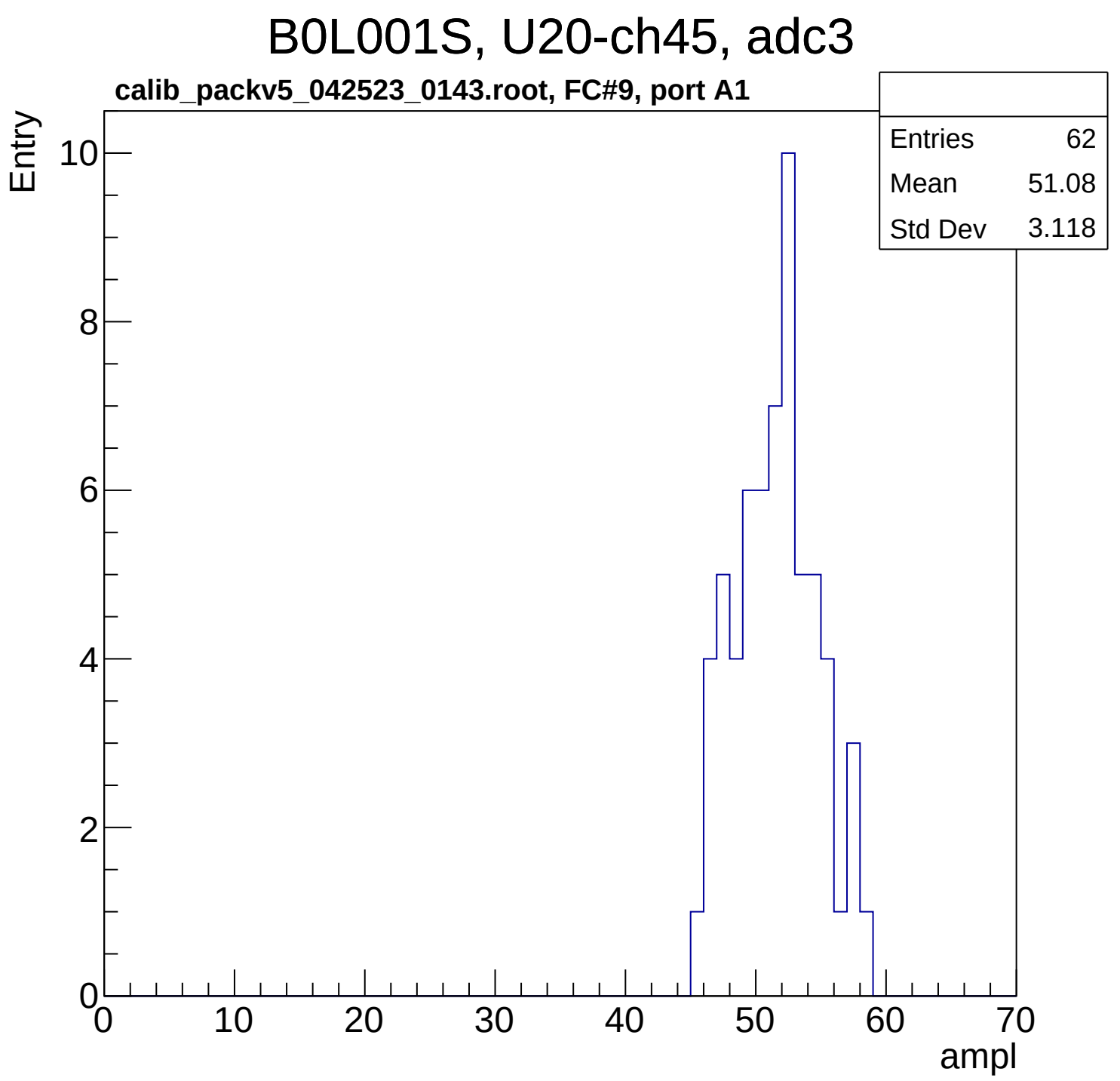
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.53
Std Dev 3.216
Gaus mean : 39.5004
Gaus Width: 3.0794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 45.19
Std Dev 3.919
Gaus mean : 45.7685
Gaus Width: 4.2047
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.08
Std Dev 3.118
Entry
ampl
0
2
4
6
8
10
0
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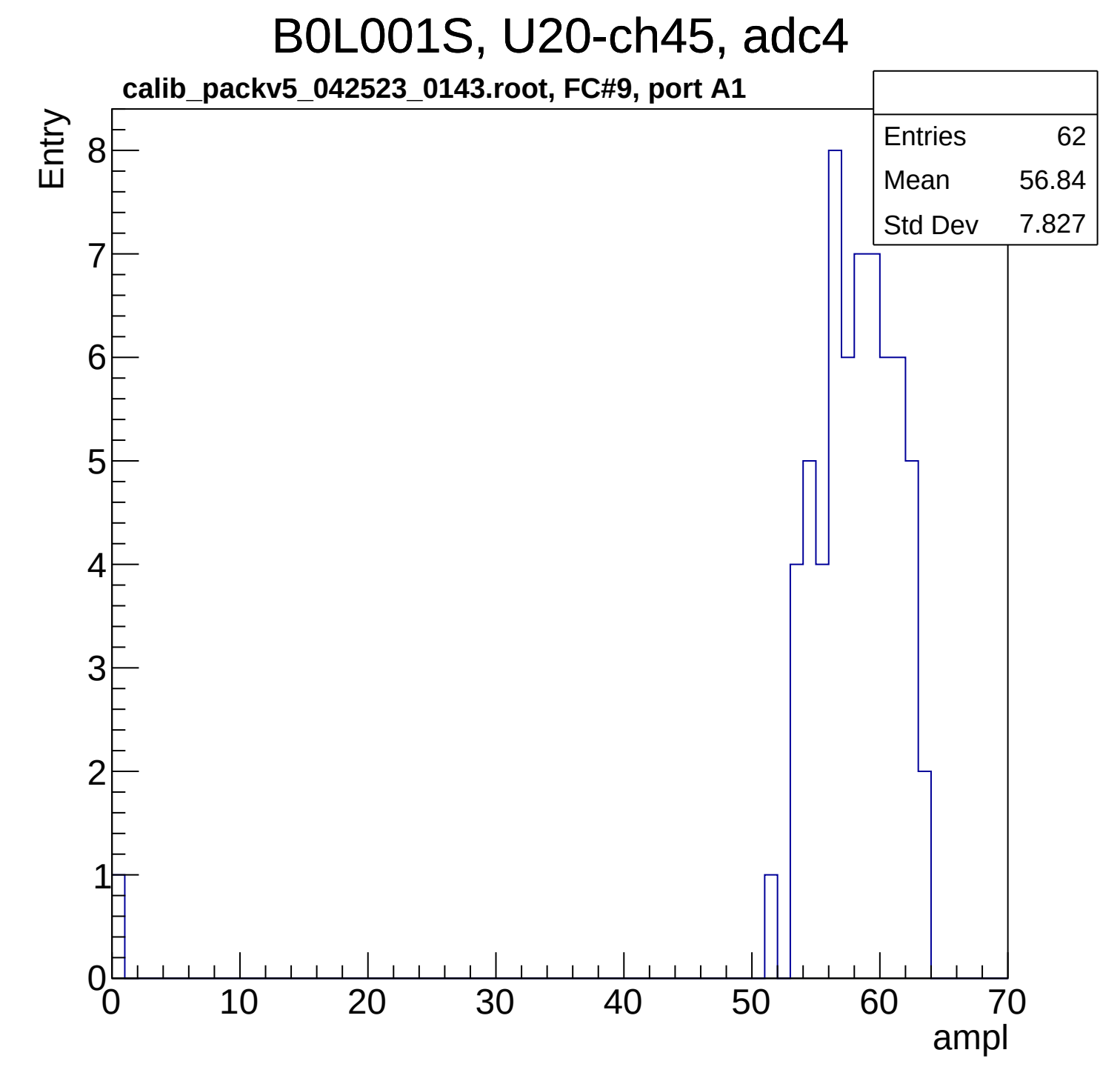
20
30
40
50
60
70


B0L001S, U20-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.84
Std Dev 7.827
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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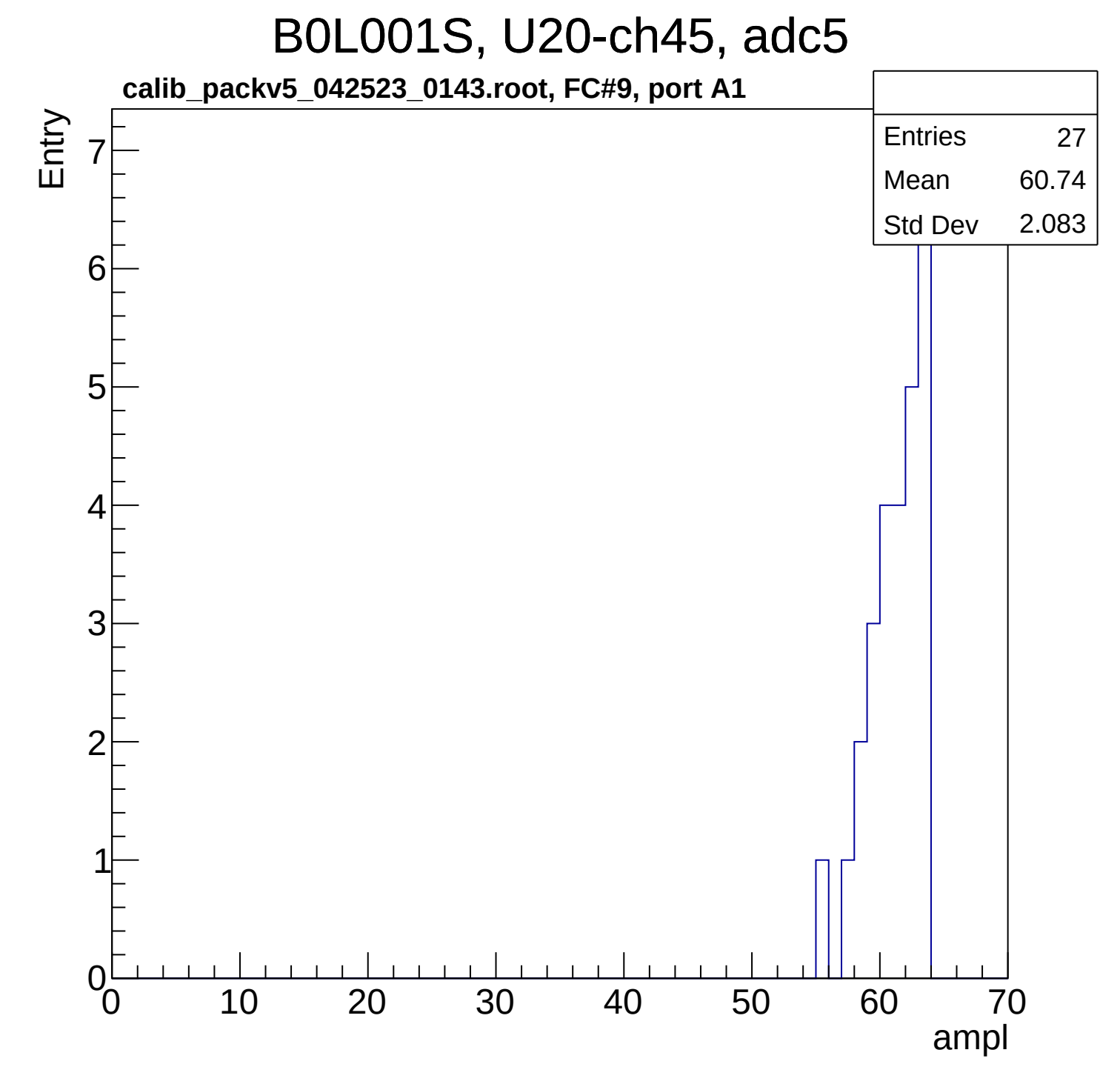
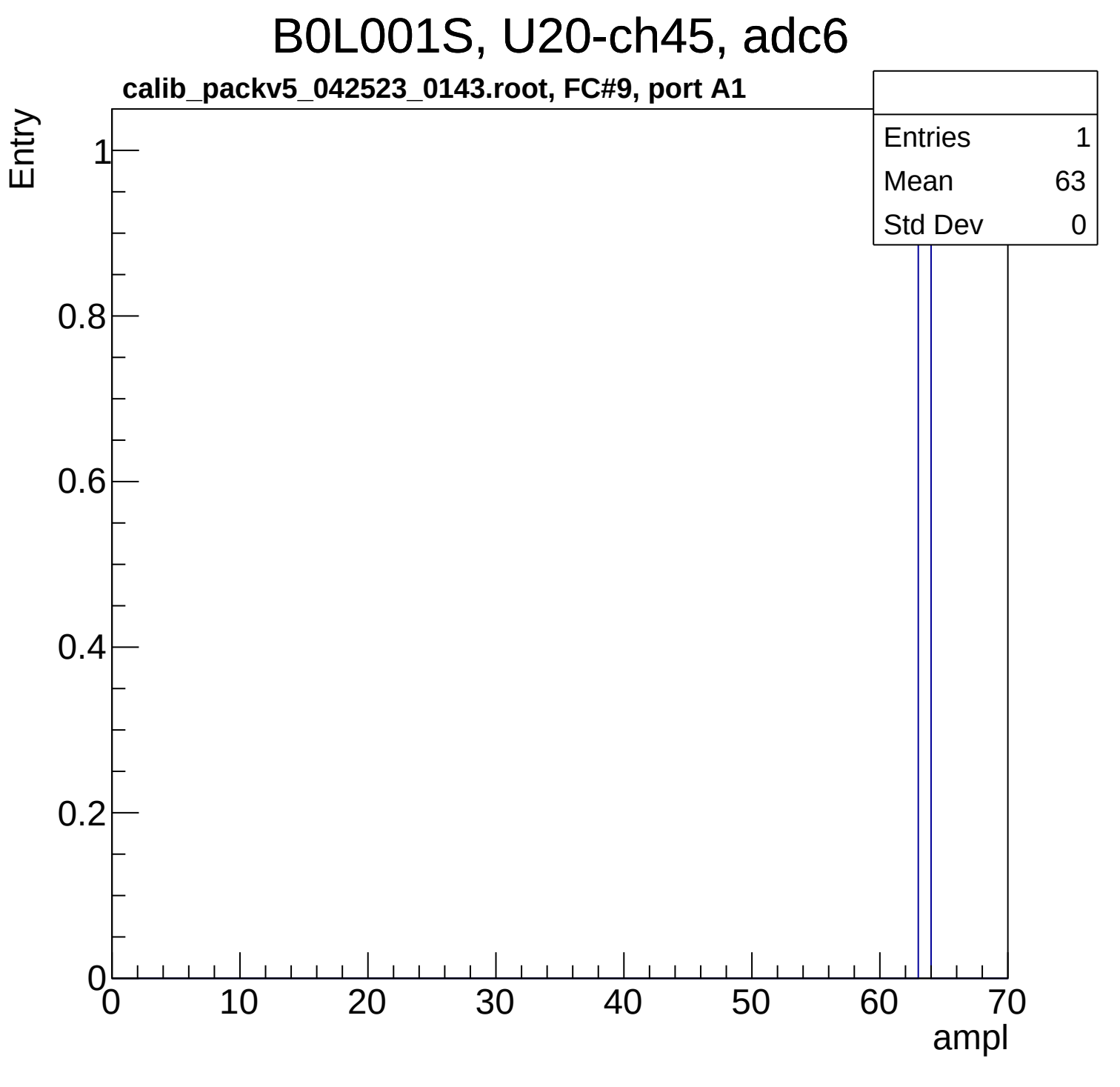
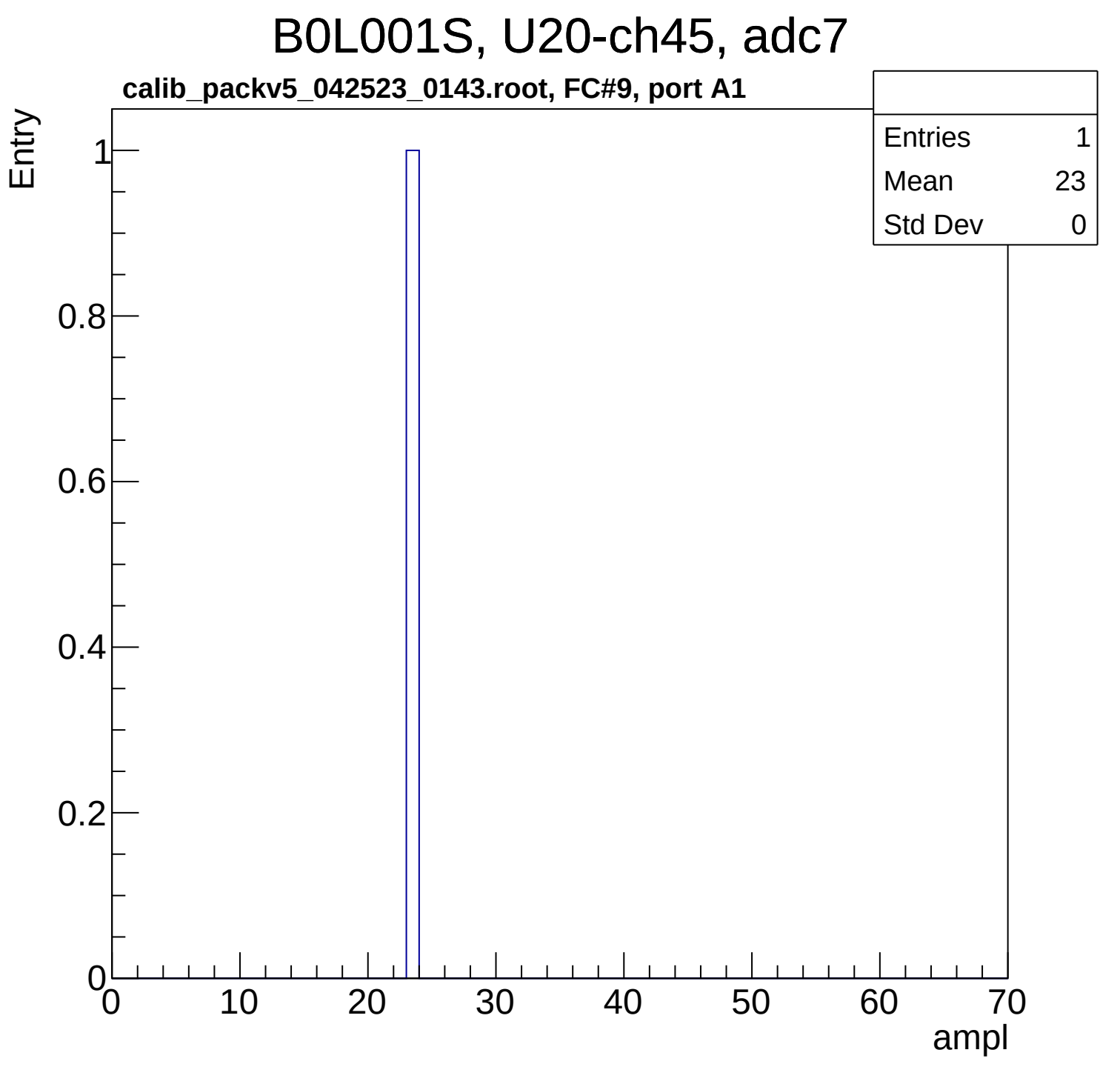

B0L001S, U20-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 60.74
Std Dev 2.083
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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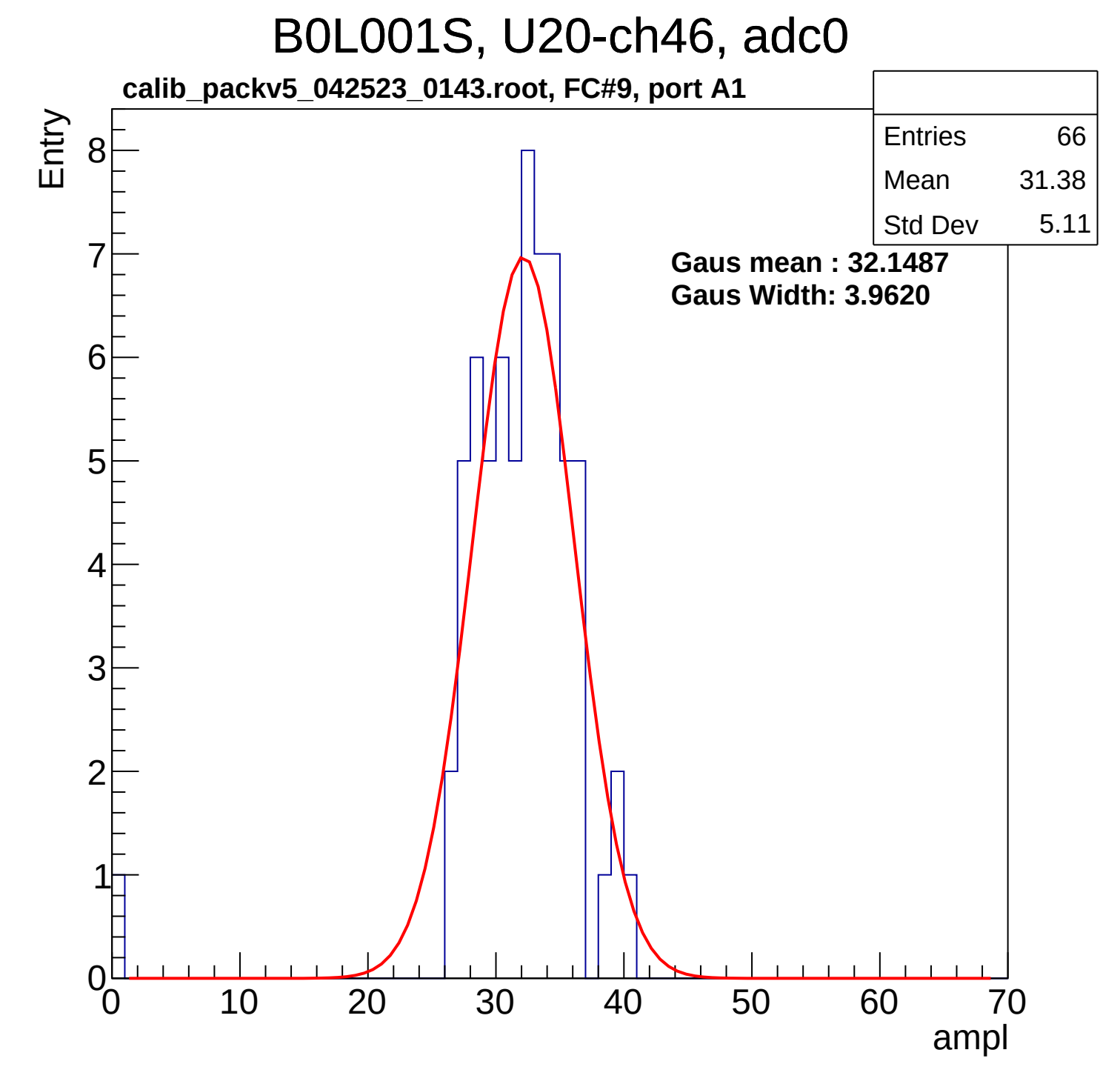
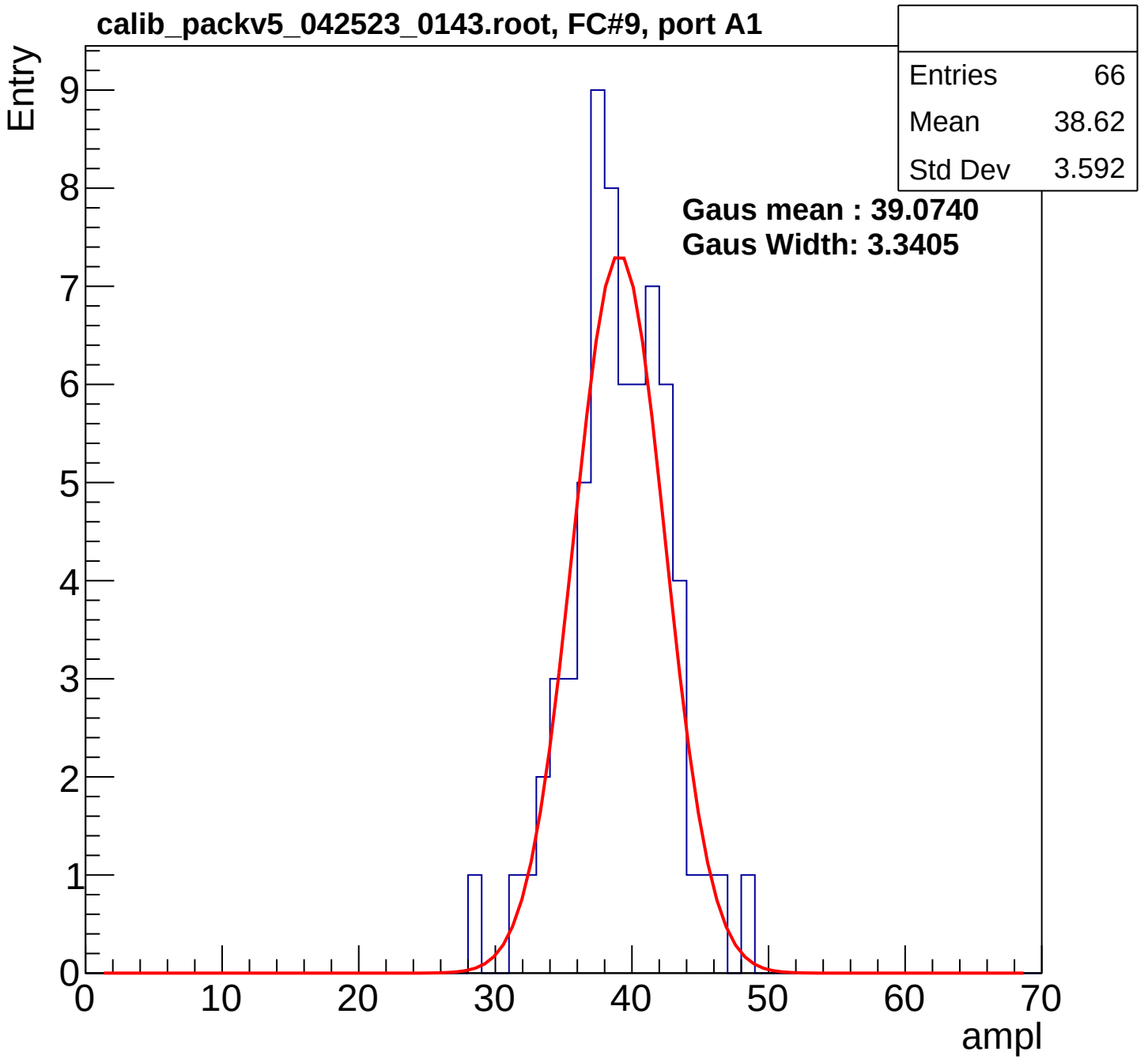
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.38
Std Dev 5.11
Gaus mean : 32.1487
Gaus Width: 3.9620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.62
Std Dev 3.592
Gaus mean : 39.0740
Gaus Width: 3.3405
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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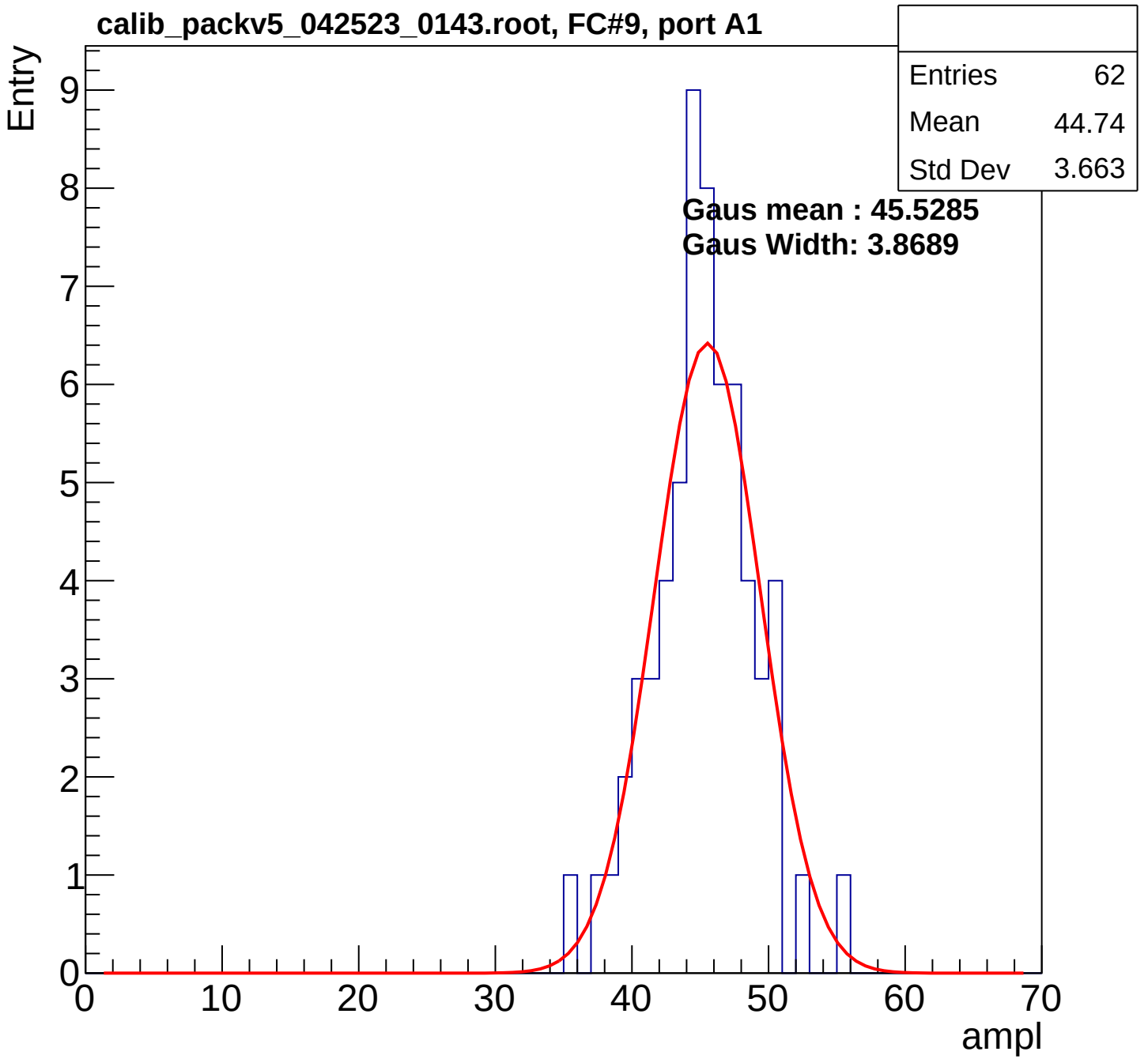
B0L001S, U20-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.74
Std Dev 3.663
Gaus mean : 45.5285
Gaus Width: 3.8689
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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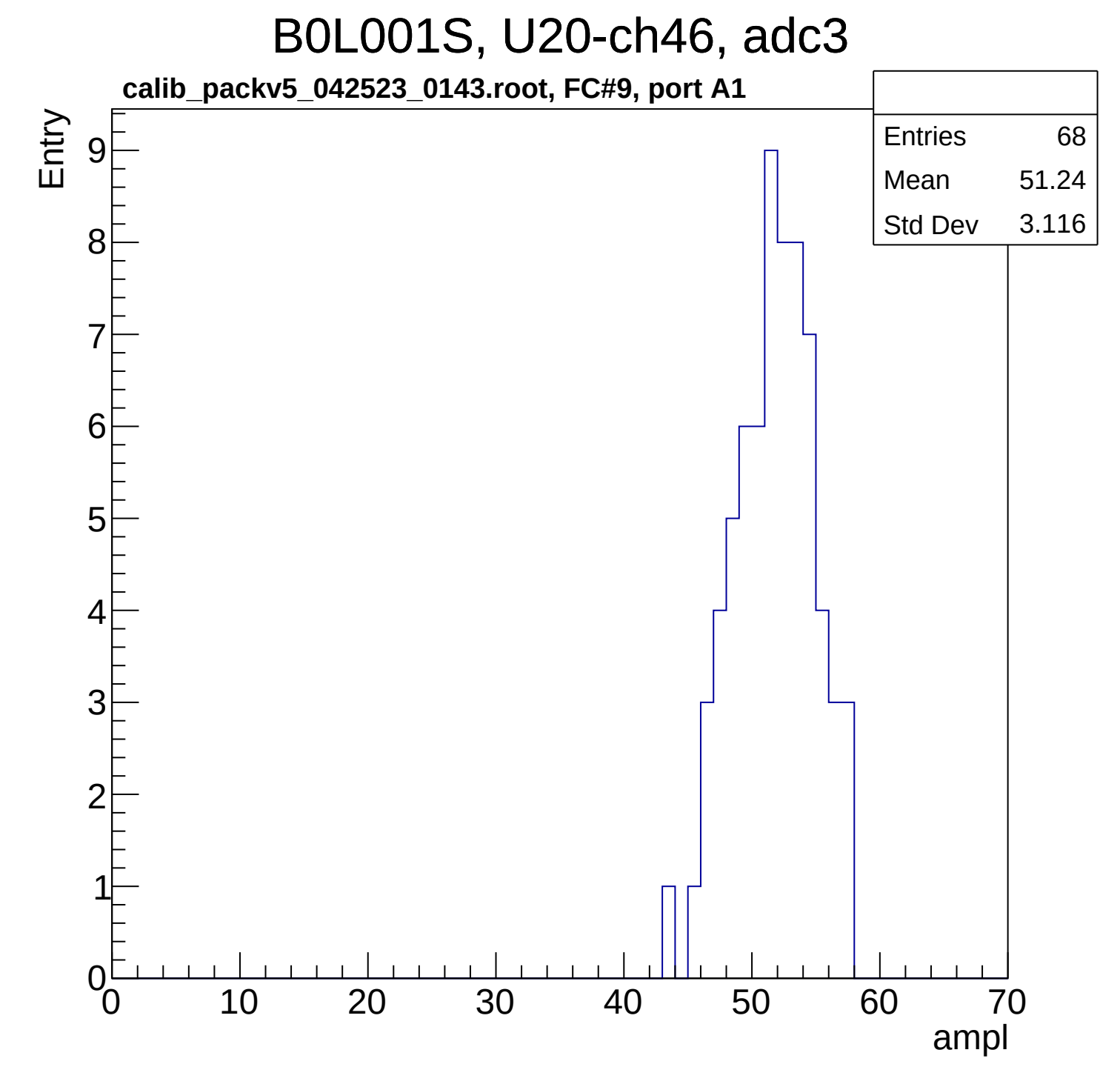
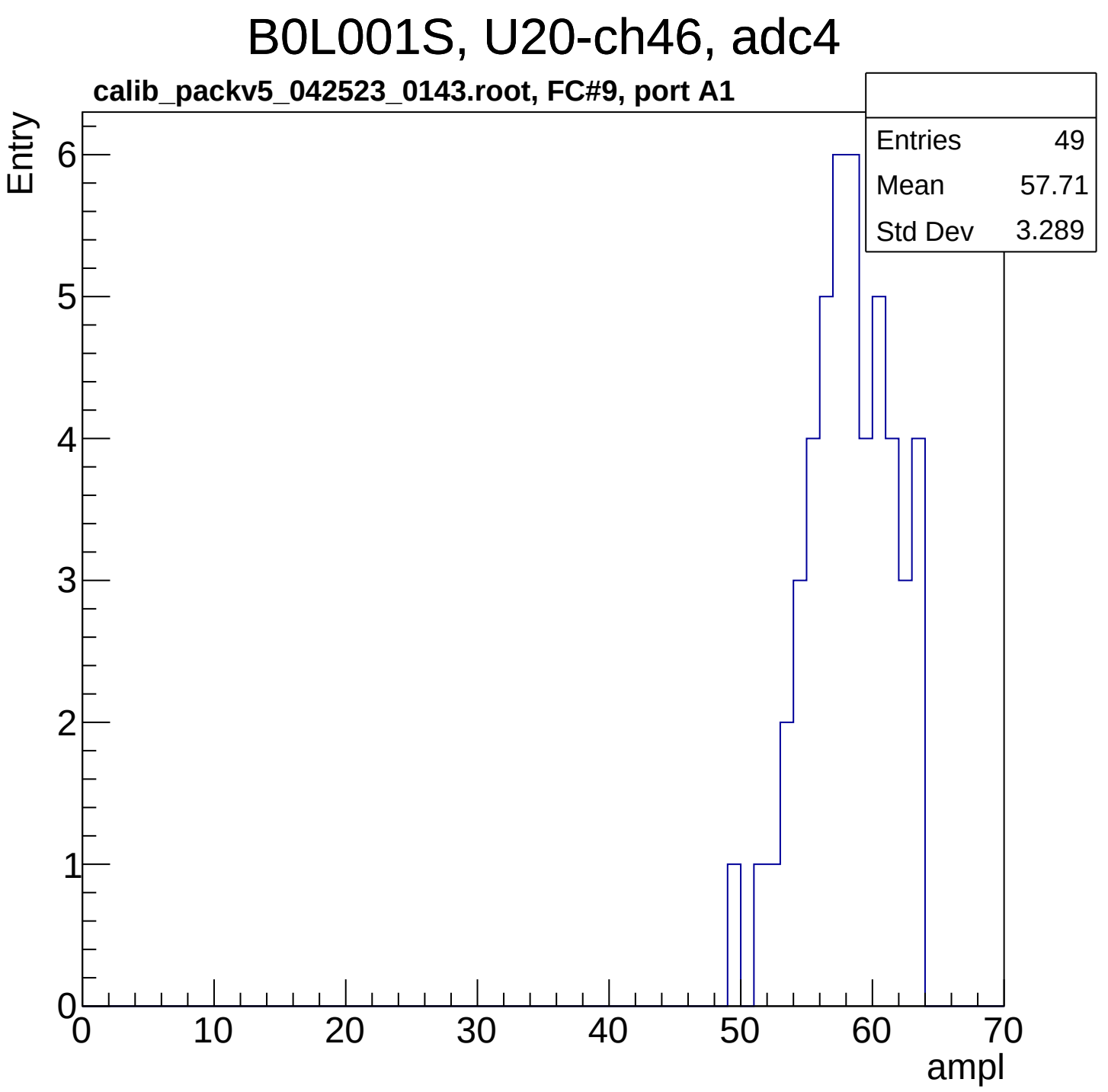
60
70


B0L001S, U20-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.24
Std Dev 3.116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.71
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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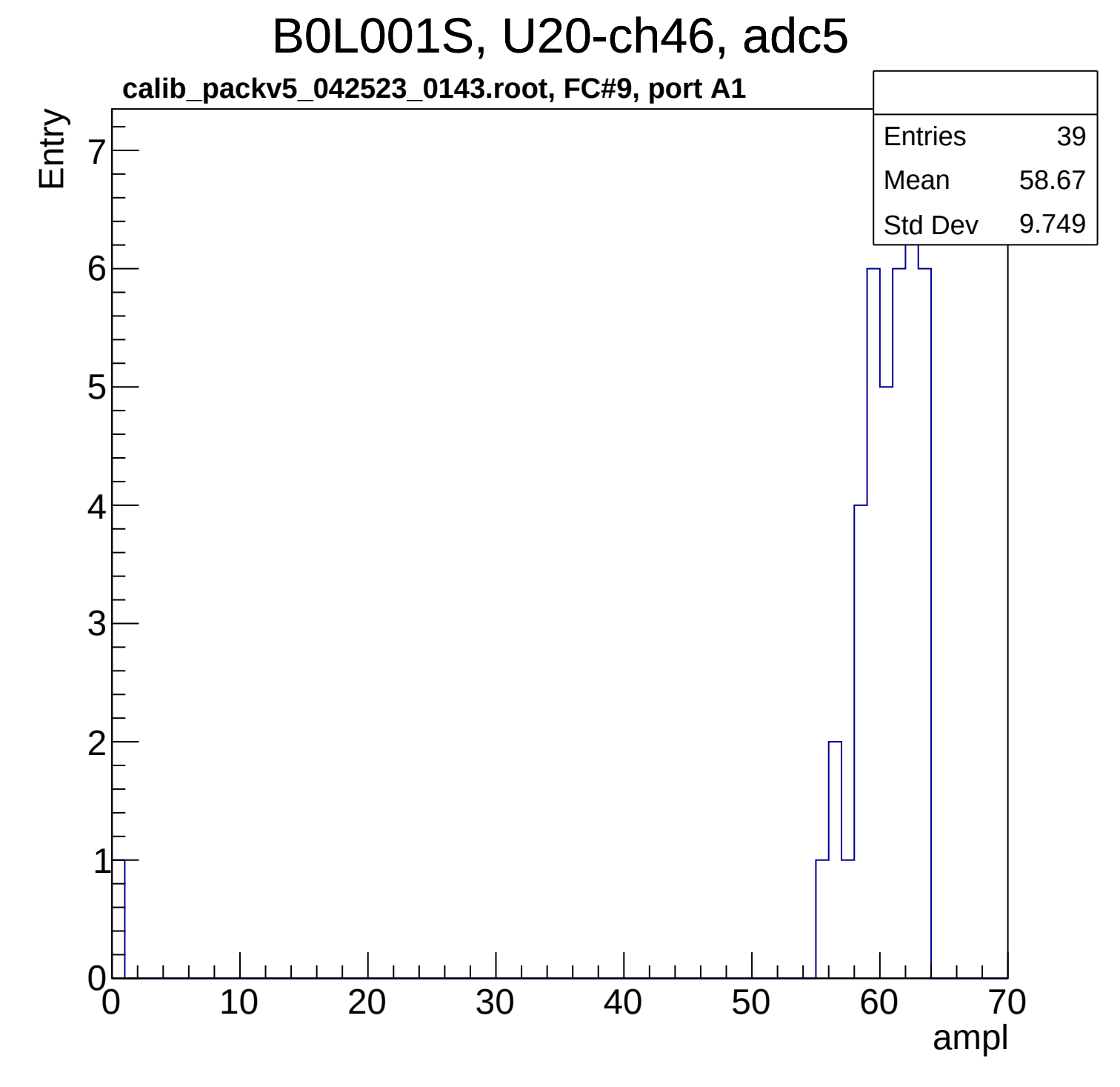
B0L001S, U20-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.67
Std Dev 9.749
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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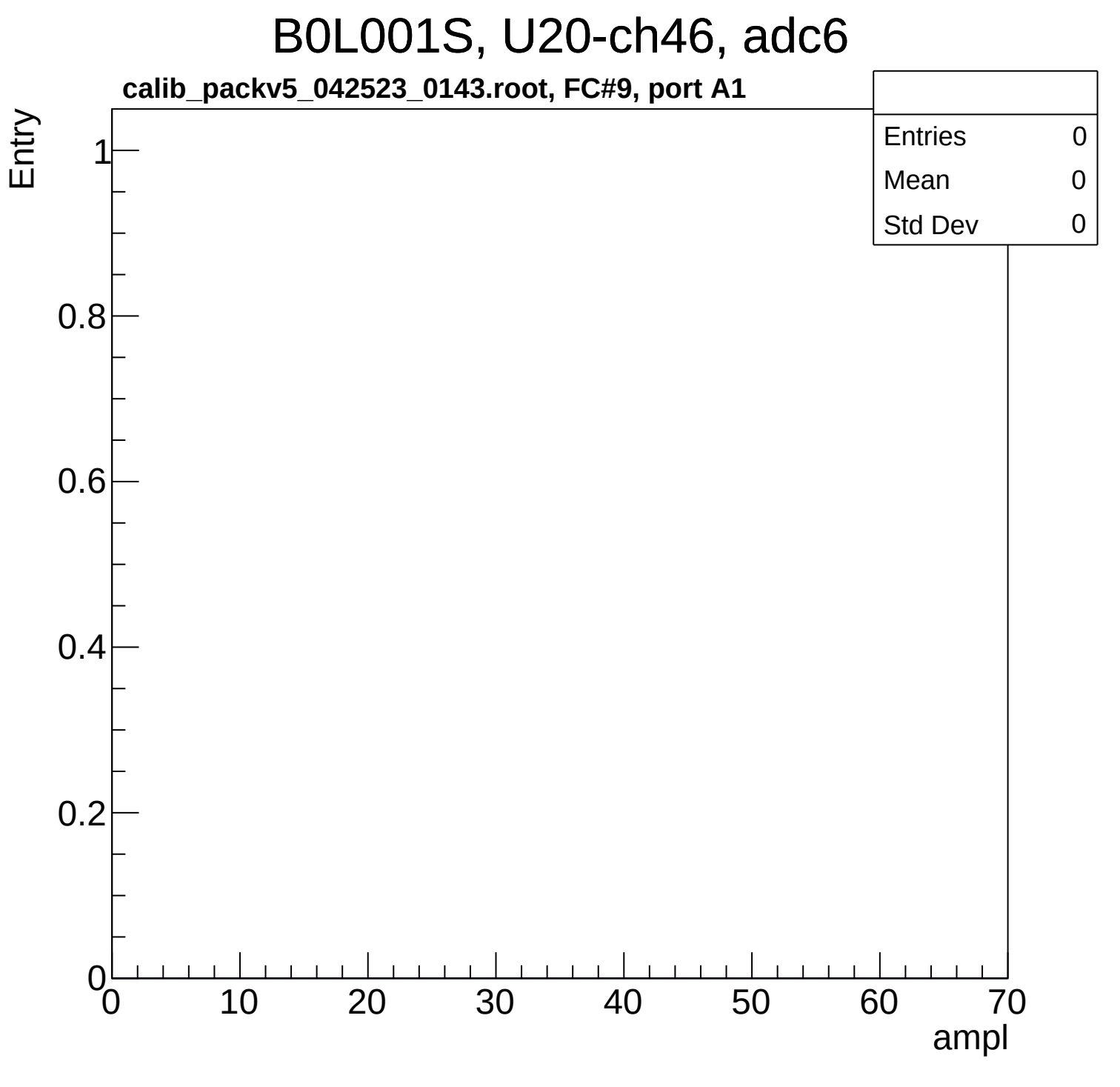
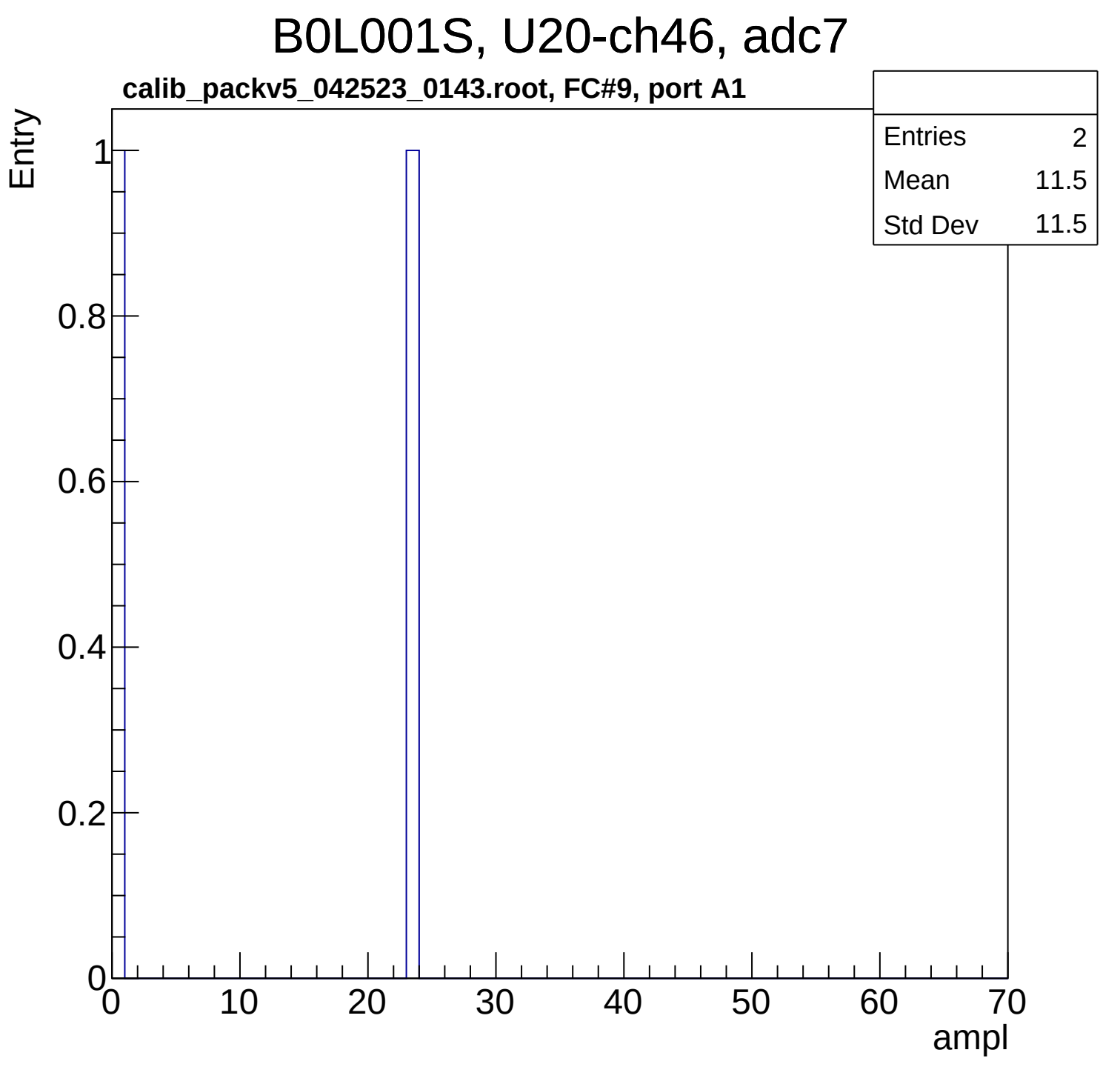

B0L001S, U20-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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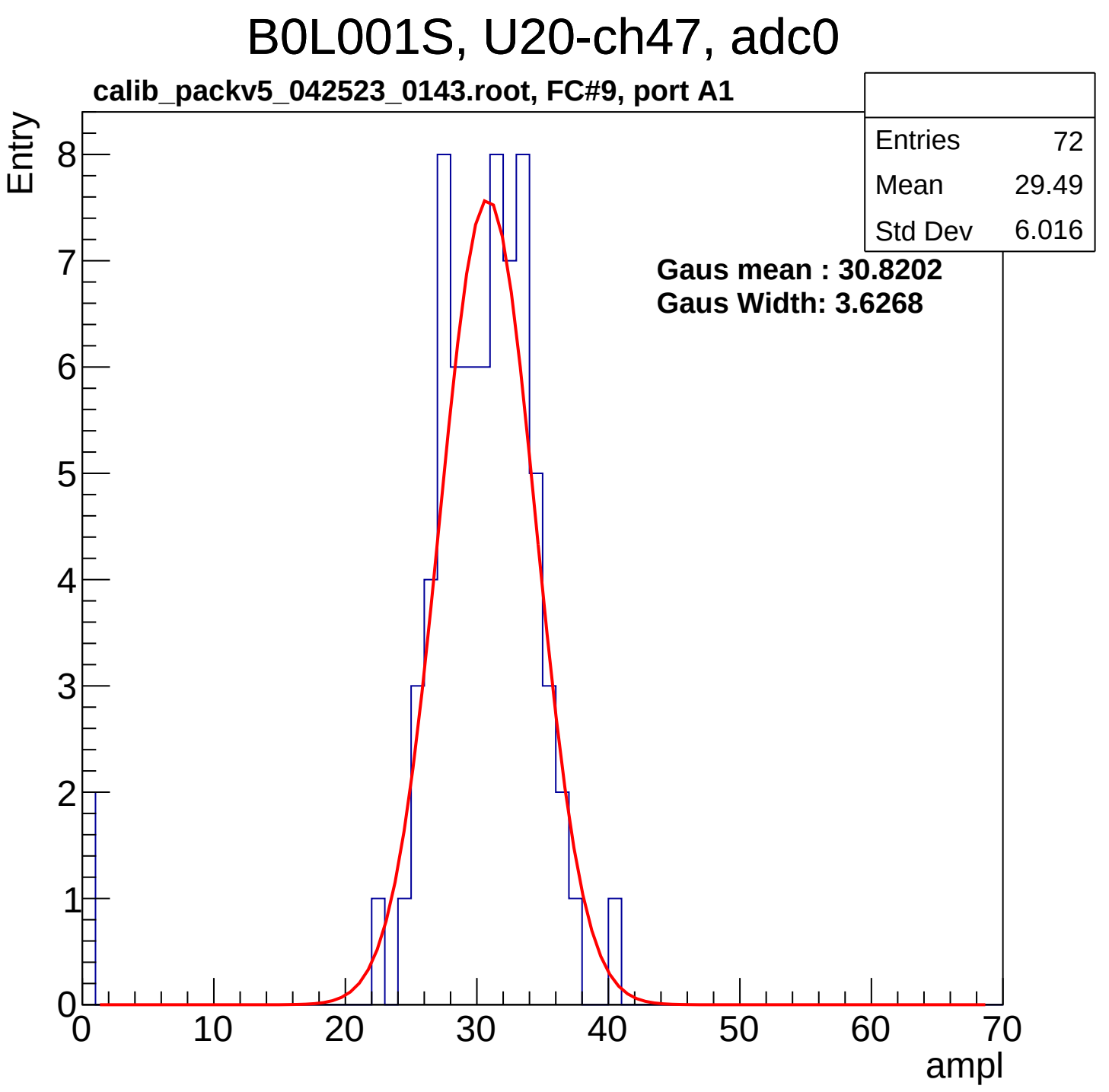
40
50
60
70
ampl


B0L001S, U20-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.49
Std Dev 6.016
Gaus mean : 30.8202
Gaus Width: 3.6268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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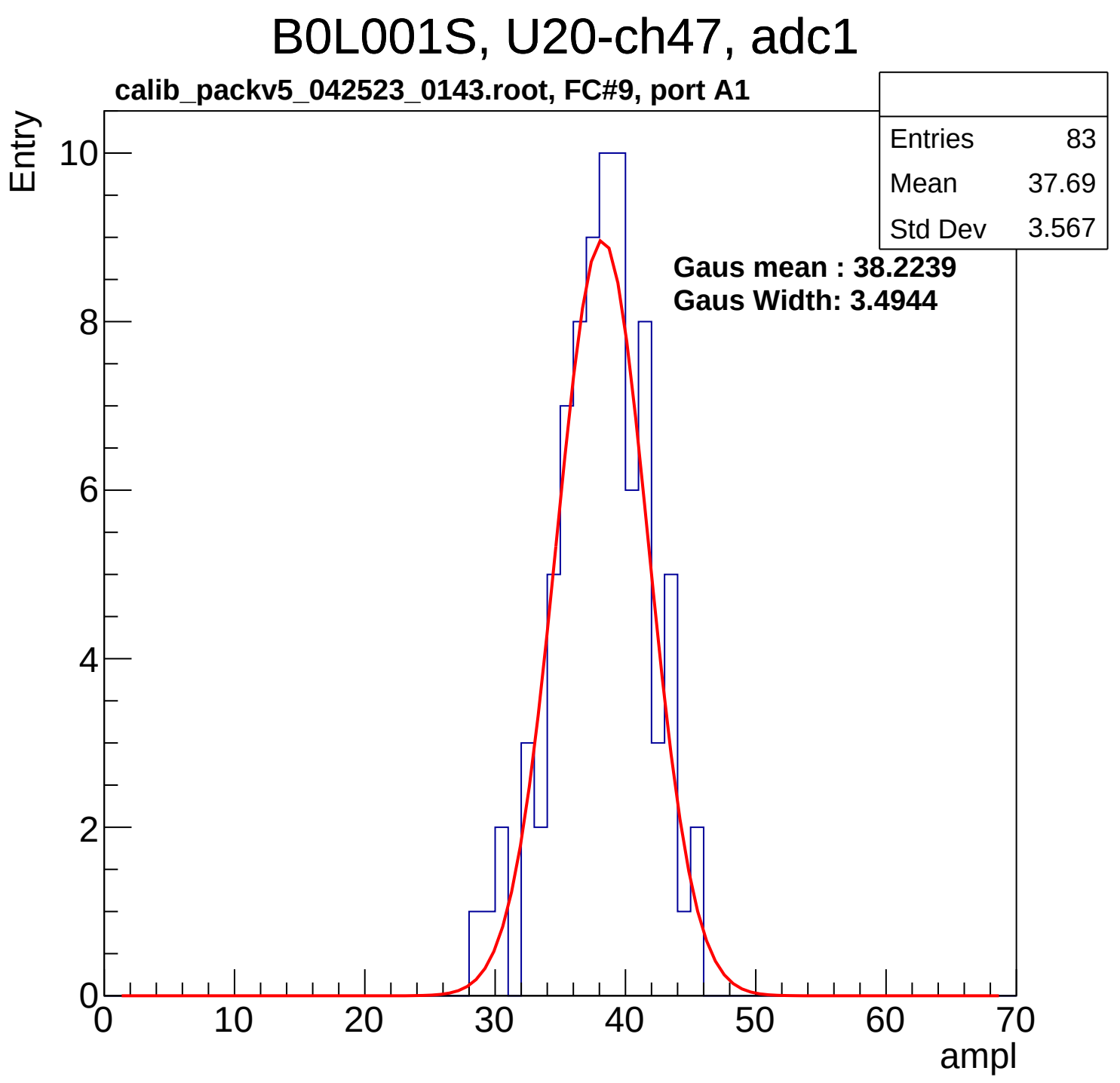
B0L001S, U20-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 37.69
Std Dev 3.567
Gaus mean : 38.2239
Gaus Width: 3.4944
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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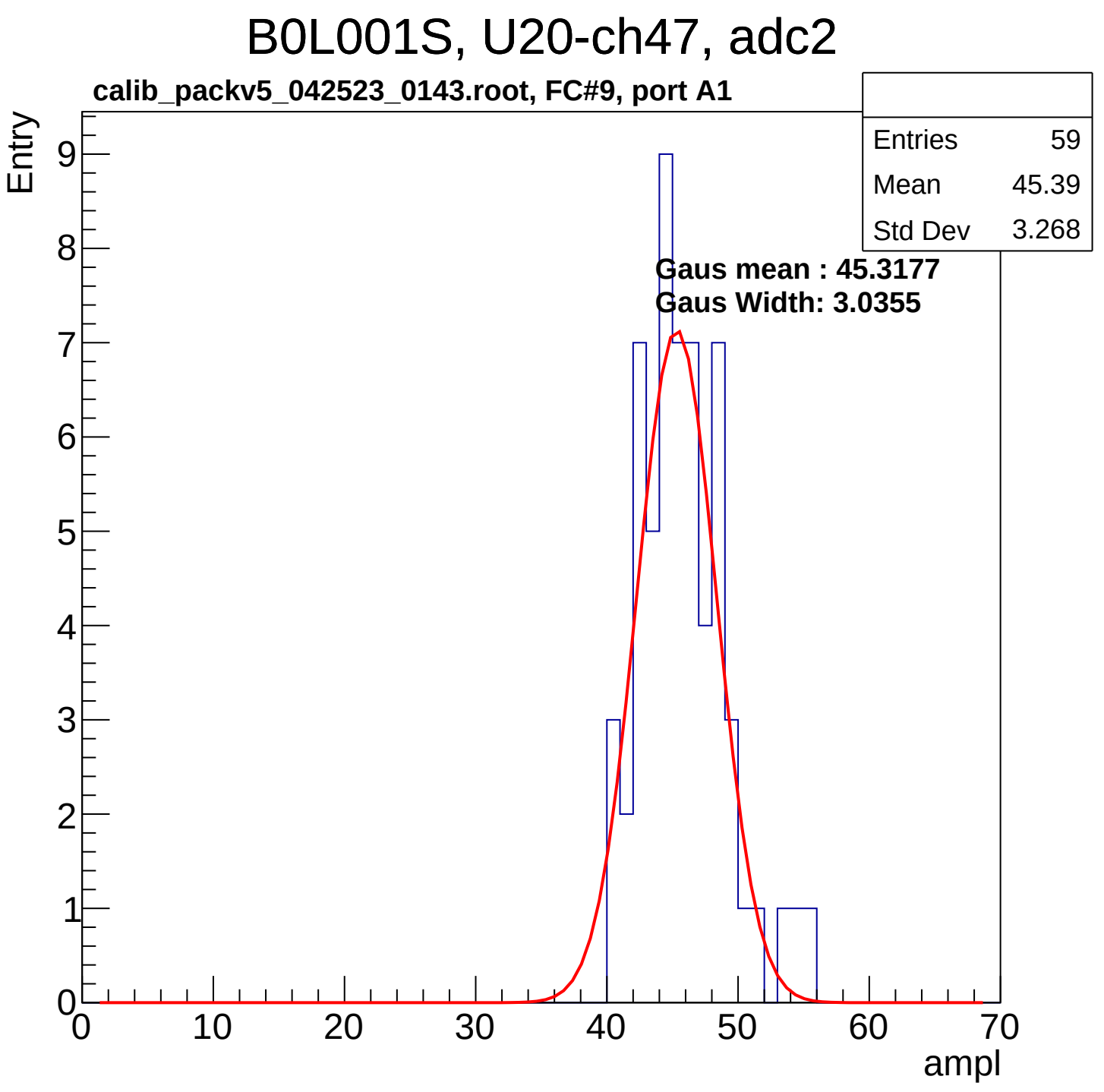
B0L001S, U20-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.39
Std Dev 3.268
Gaus mean : 45.3177
Gaus Width: 3.0355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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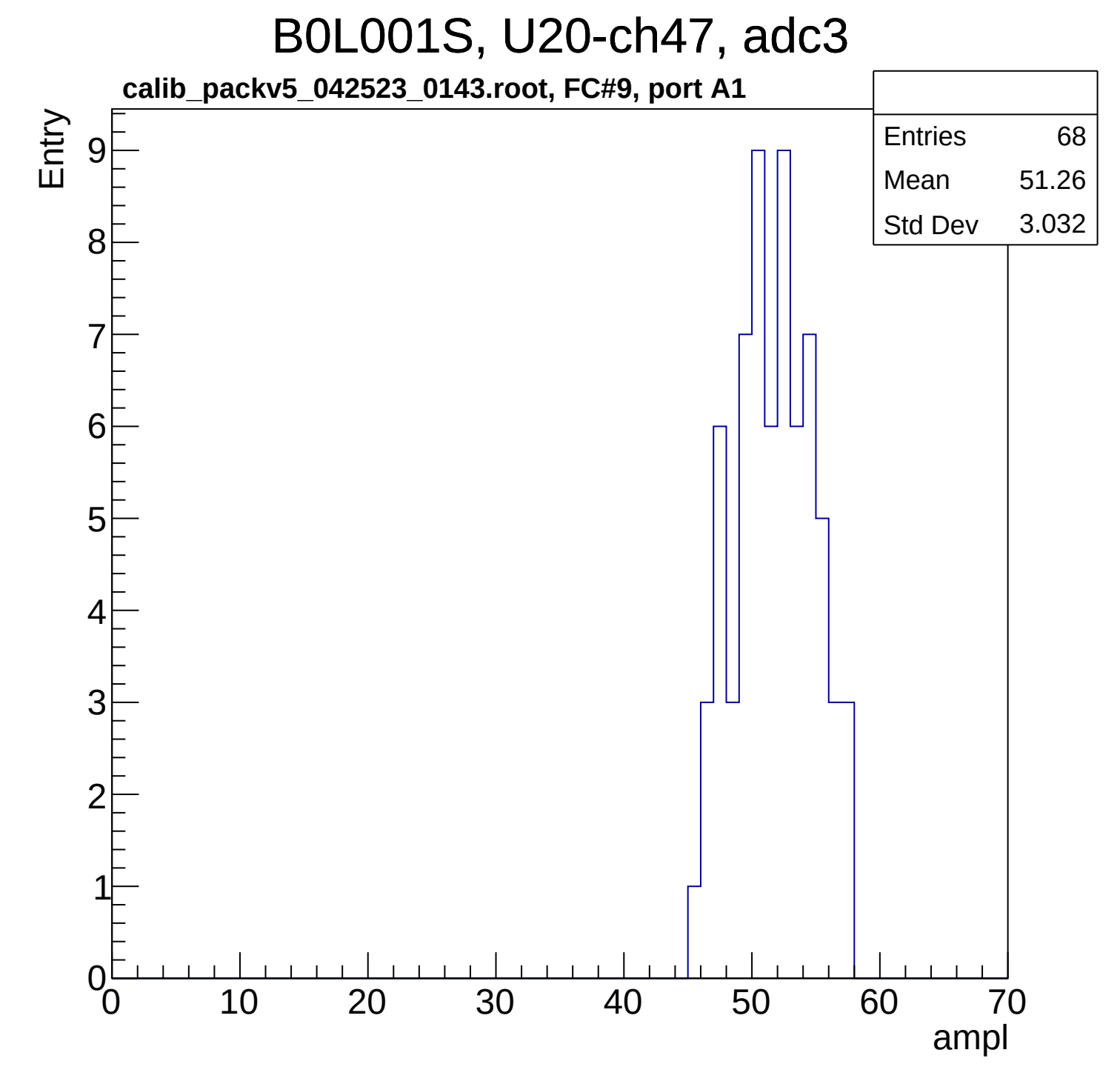
B0L001S, U20-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.26
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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60
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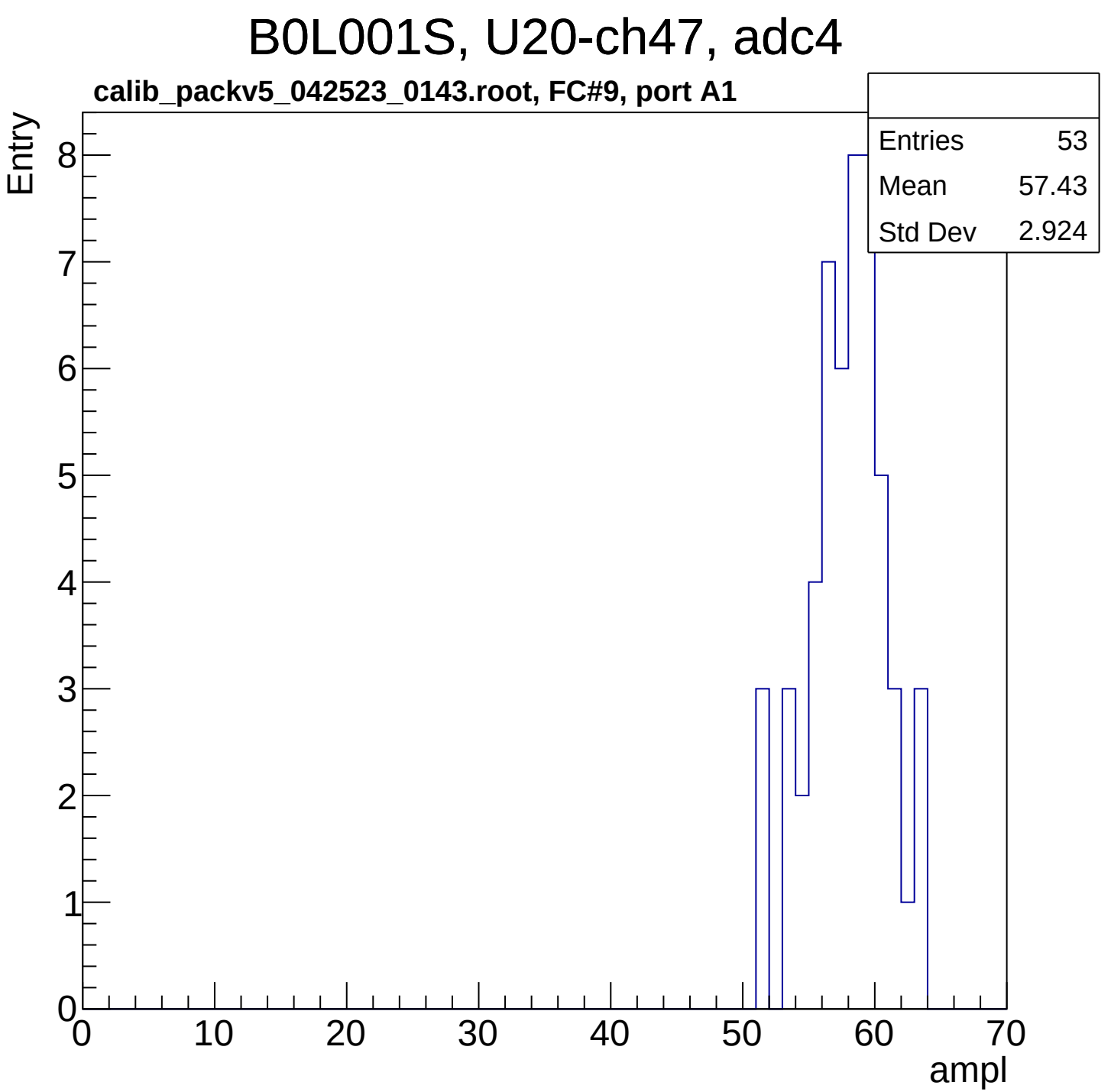
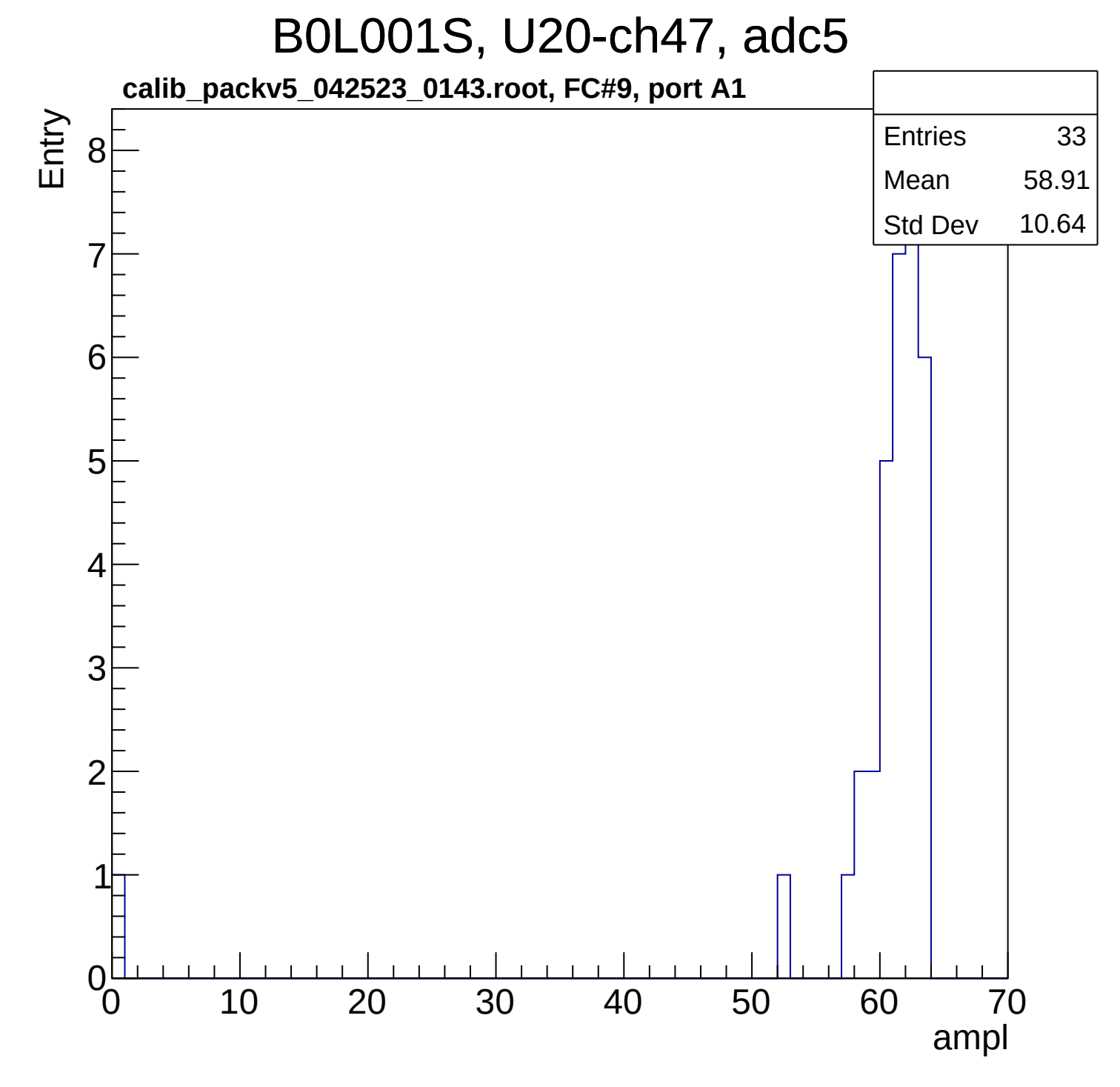

B0L001S, U20-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.43
Std Dev 2.924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.91
Std Dev 10.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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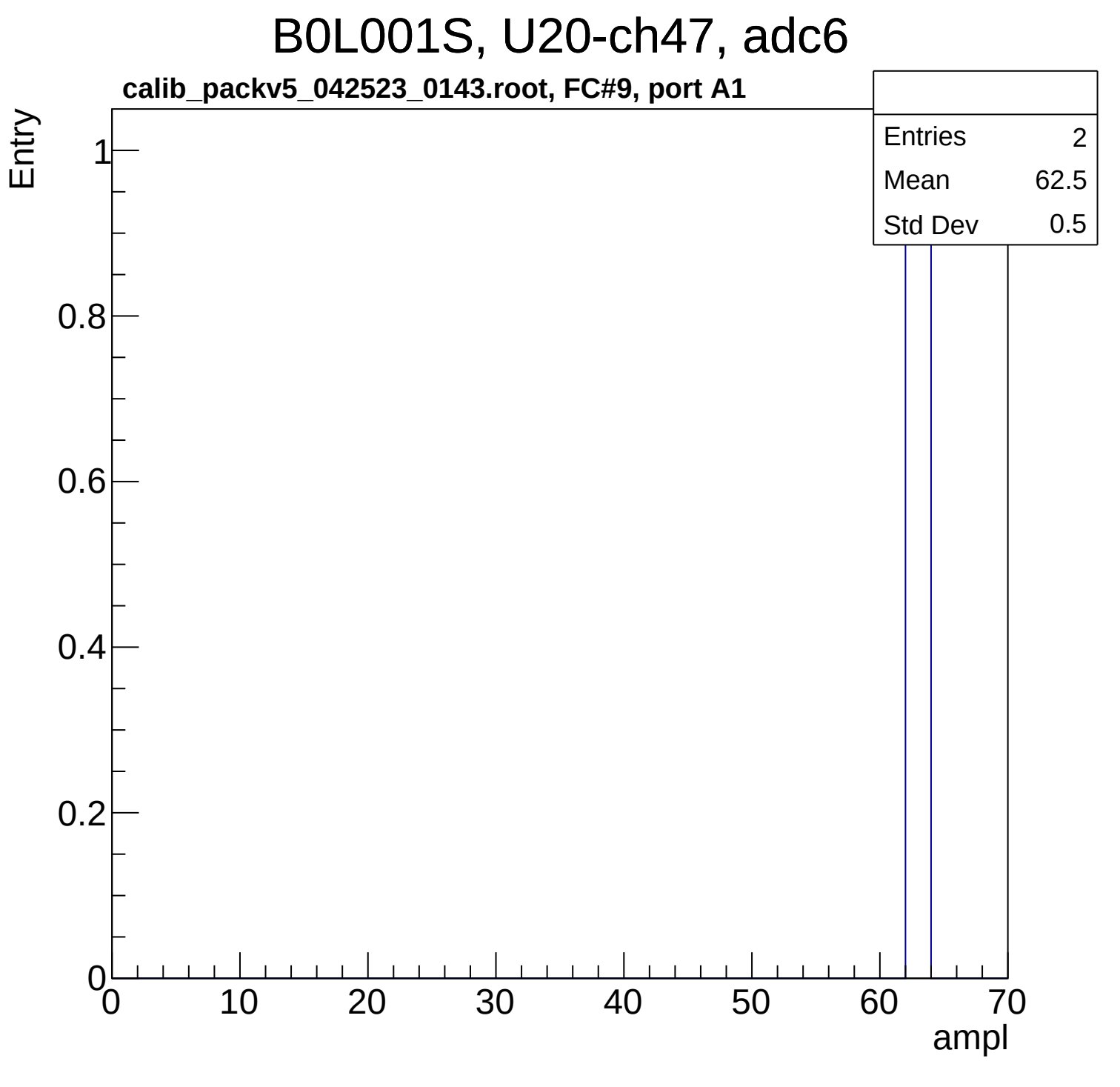
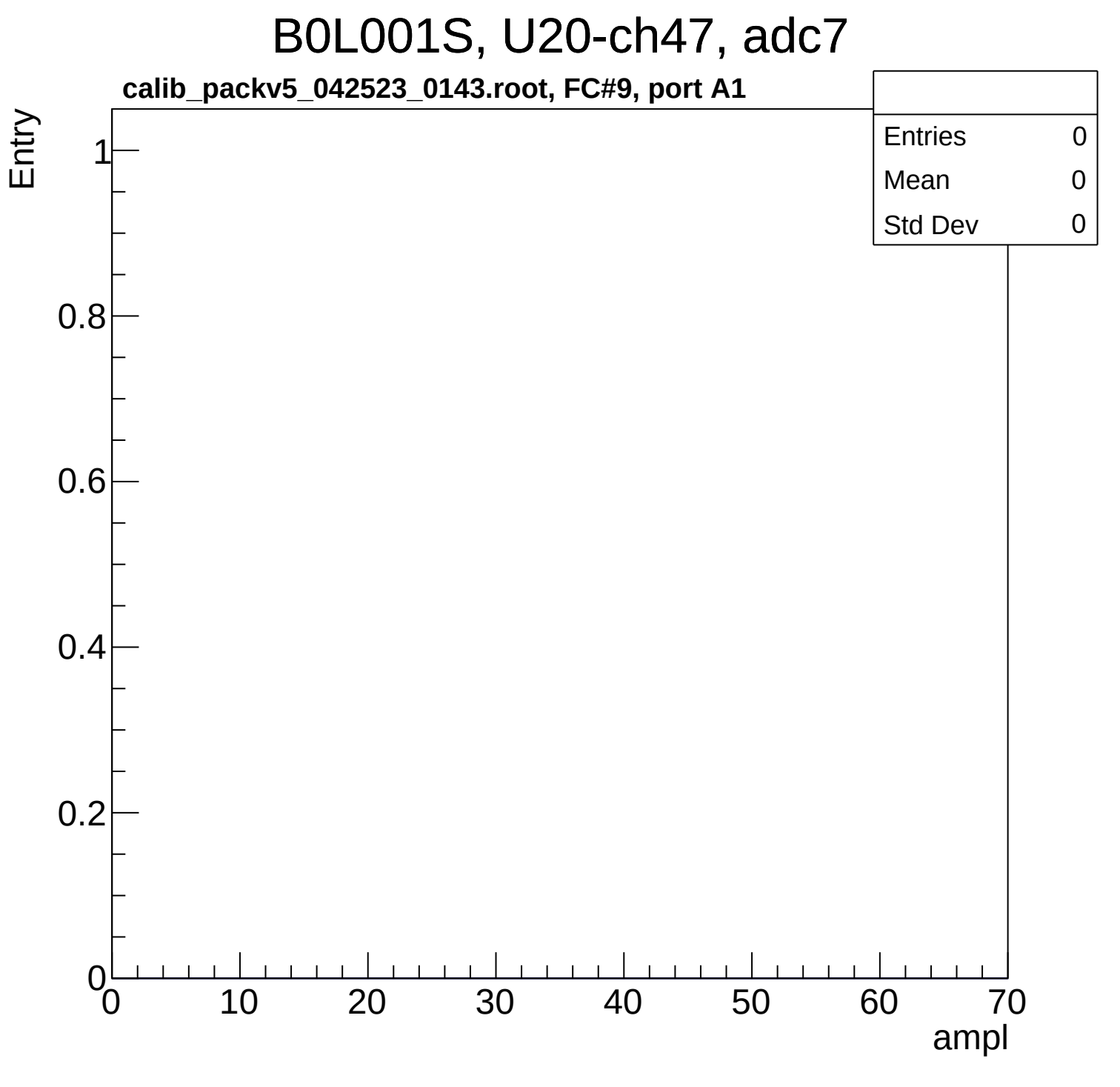
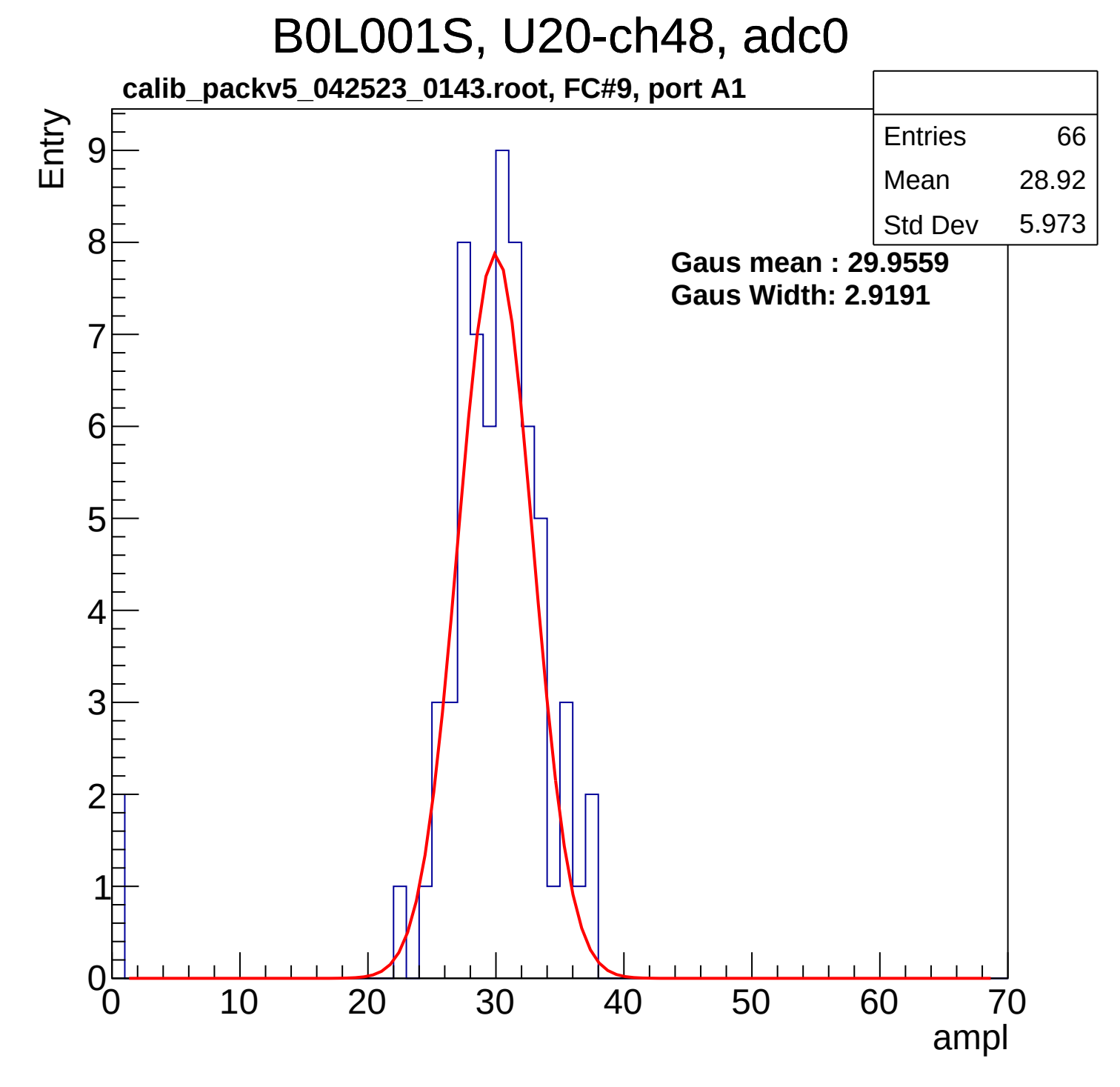
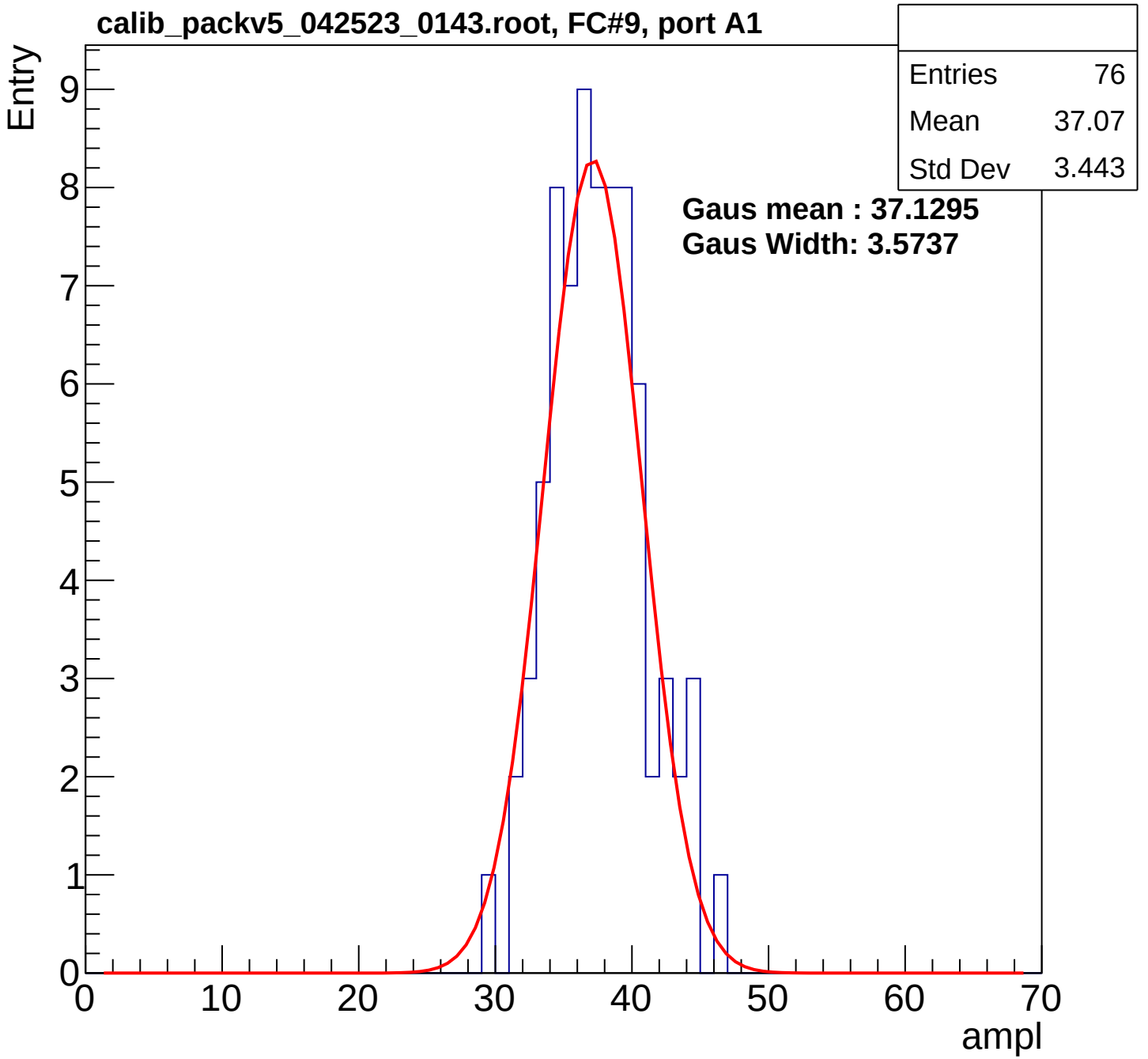
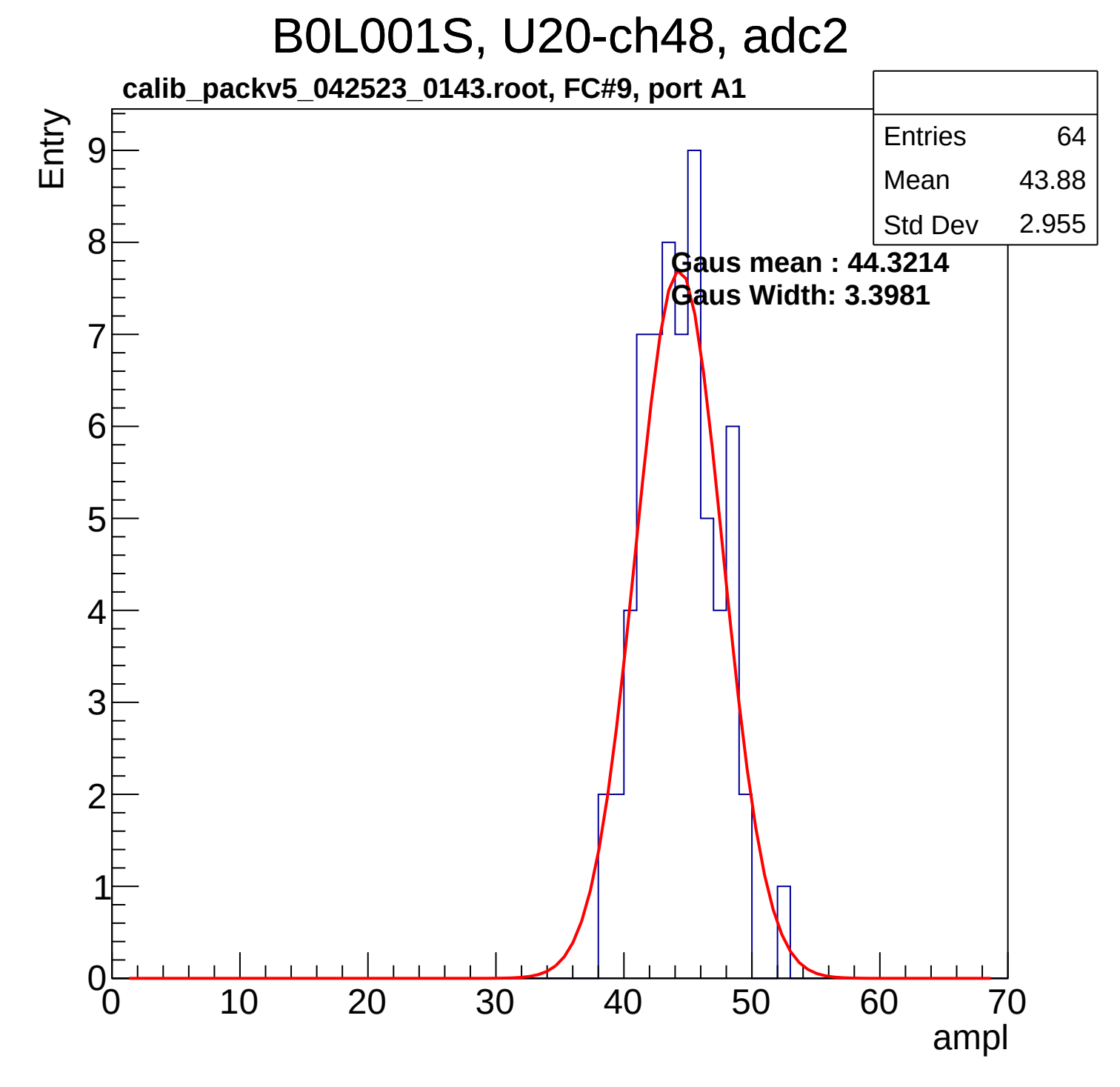

B0L001S, U20-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.92
Std Dev 5.973
Gaus mean : 29.9559
Gaus Width: 2.9191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.07
Std Dev 3.443
Gaus mean : 37.1295
Gaus Width: 3.5737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.88
Std Dev 2.955
Gaus mean : 44.3214
Gaus Width: 3.3981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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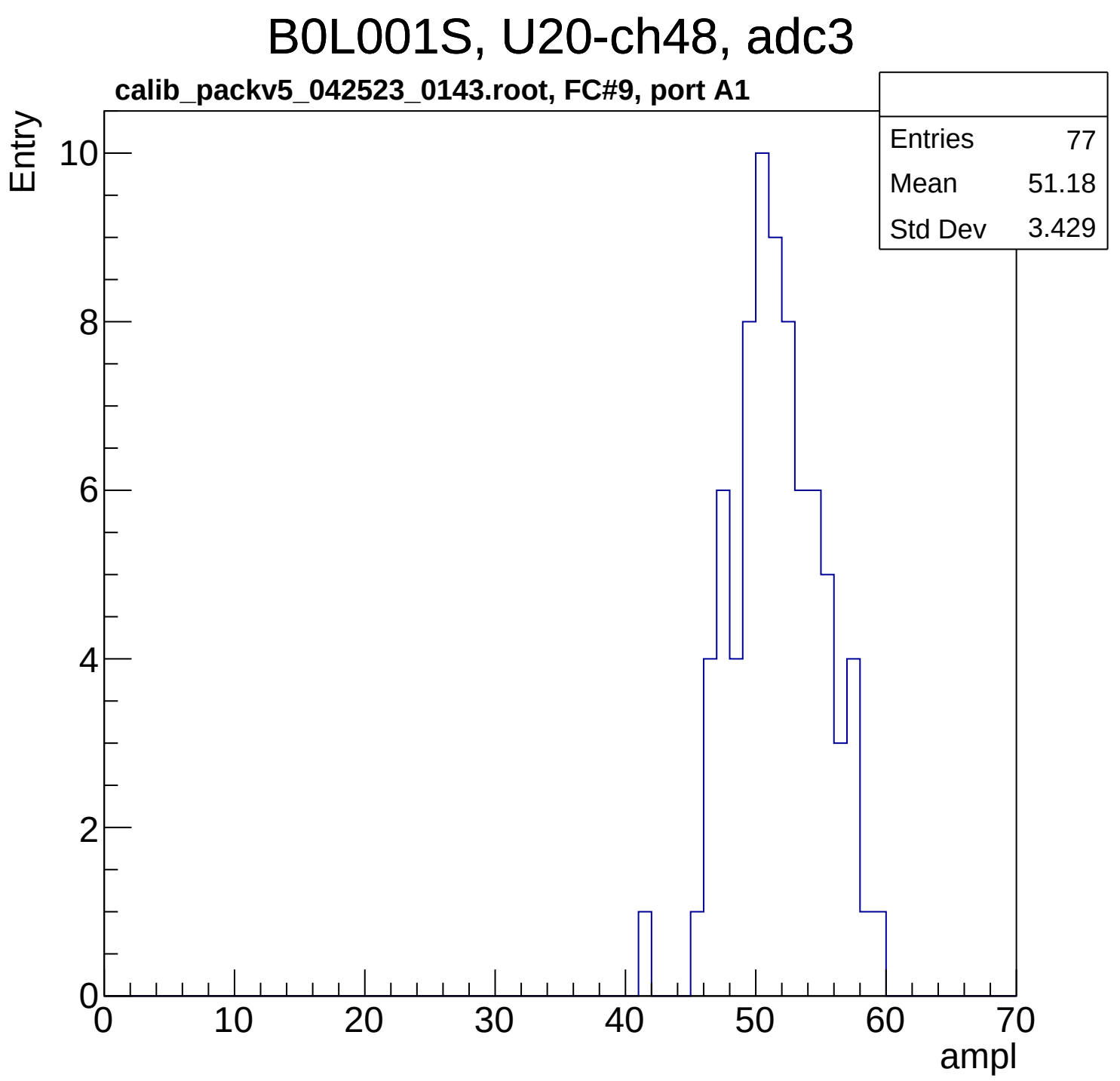
60
70


B0L001S, U20-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.18
Std Dev 3.429
Entry
ampl
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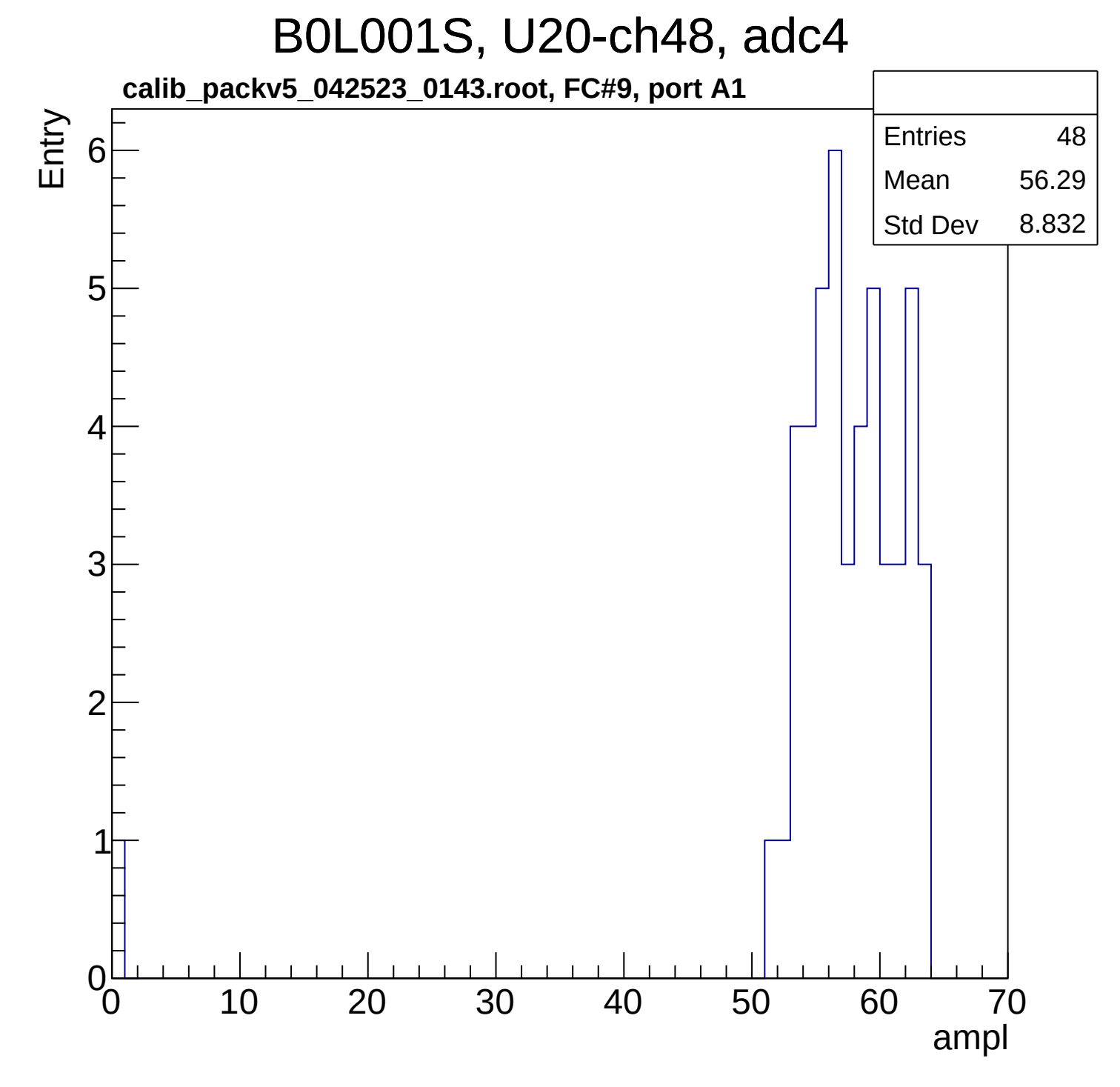
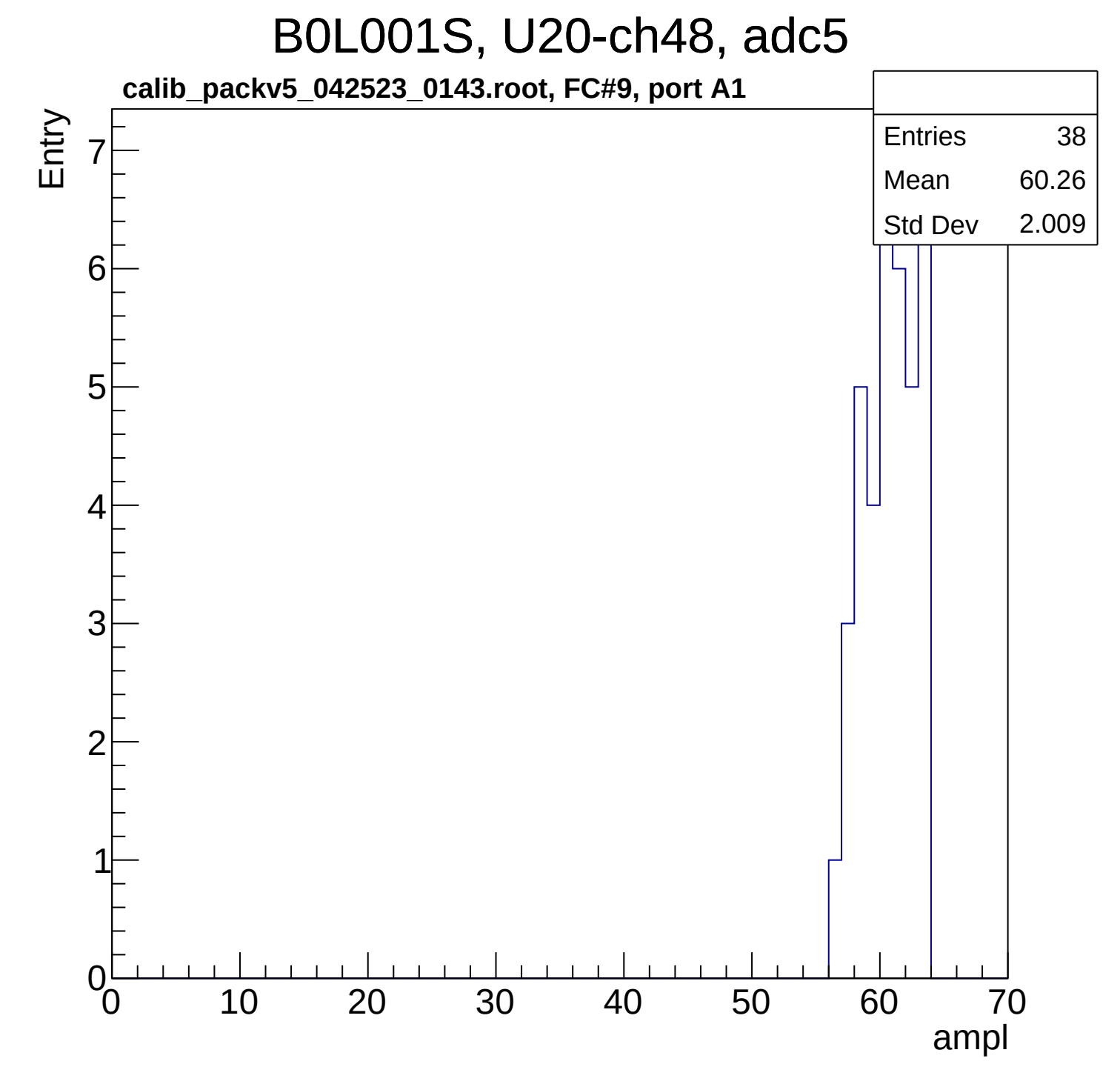
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.29
Std Dev 8.832
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.26
Std Dev 2.009
Entry
ampl
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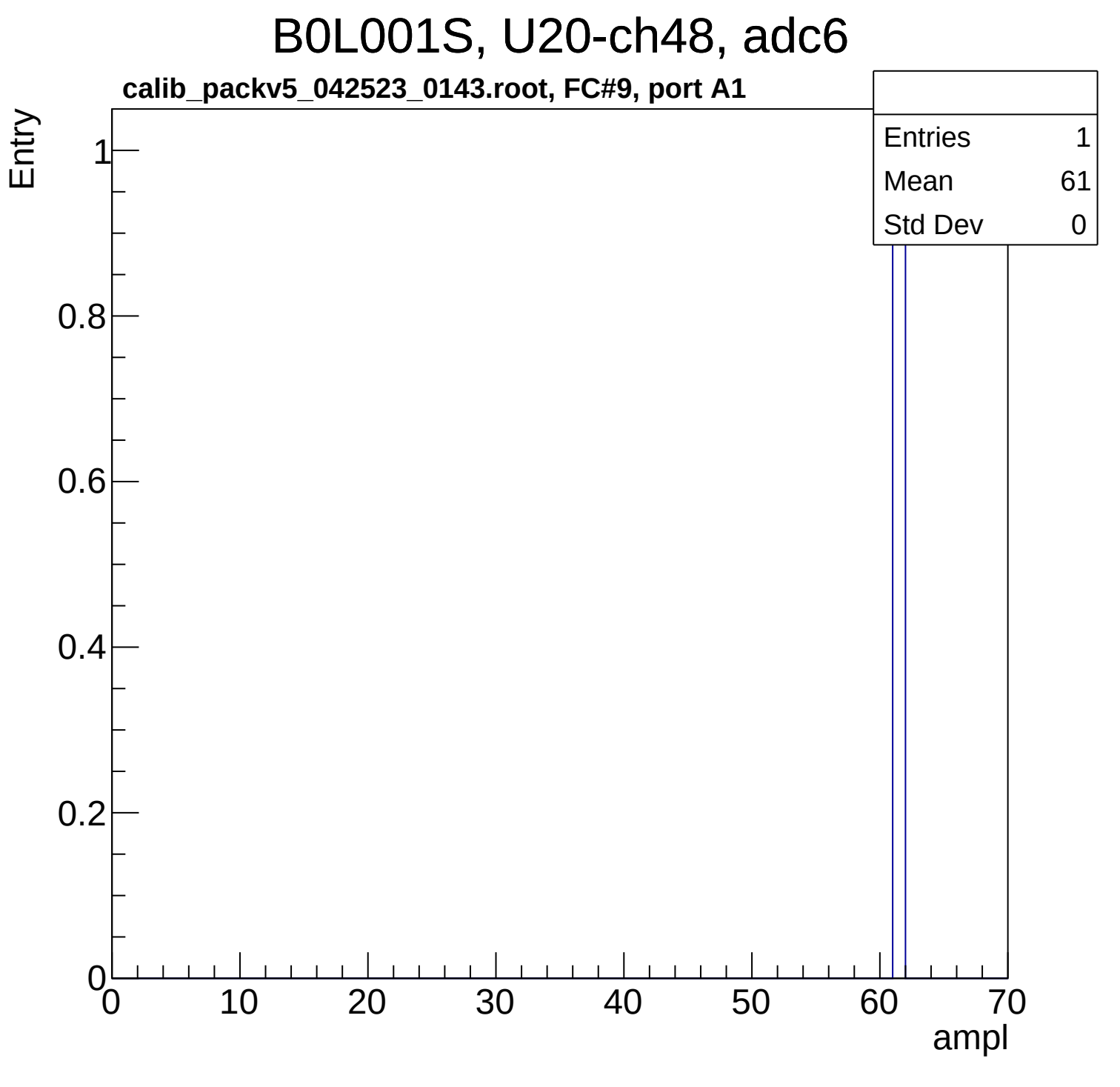
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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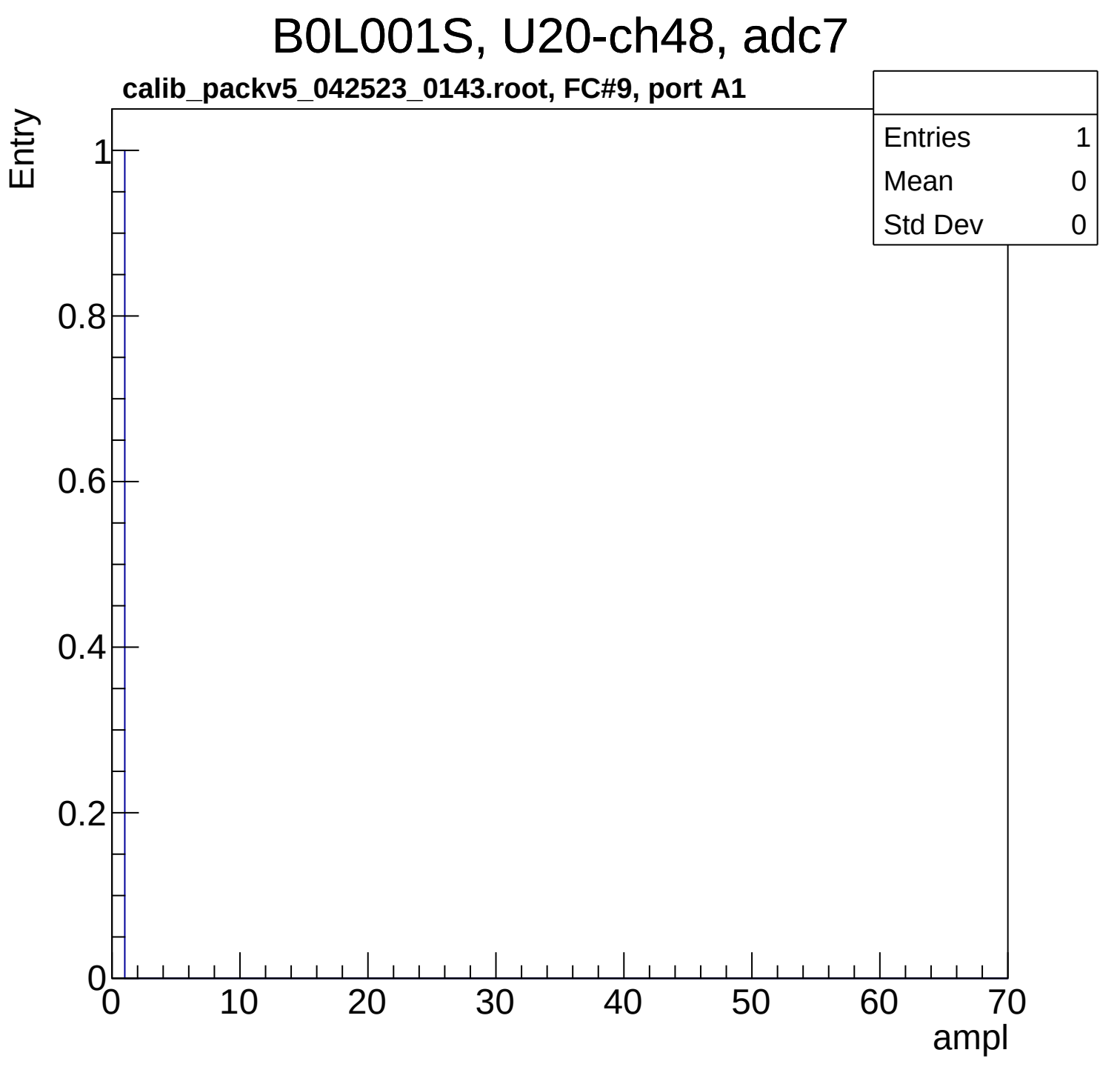
50
60
70
ampl


B0L001S, U20-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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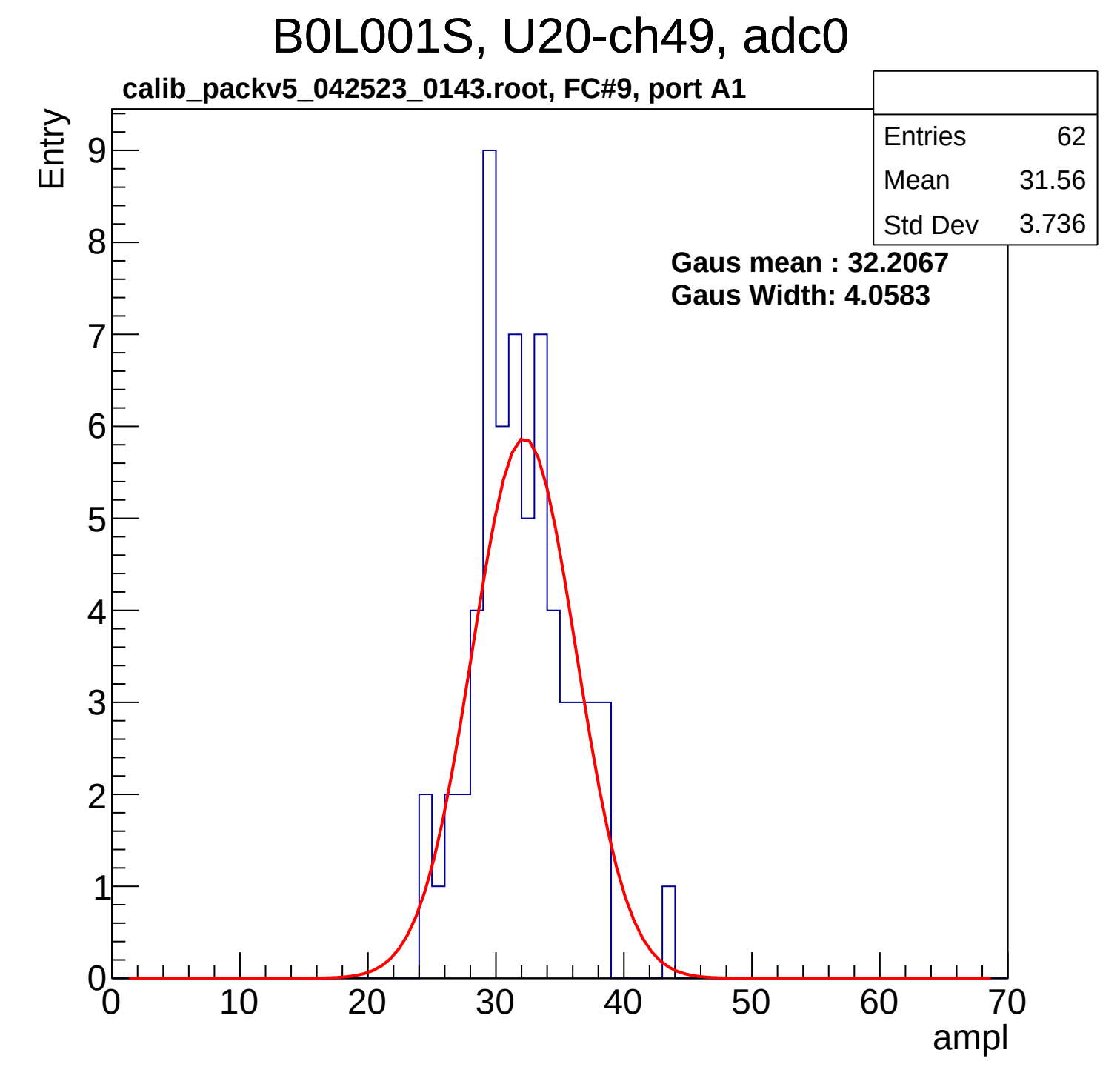
50
60
70
ampl


B0L001S, U20-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.56
Std Dev 3.736
Gaus mean : 32.2067
Gaus Width: 4.0583
Entry
ampl
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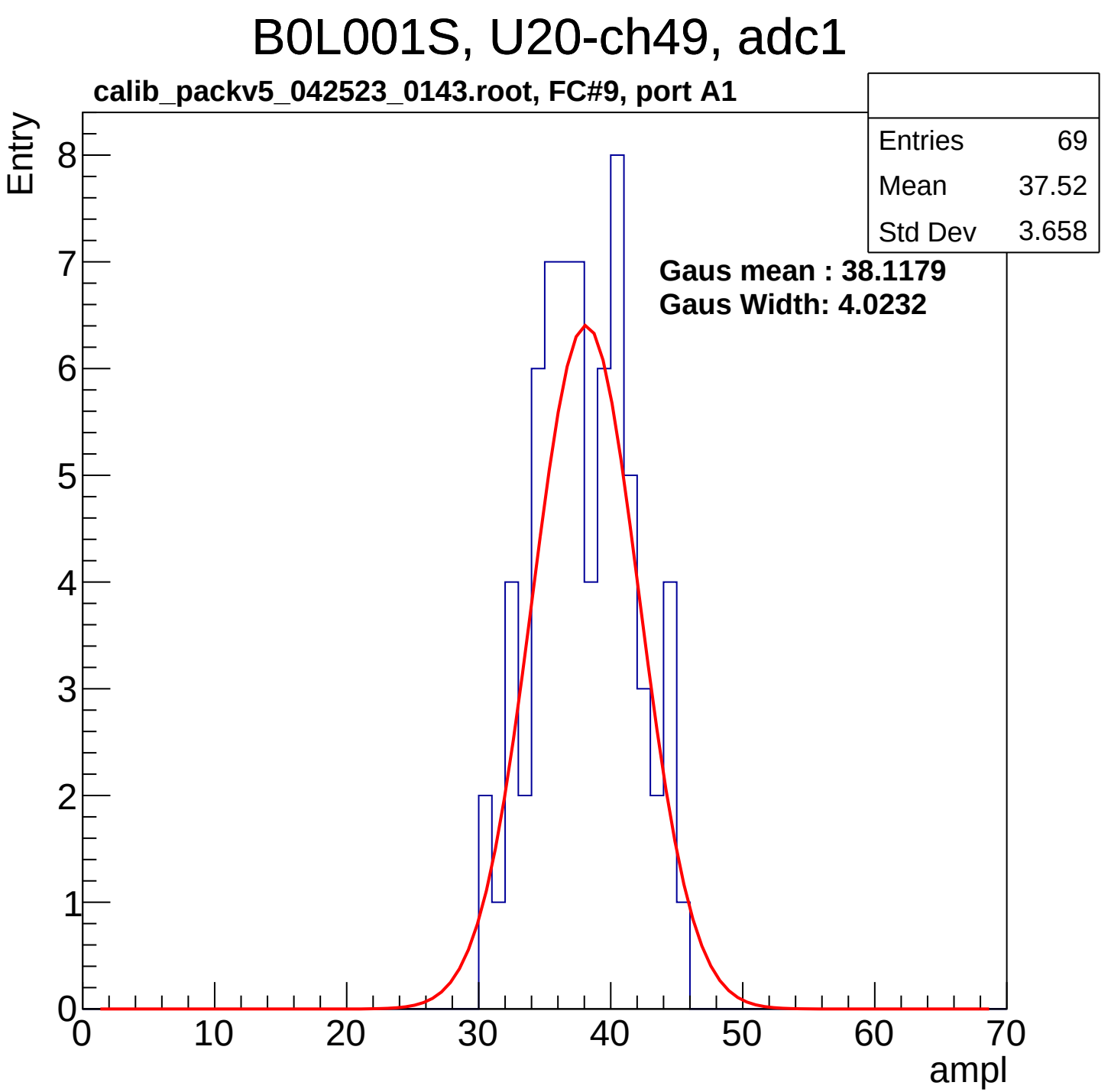
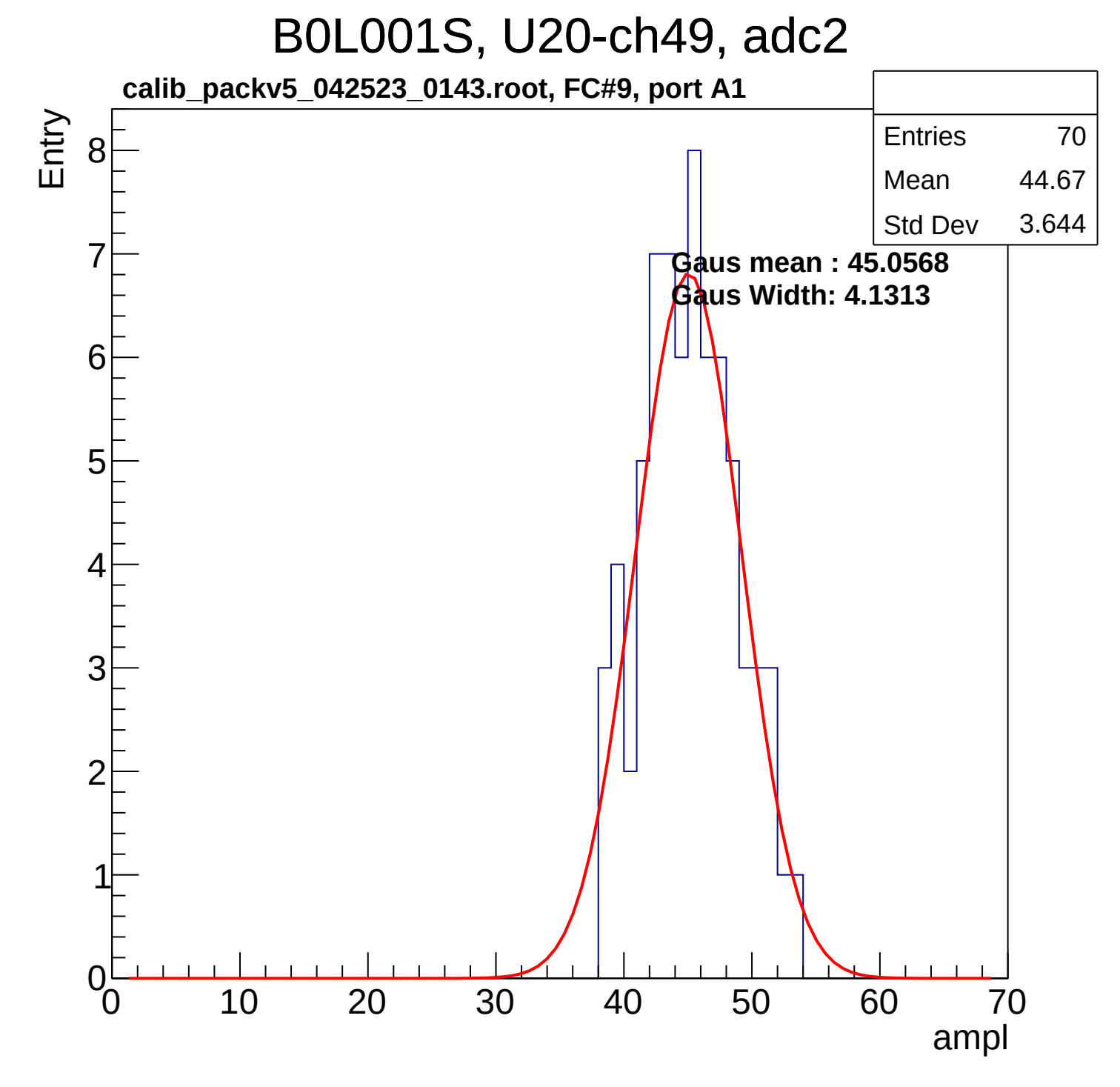
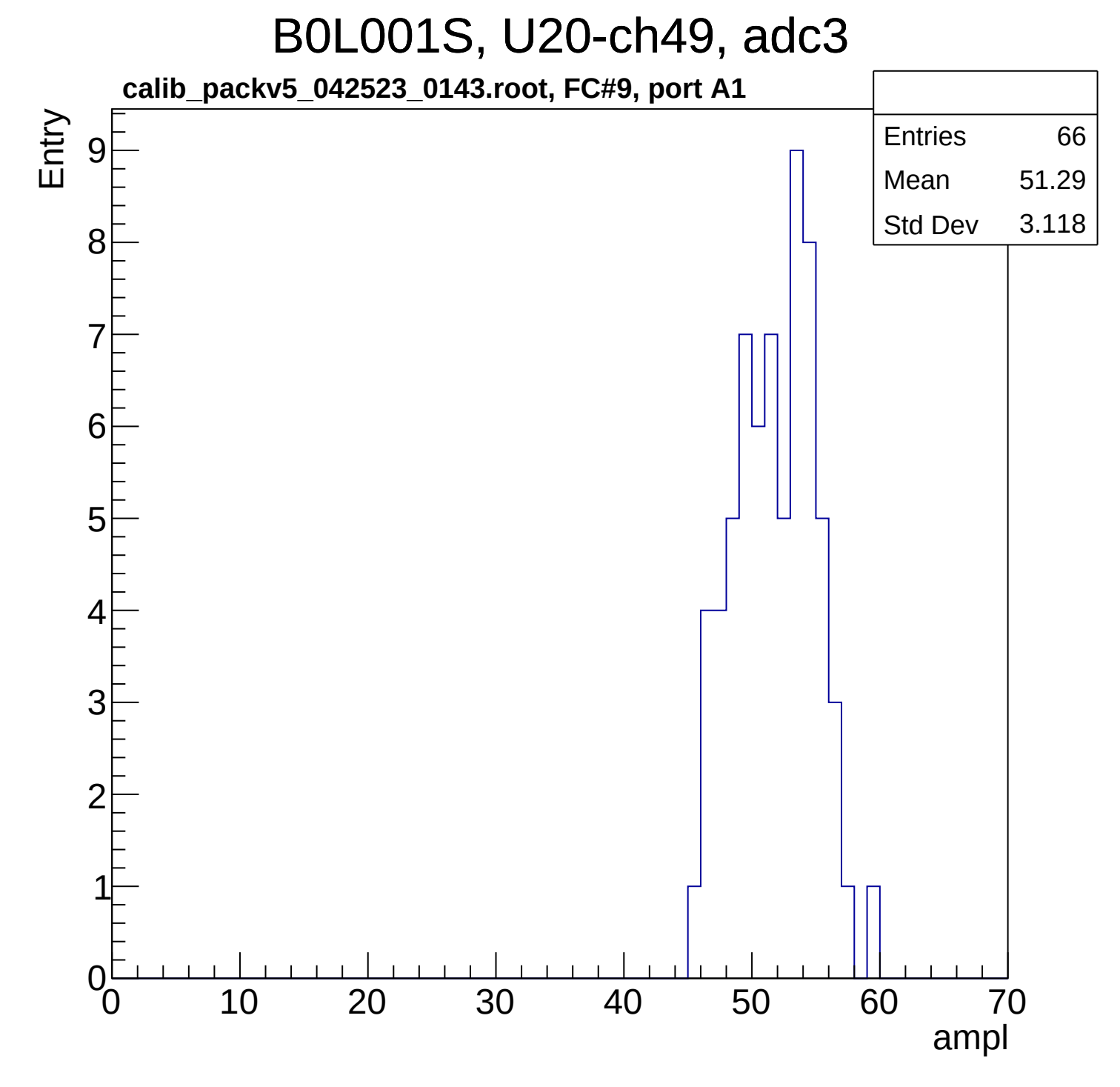
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.52
Std Dev 3.658
Gaus mean : 38.1179
Gaus Width: 4.0232
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.67
Std Dev 3.644
Gaus mean : 45.0568
Gaus Width: 4.1313
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.29
Std Dev 3.118
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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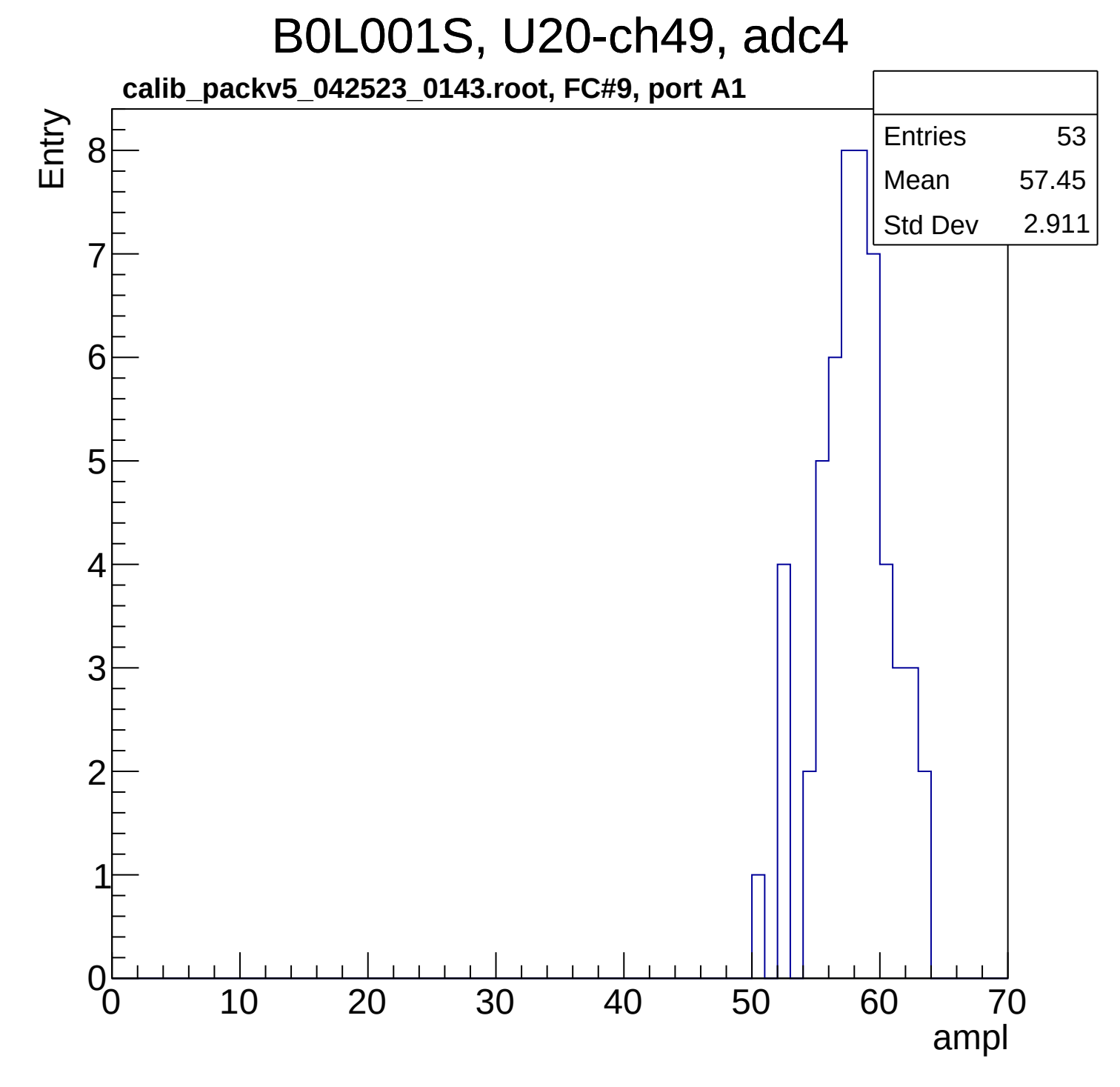
B0L001S, U20-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.45
Std Dev 2.911
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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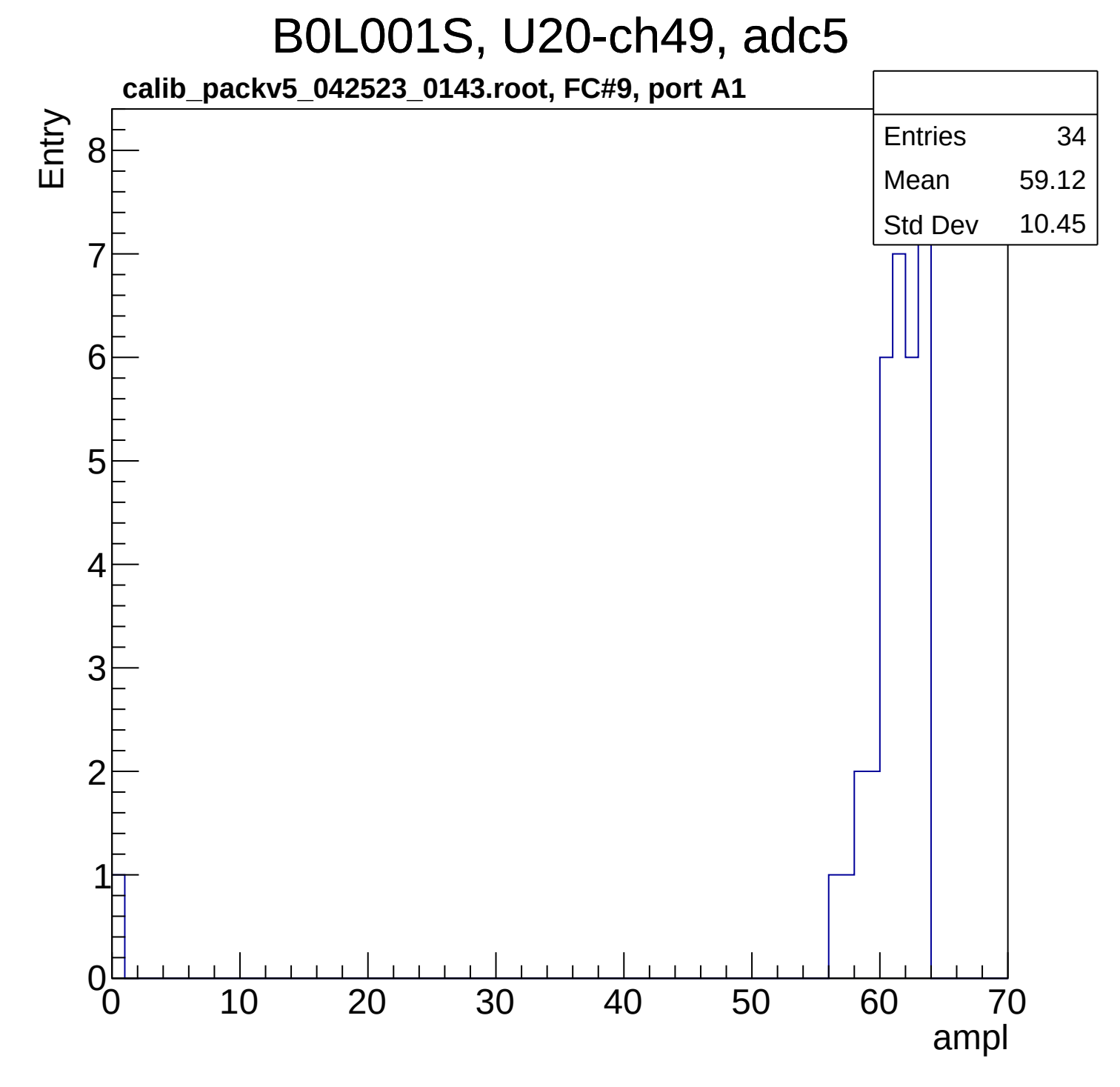
B0L001S, U20-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.12
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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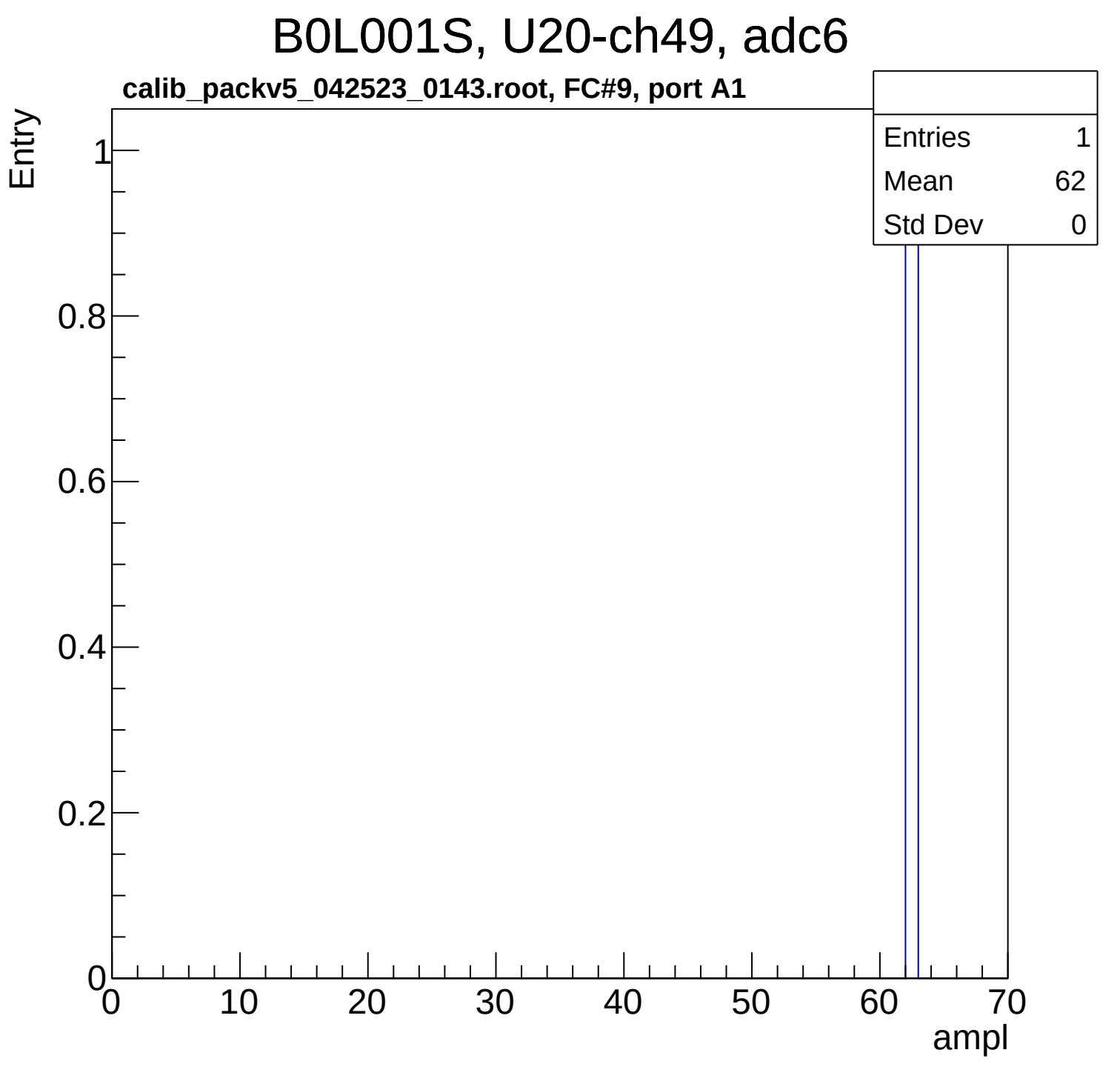
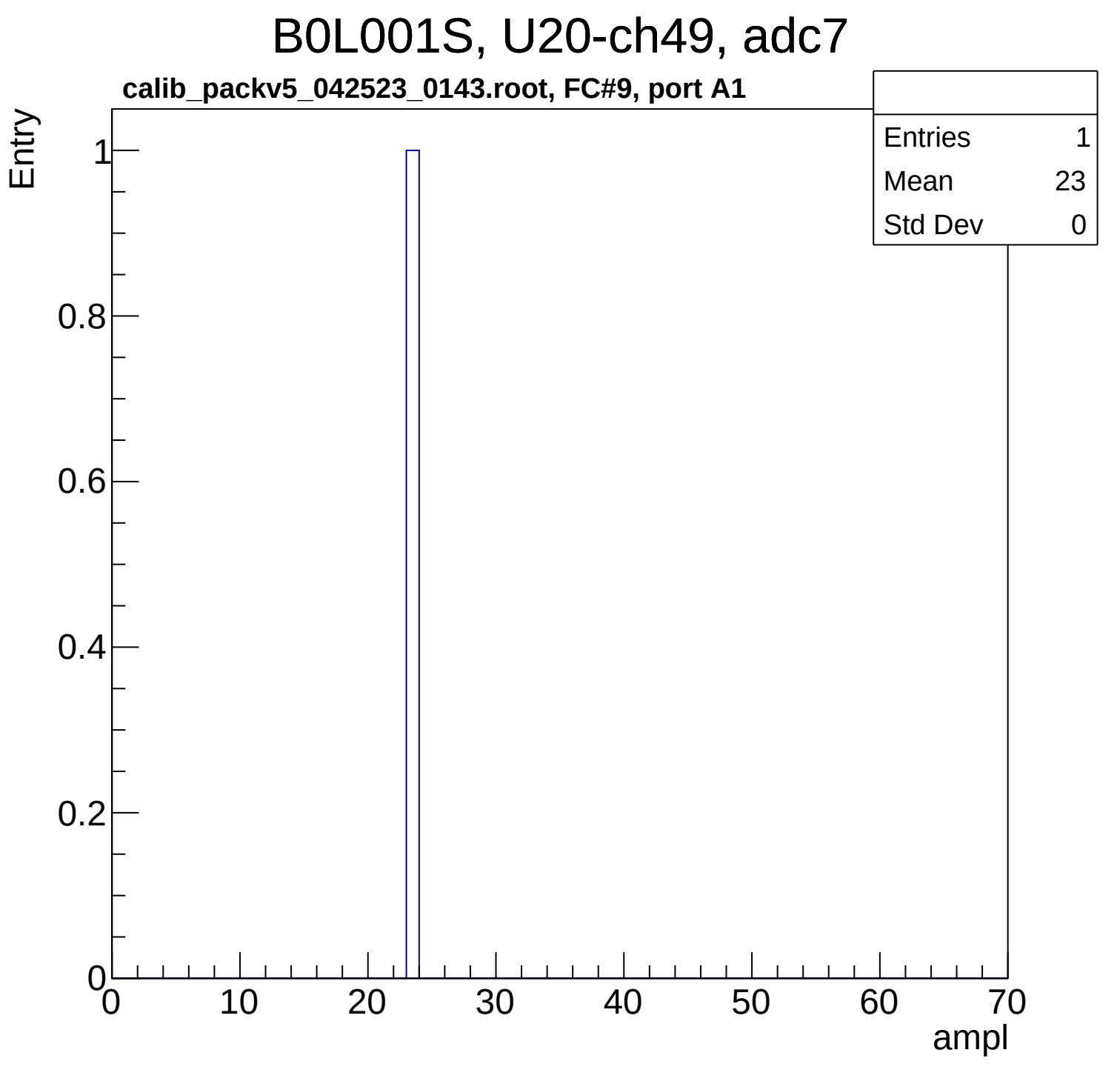

B0L001S, U20-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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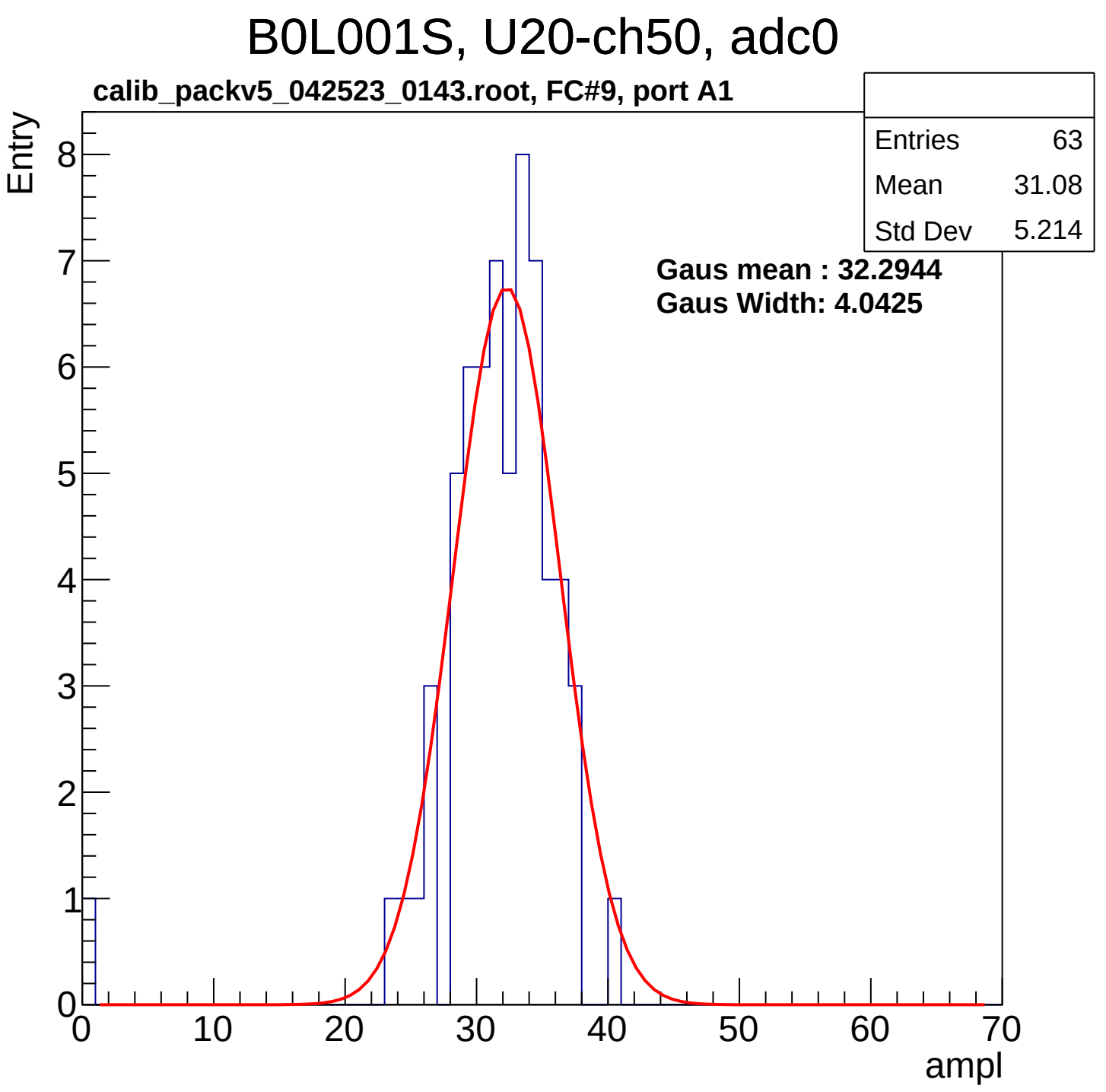
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.08
Std Dev 5.214
Gaus mean : 32.2944
Gaus Width: 4.0425
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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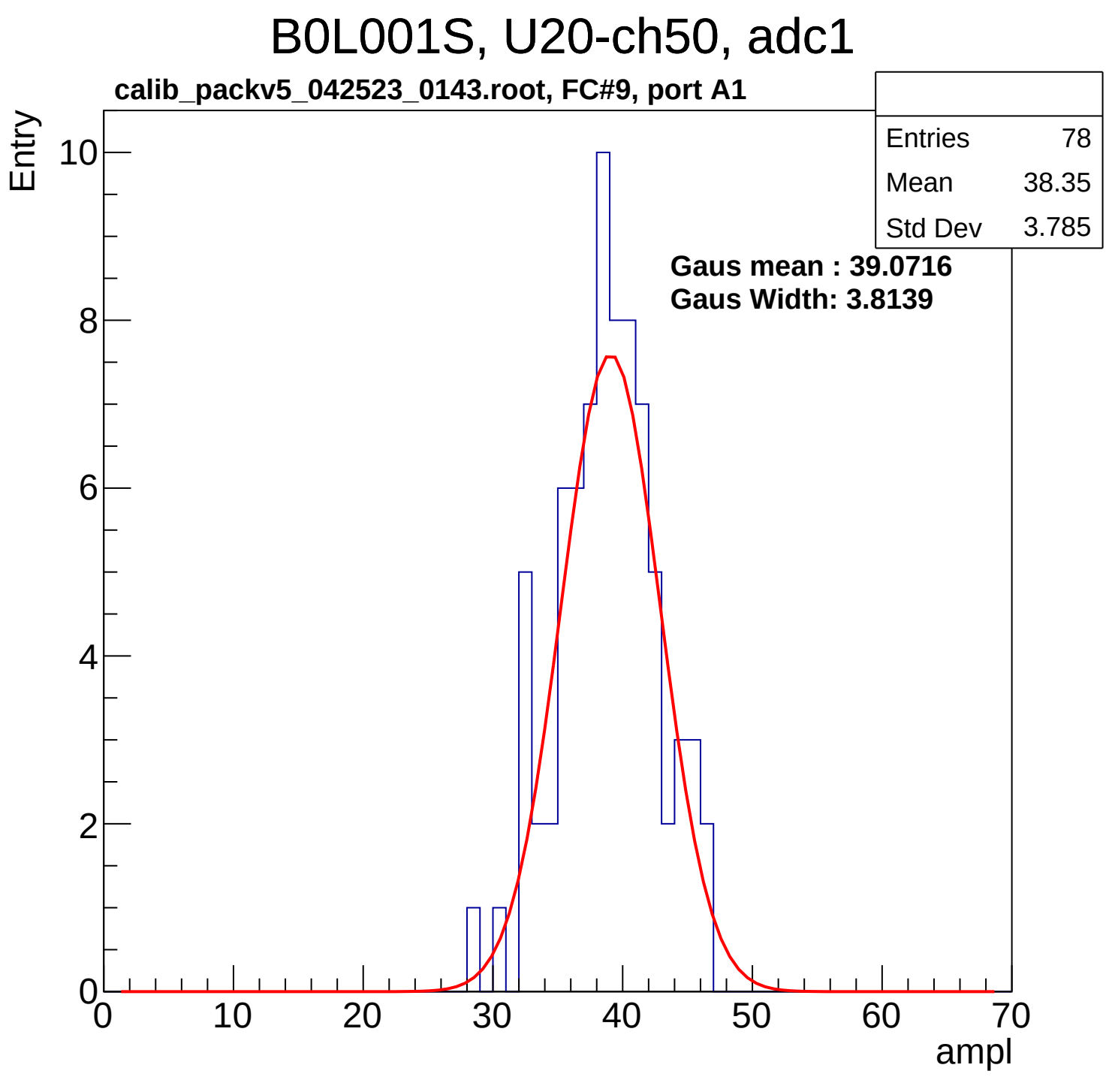
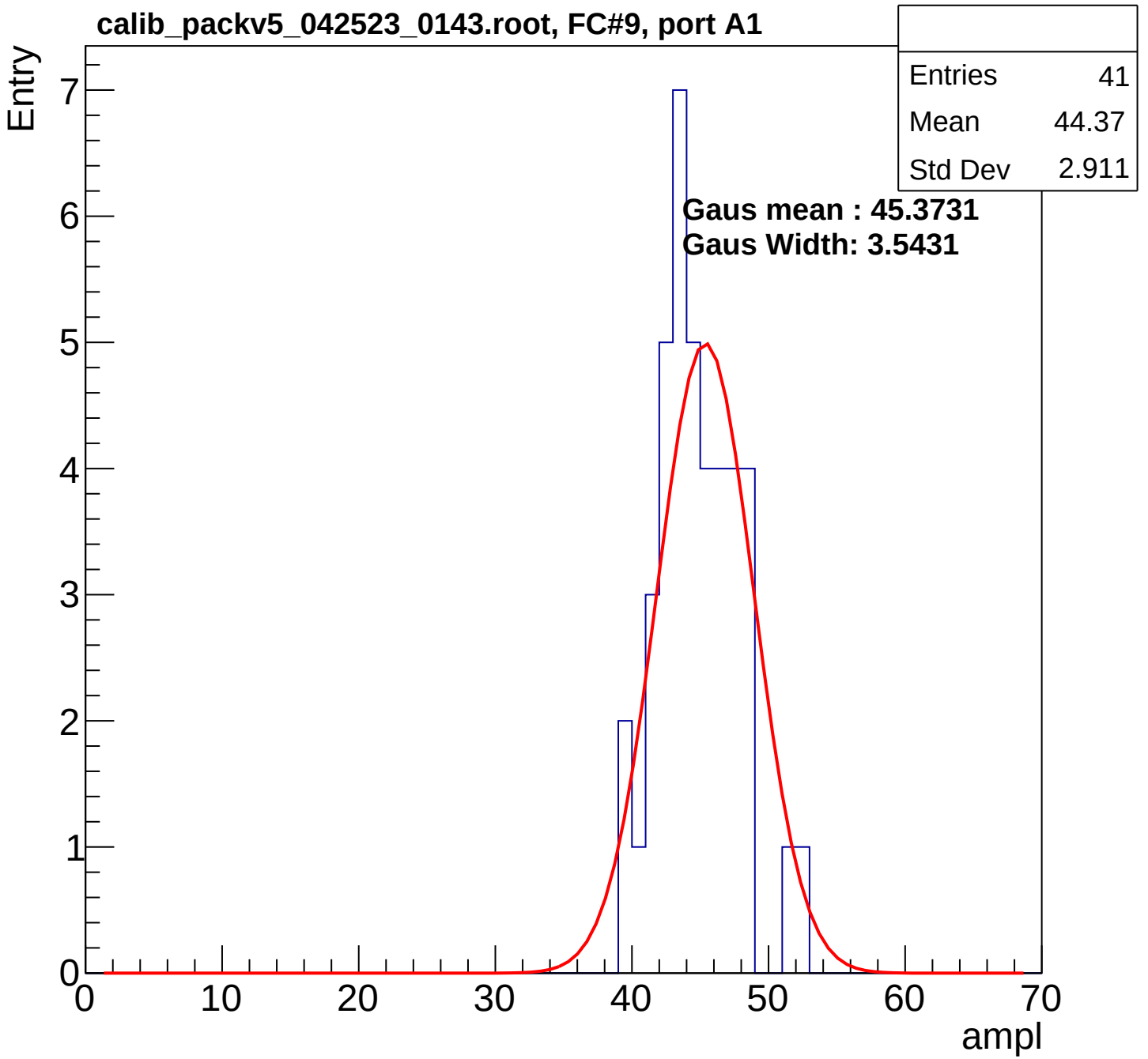
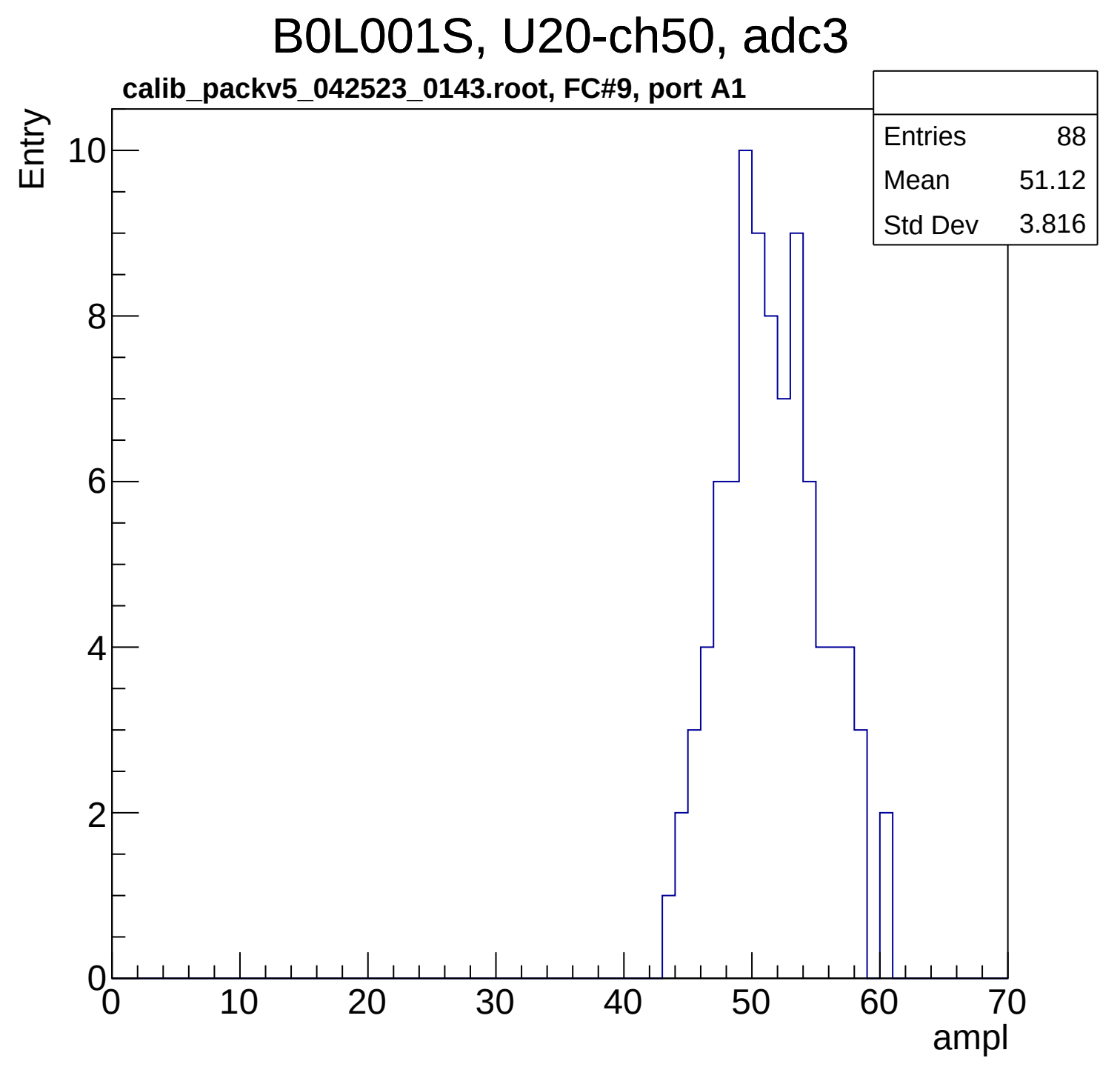

B0L001S, U20-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.35
Std Dev 3.785
Gaus mean : 39.0716
Gaus Width: 3.8139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 44.37
Std Dev 2.911
Gaus mean : 45.3731
Gaus Width: 3.5431
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 51.12
Std Dev 3.816
Entry
ampl
0
2
4
6
8
10
0
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40
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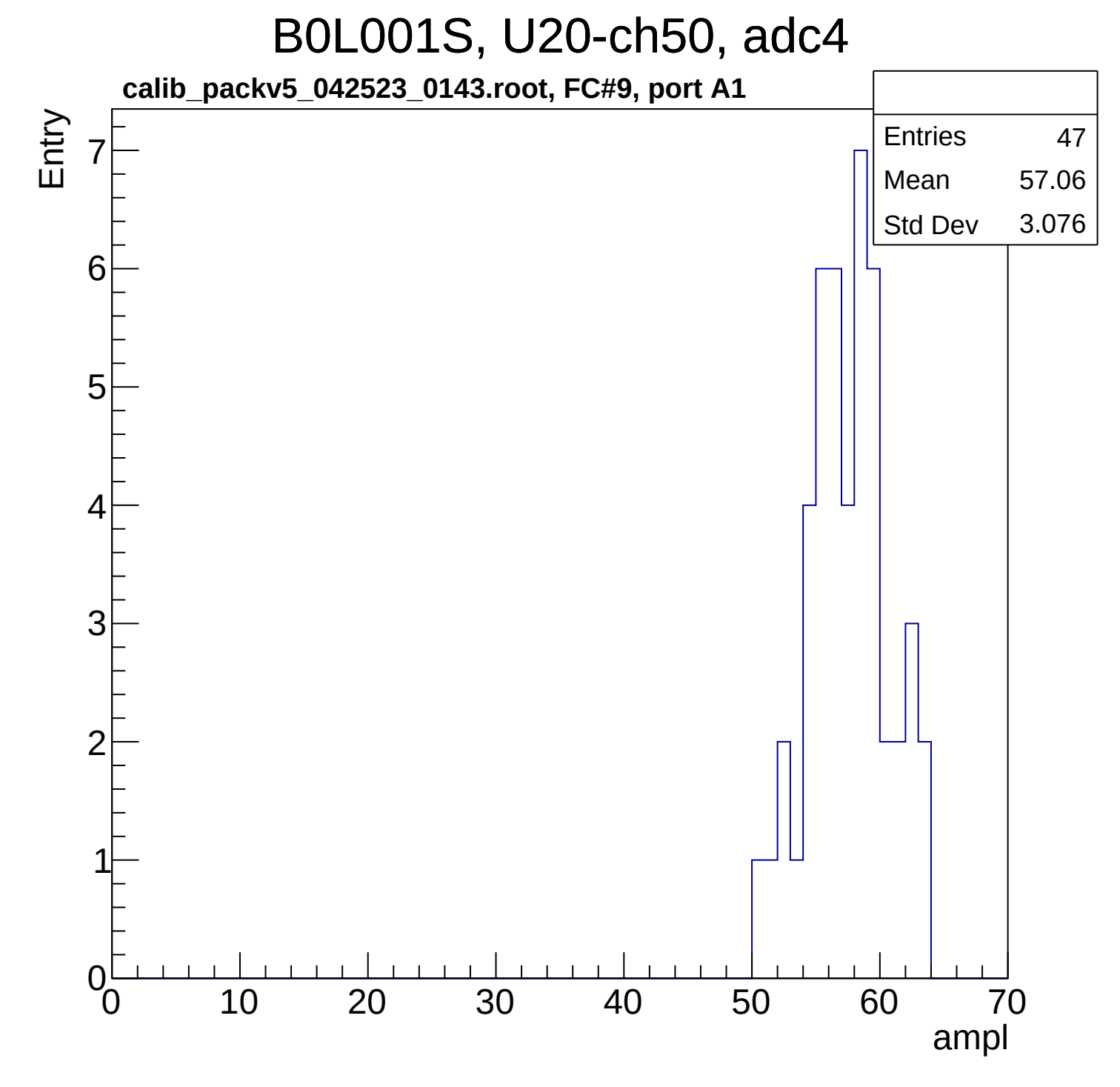
B0L001S, U20-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 57.06
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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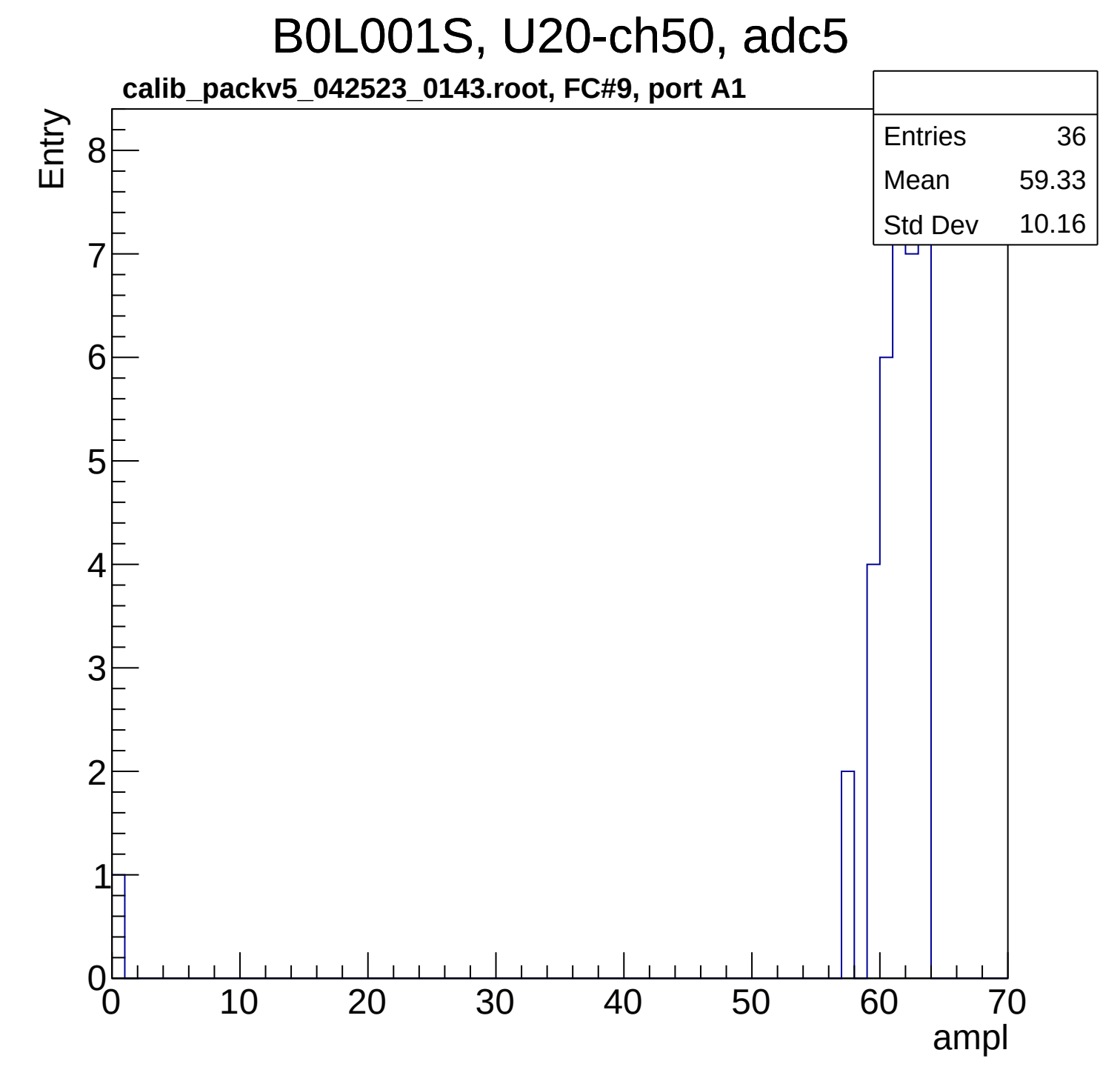
B0L001S, U20-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.33
Std Dev 10.16
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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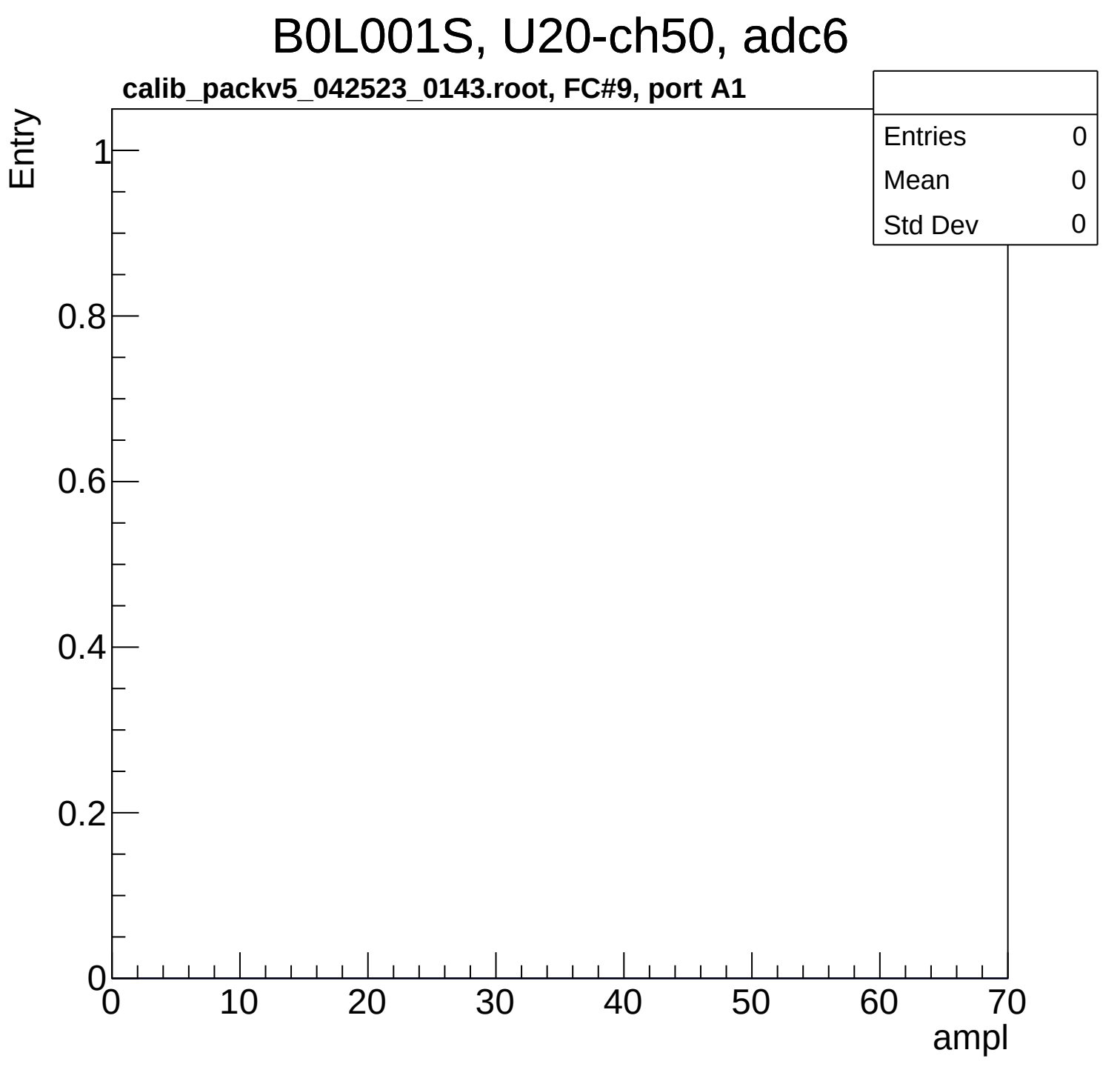
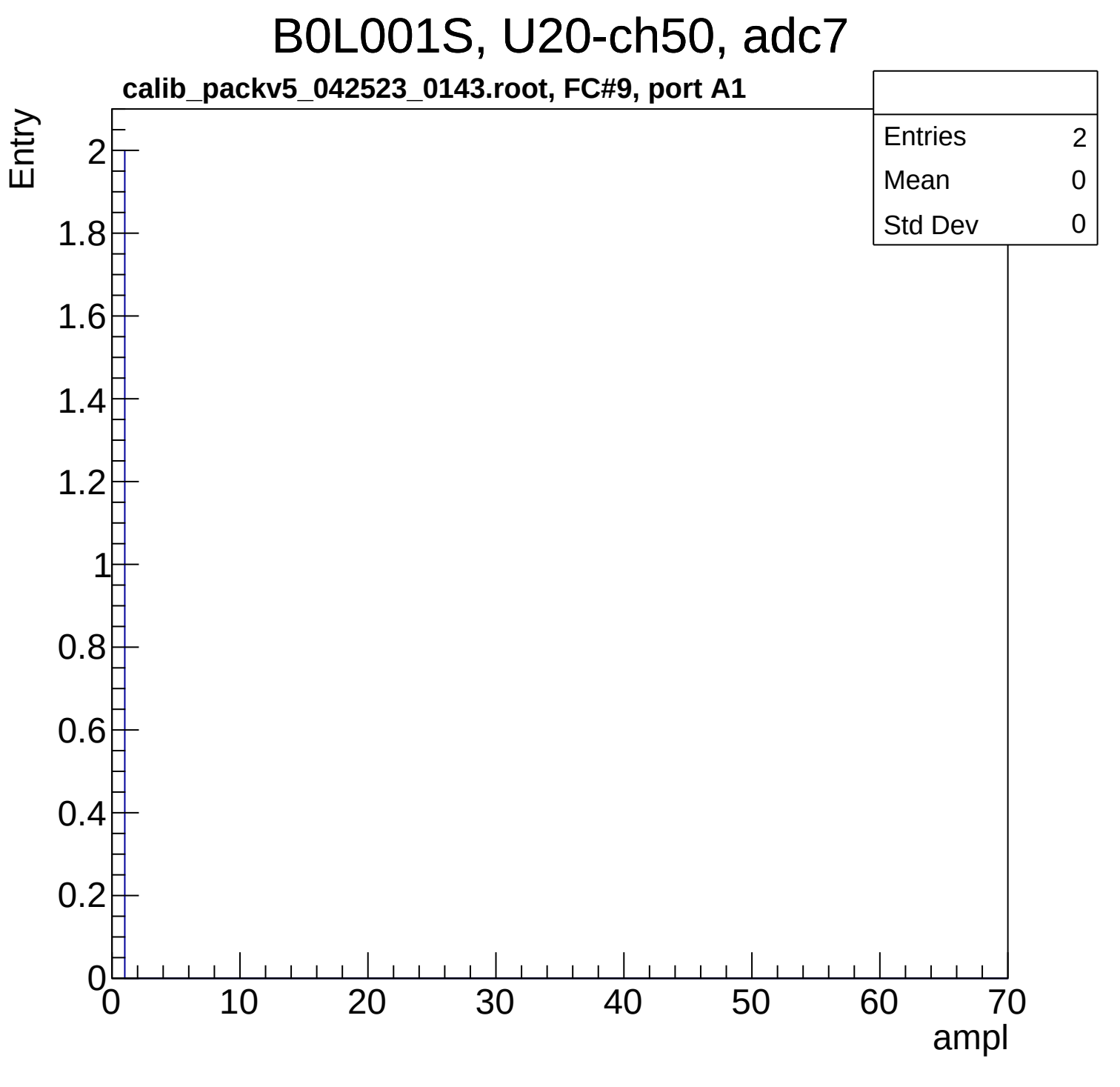
60
70


B0L001S, U20-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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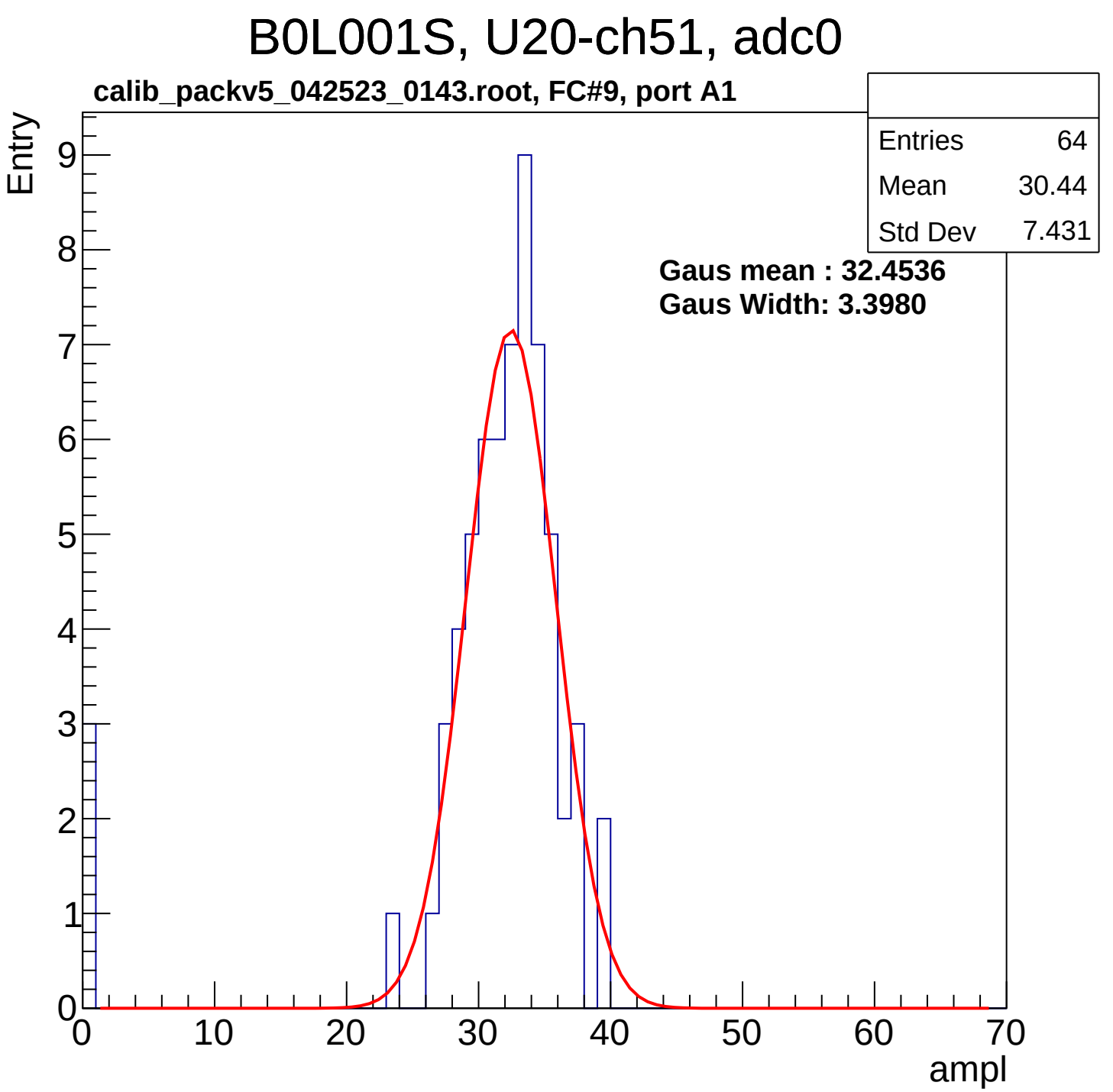
B0L001S, U20-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.44
Std Dev 7.431
Gaus mean : 32.4536
Gaus Width: 3.3980
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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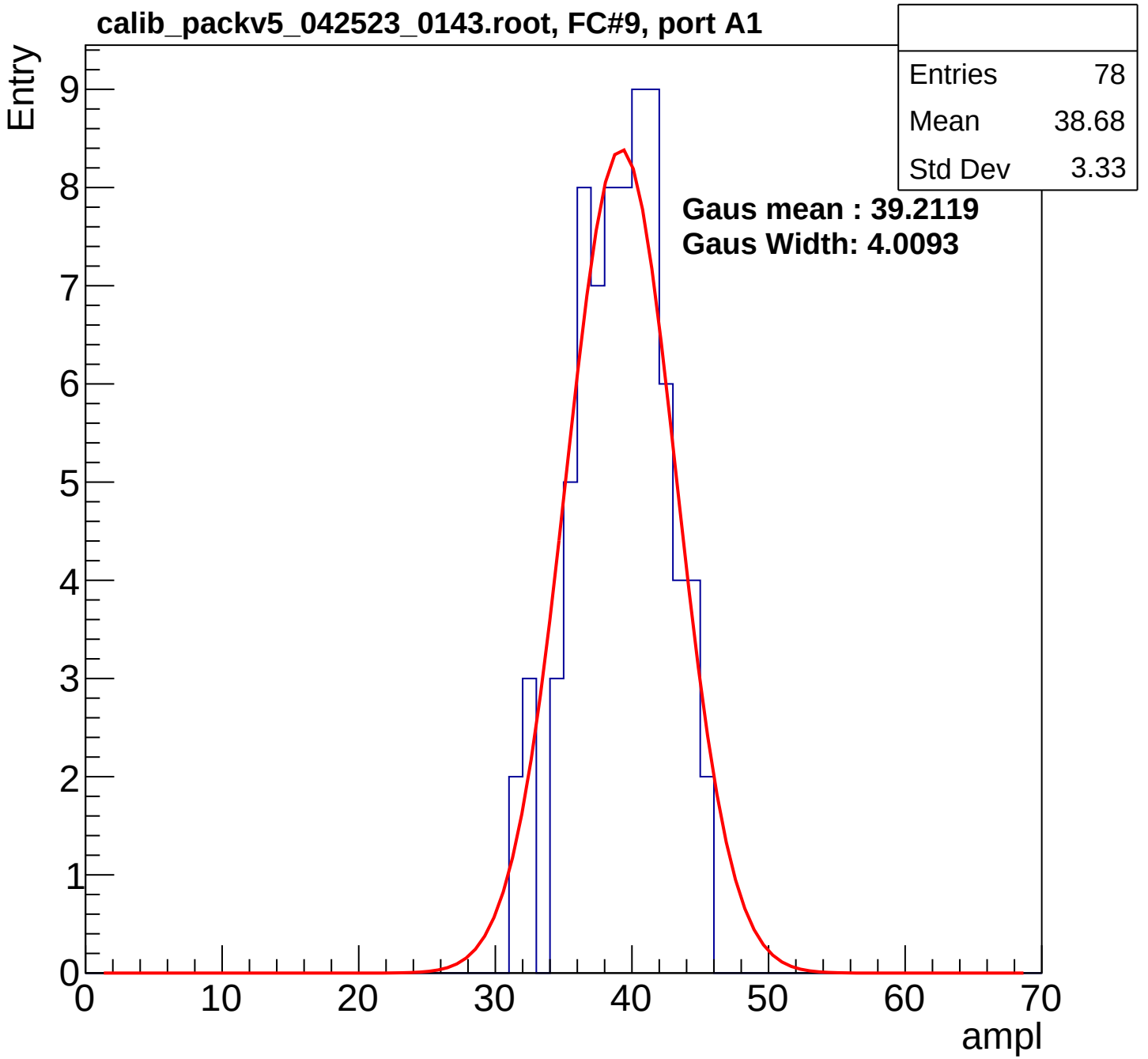
B0L001S, U20-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.68
Std Dev 3.33
Gaus mean : 39.2119
Gaus Width: 4.0093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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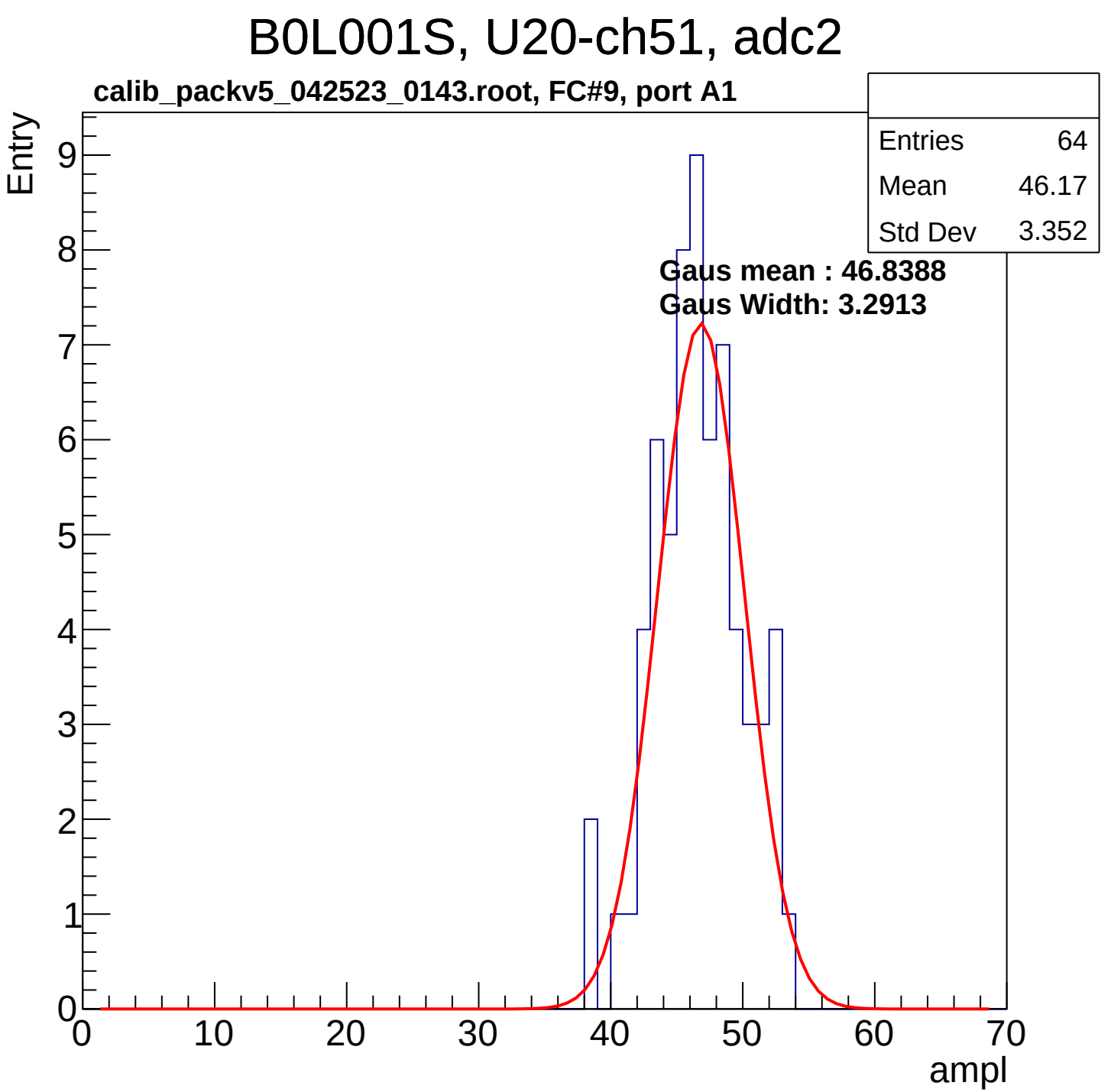
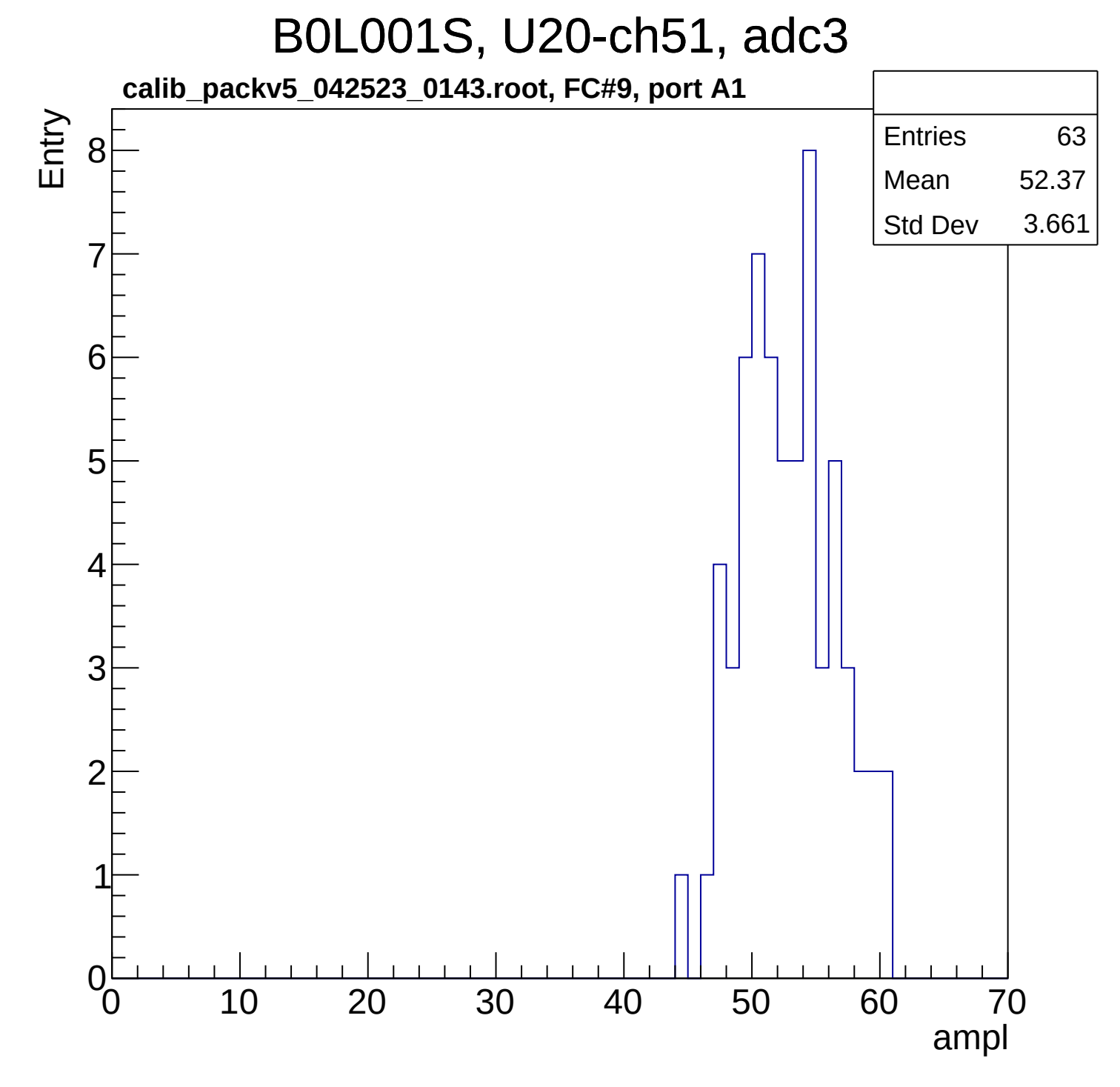

B0L001S, U20-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 46.17
Std Dev 3.352
Gaus mean : 46.8388
Gaus Width: 3.2913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 52.37
Std Dev 3.661
Entry
ampl
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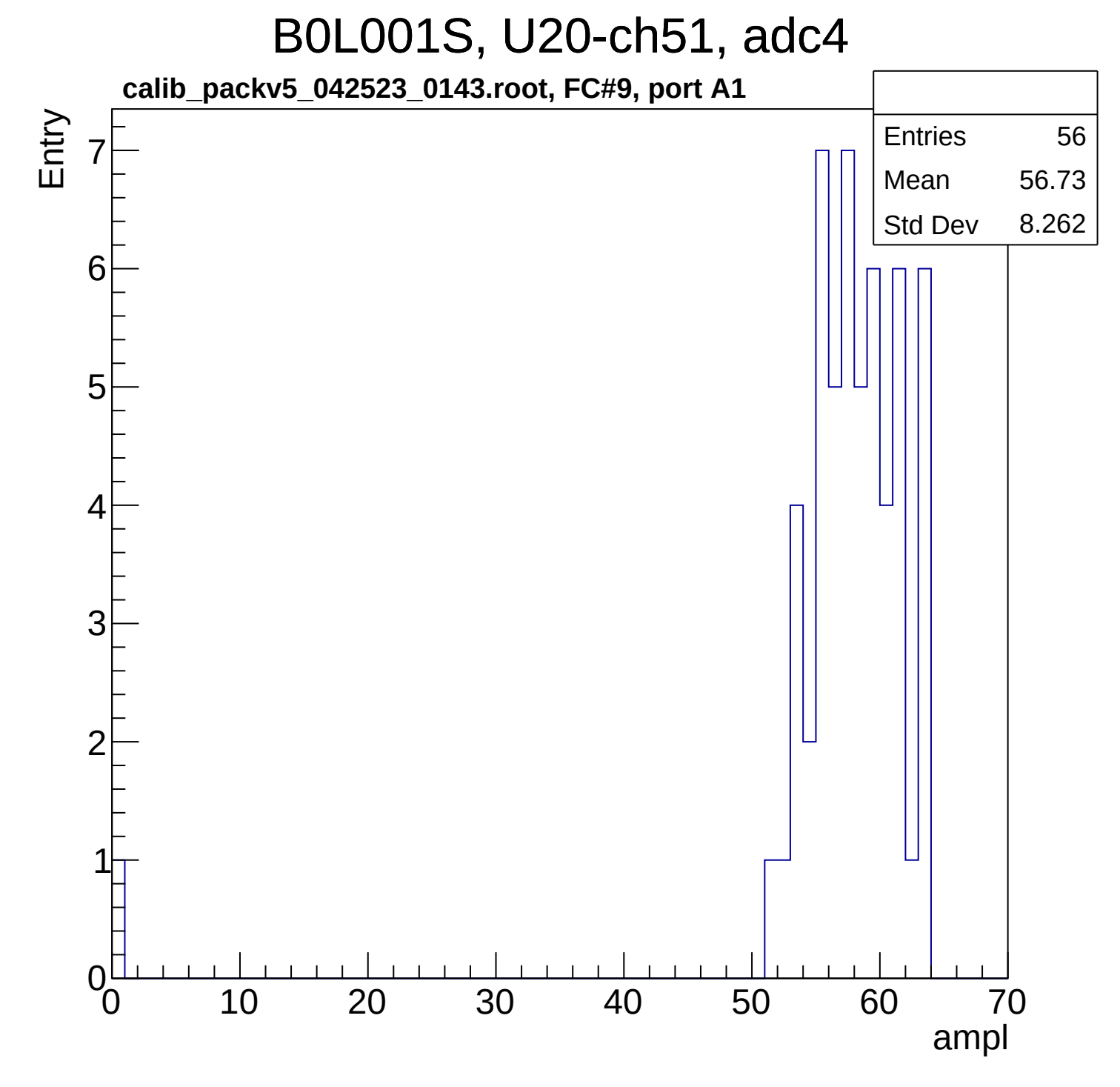
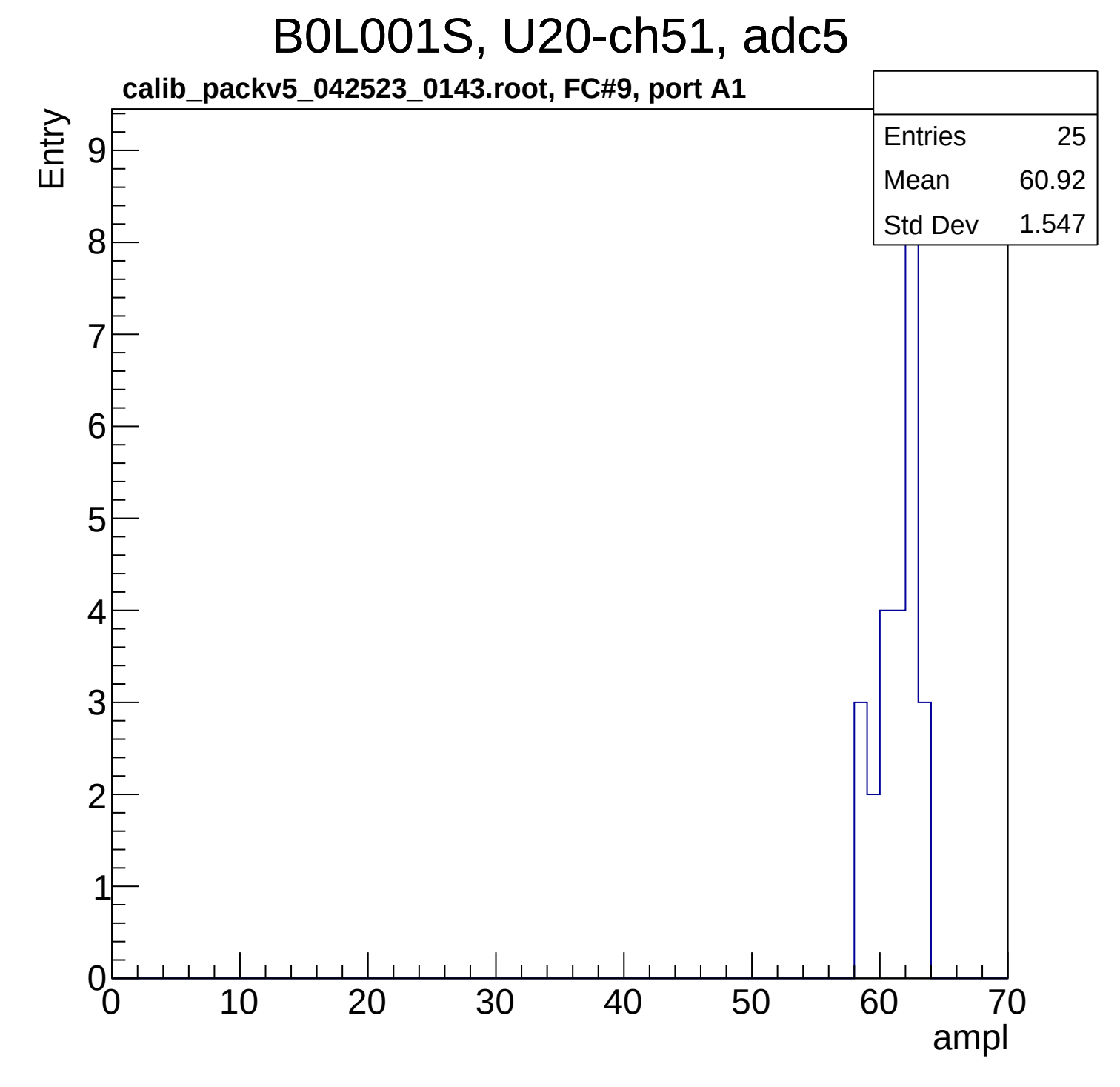
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.73
Std Dev 8.262
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 60.92
Std Dev 1.547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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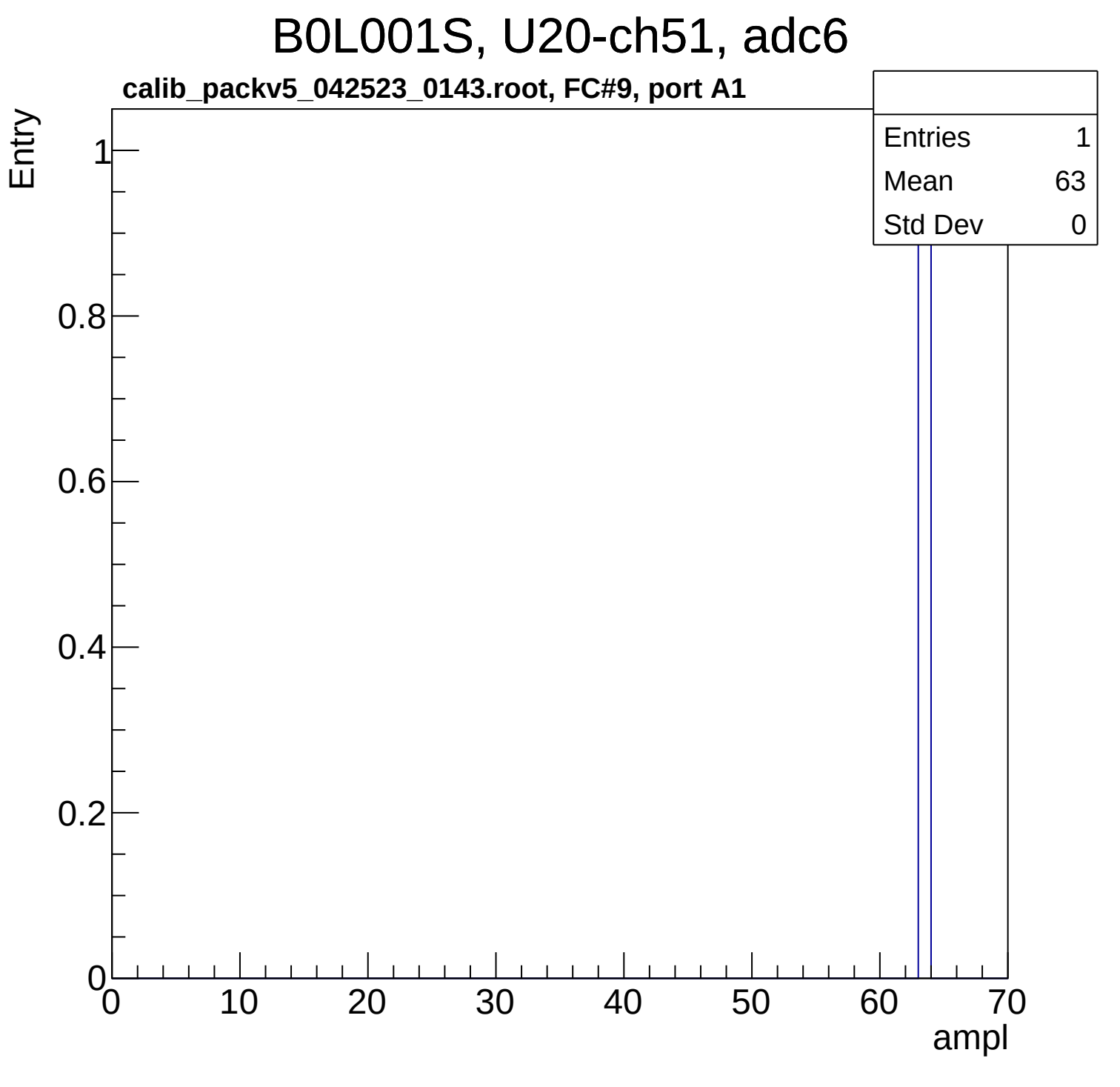
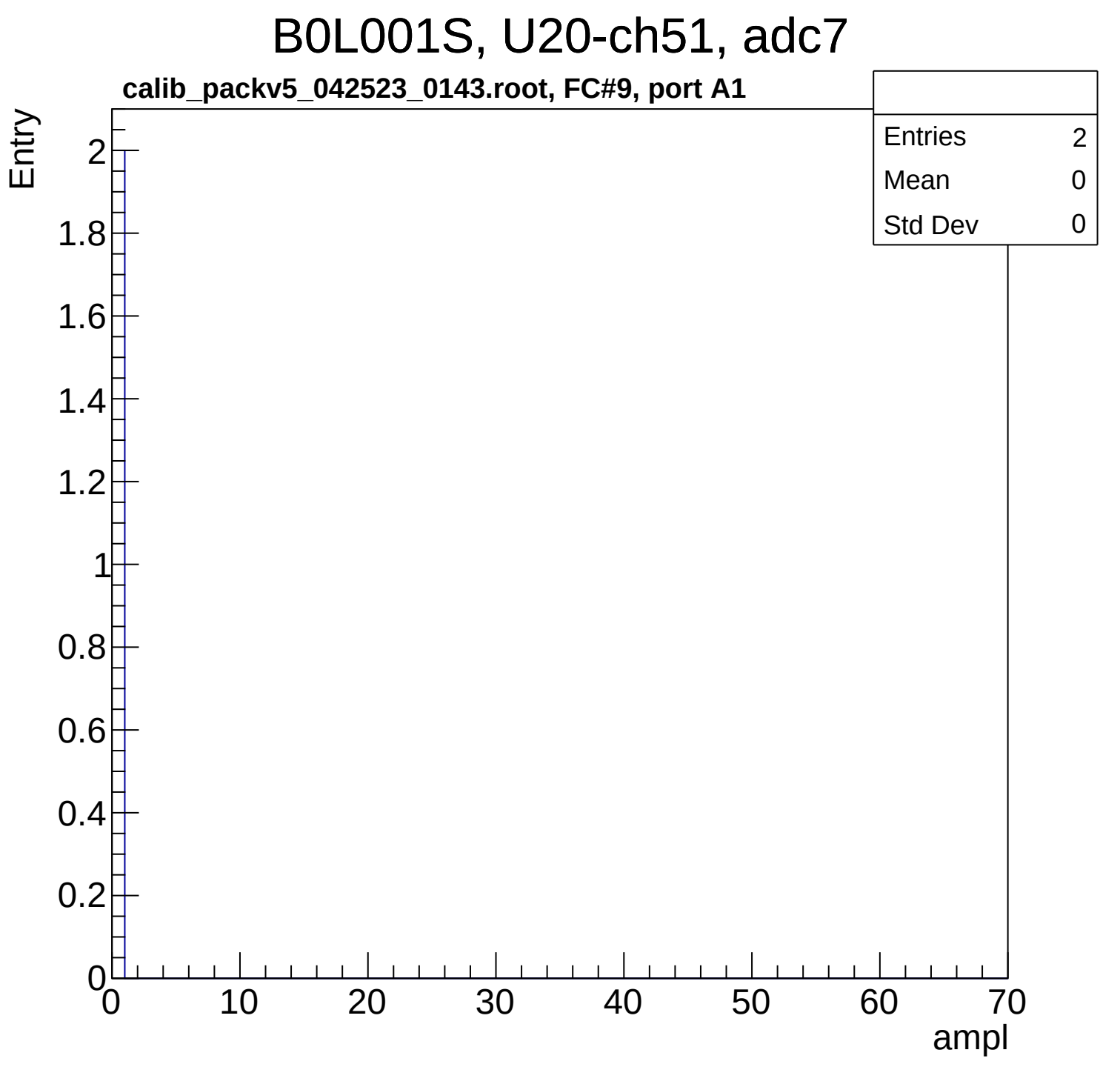
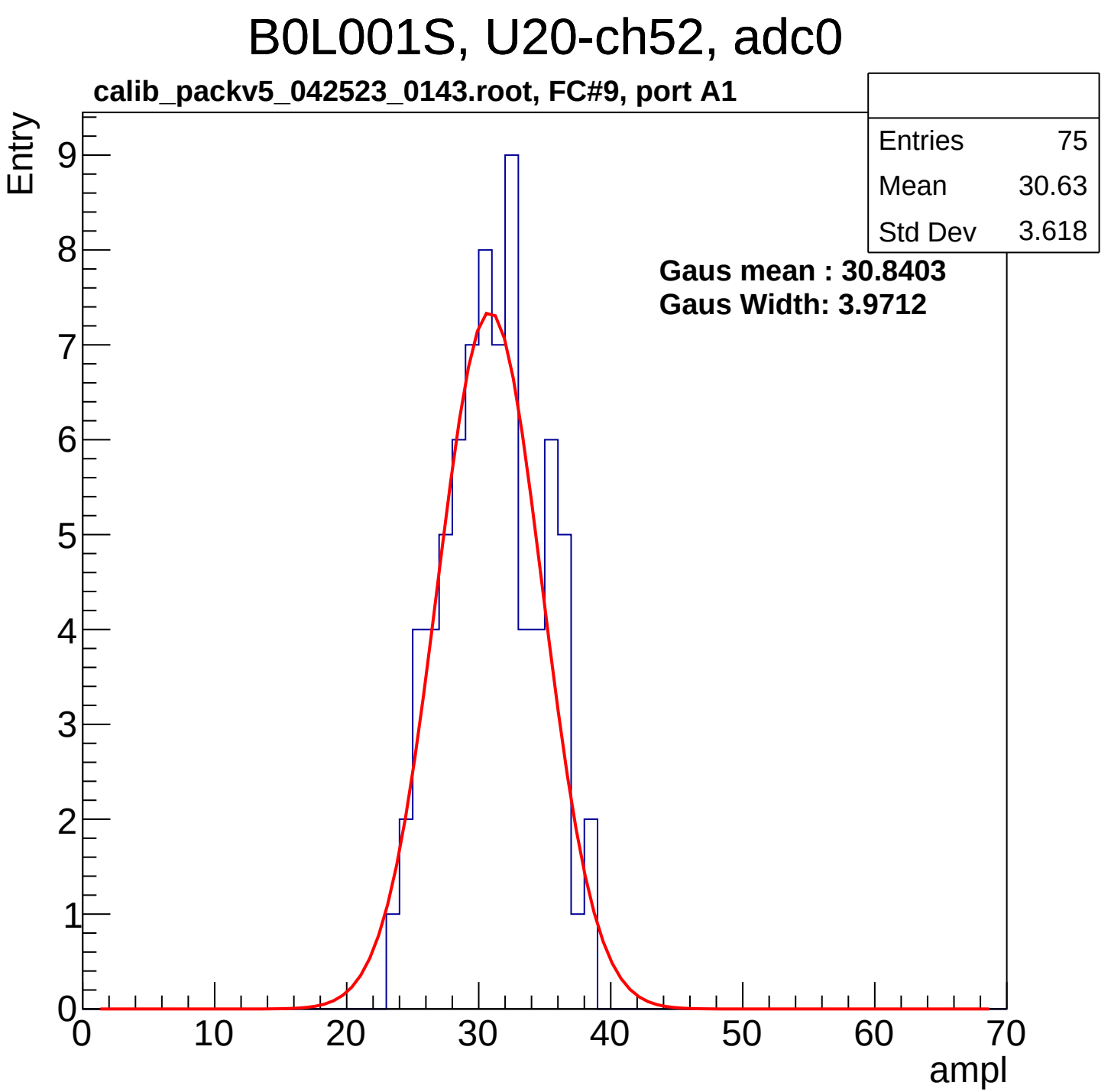
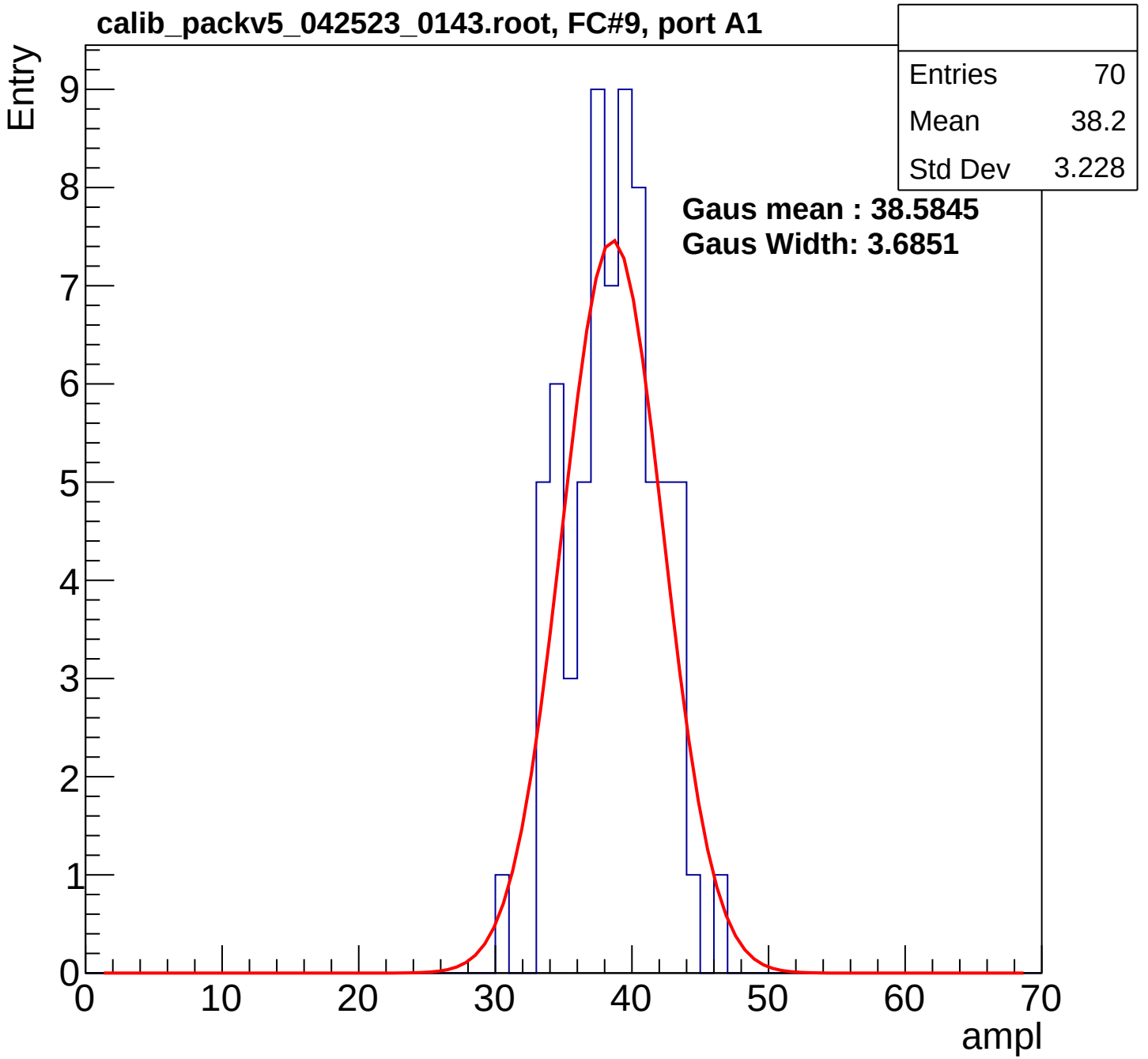
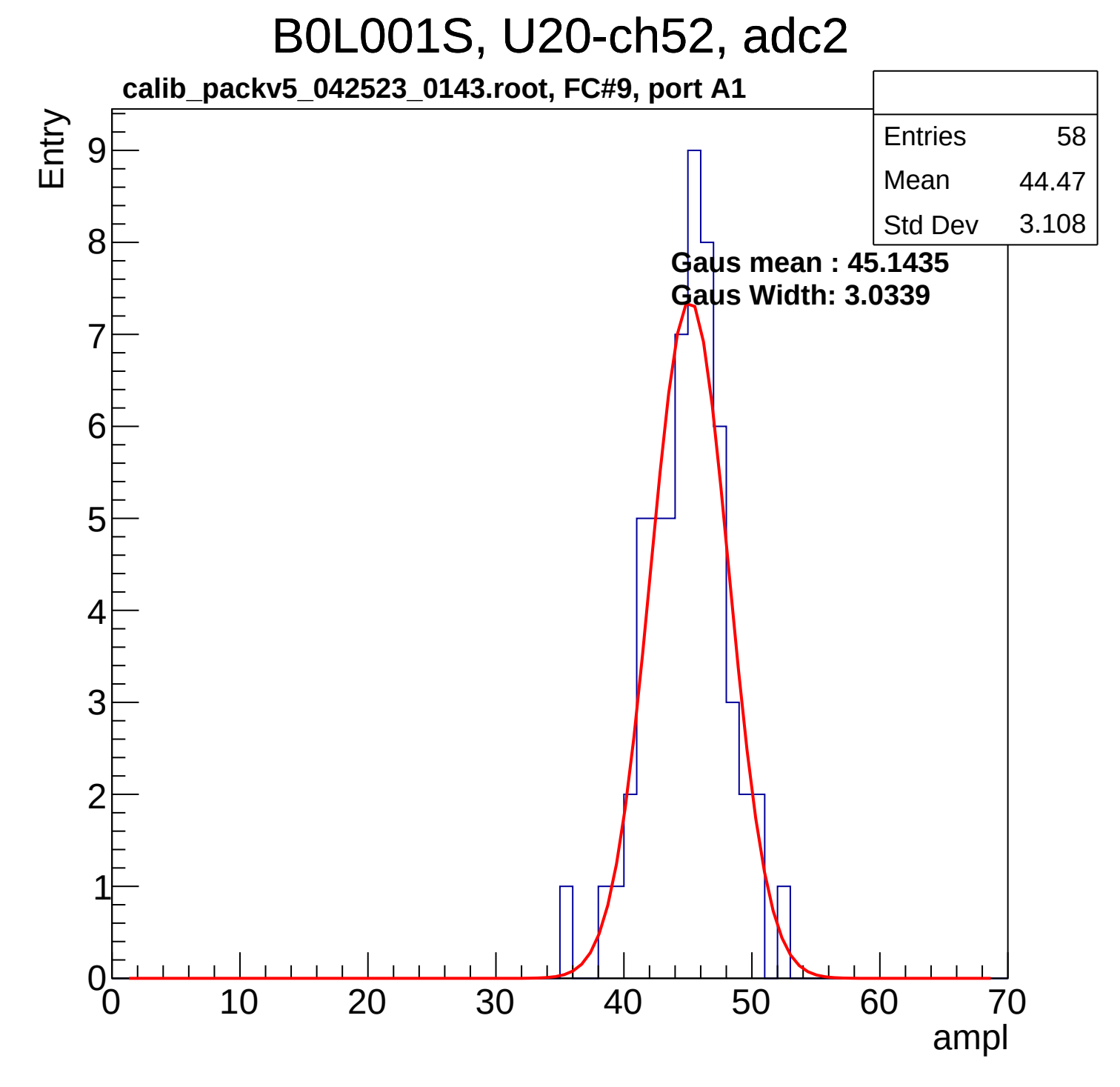

B0L001S, U20-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.63
Std Dev 3.618
Gaus mean : 30.8403
Gaus Width: 3.9712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 38.2
Std Dev 3.228
Gaus mean : 38.5845
Gaus Width: 3.6851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.47
Std Dev 3.108
Gaus mean : 45.1435
Gaus Width: 3.0339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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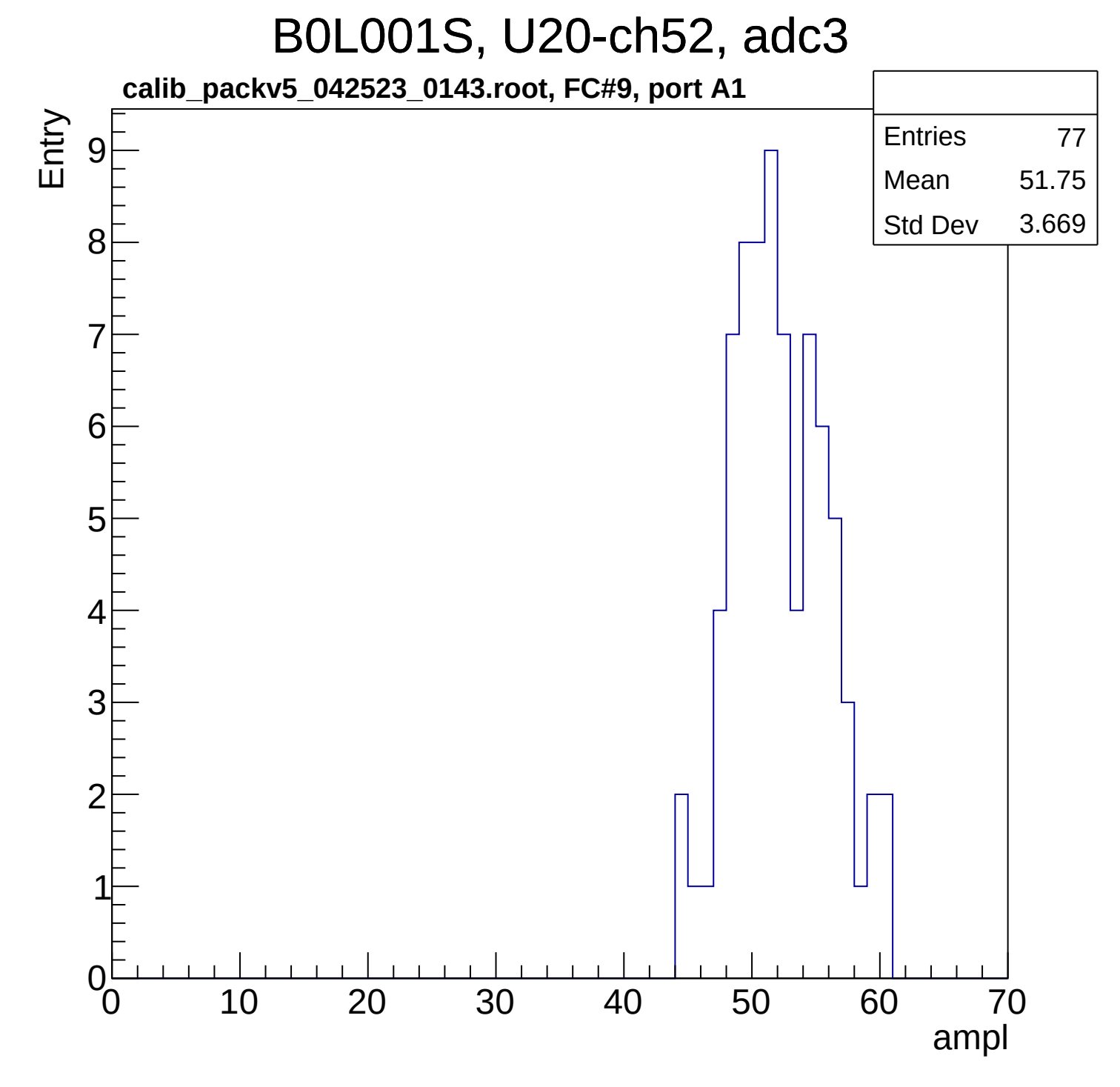
B0L001S, U20-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.75
Std Dev 3.669
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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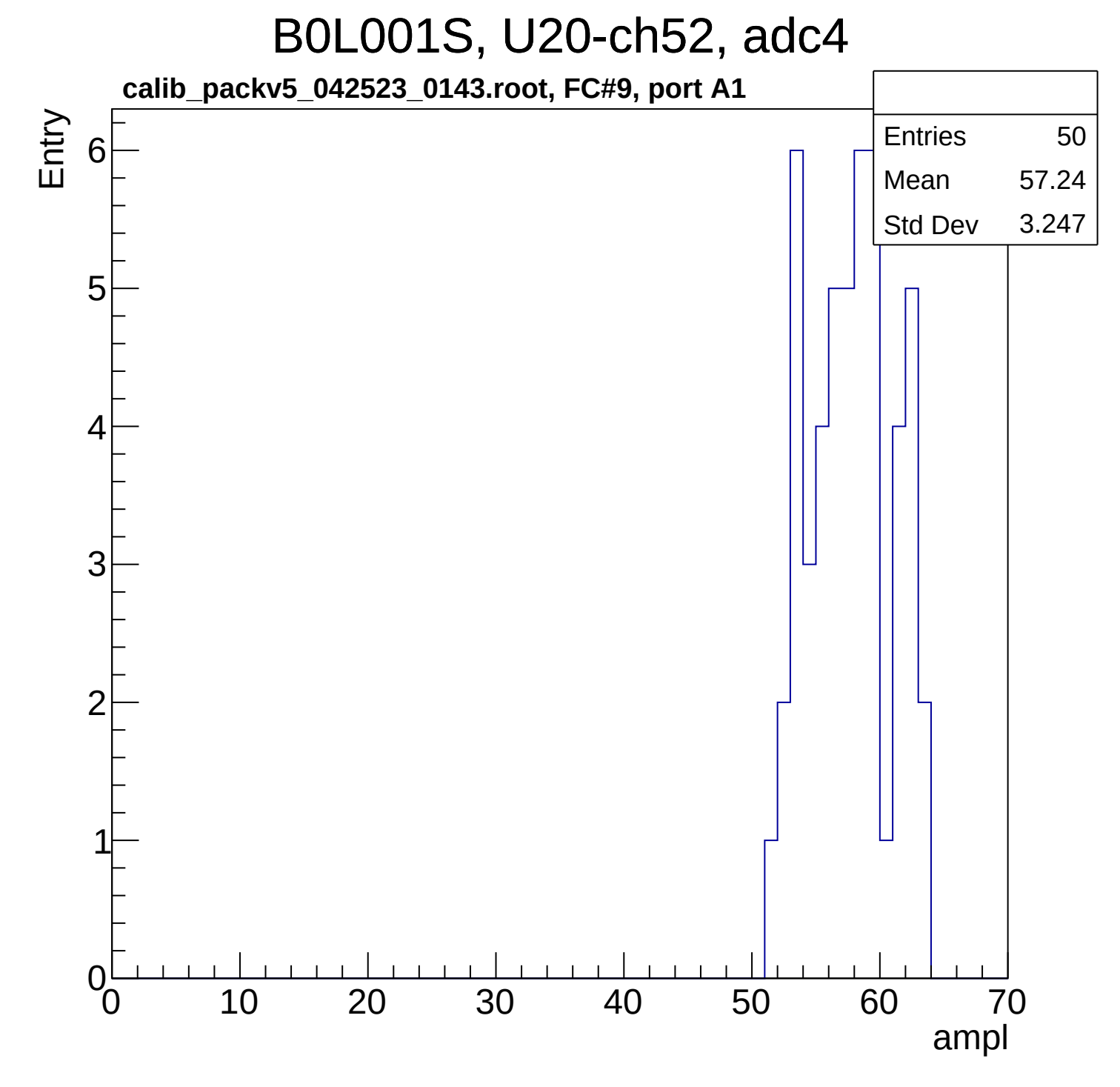
B0L001S, U20-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.24
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
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0
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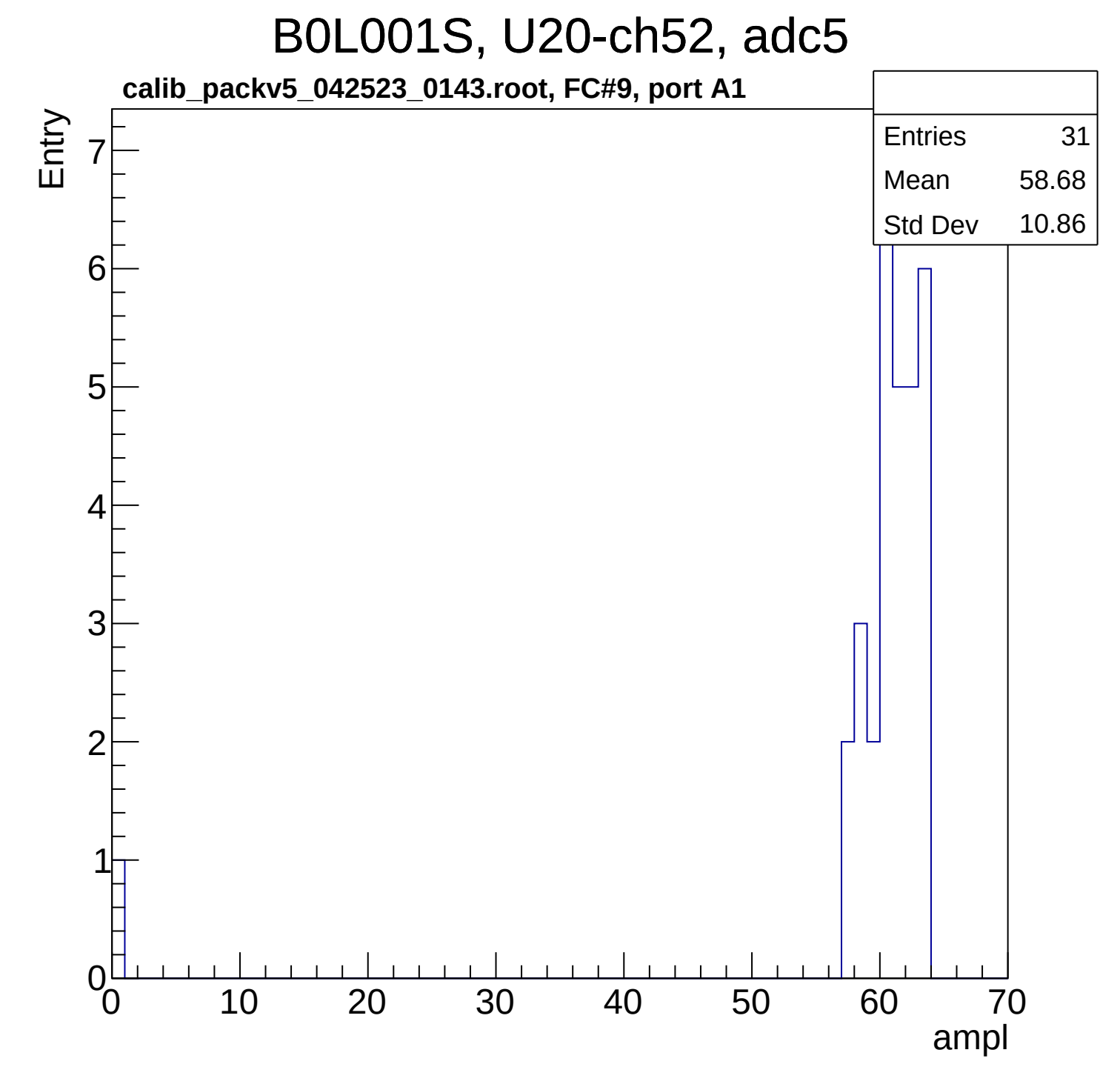
20
30
40
50
60
70


B0L001S, U20-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.68
Std Dev 10.86
Entry
ampl
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1
2
3
4
5
6
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0
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20
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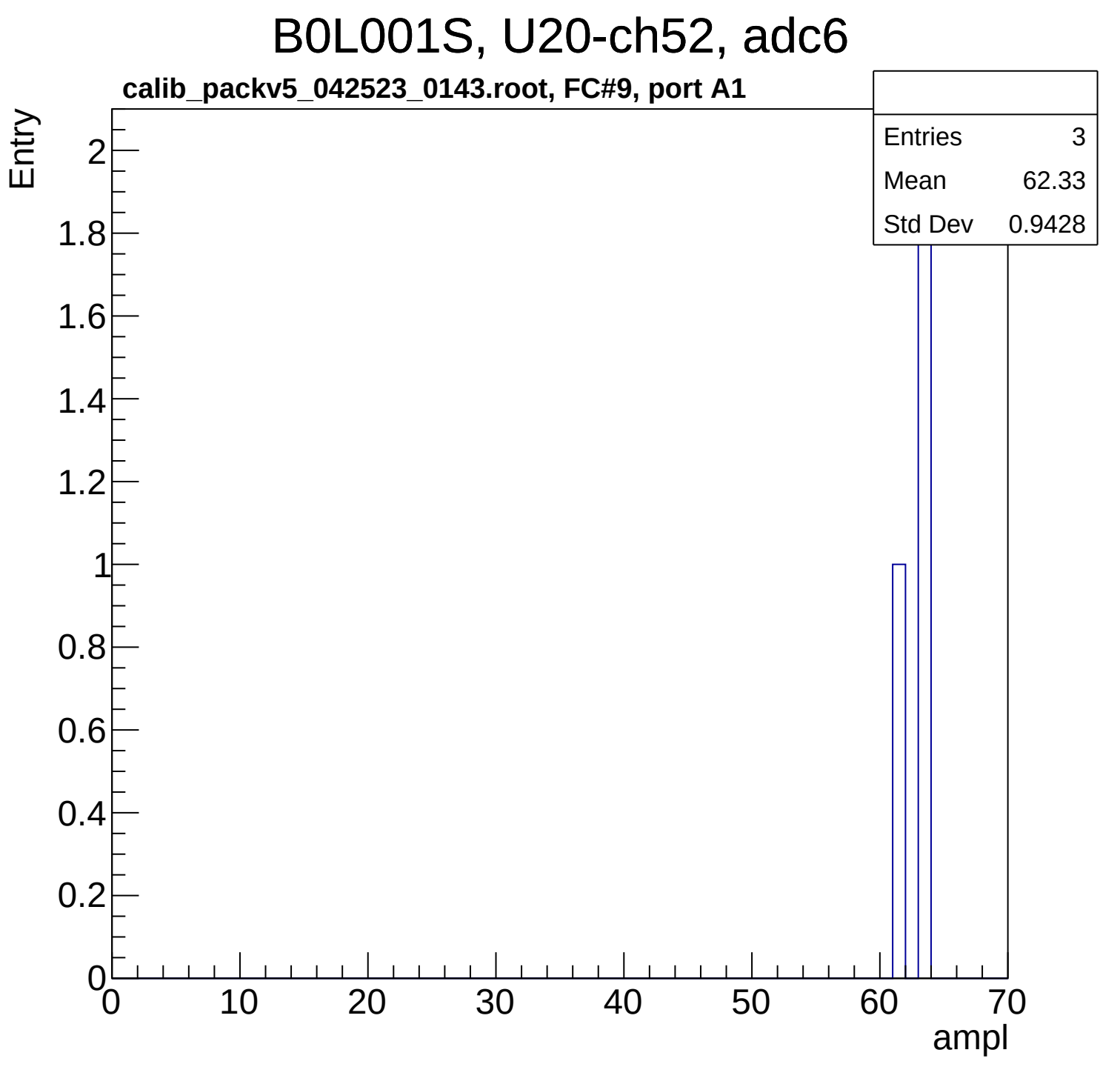
40
50
60
70


B0L001S, U20-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
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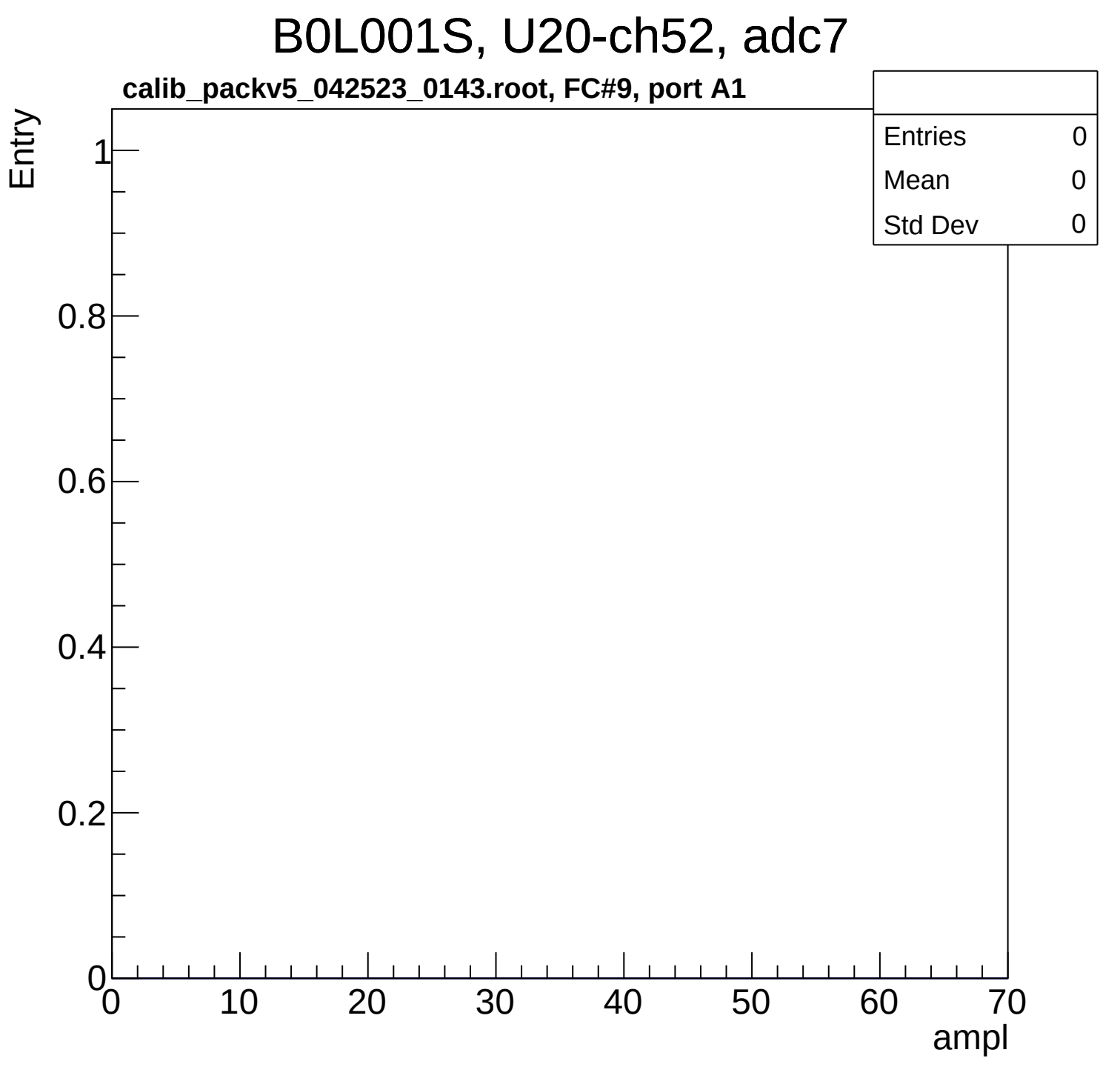
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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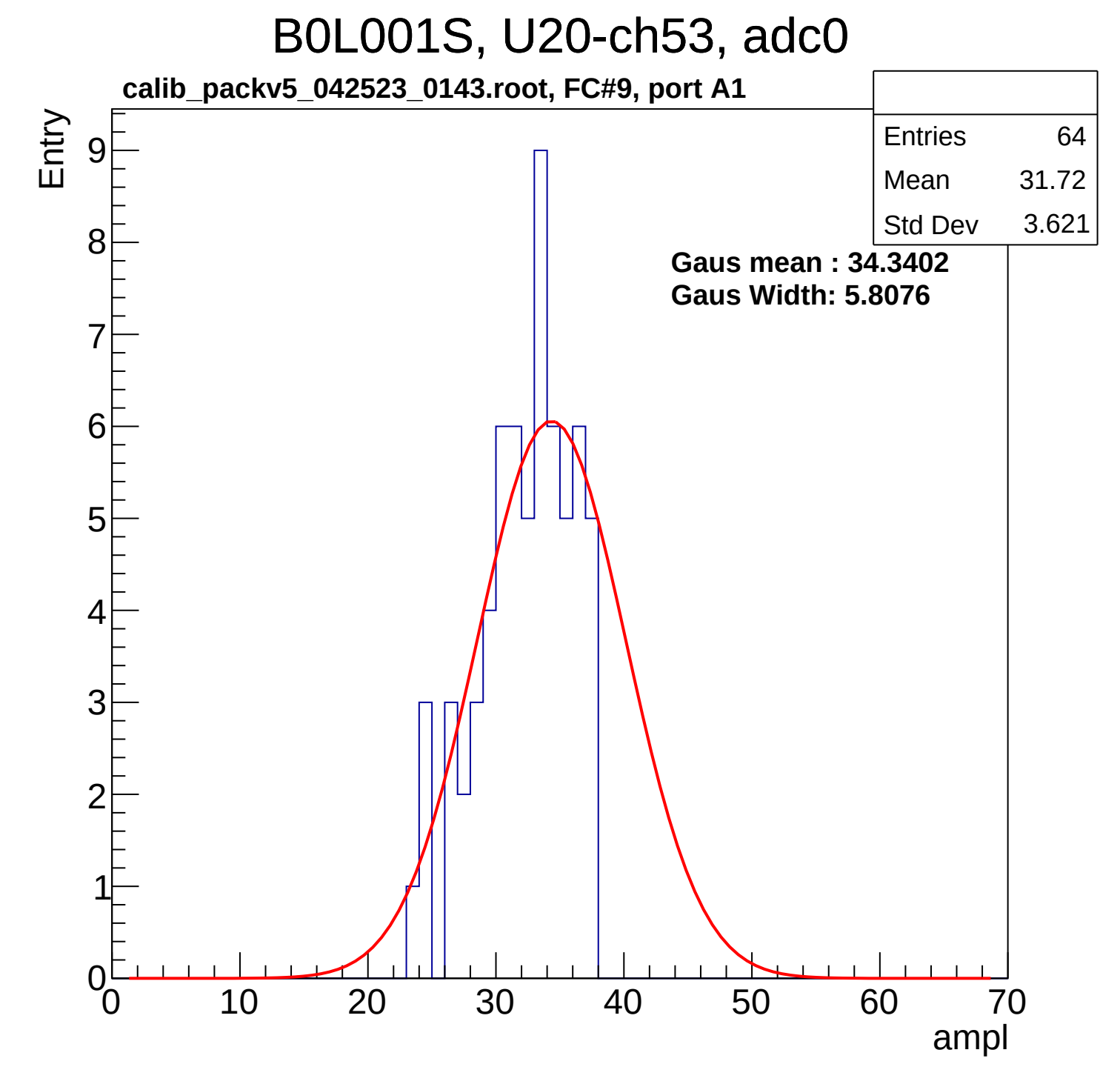
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 31.72
Std Dev 3.621
Gaus mean : 34.3402
Gaus Width: 5.8076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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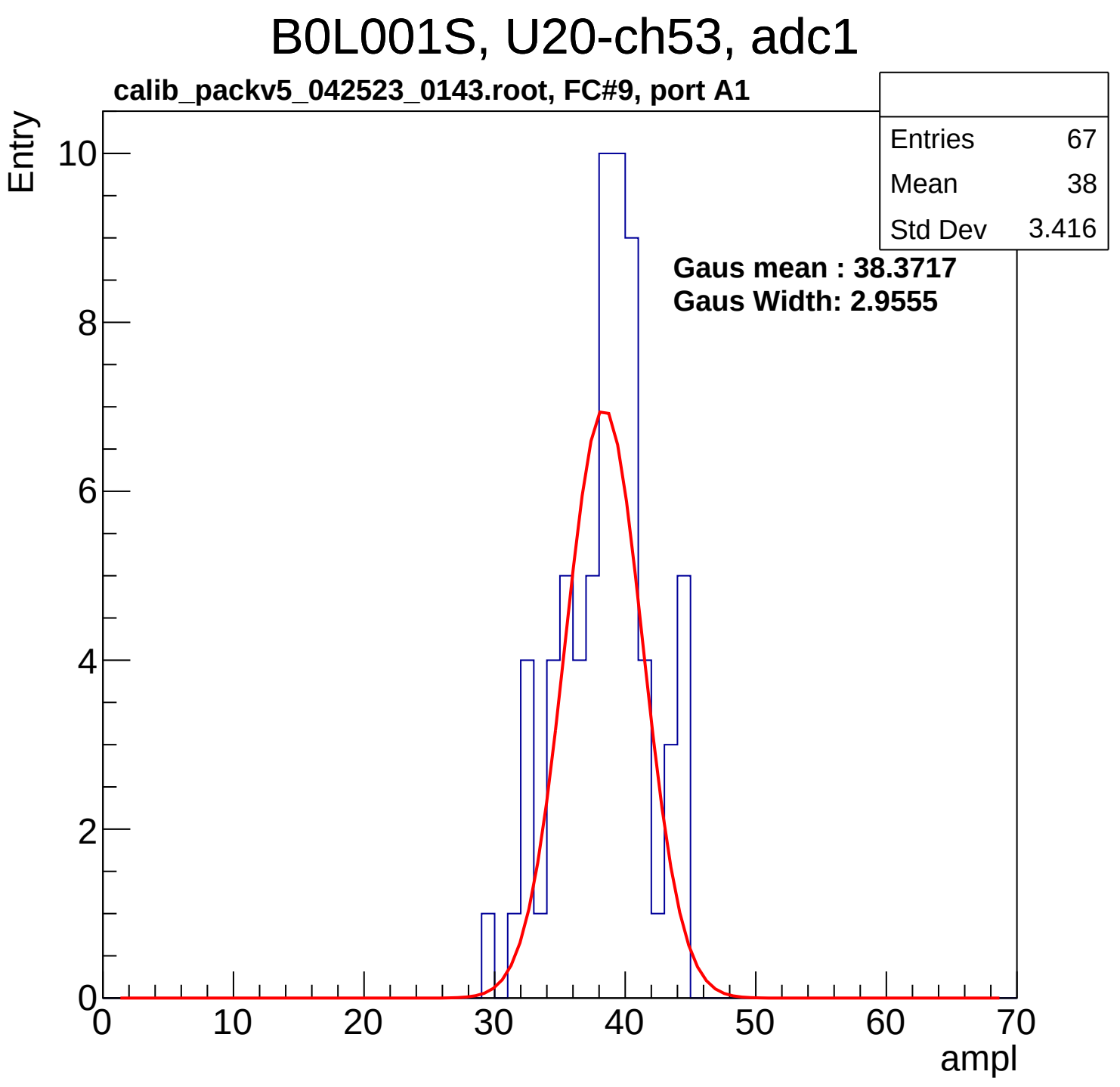
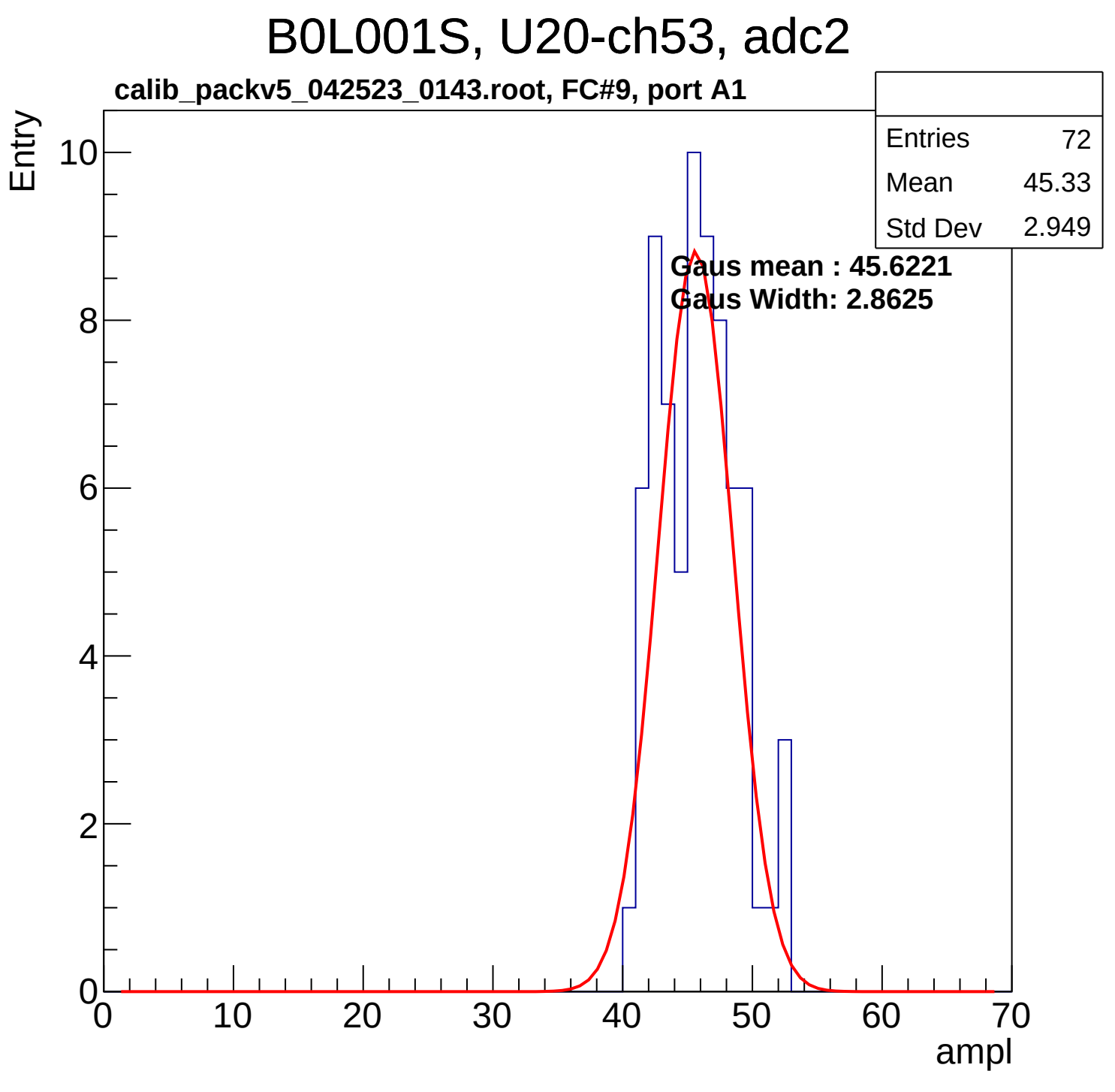
40
50
60
70


B0L001S, U20-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38
Std Dev 3.416
Gaus mean : 38.3717
Gaus Width: 2.9555
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 45.33
Std Dev 2.949
Gaus mean : 45.6221
Gaus Width: 2.8625
Entry
ampl
0
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4
6
8
10
0
10
20
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40
50
60
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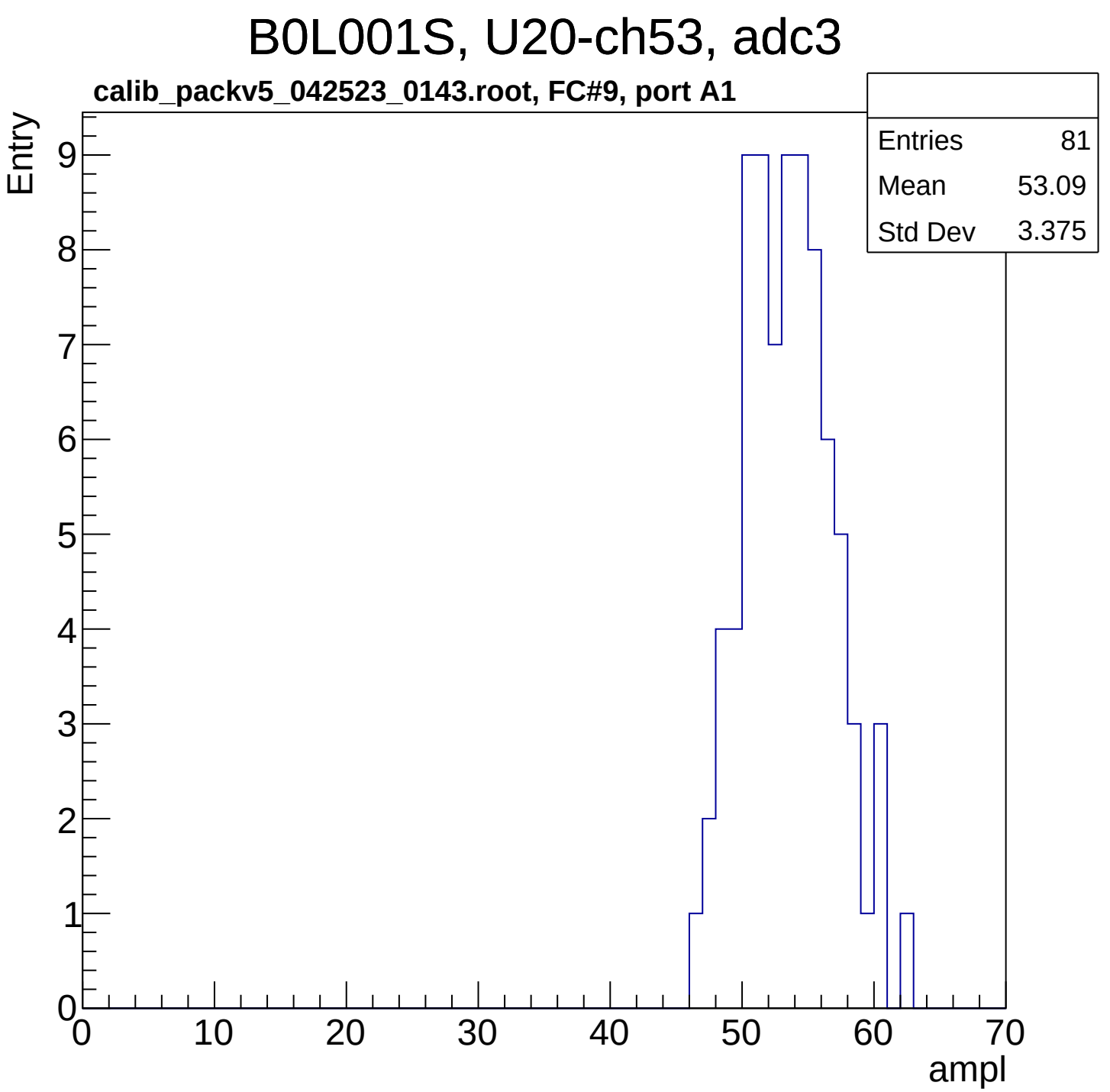
B0L001S, U20-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 53.09
Std Dev 3.375
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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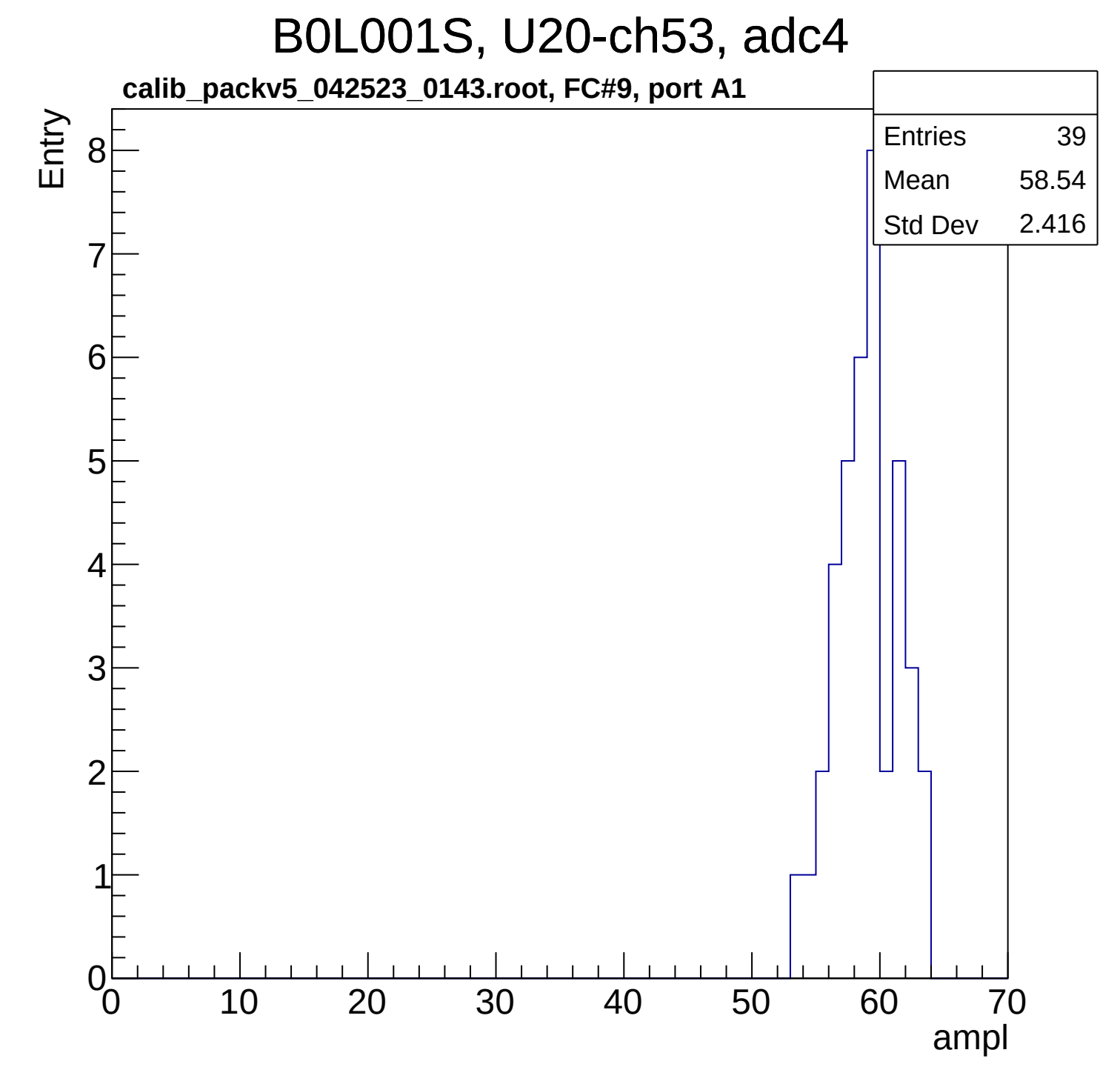
B0L001S, U20-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.54
Std Dev 2.416
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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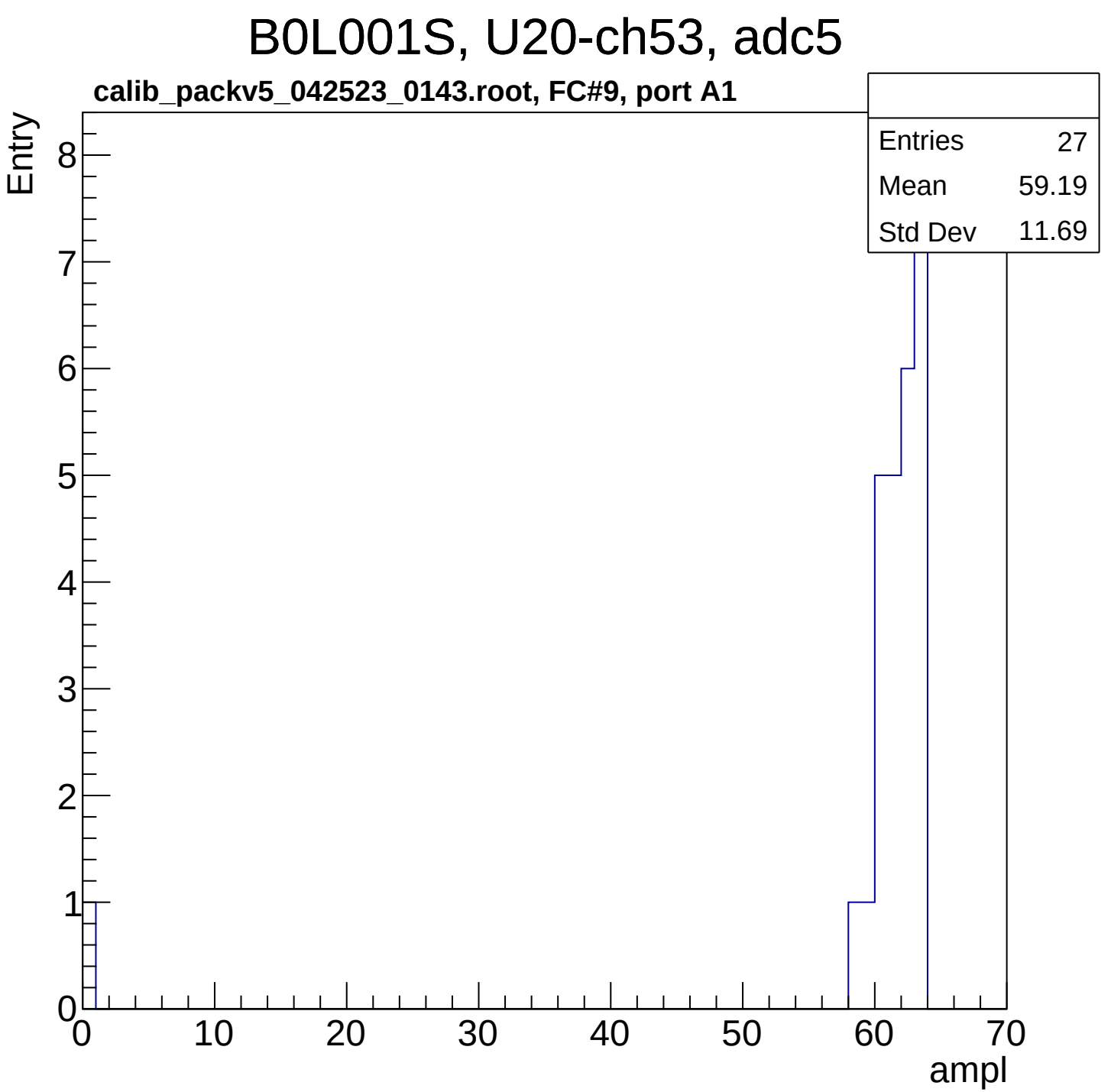
B0L001S, U20-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 59.19
Std Dev 11.69
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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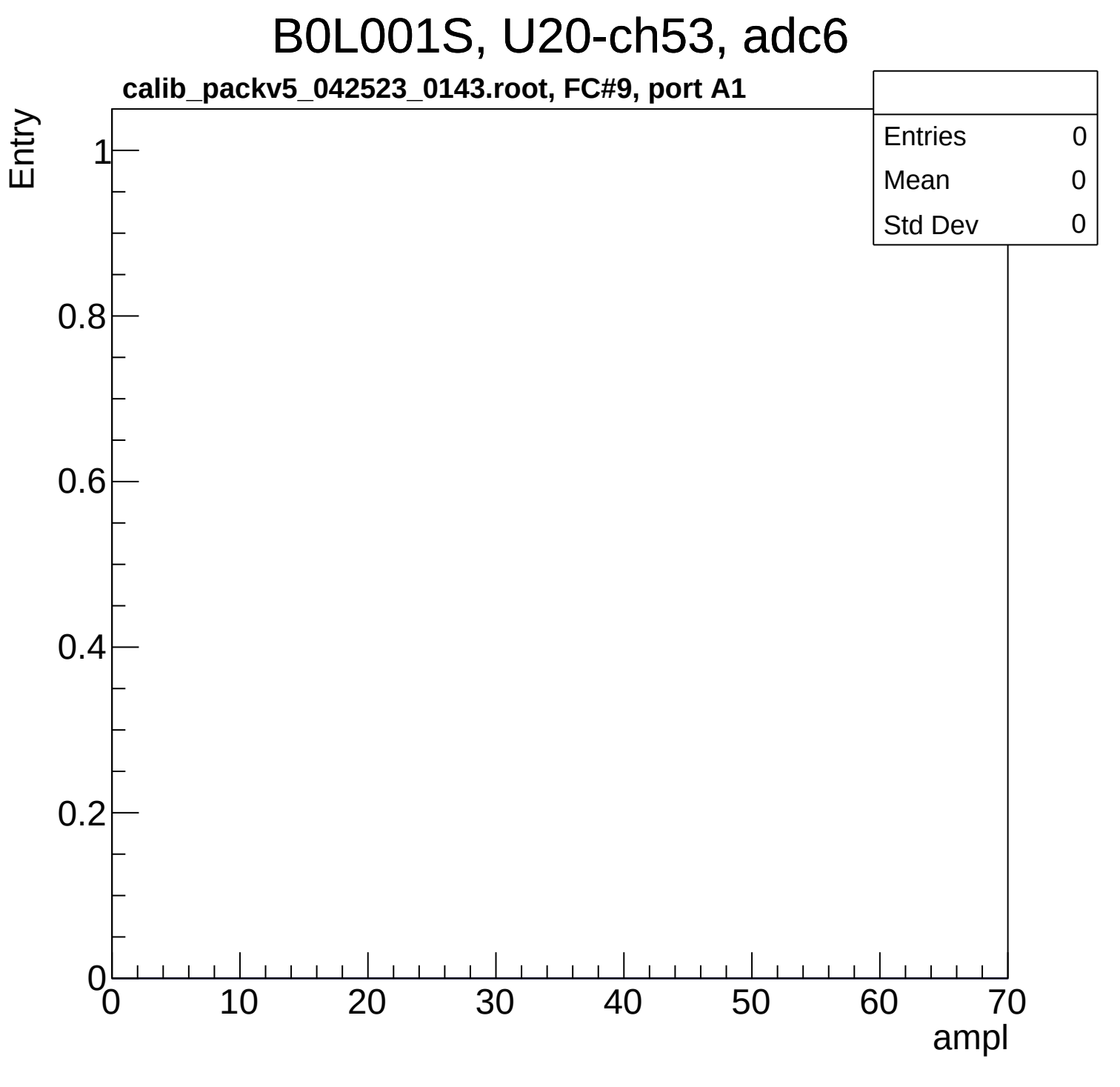
B0L001S, U20-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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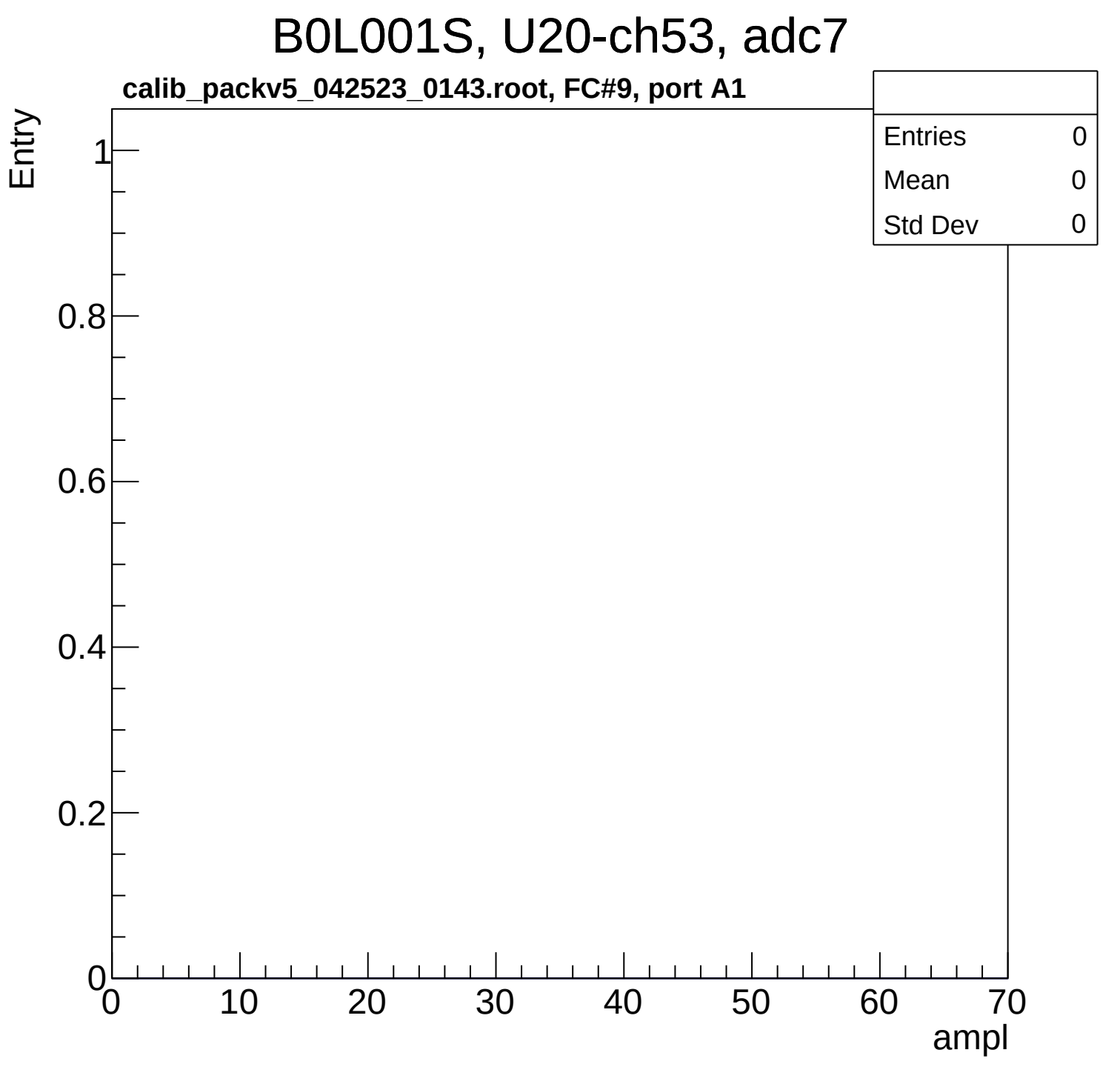
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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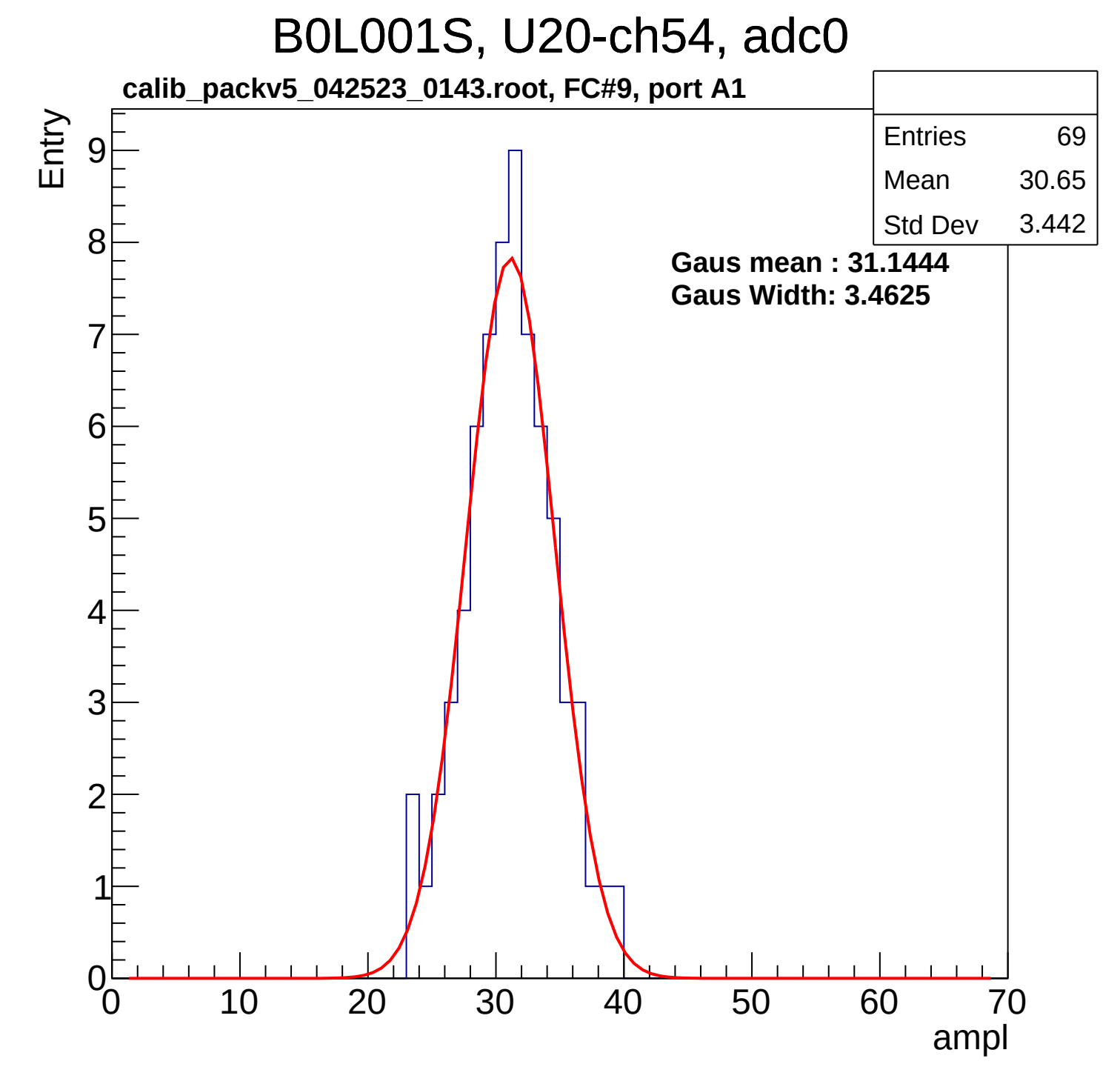
50
60
70
ampl


B0L001S, U20-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.65
Std Dev 3.442
Gaus mean : 31.1444
Gaus Width: 3.4625
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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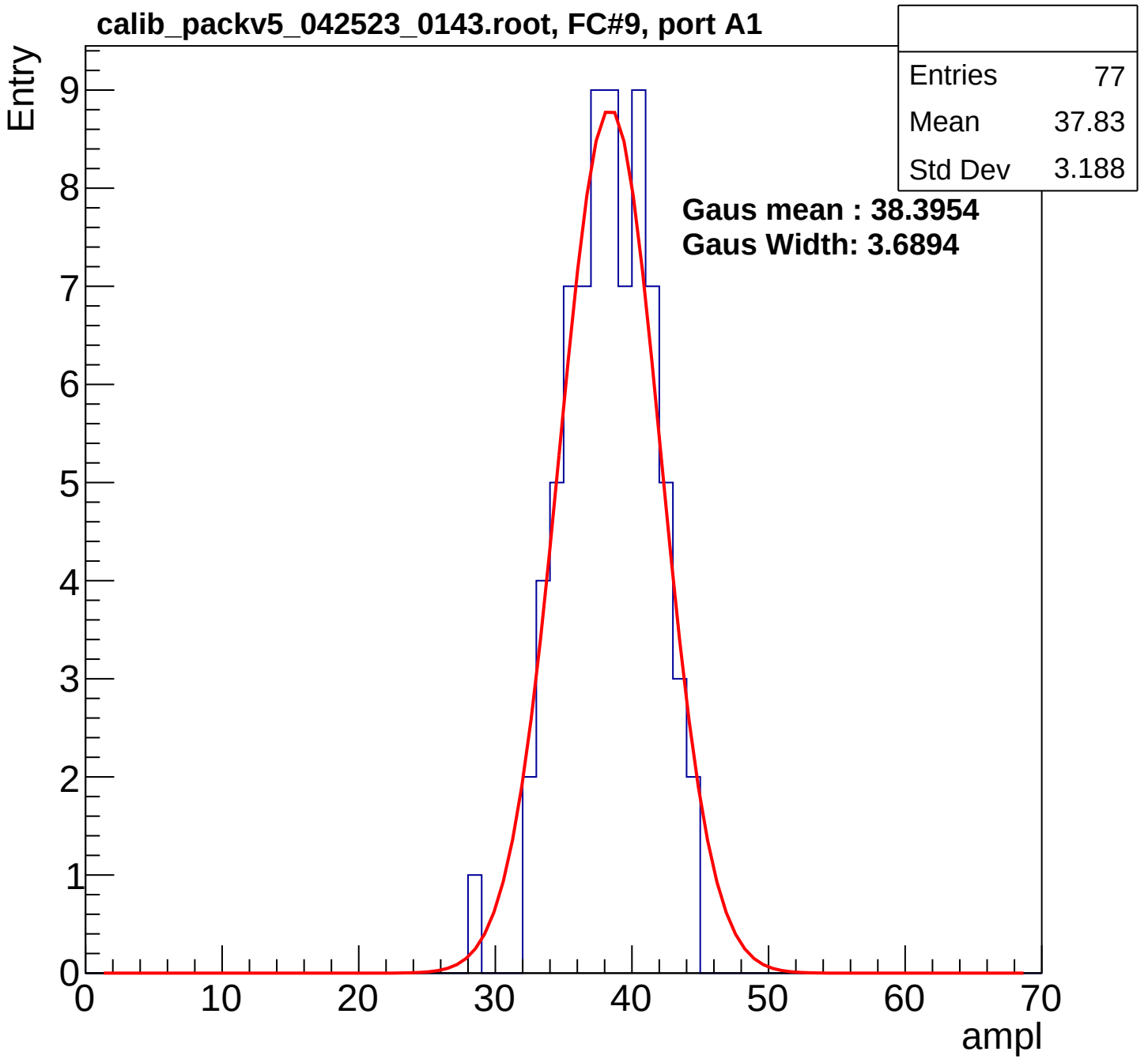
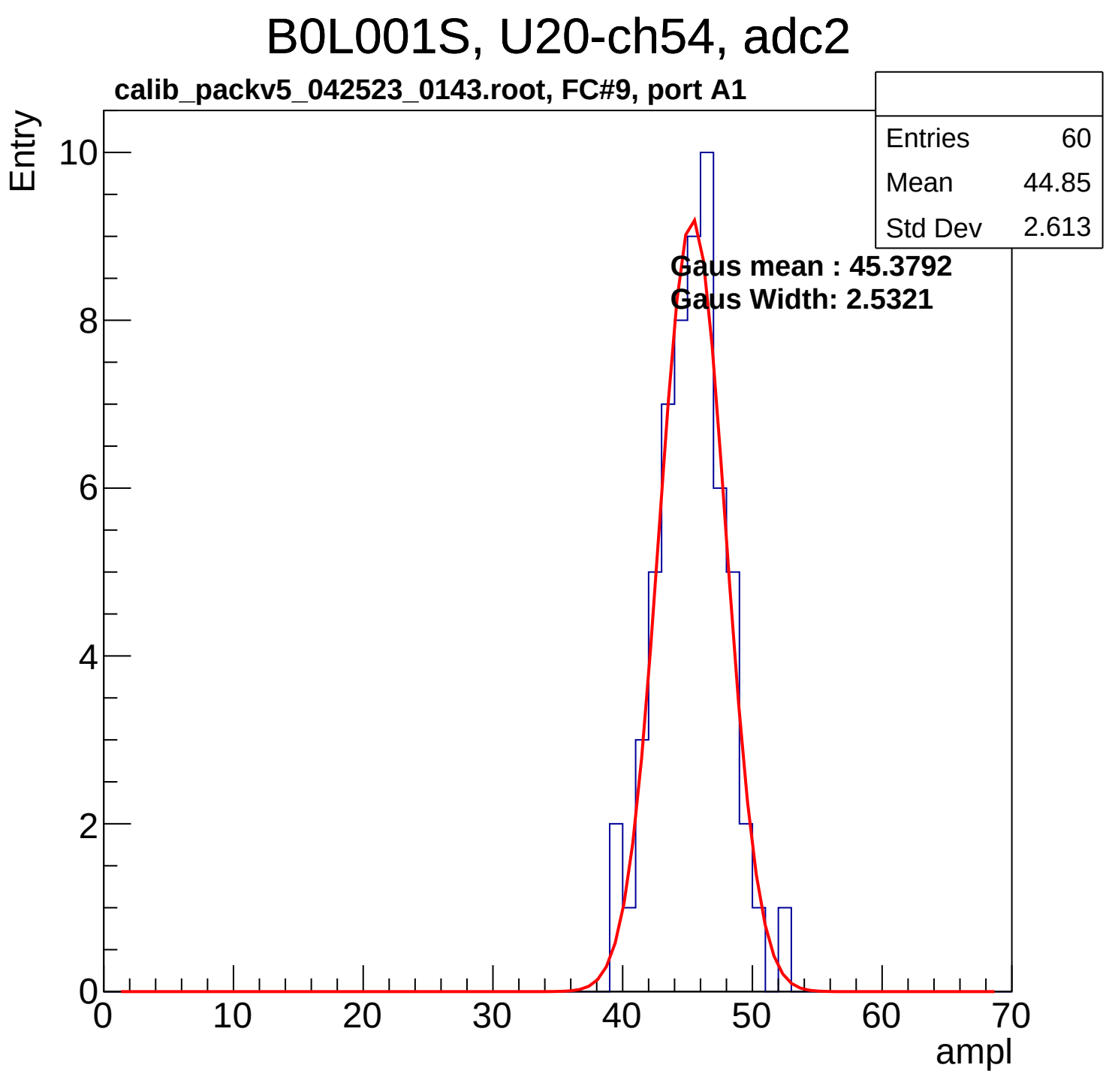
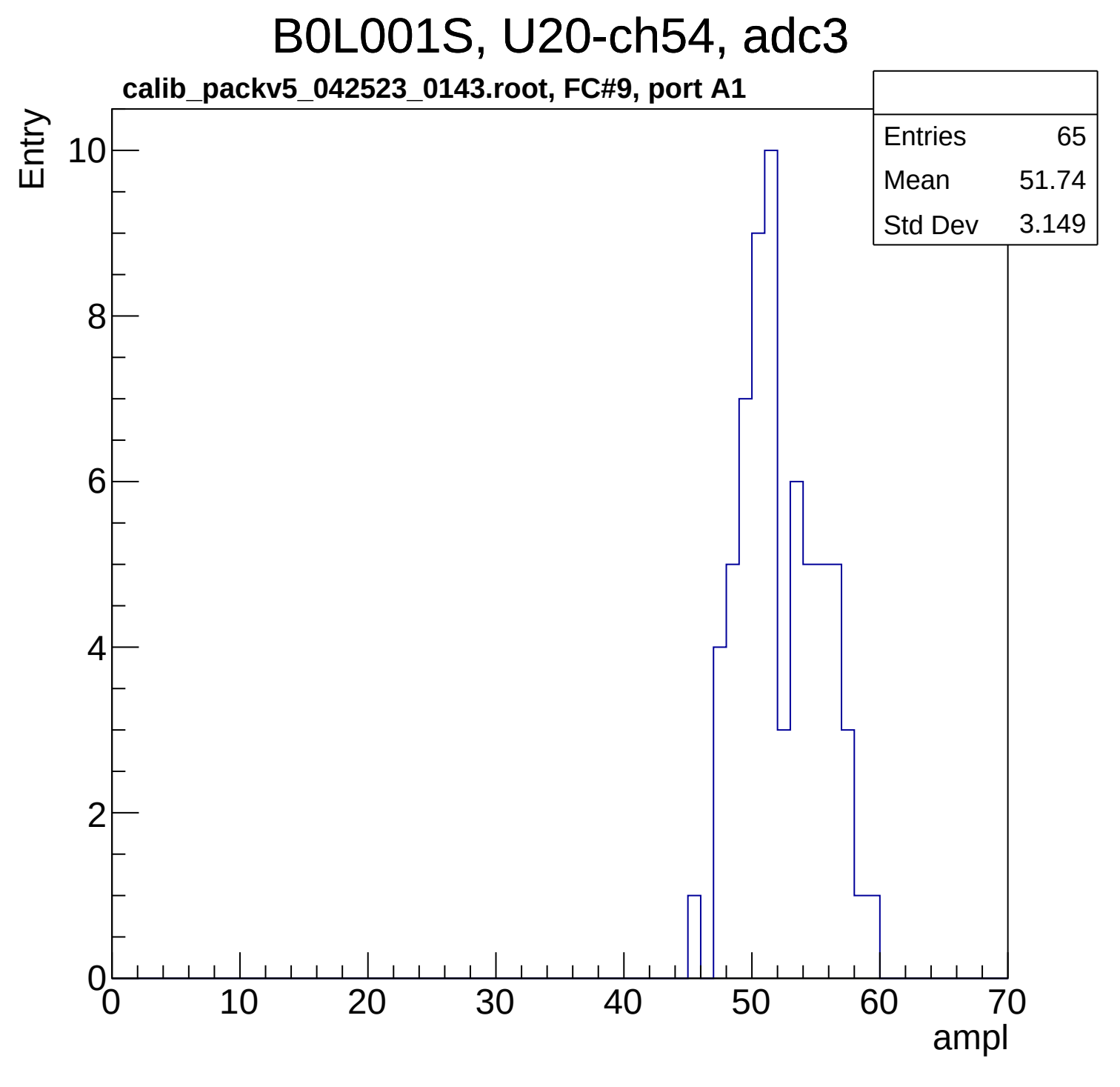
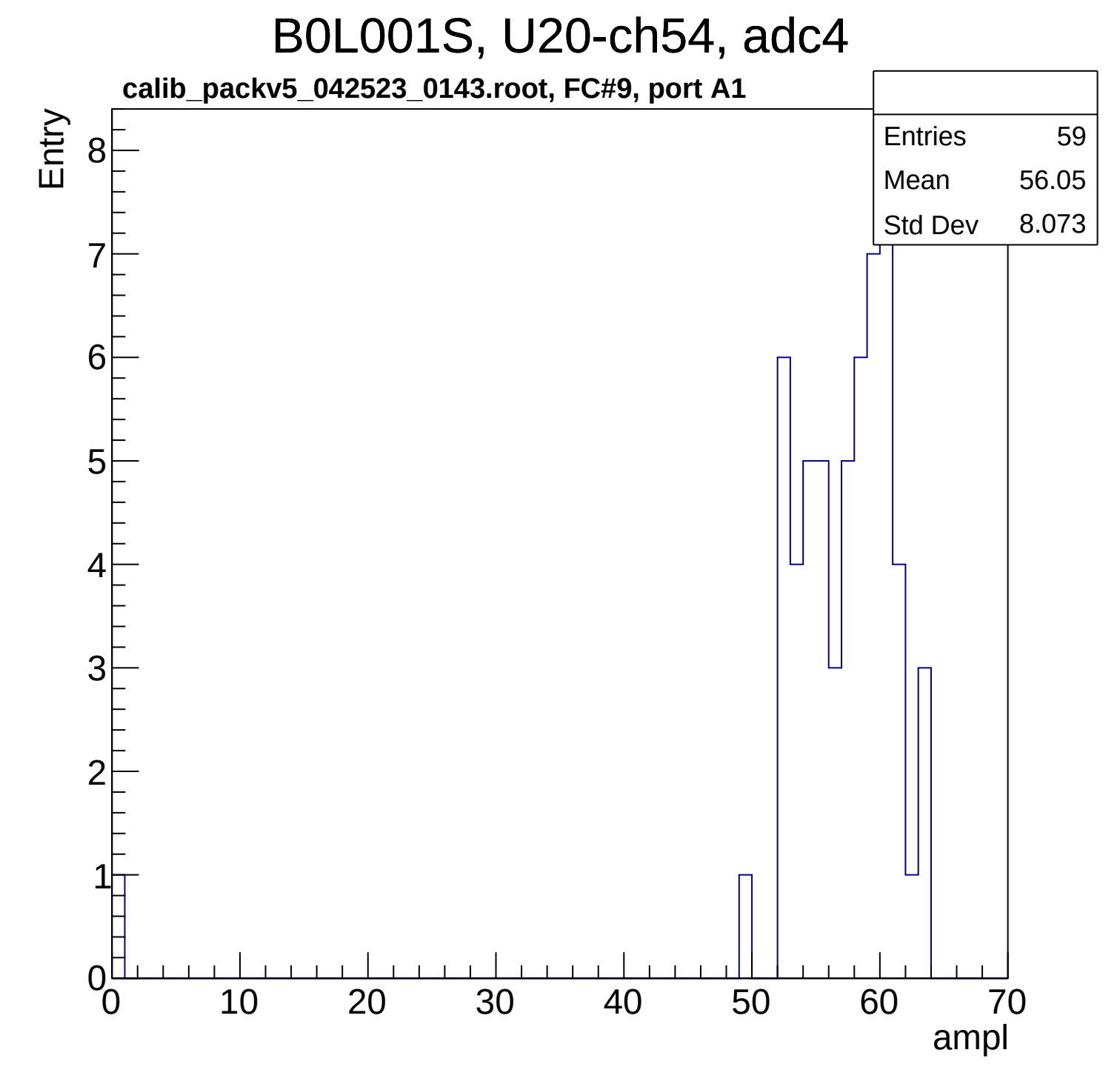
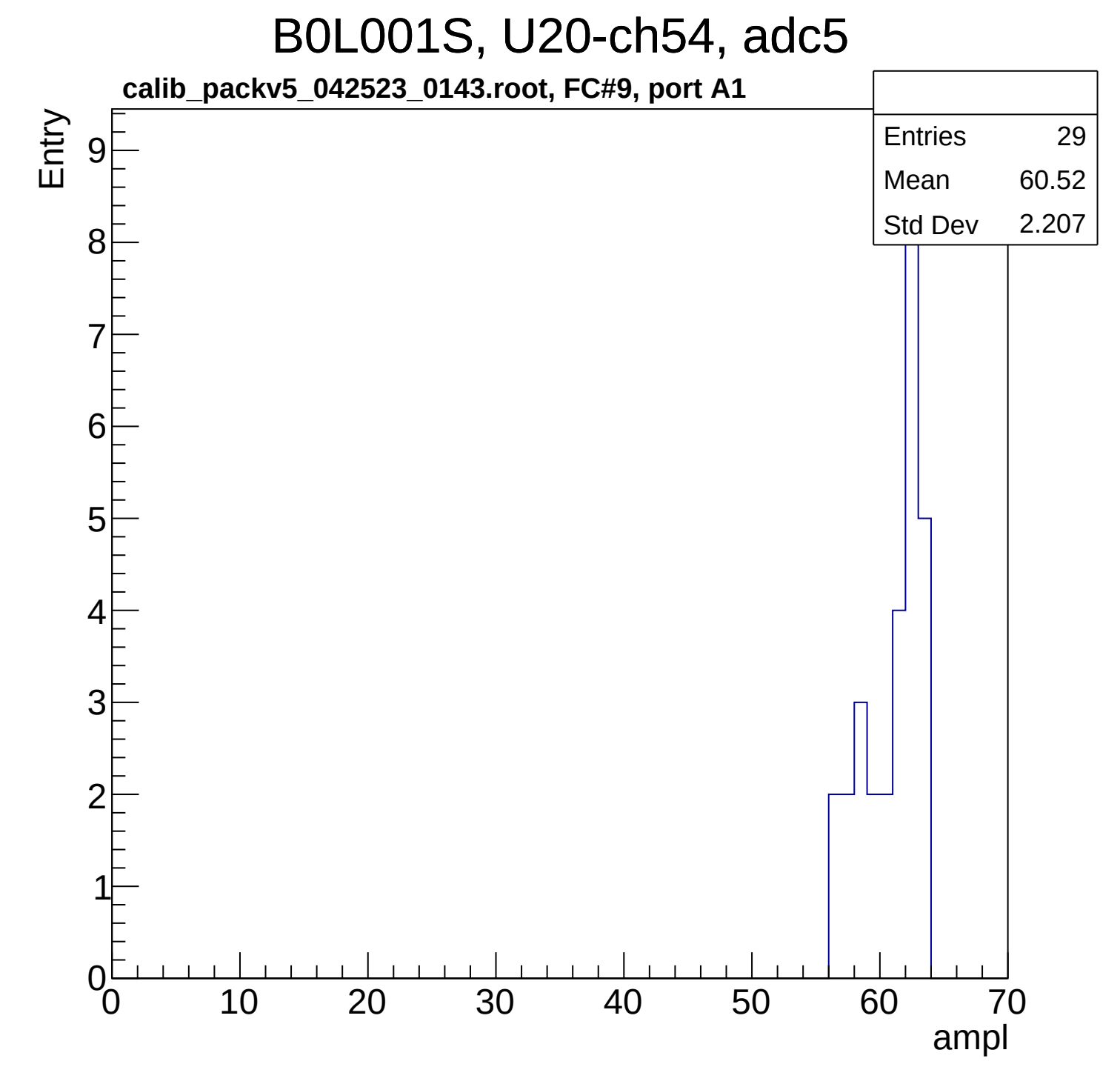
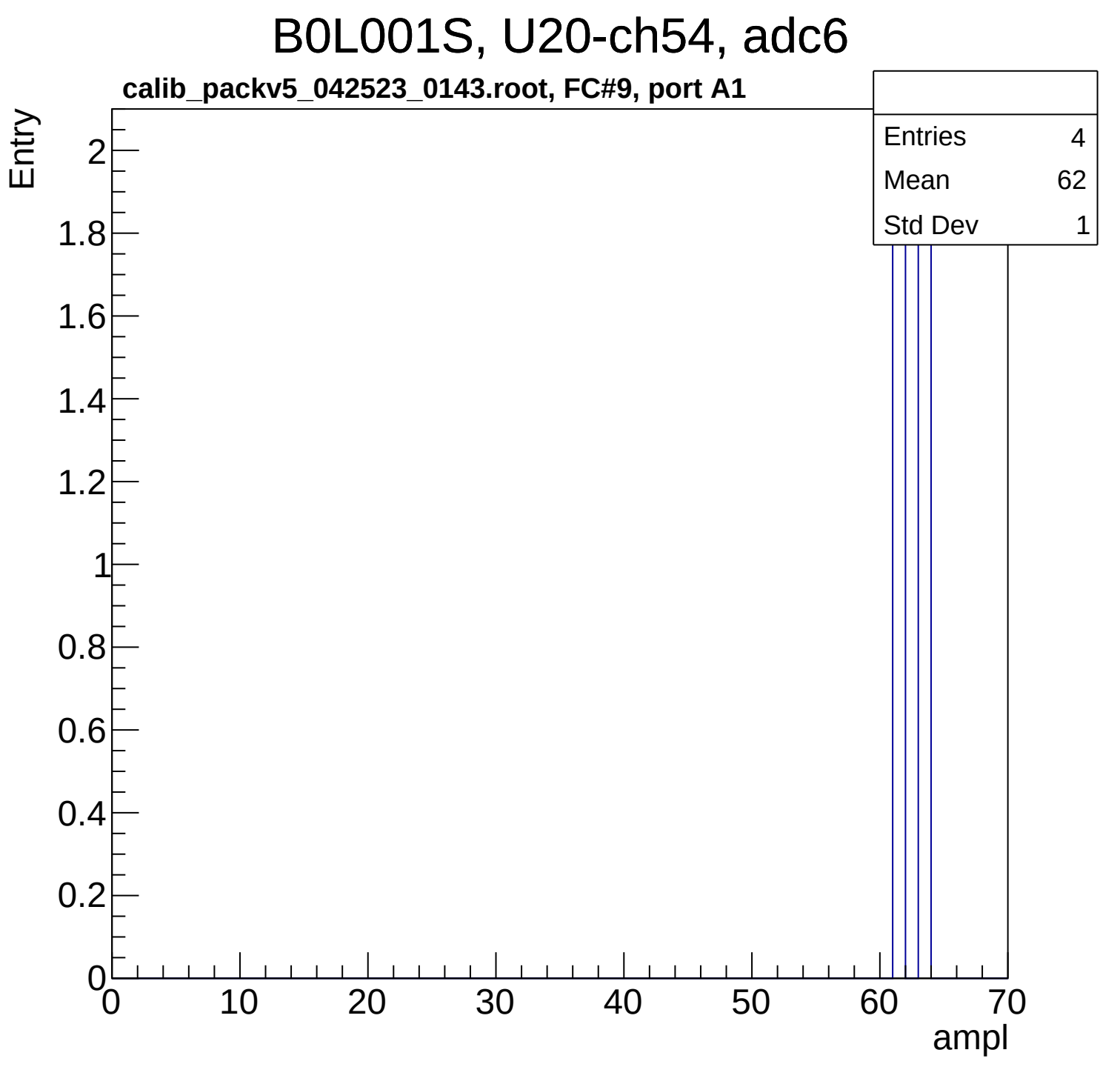

B0L001S, U20-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.83
Std Dev 3.188
Gaus mean : 38.3954
Gaus Width: 3.6894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

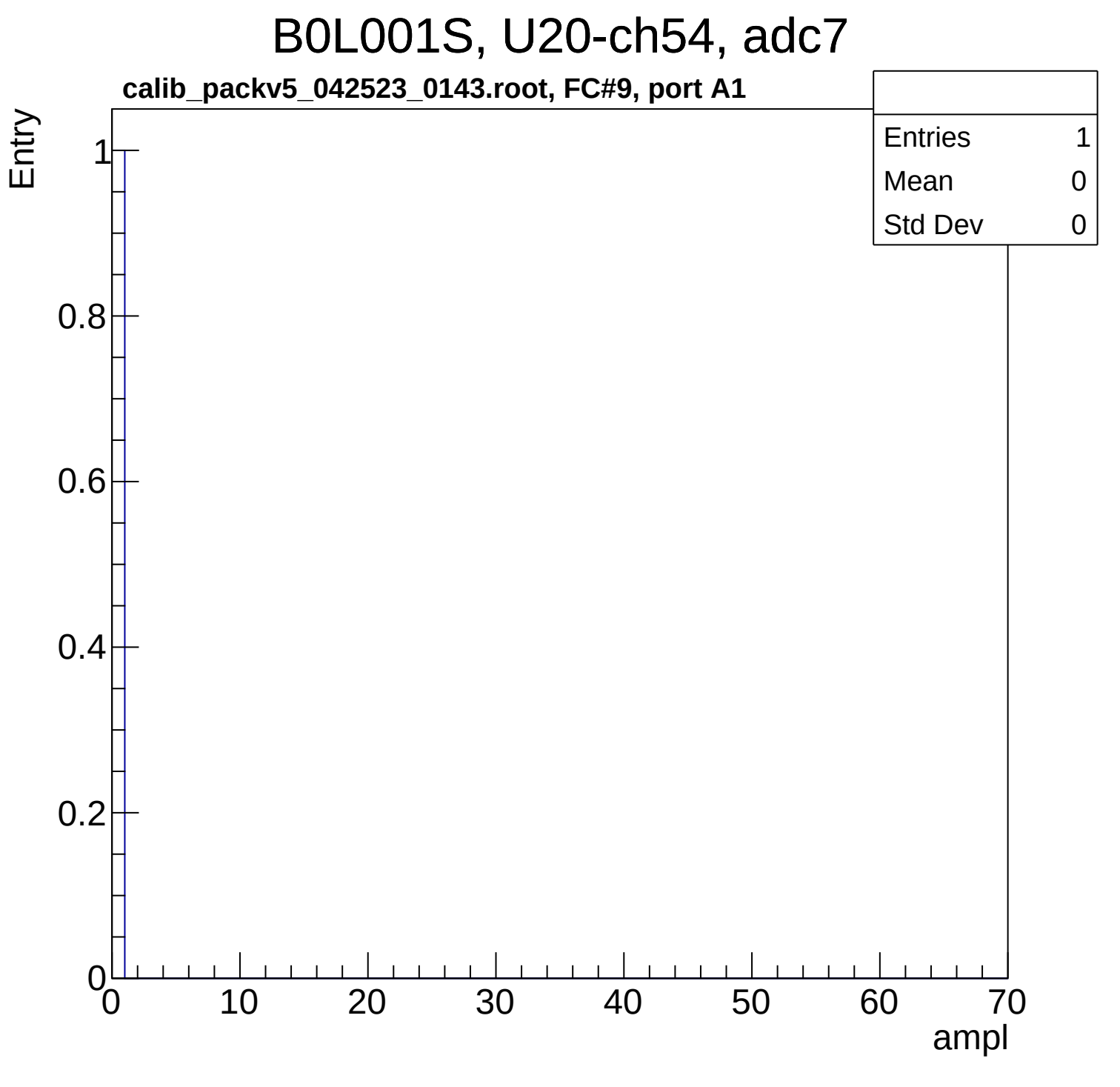

B0L001S, U20-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.85
Std Dev 2.613
Gaus mean : 45.3792
Gaus Width: 2.5321
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

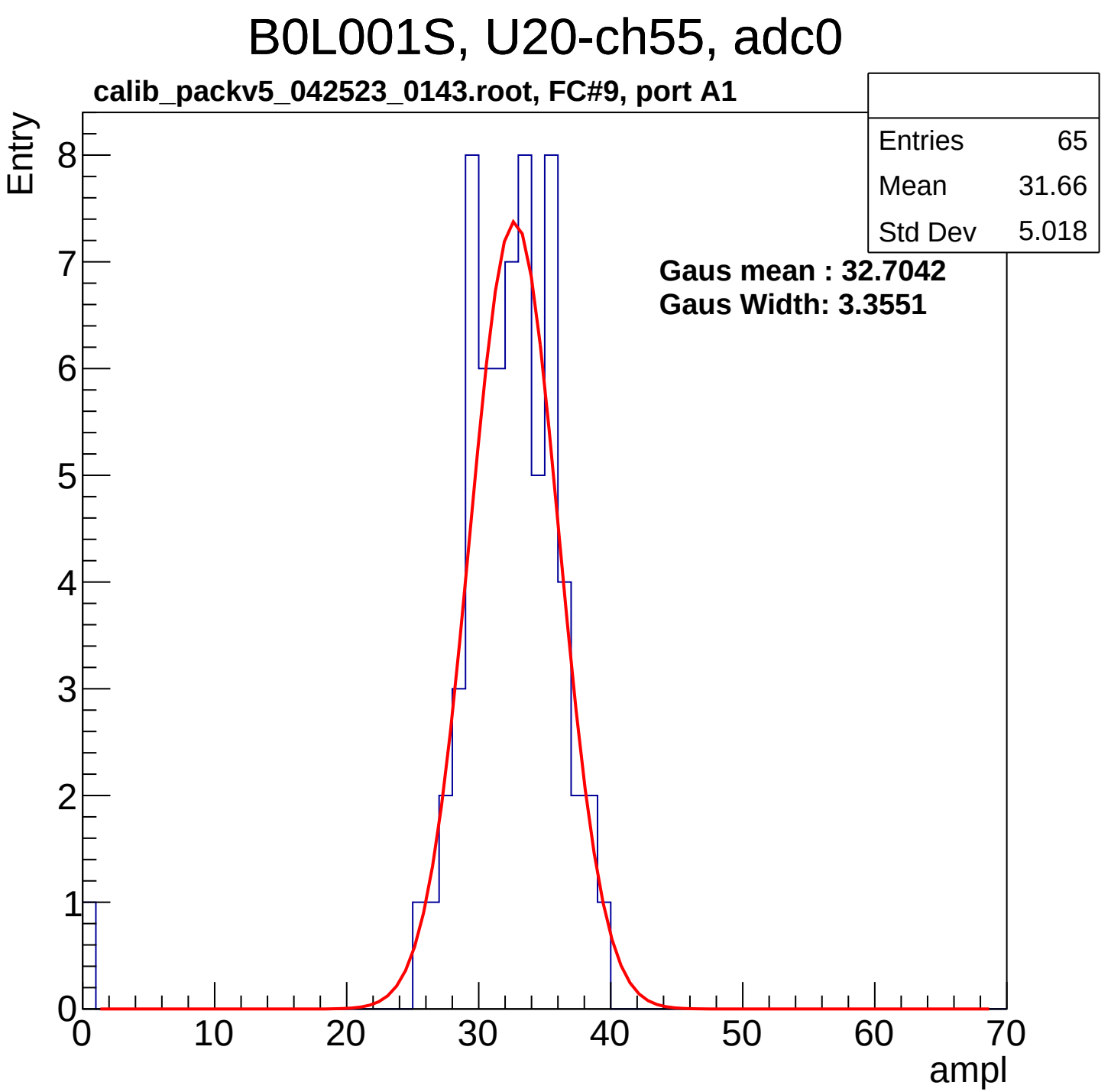

B0L001S, U20-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.74
Std Dev 3.149
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

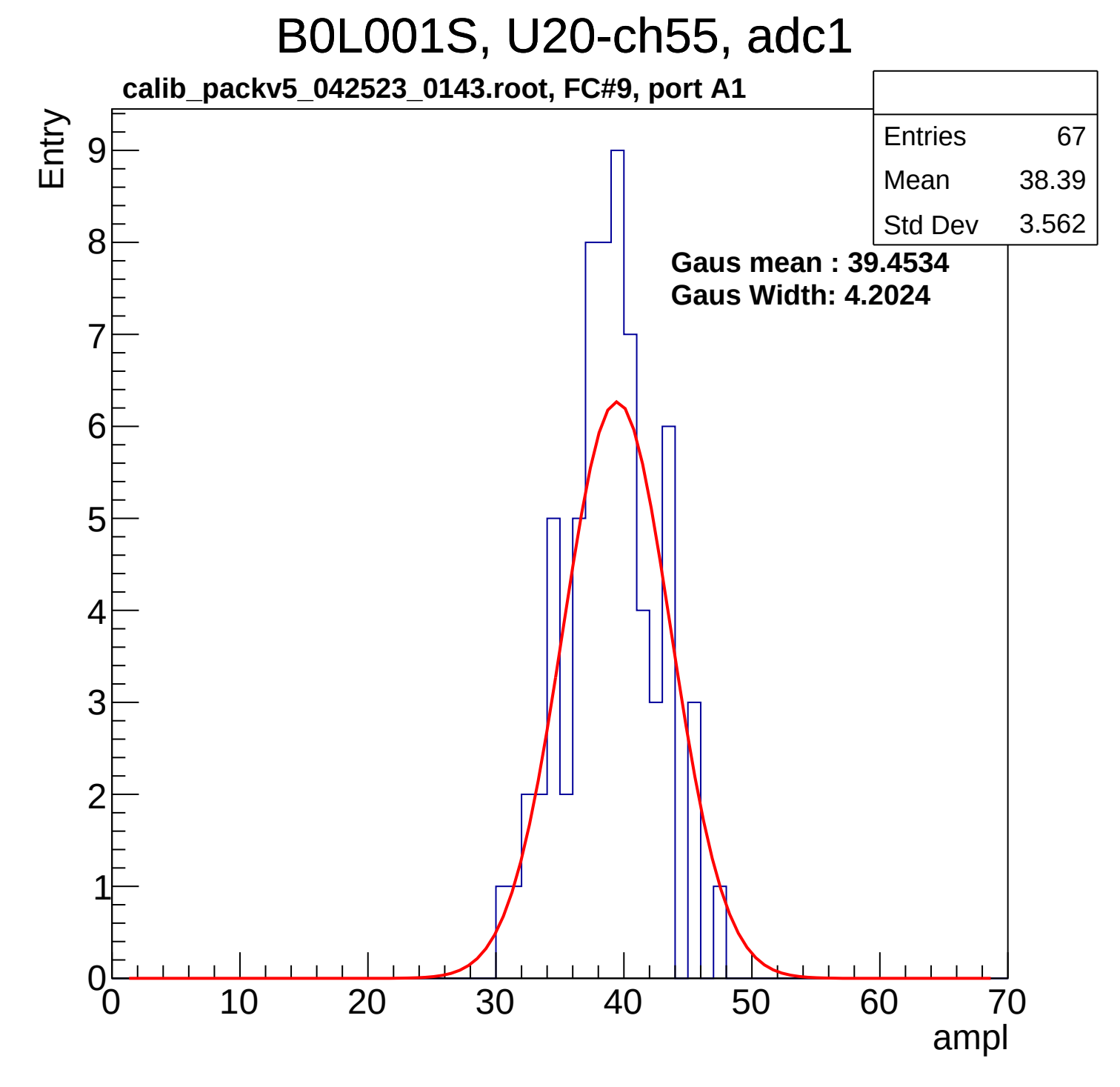

B0L001S, U20-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.05
Std Dev 8.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

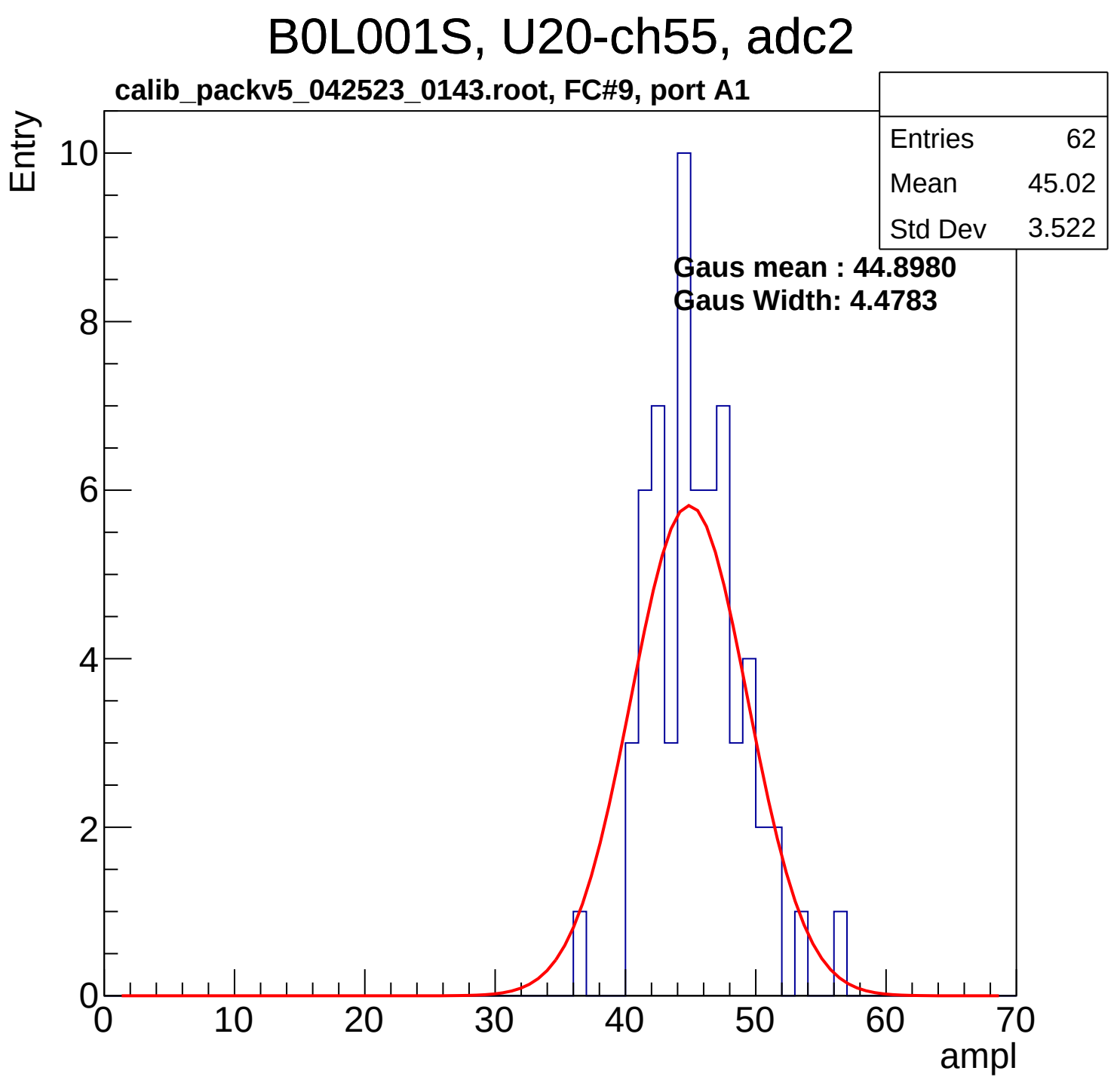

B0L001S, U20-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.52
Std Dev 2.207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

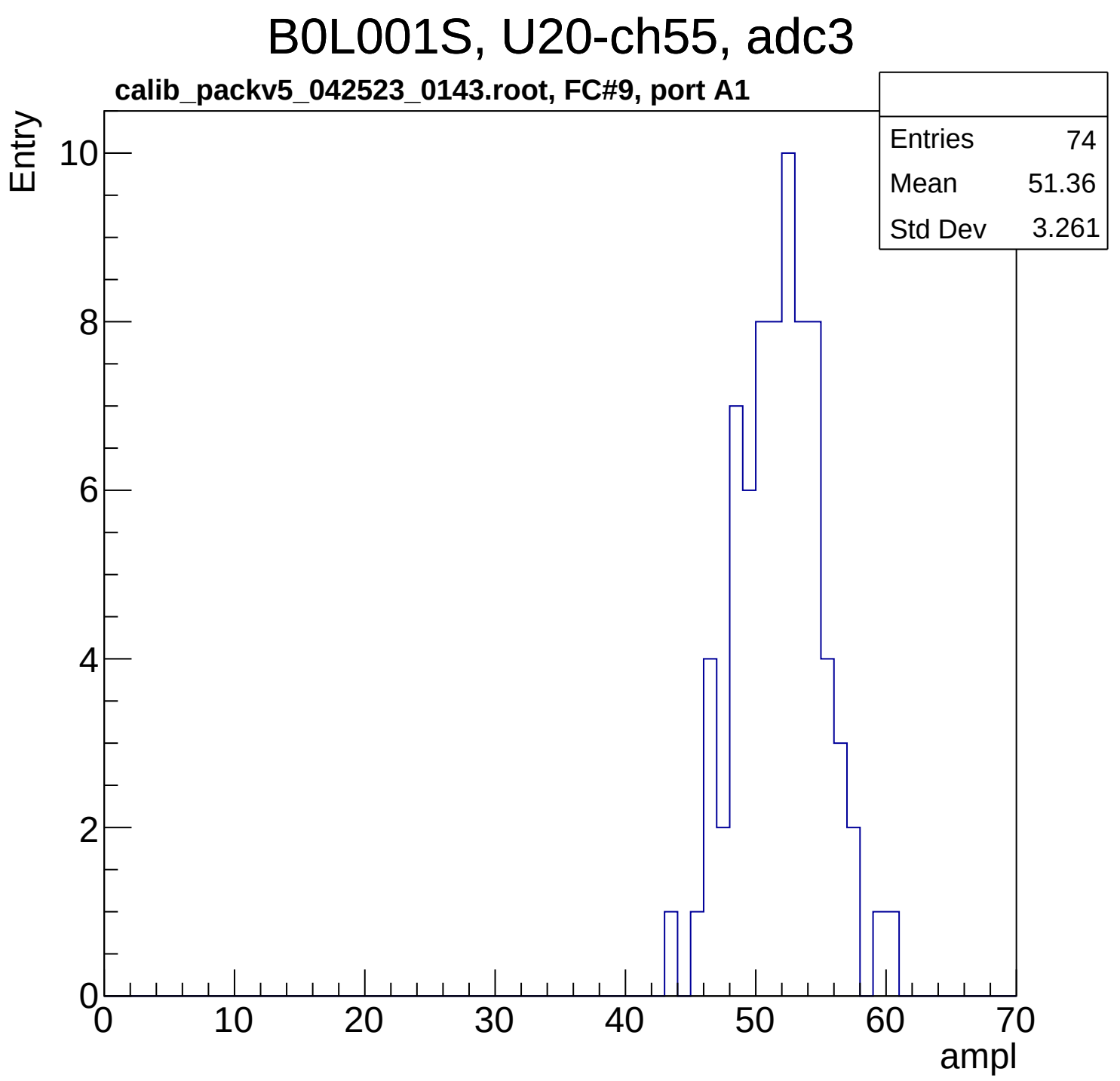

B0L001S, U20-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

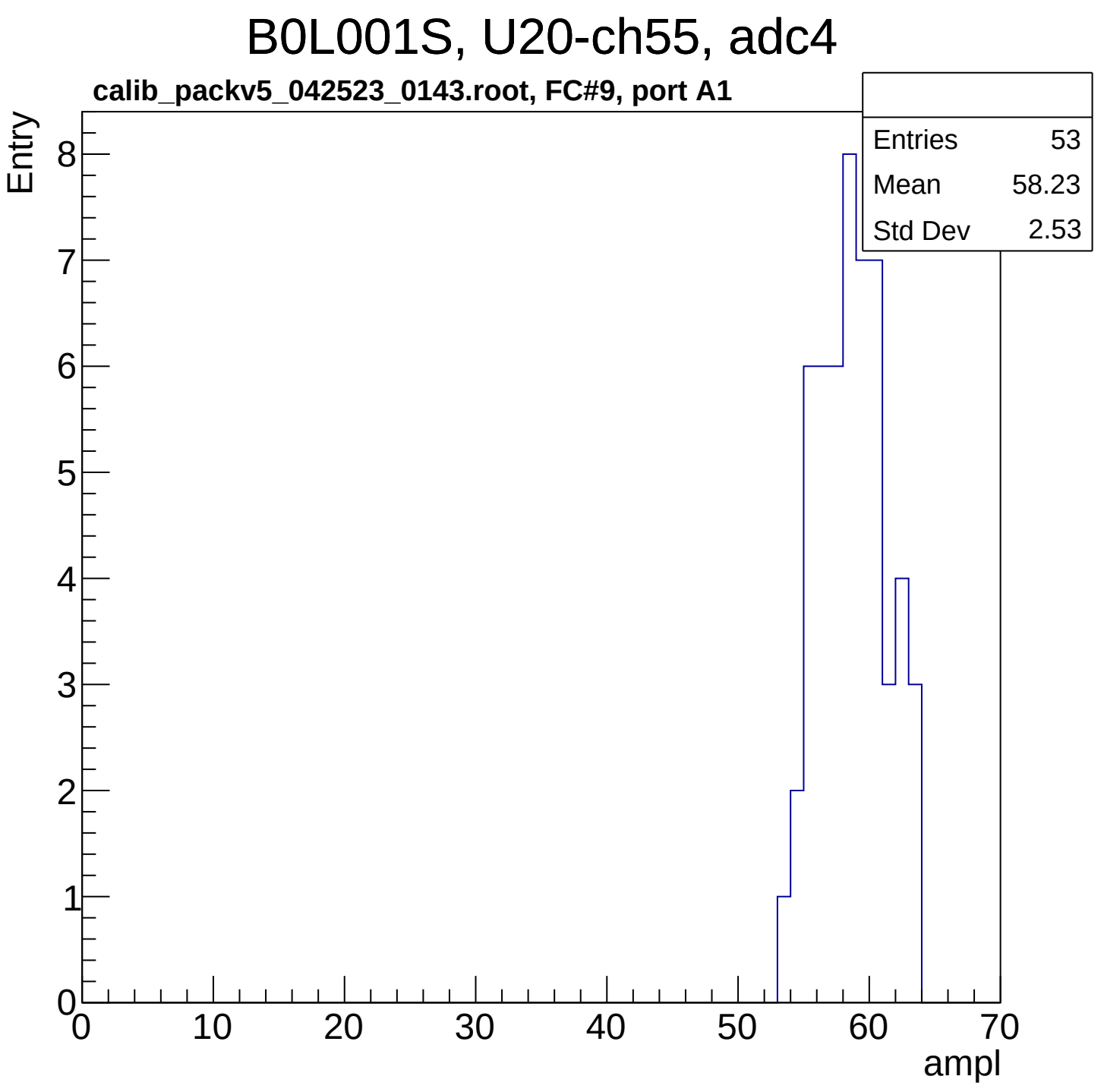

B0L001S, U20-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

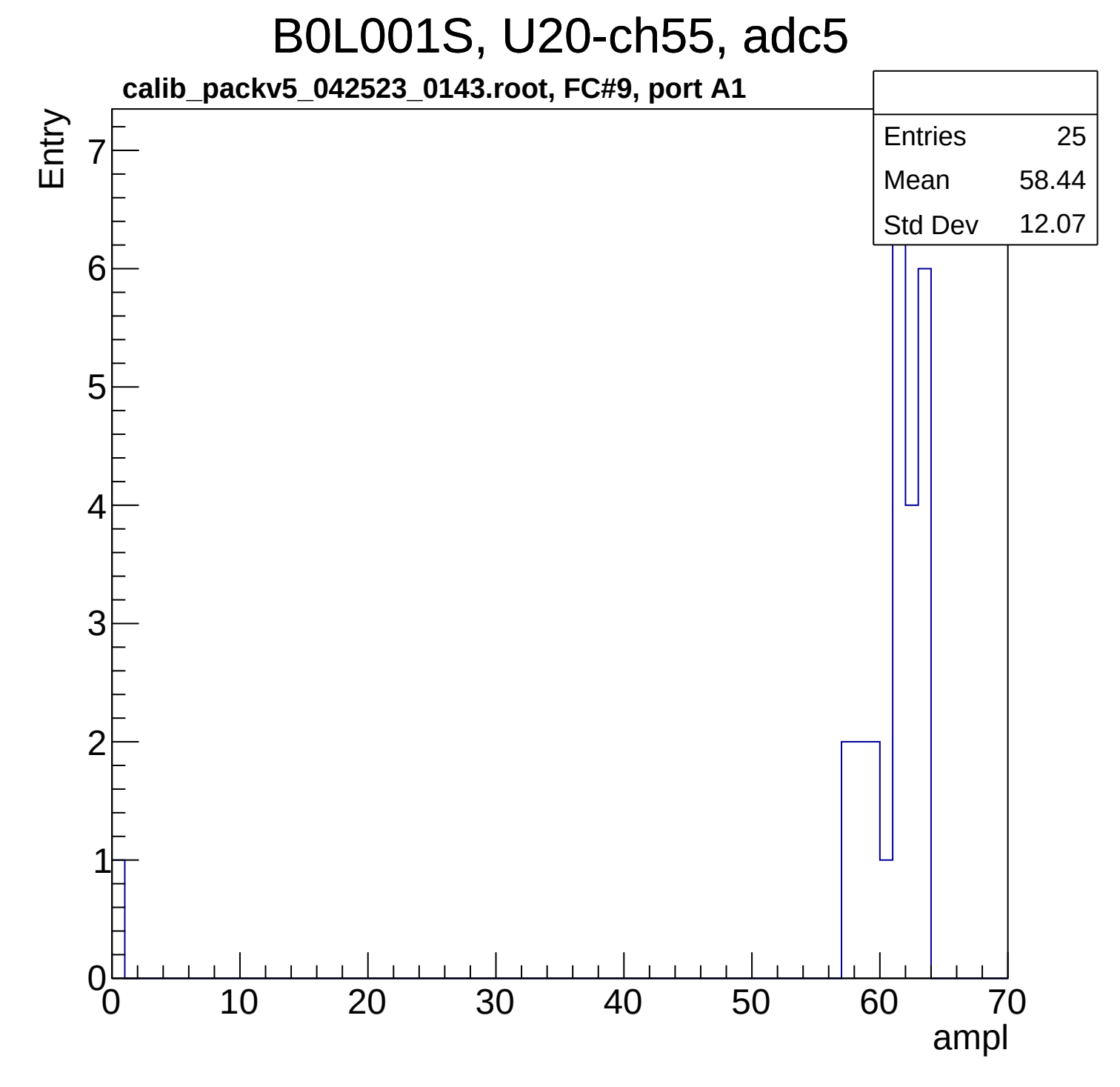

B0L001S, U20-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 31.66
Std Dev 5.018
Gaus mean : 32.7042
Gaus Width: 3.3551
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

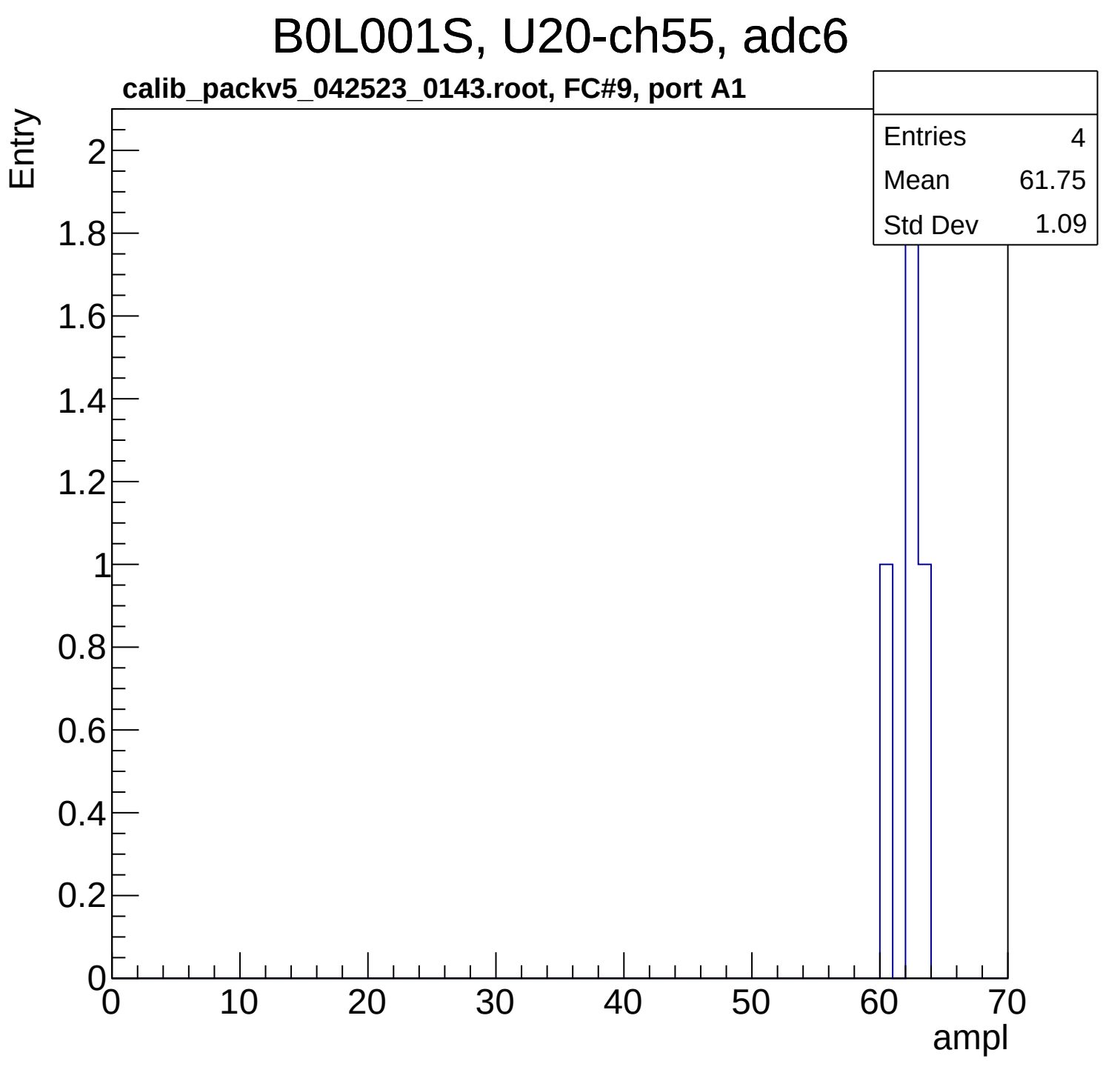

B0L001S, U20-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.39
Std Dev 3.562
Gaus mean : 39.4534
Gaus Width: 4.2024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

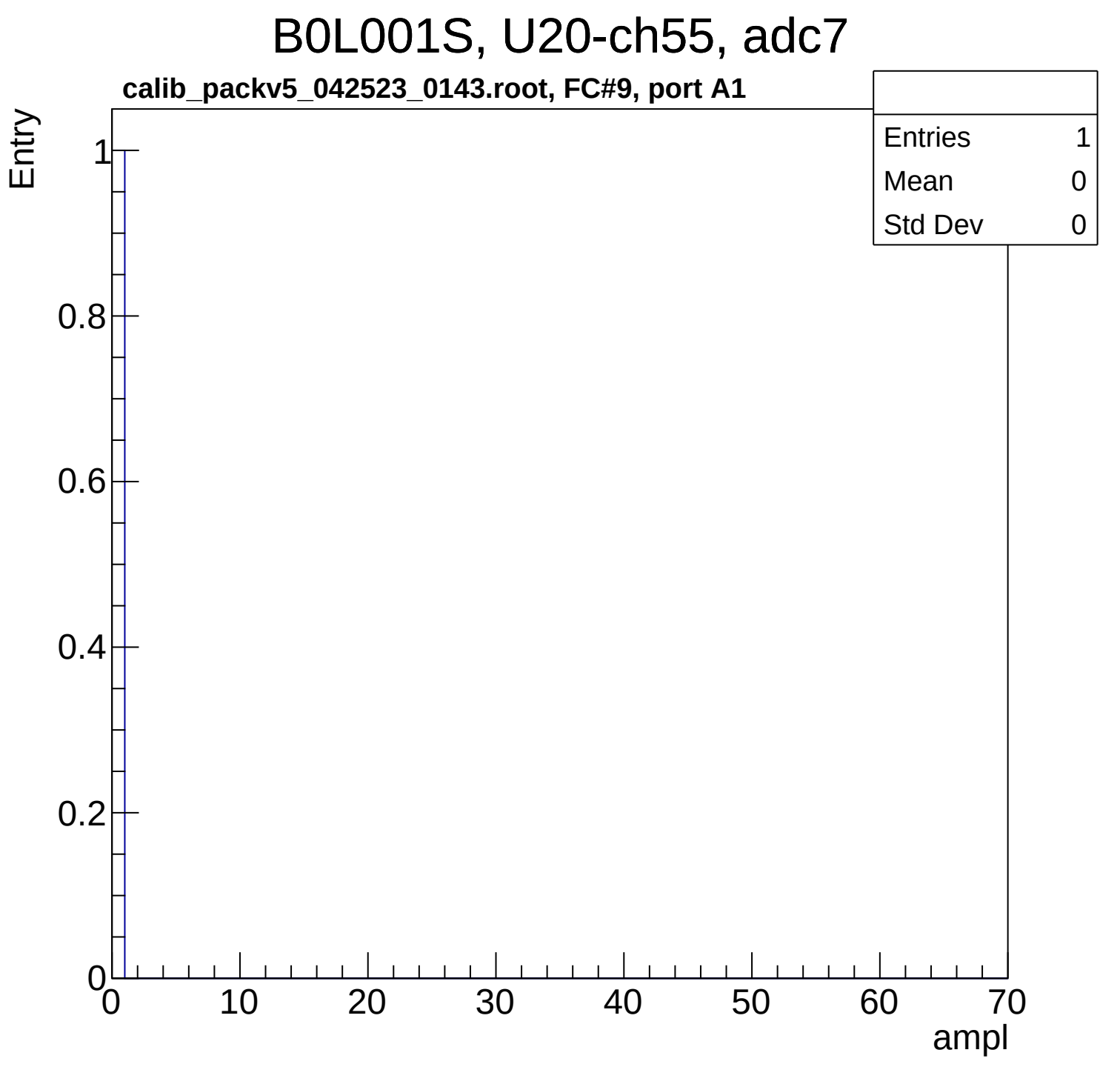

B0L001S, U20-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 45.02
Std Dev 3.522
Gaus mean : 44.8980
Gaus Width: 4.4783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

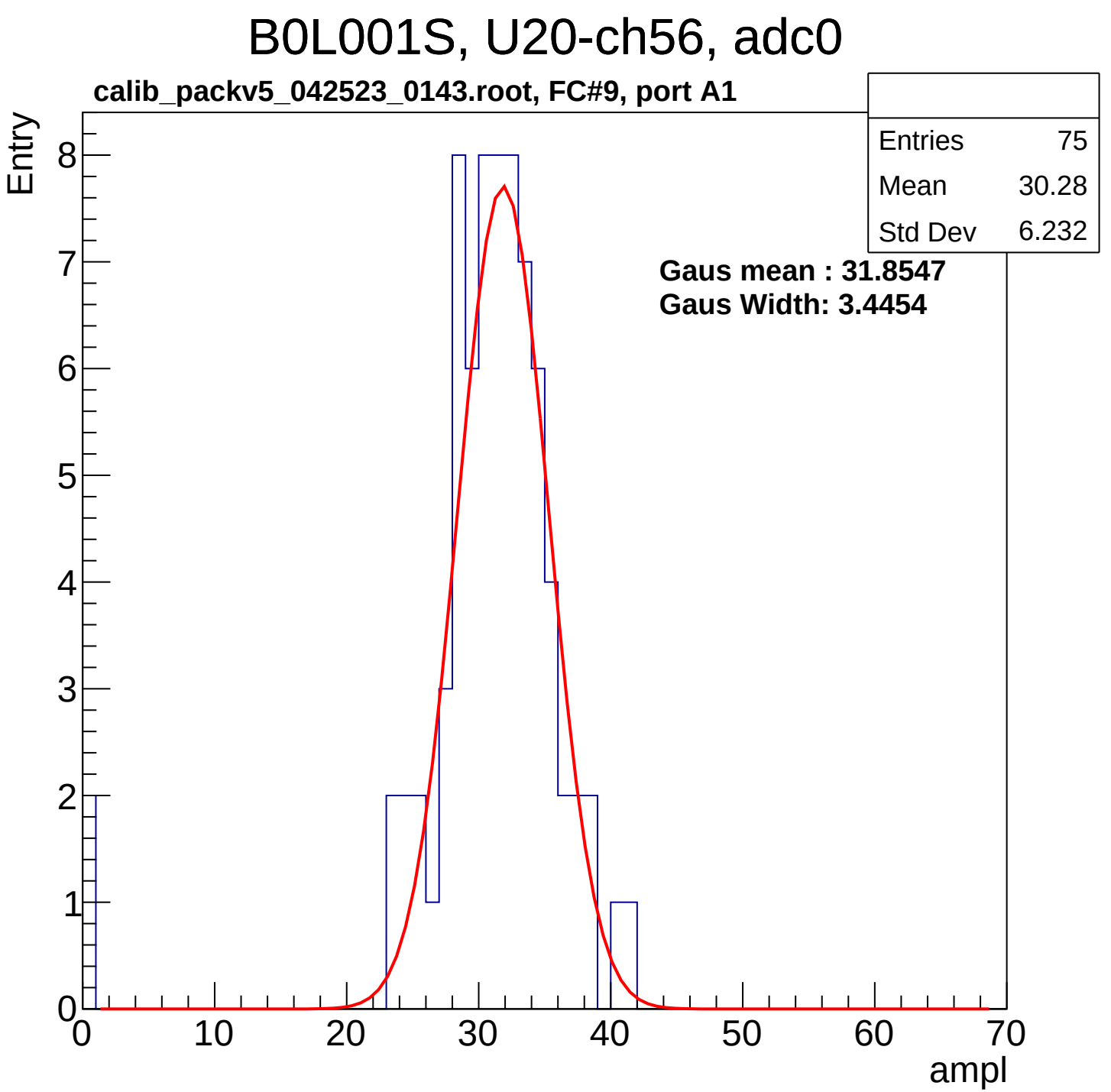

B0L001S, U20-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.36
Std Dev 3.261
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

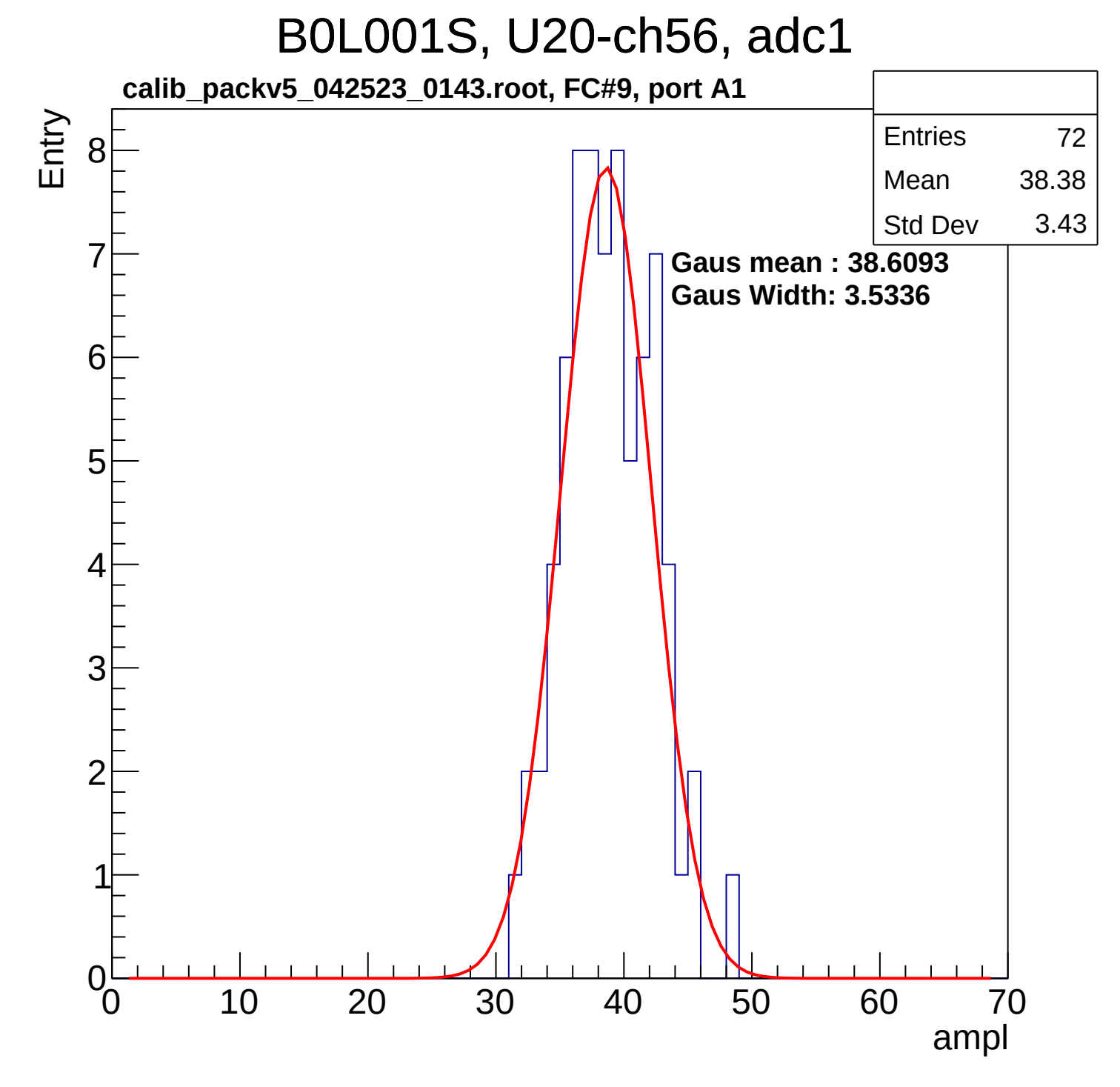

B0L001S, U20-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.23
Std Dev 2.53
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

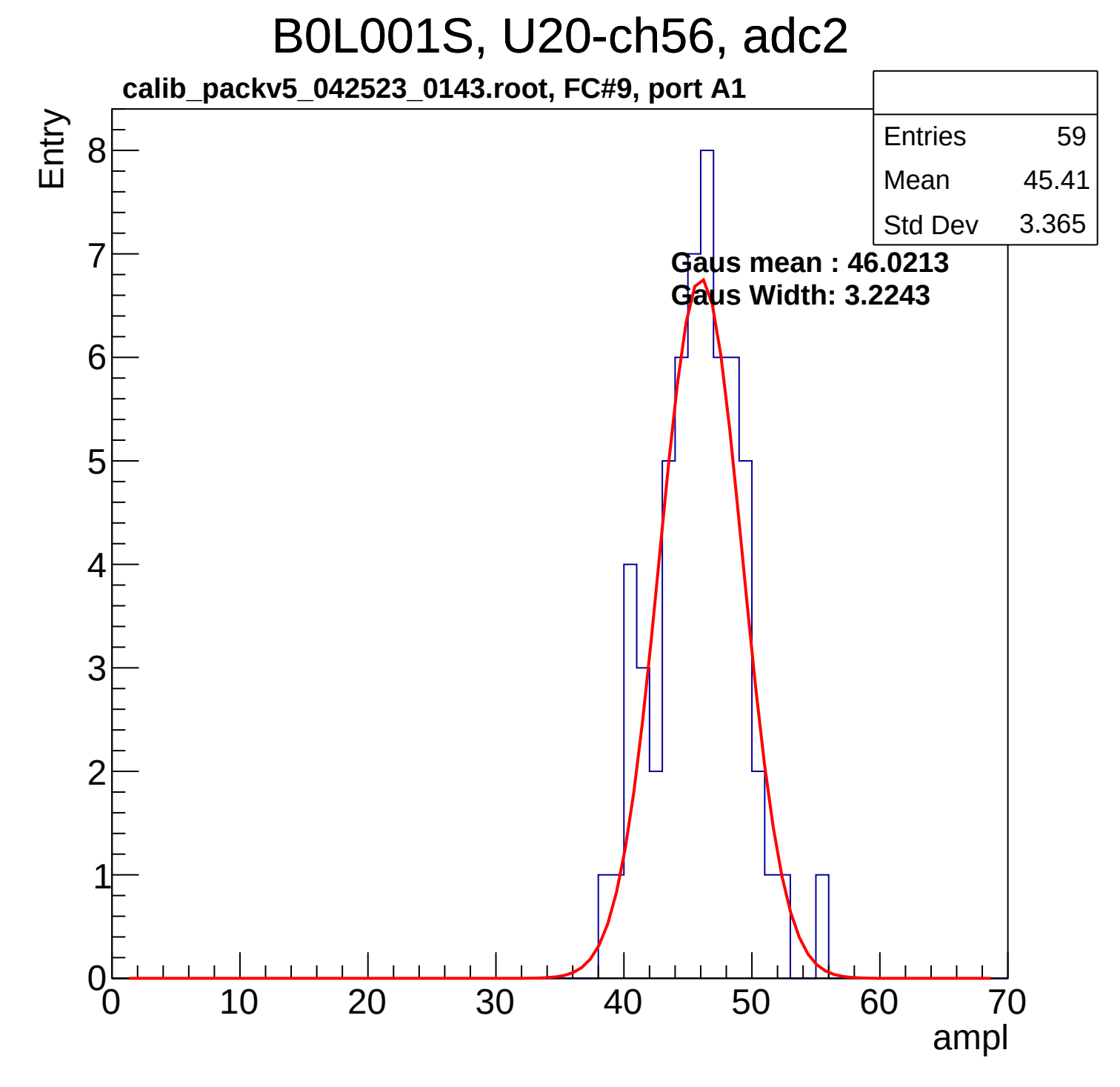

B0L001S, U20-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.44
Std Dev 12.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.28
Std Dev 6.232
Gaus mean : 31.8547
Gaus Width: 3.4454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.38
Std Dev 3.43
Gaus mean : 38.6093
Gaus Width: 3.5336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.41
Std Dev 3.365
Gaus mean : 46.0213
Gaus Width: 3.2243
Entry
ampl
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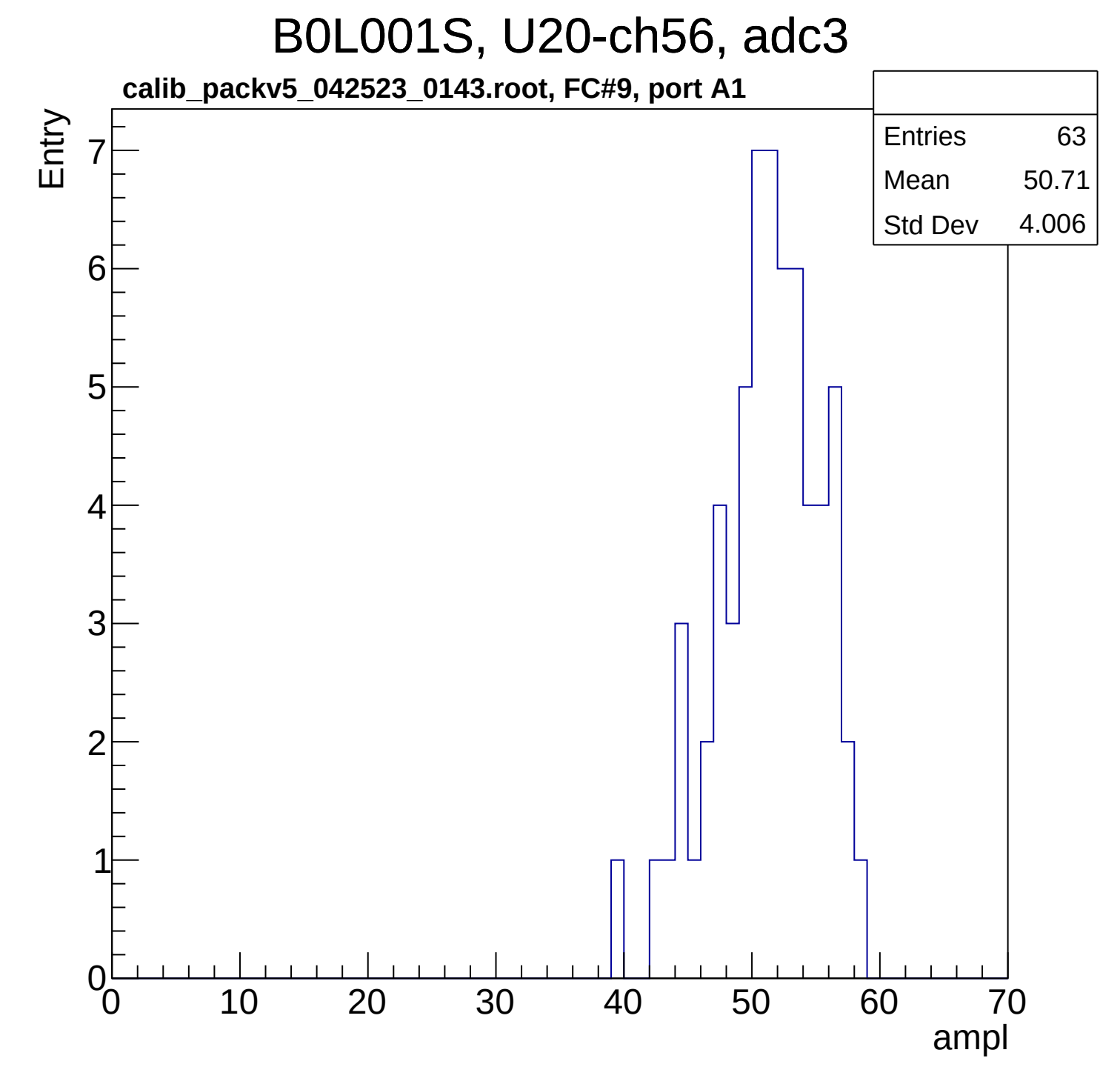
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.71
Std Dev 4.006
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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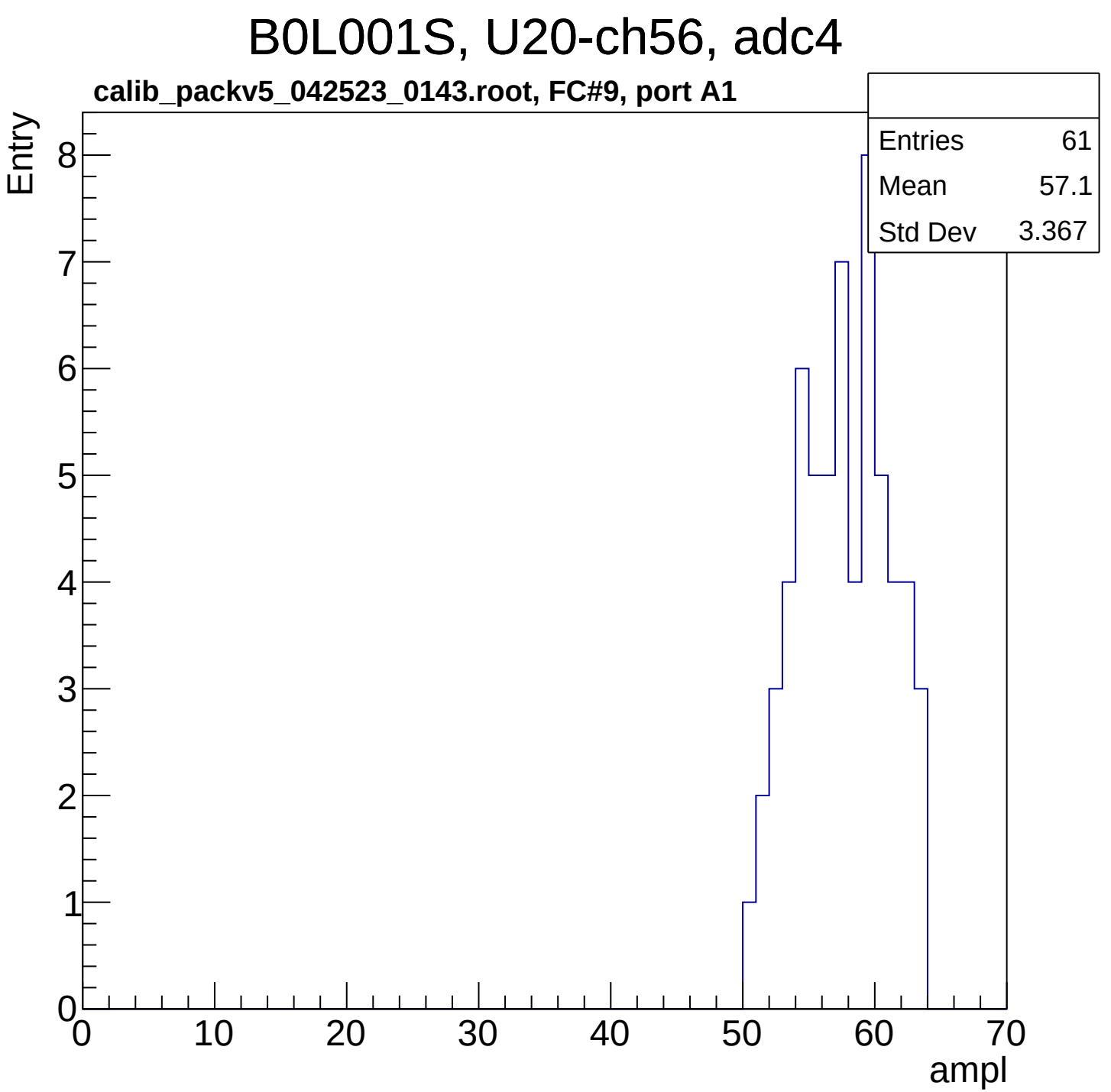
60
70


B0L001S, U20-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.1
Std Dev 3.367
Entry
ampl
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1
2
3
4
5
6
7
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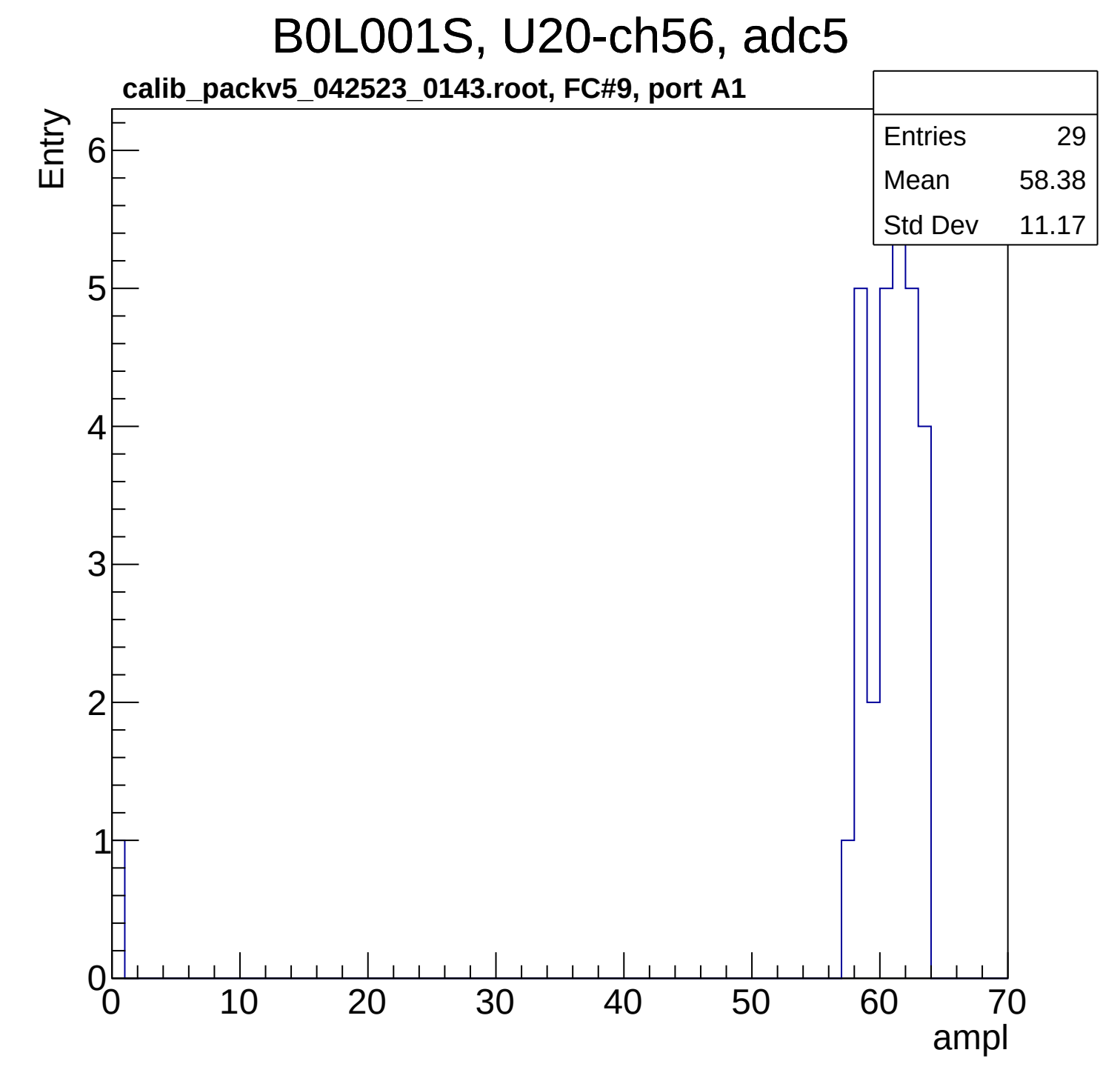
0
10
20
30
40
50
60
70


B0L001S, U20-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.38
Std Dev 11.17
Entry
0
1
2
3
4
5
6
0
10
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30
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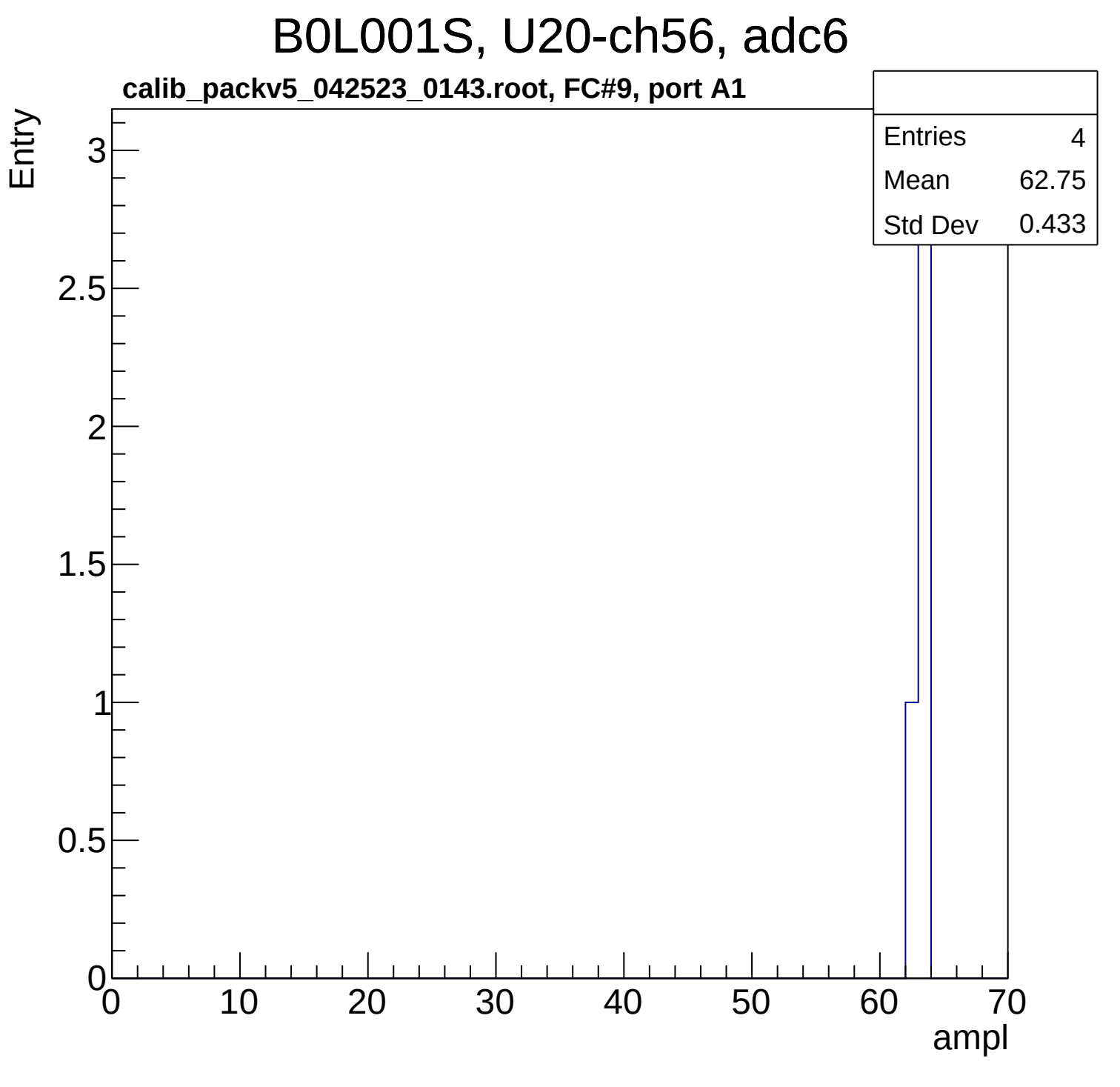
50
60
70
ampl


B0L001S, U20-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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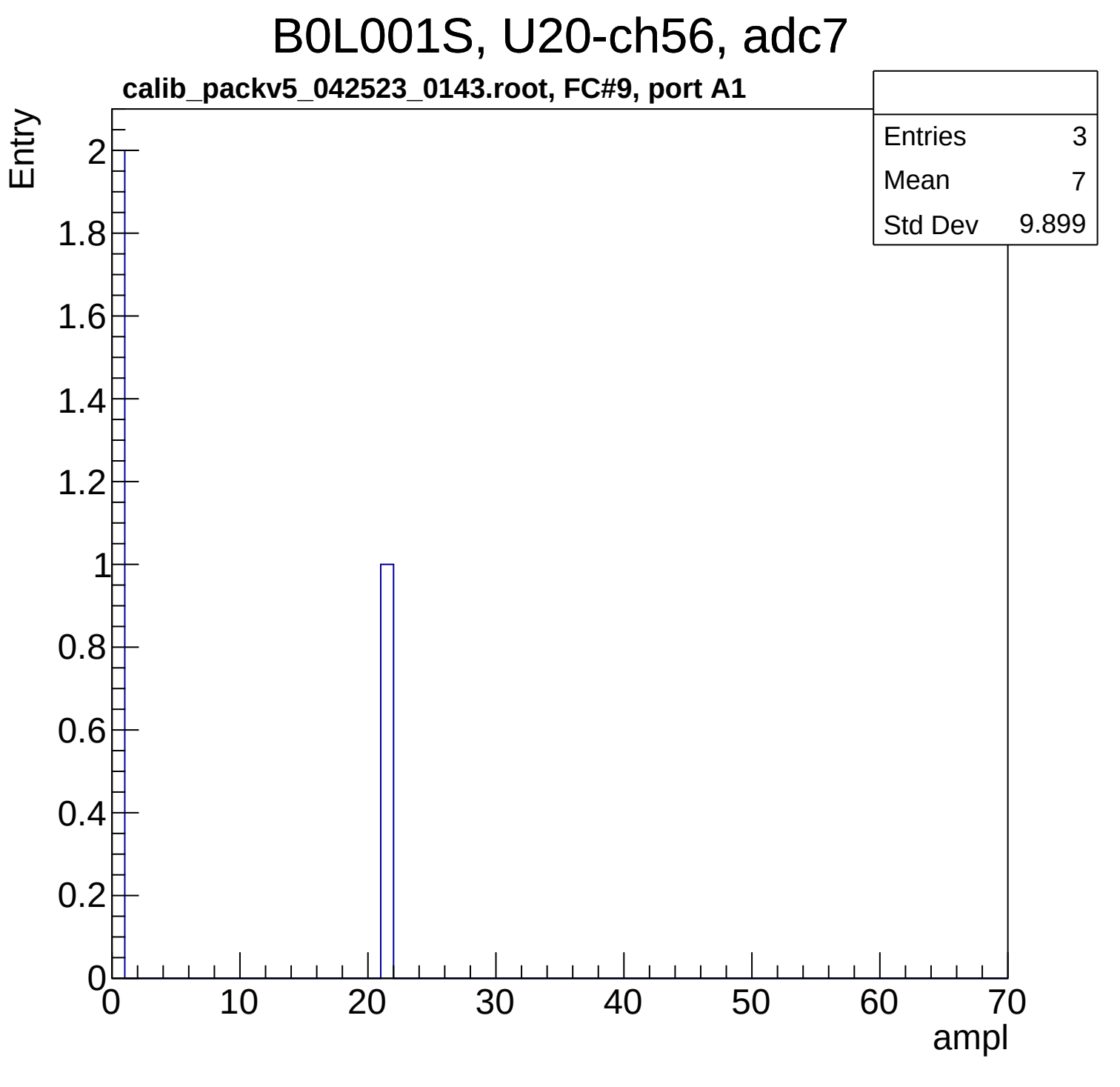
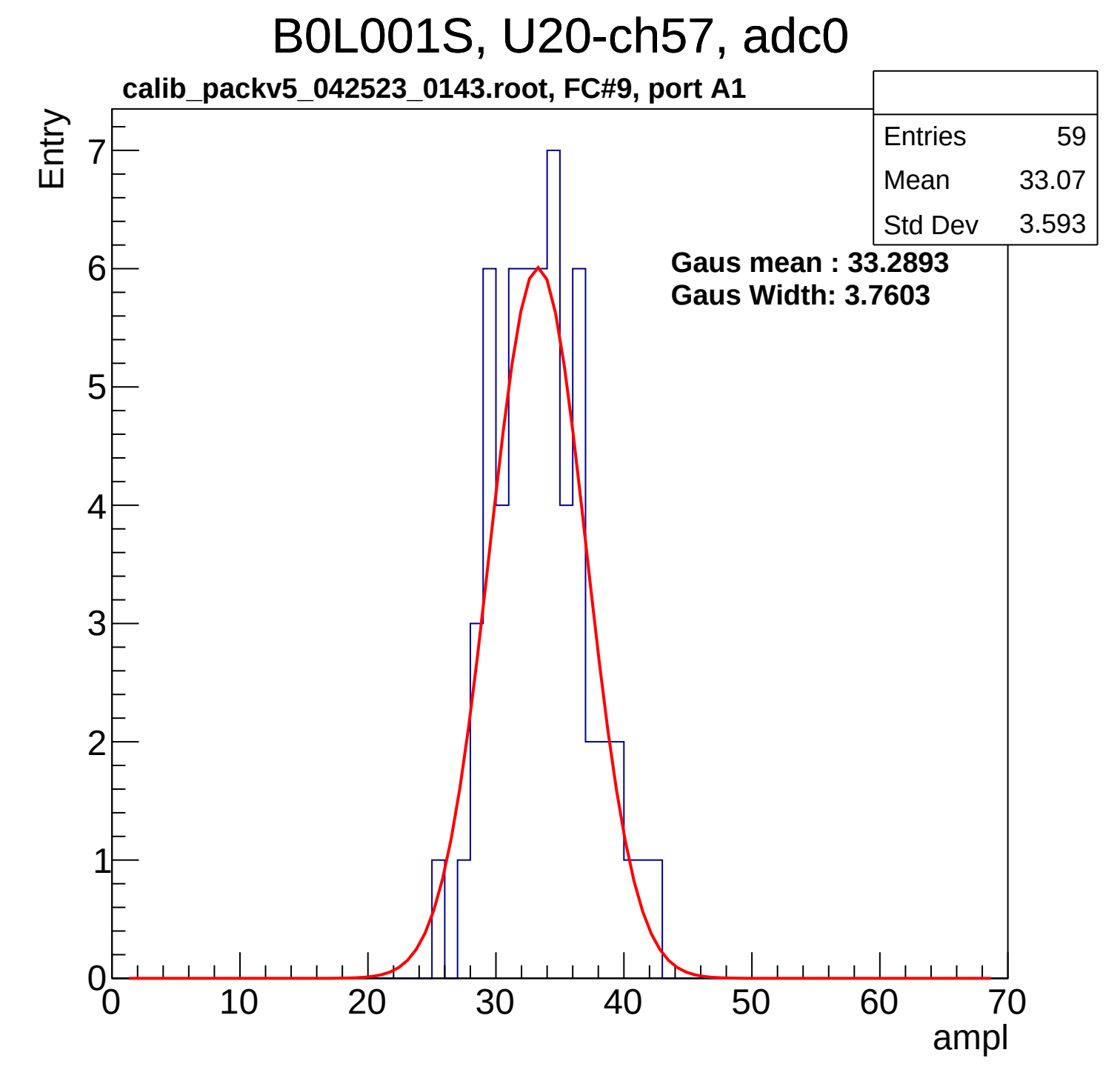
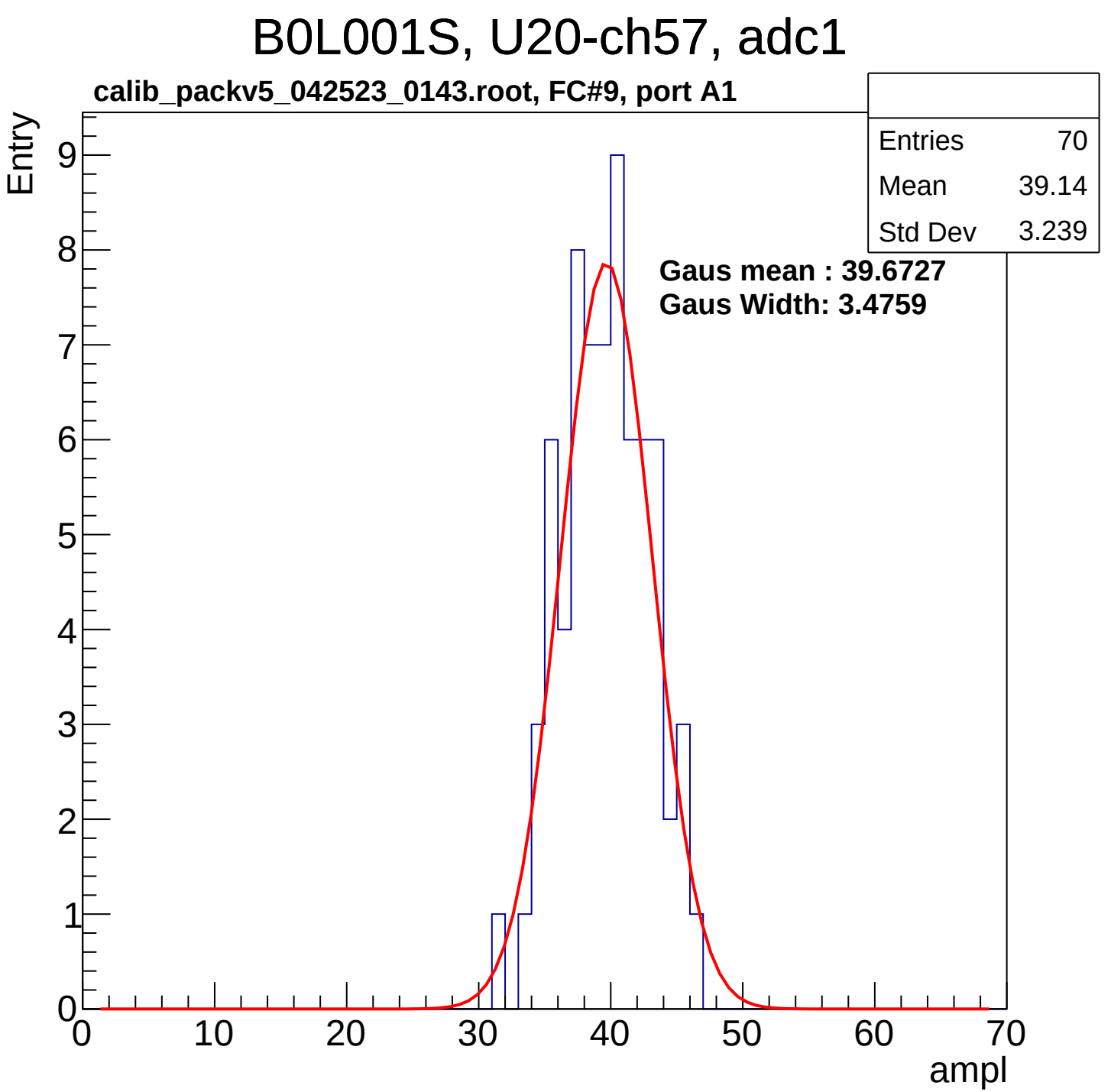
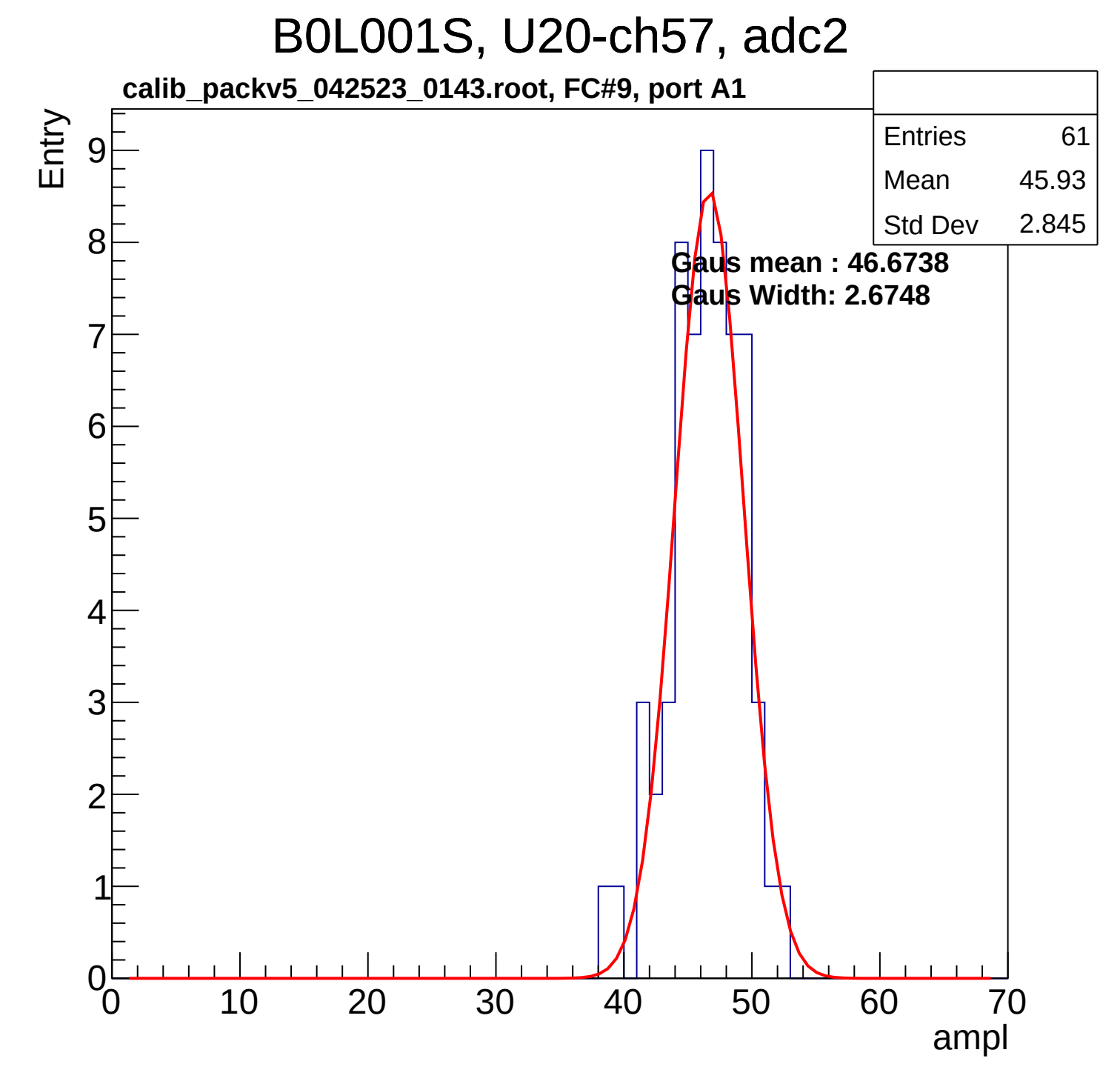
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 33.07
Std Dev 3.593
Gaus mean : 33.2893
Gaus Width: 3.7603
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 39.14
Std Dev 3.239
Gaus mean : 39.6727
Gaus Width: 3.4759
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.93
Std Dev 2.845
Gaus mean : 46.6738
Gaus Width: 2.6748
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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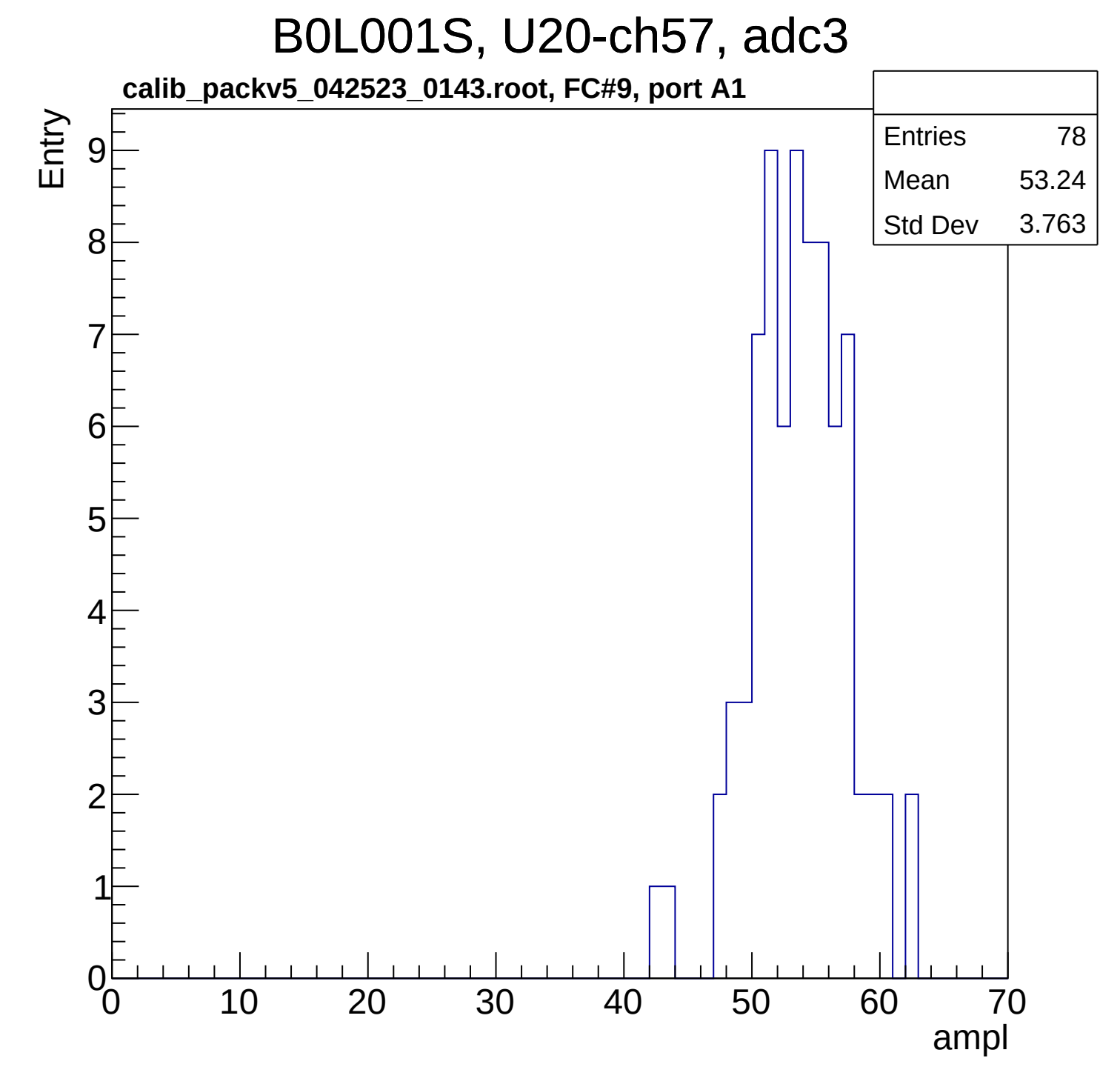
B0L001S, U20-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 53.24
Std Dev 3.763
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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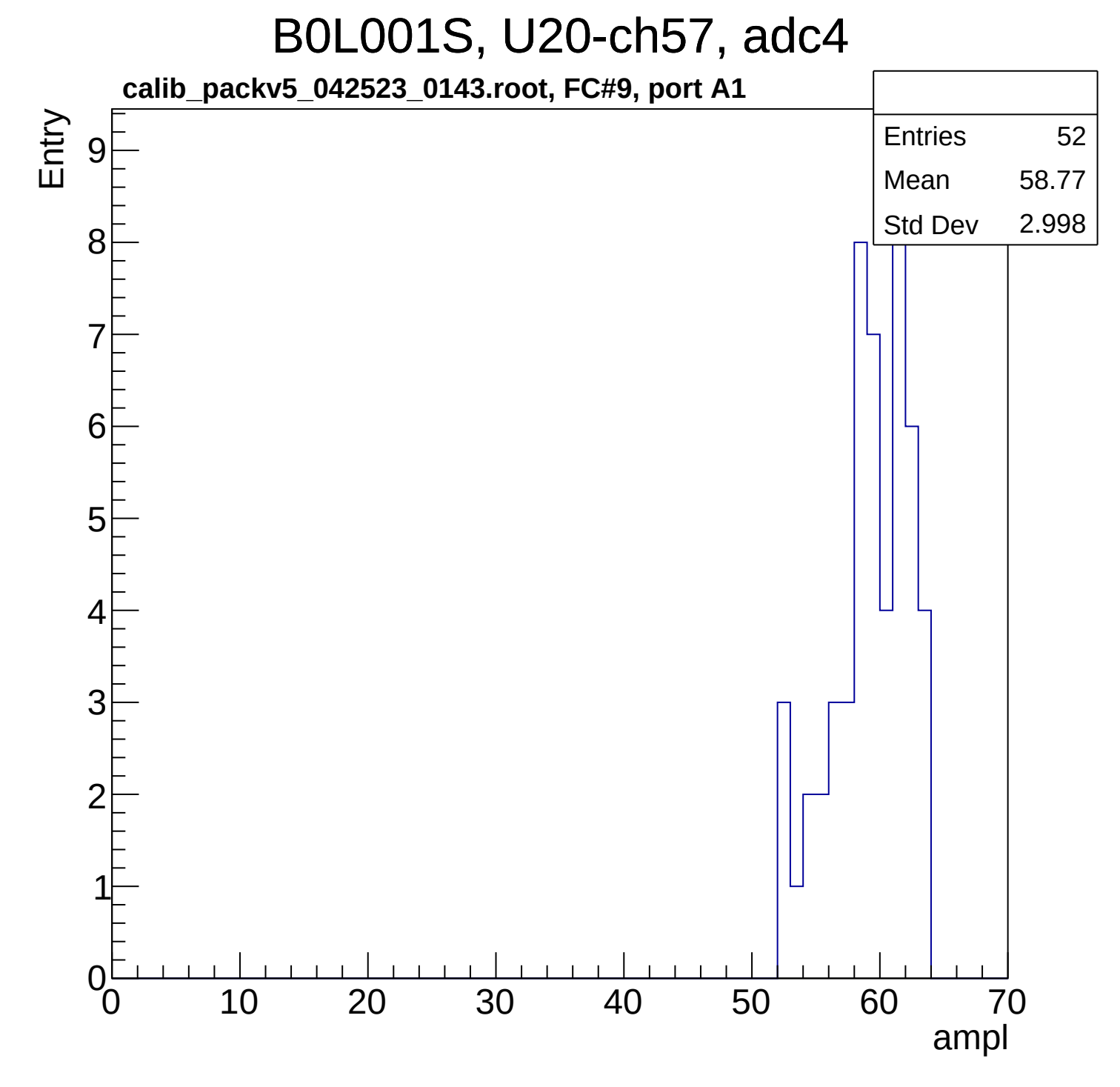
B0L001S, U20-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.77
Std Dev 2.998
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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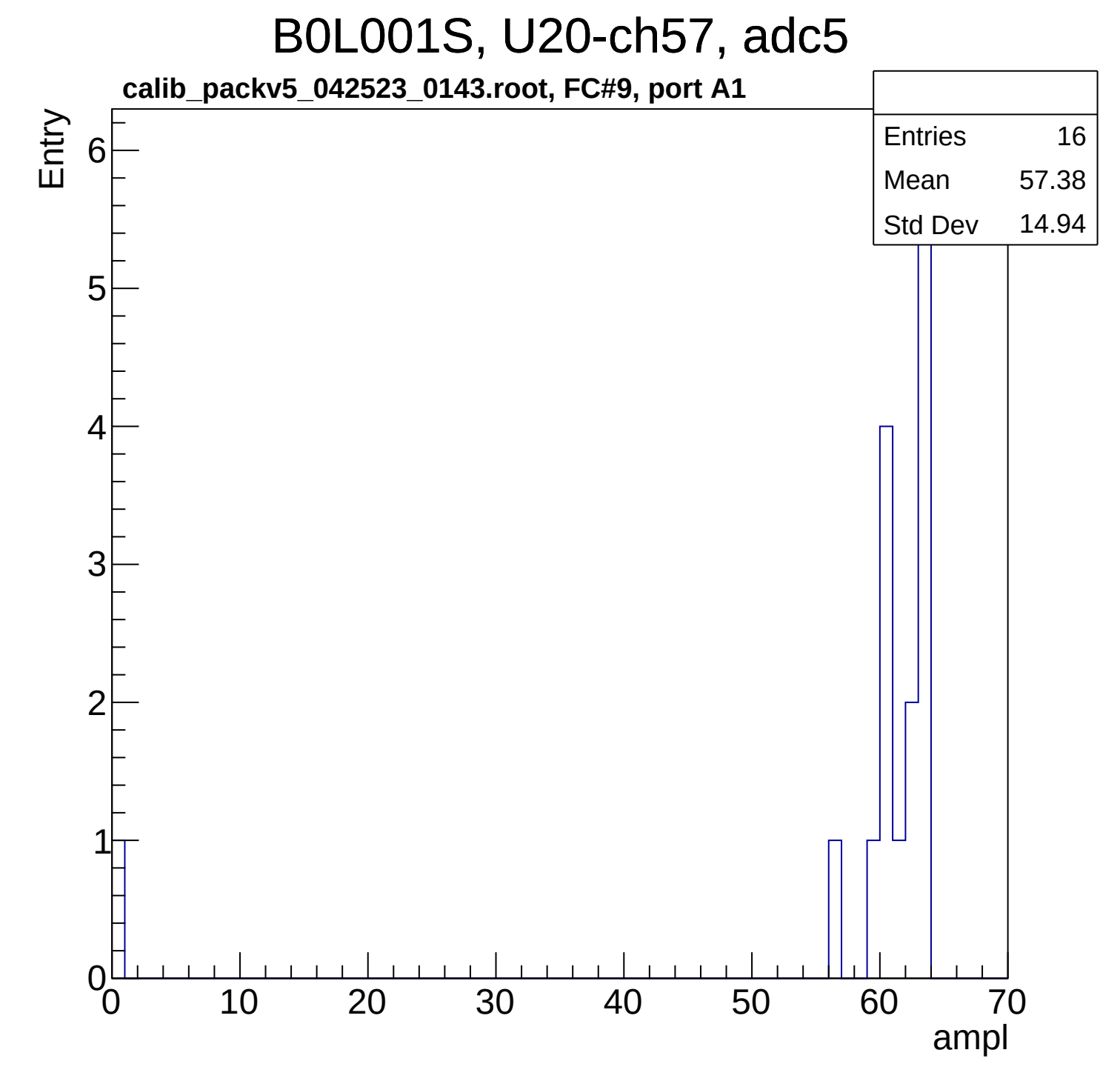
B0L001S, U20-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 57.38
Std Dev 14.94
Entry
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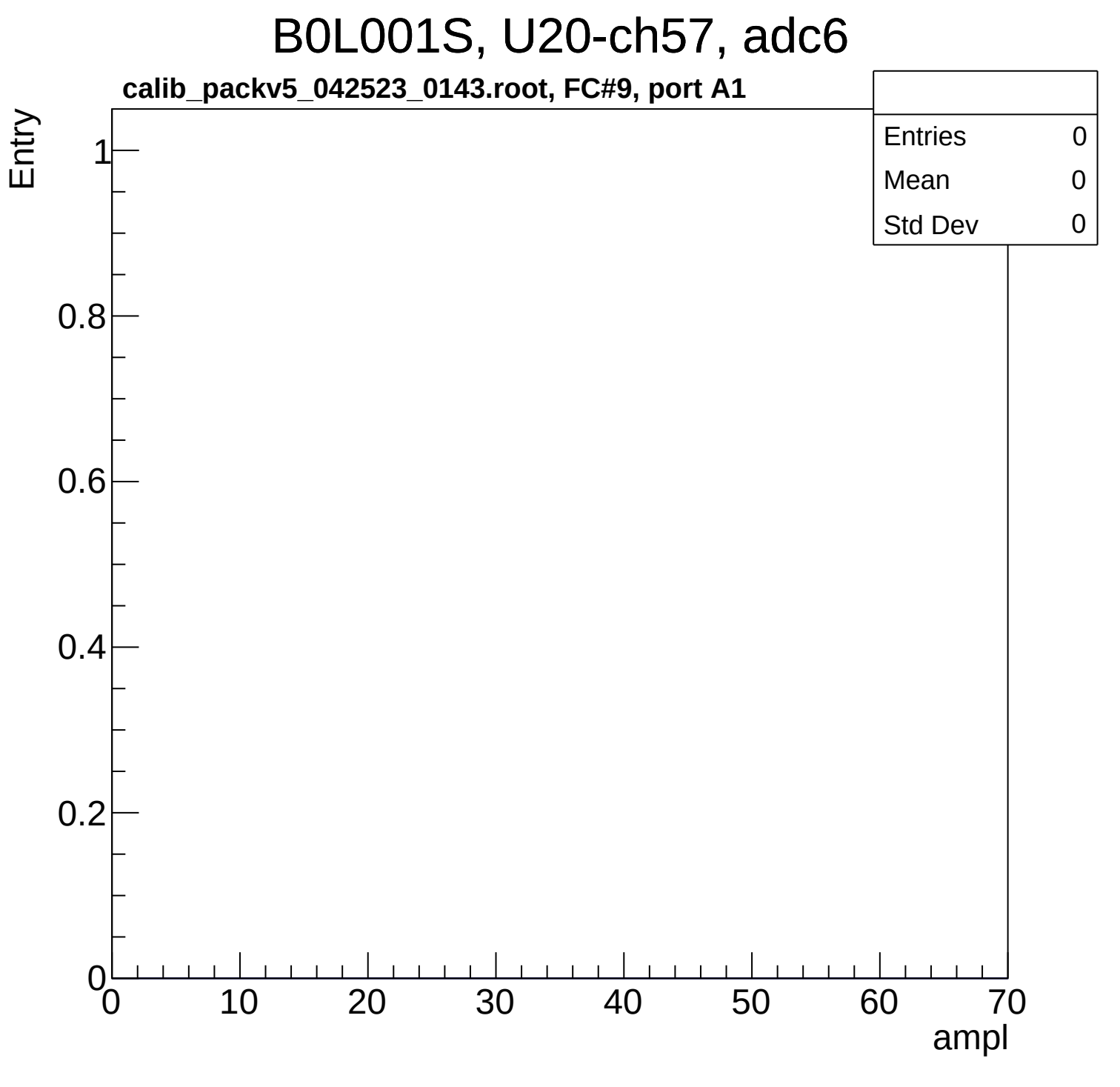
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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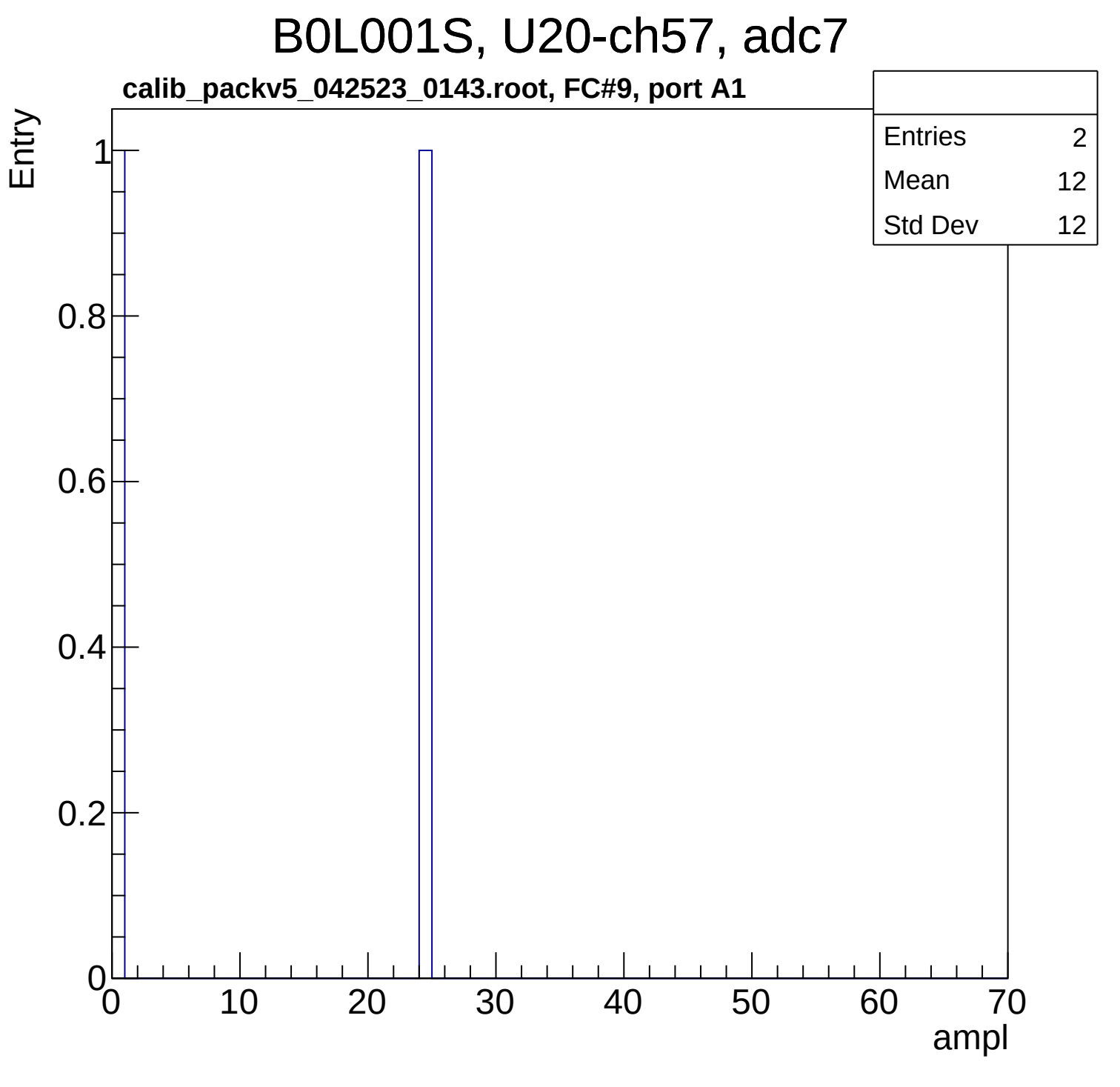
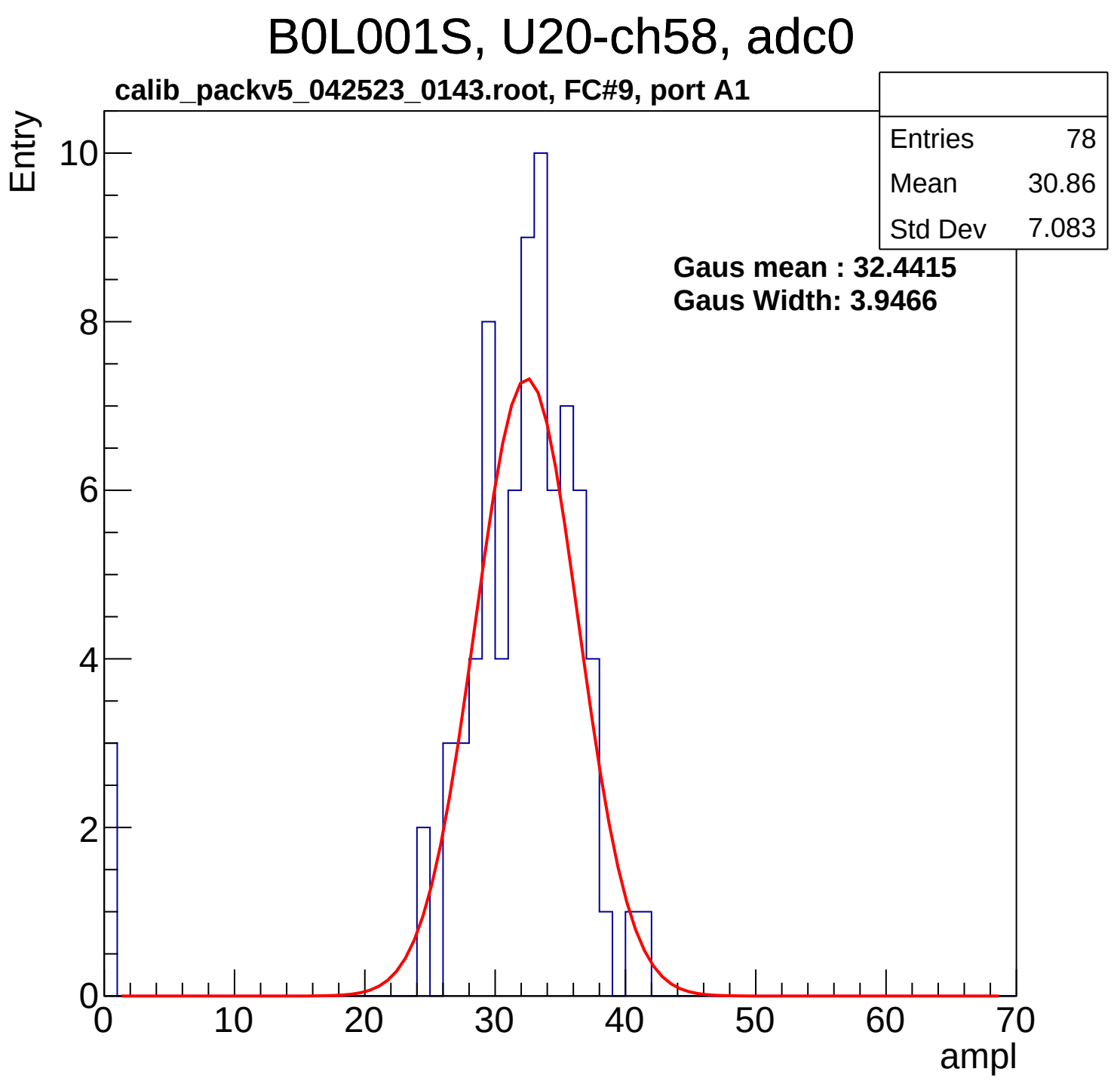
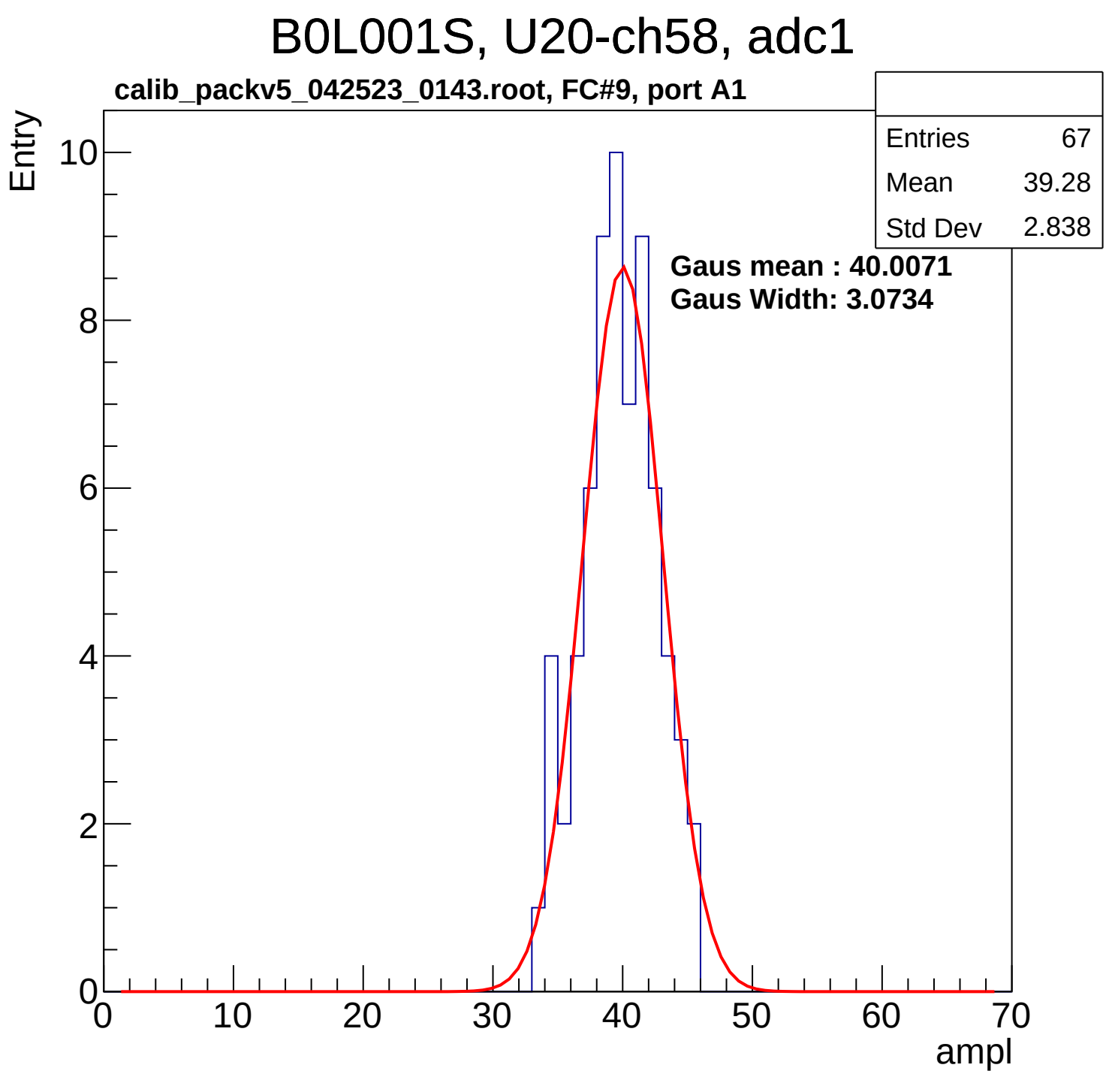
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.86
Std Dev 7.083
Gaus mean : 32.4415
Gaus Width: 3.9466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 39.28
Std Dev 2.838
Gaus mean : 40.0071
Gaus Width: 3.0734
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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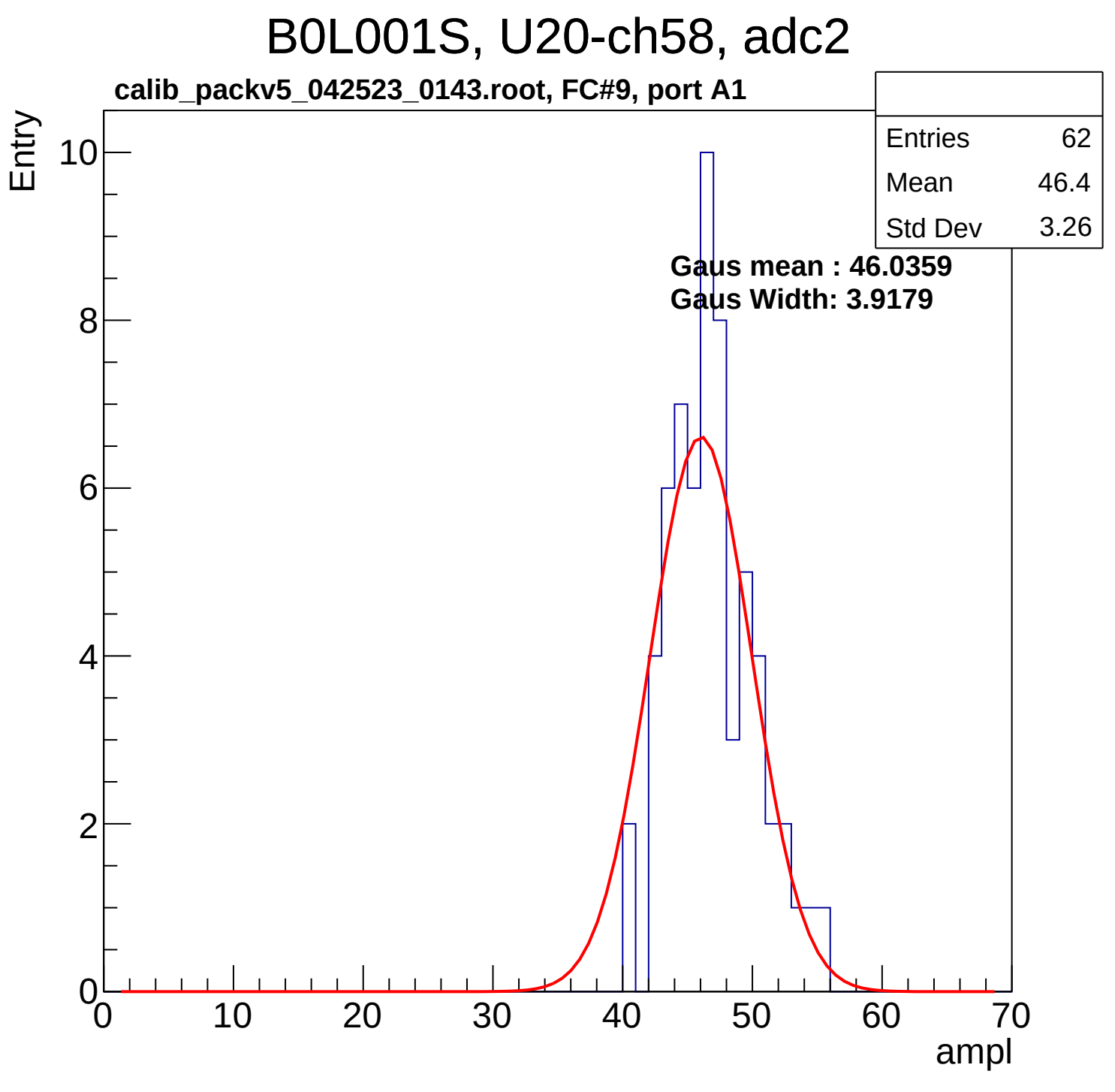
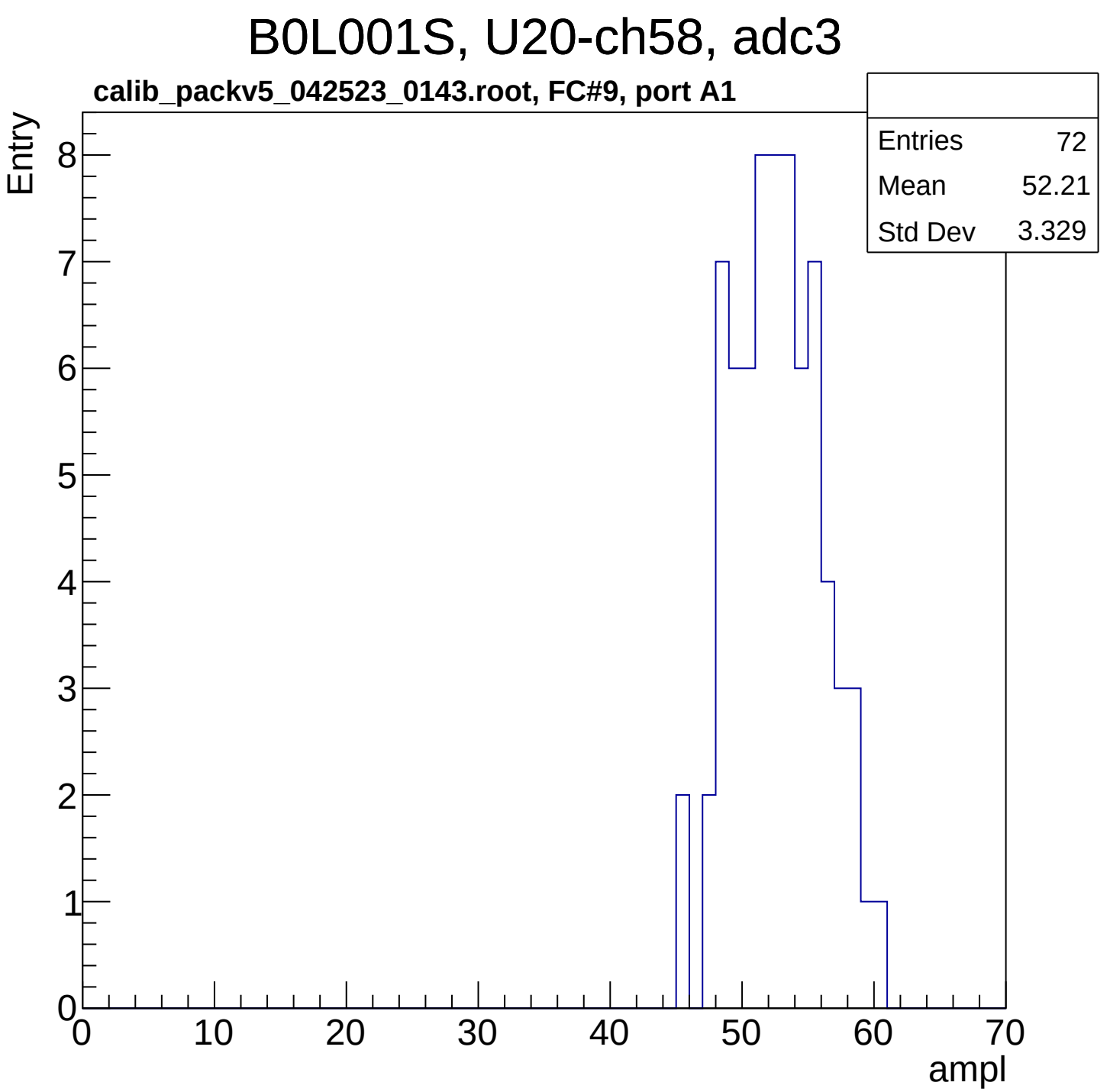
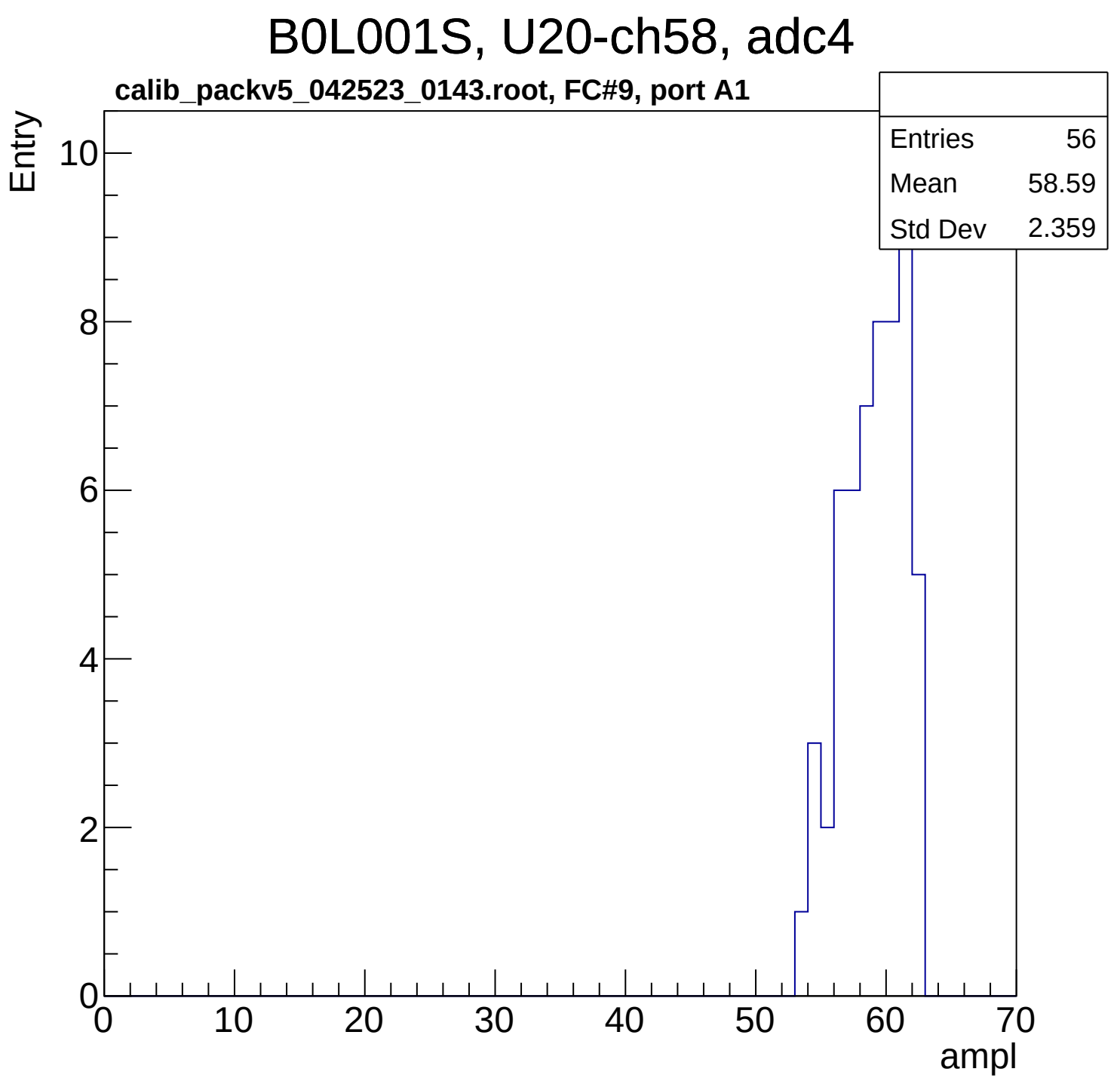
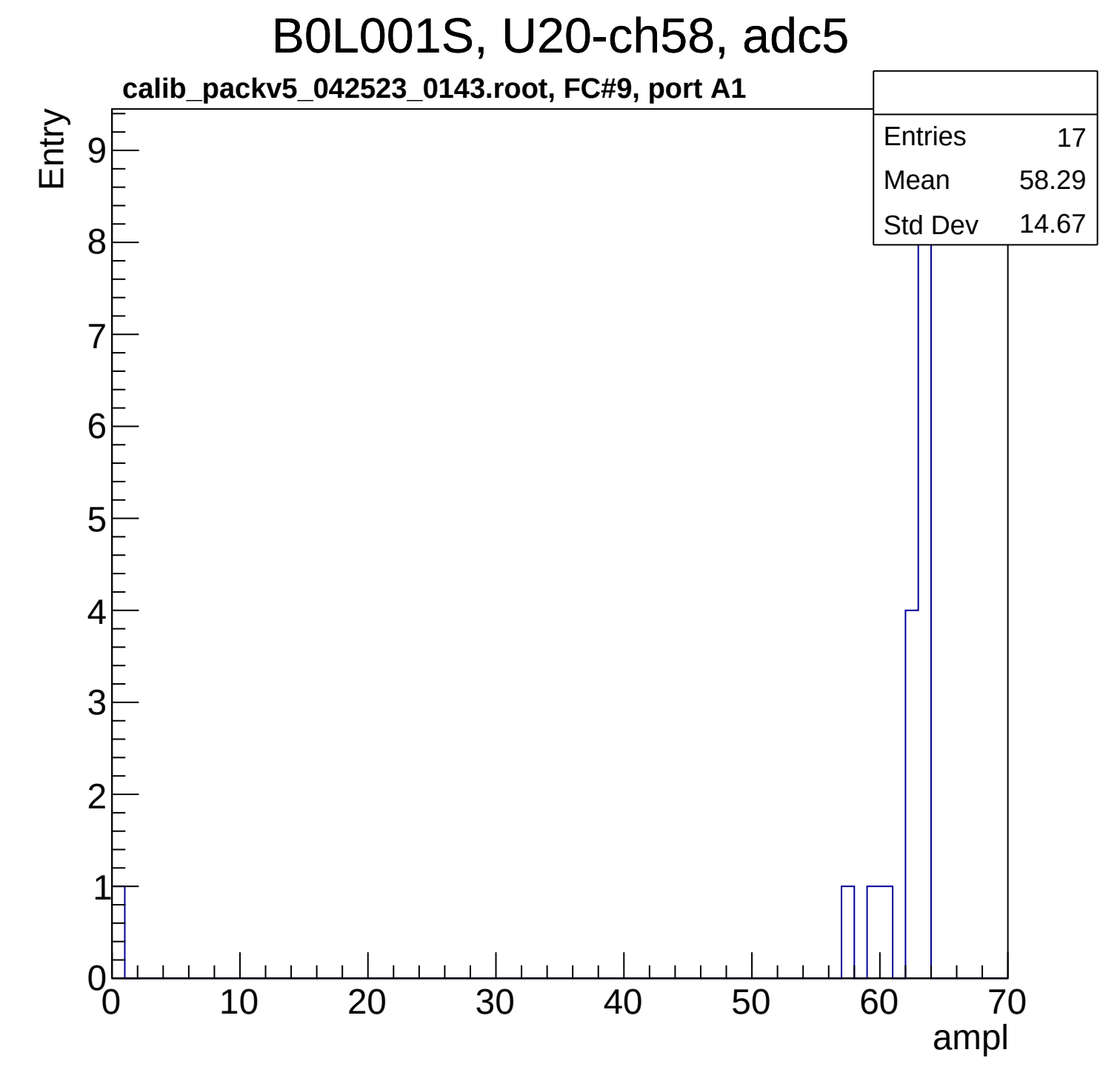
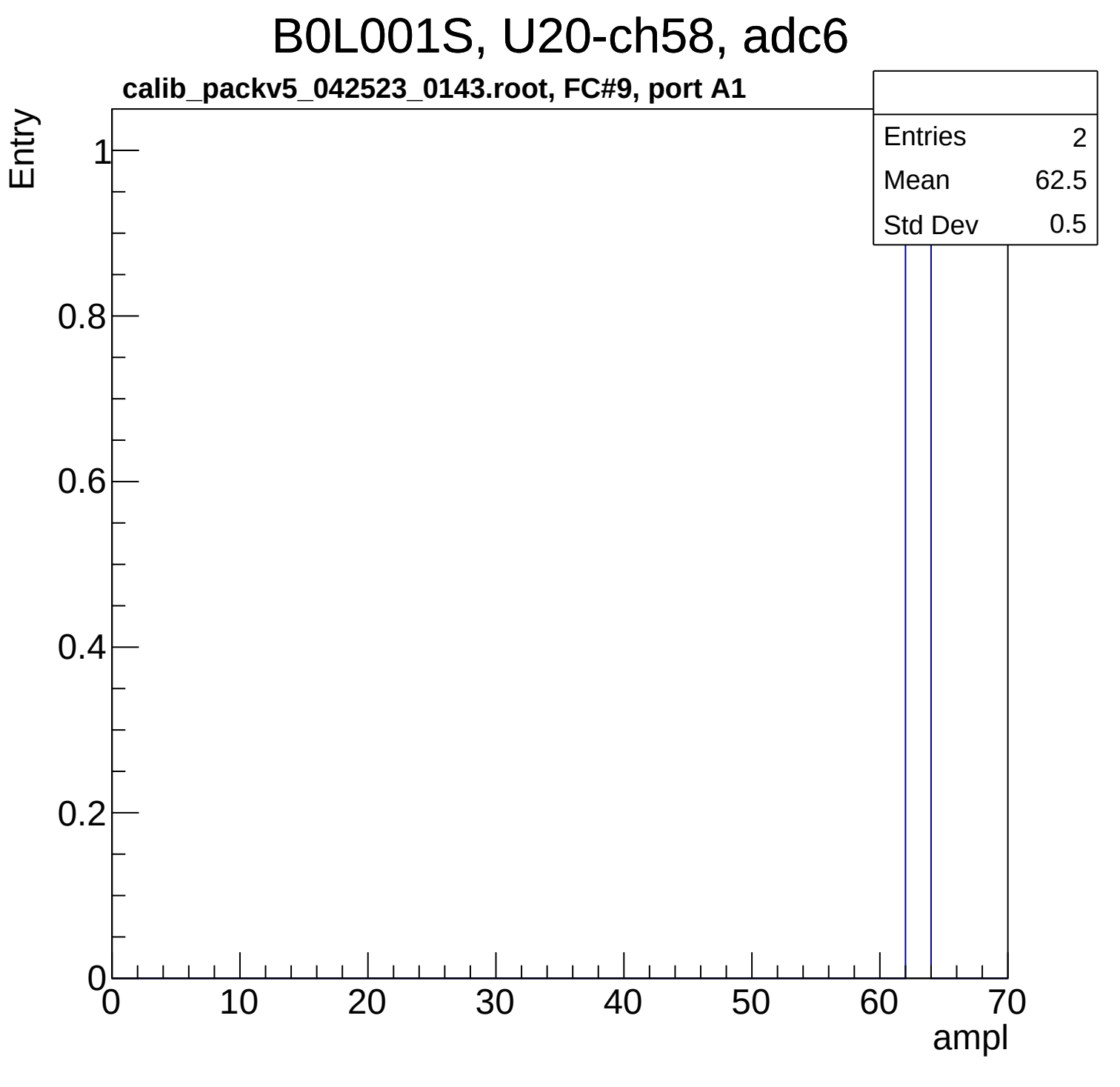
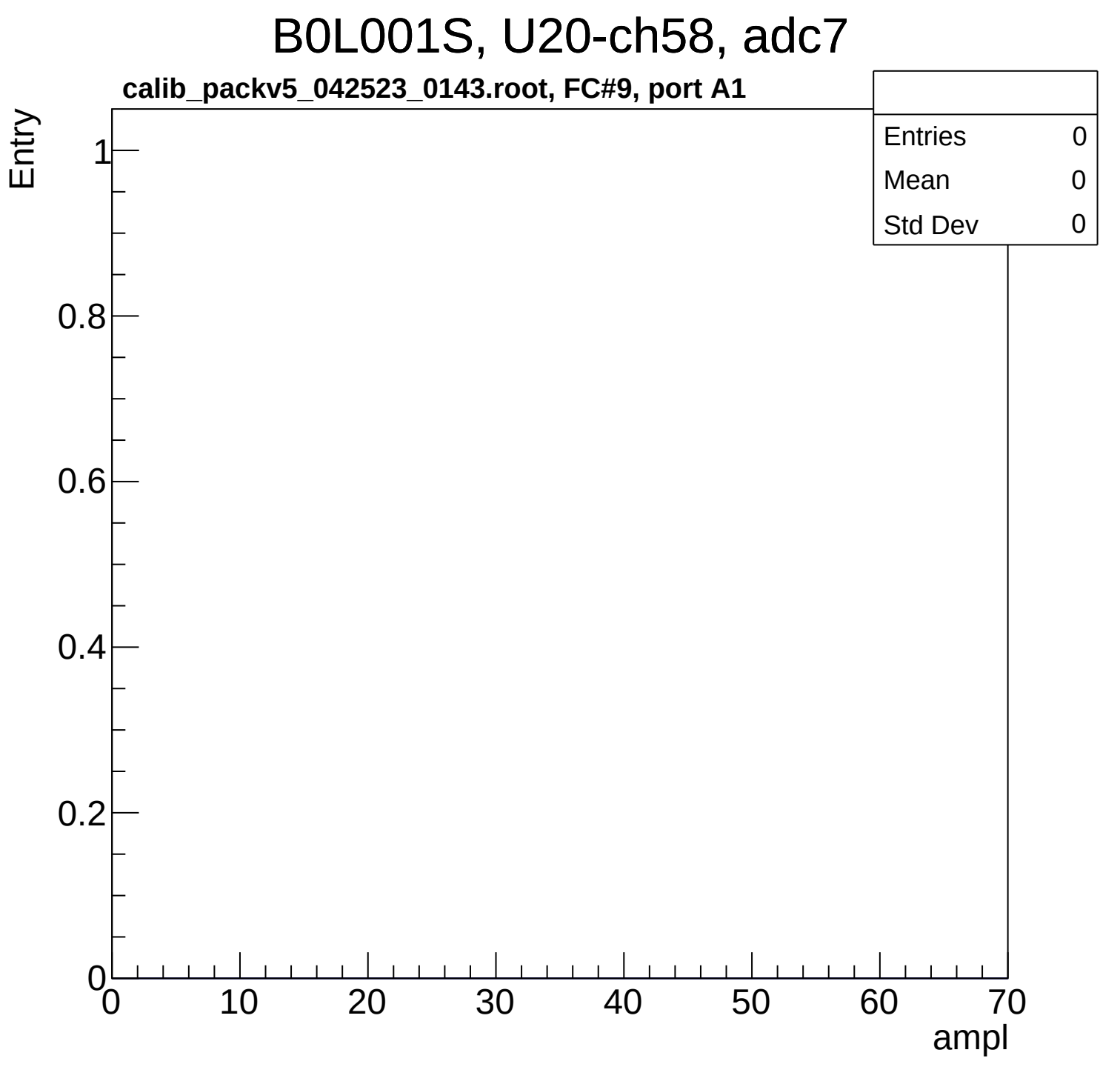

B0L001S, U20-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 46.4
Std Dev 3.26
Gaus mean : 46.0359
Gaus Width: 3.9179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

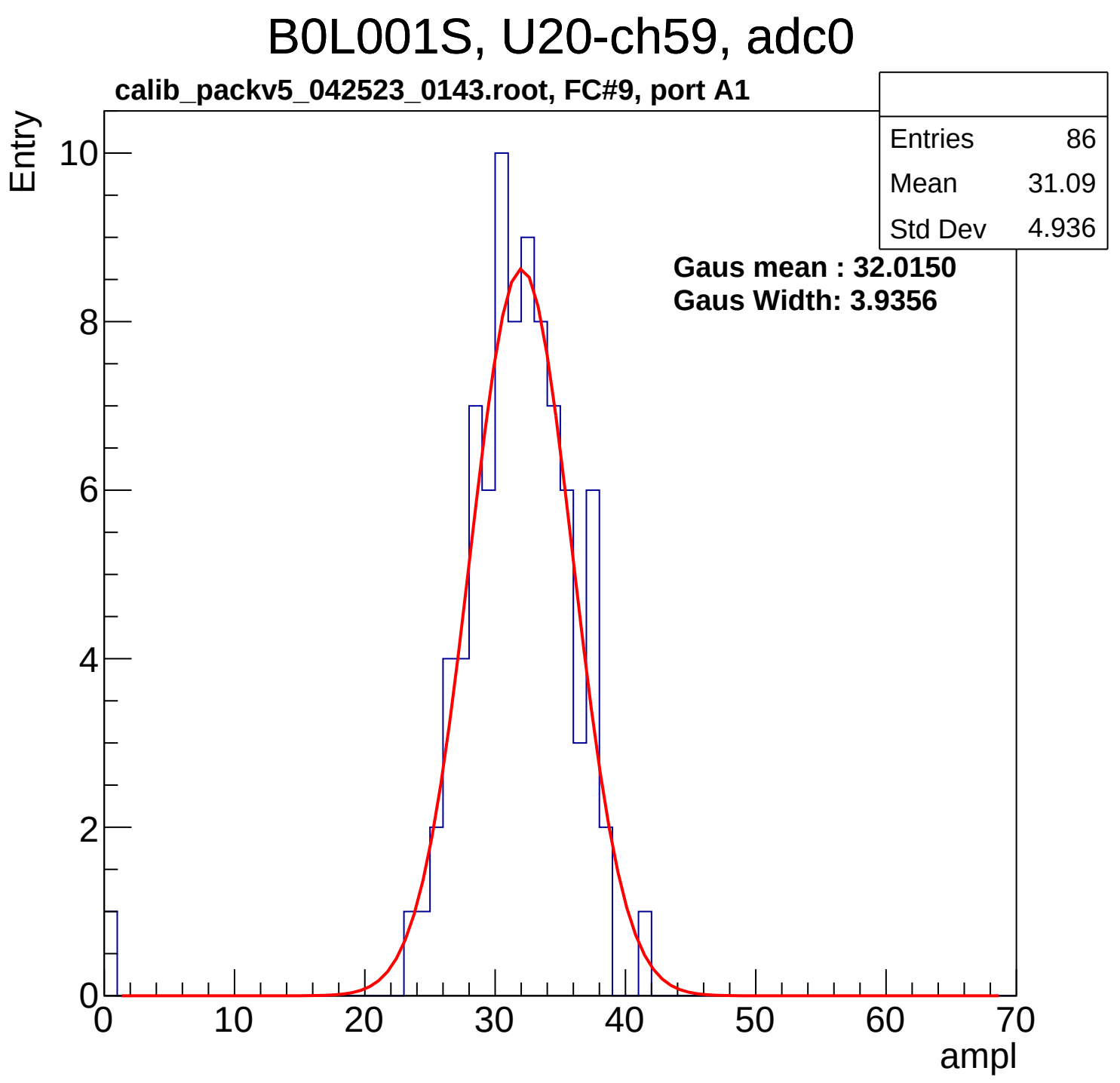

B0L001S, U20-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.21
Std Dev 3.329
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

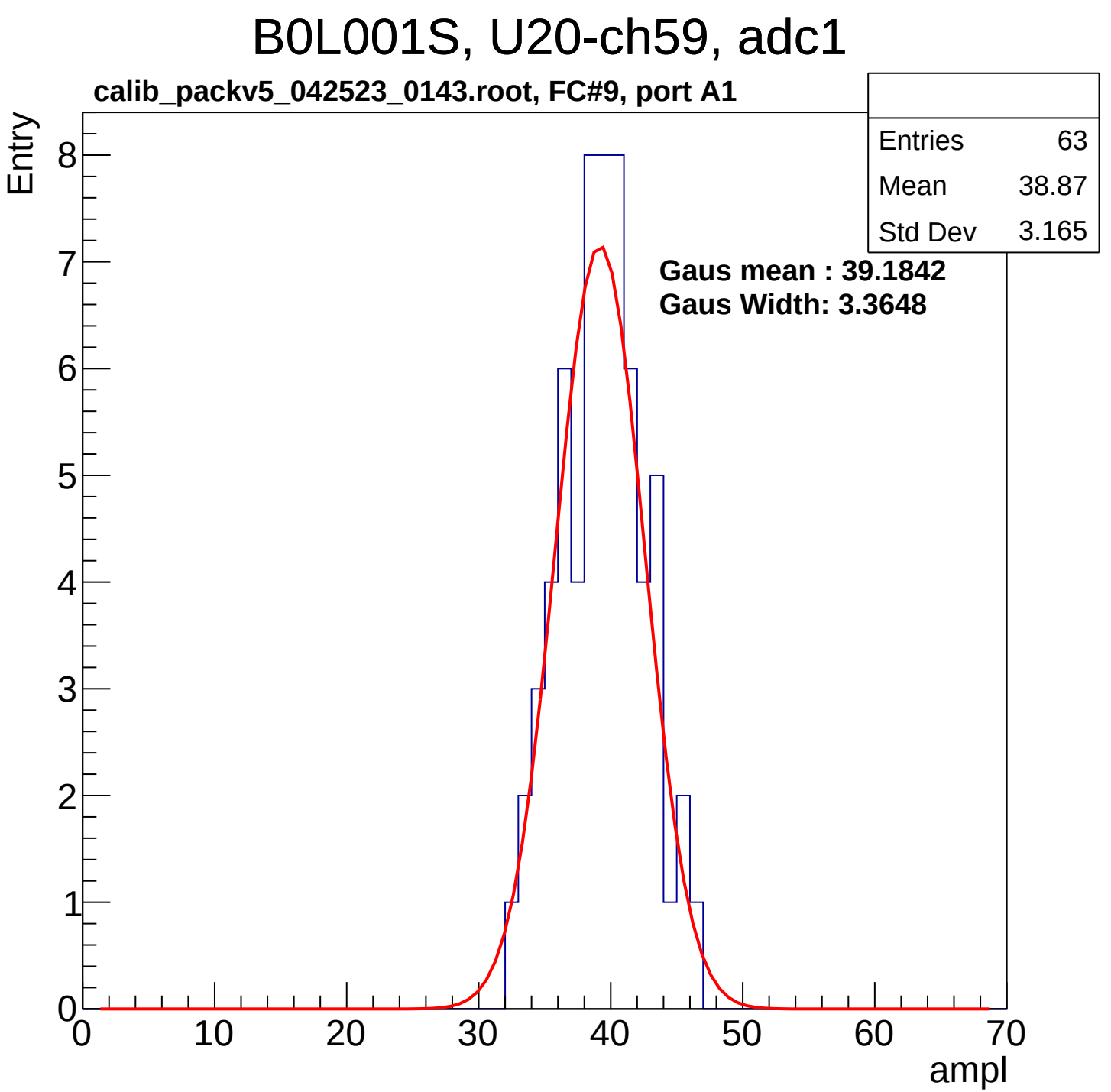

B0L001S, U20-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.59
Std Dev 2.359
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

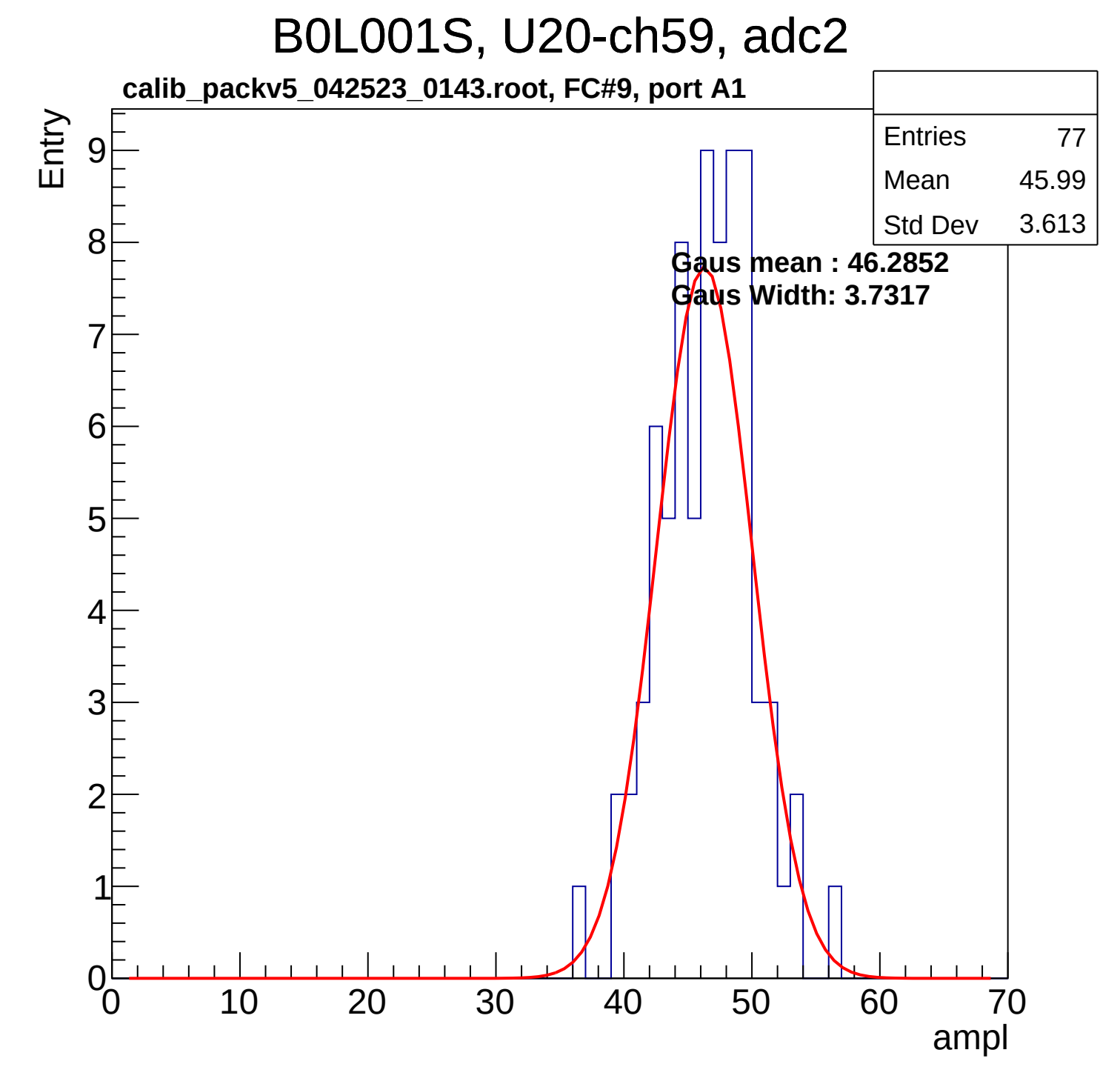

B0L001S, U20-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 58.29
Std Dev 14.67
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

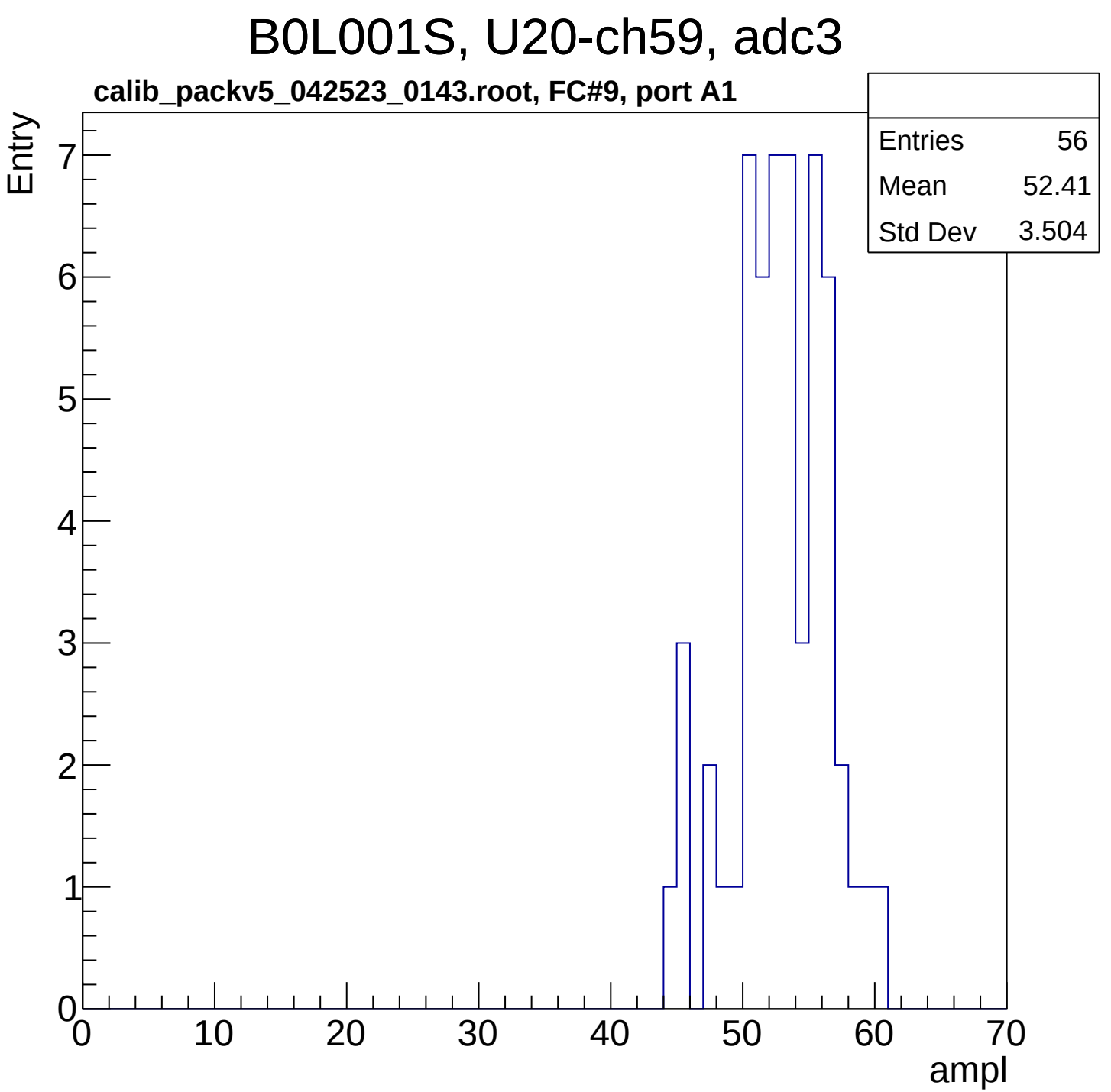

B0L001S, U20-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

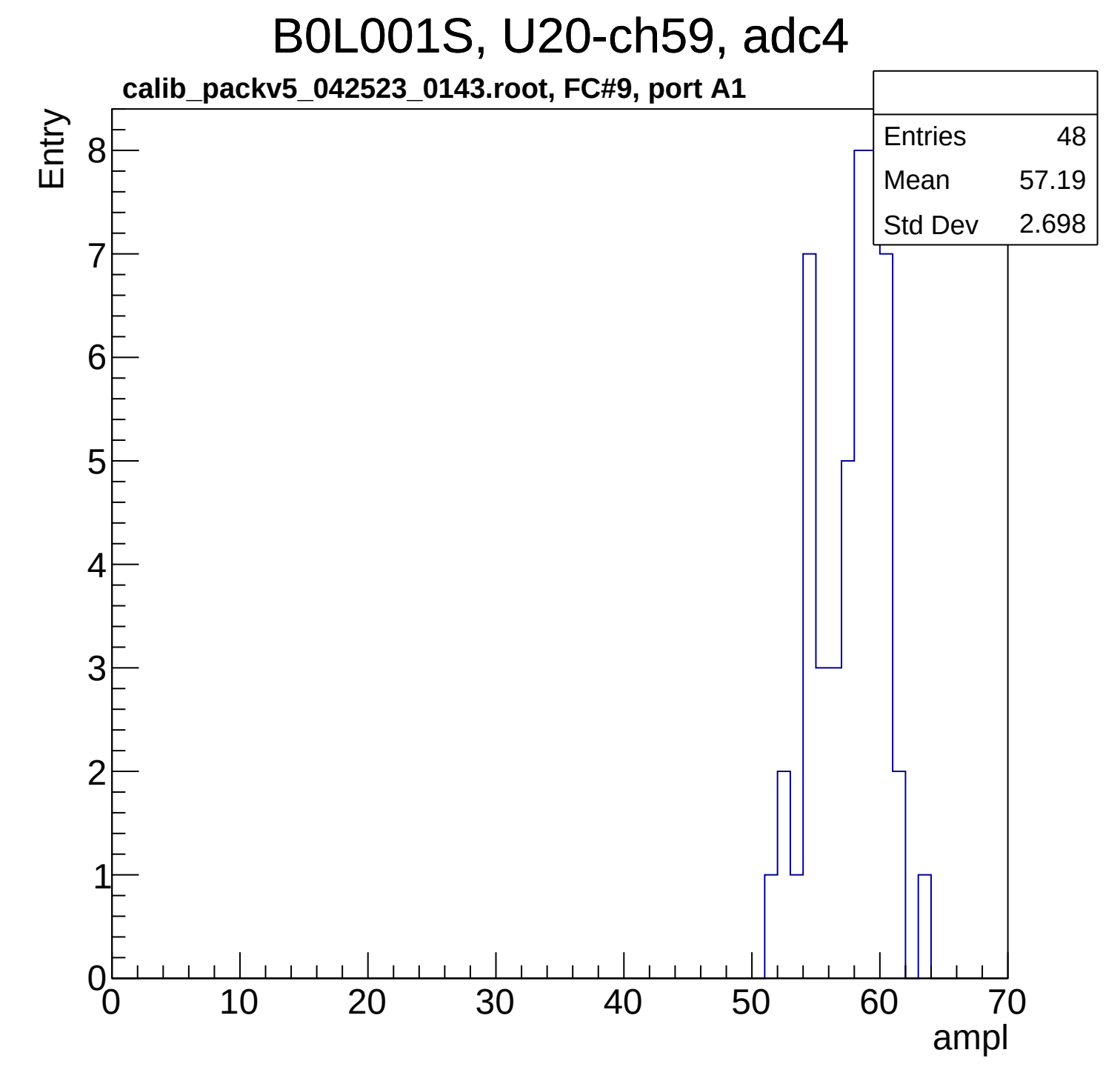

B0L001S, U20-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

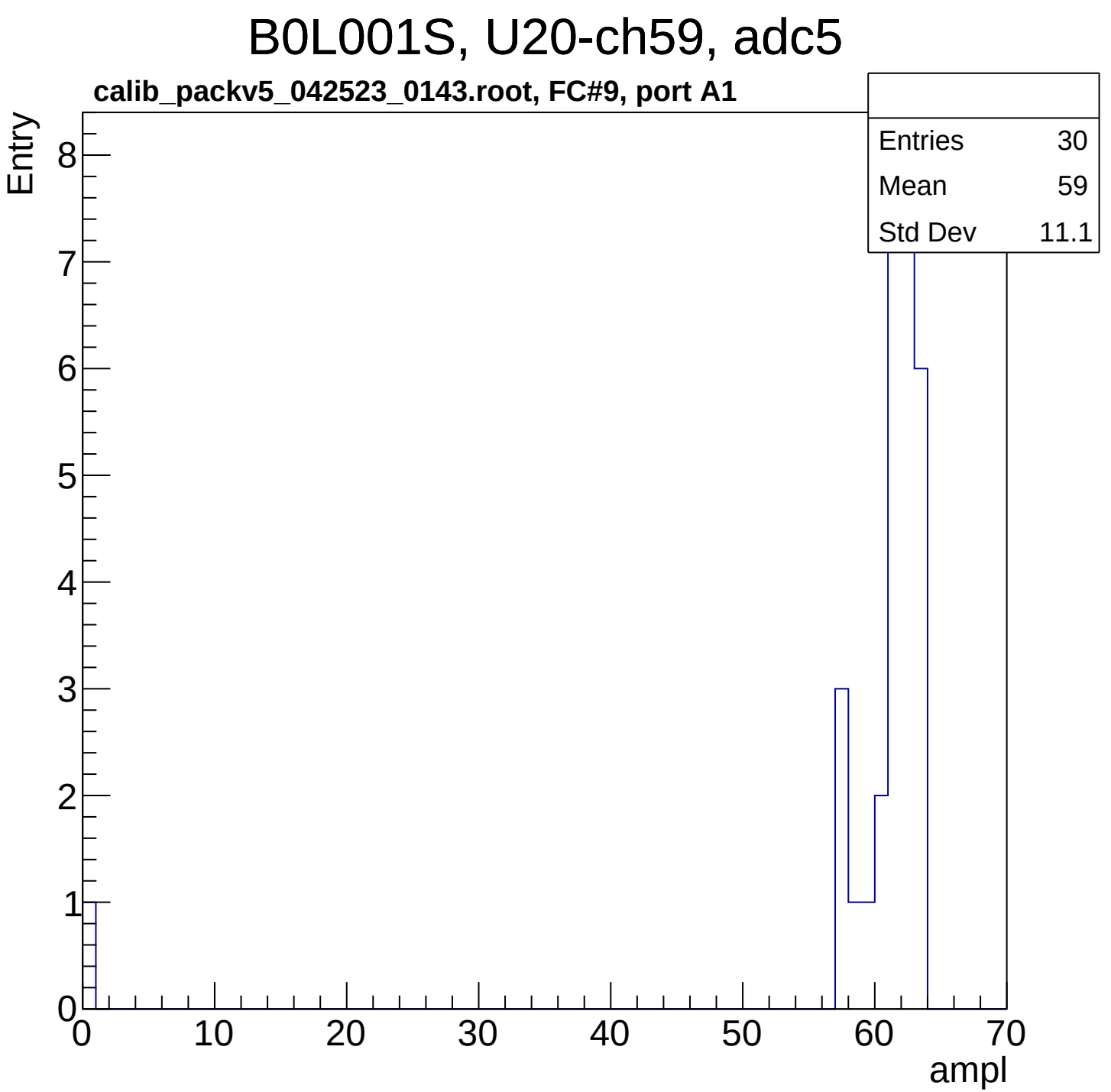

B0L001S, U20-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 31.09
Std Dev 4.936
Gaus mean : 32.0150
Gaus Width: 3.9356
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

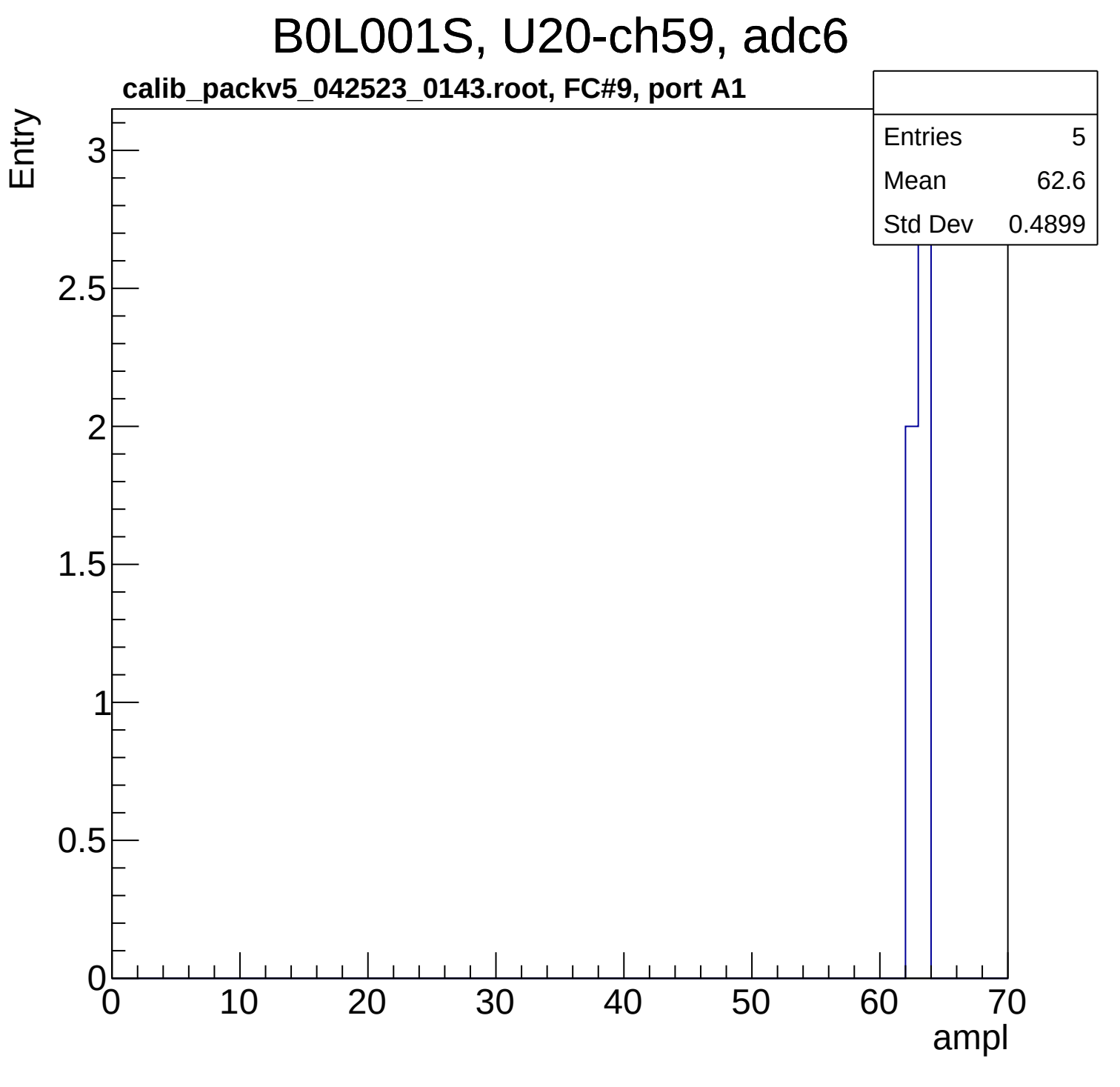

B0L001S, U20-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.87
Std Dev 3.165
Gaus mean : 39.1842
Gaus Width: 3.3648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 45.99
Std Dev 3.613
Gaus mean : 46.2852
Gaus Width: 3.7317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 52.41
Std Dev 3.504
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.19
Std Dev 2.698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59
Std Dev 11.1
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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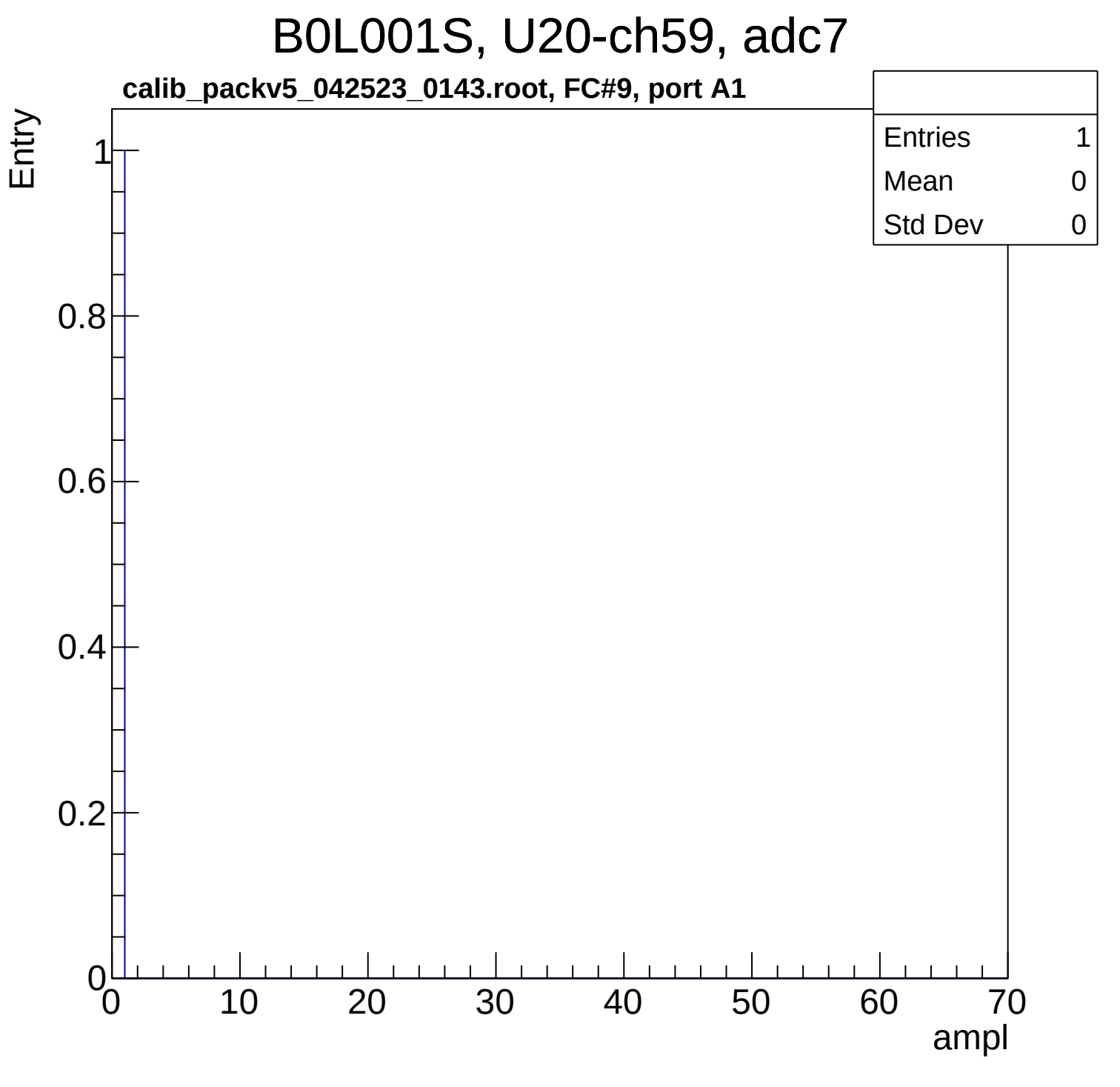
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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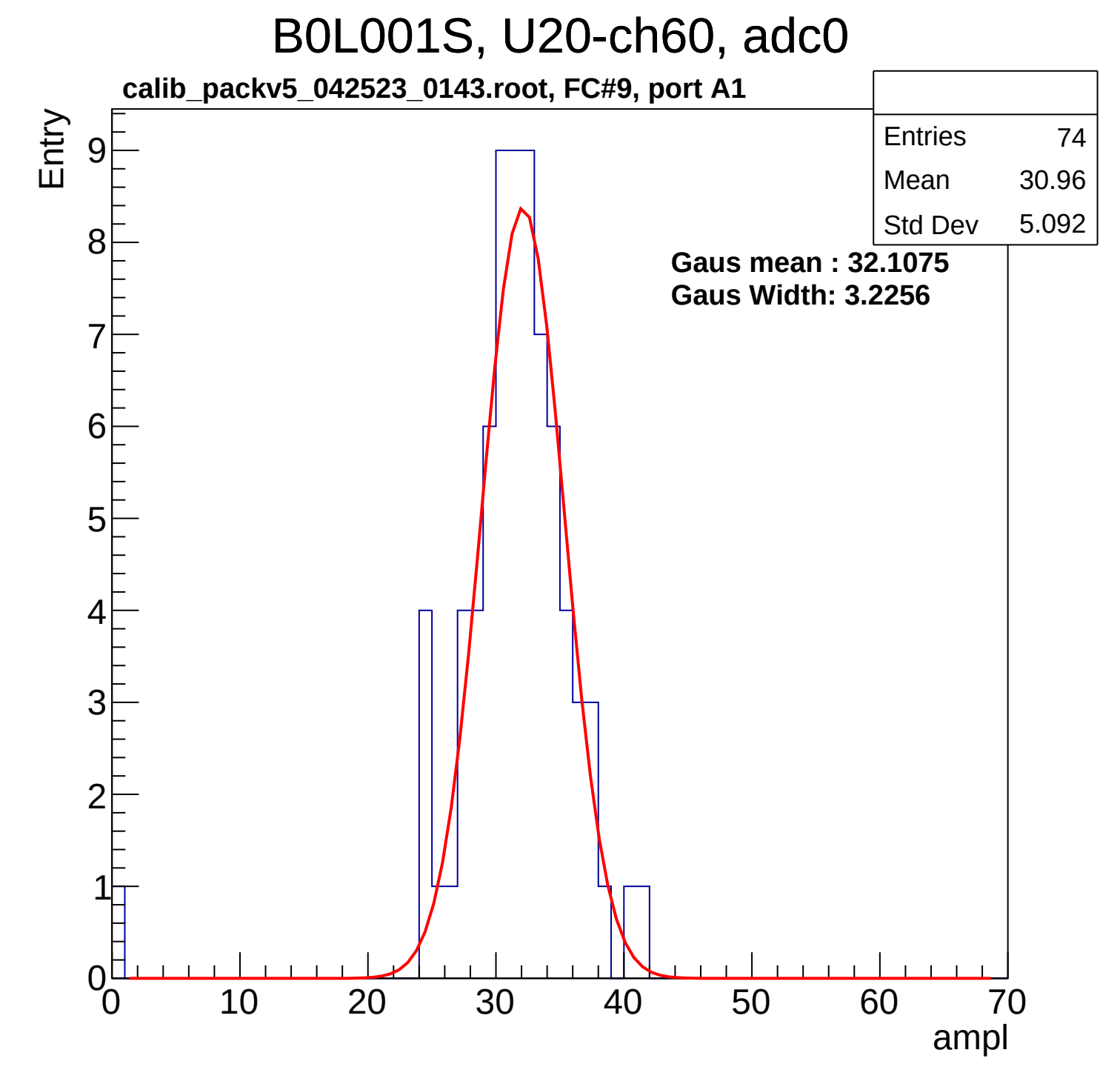
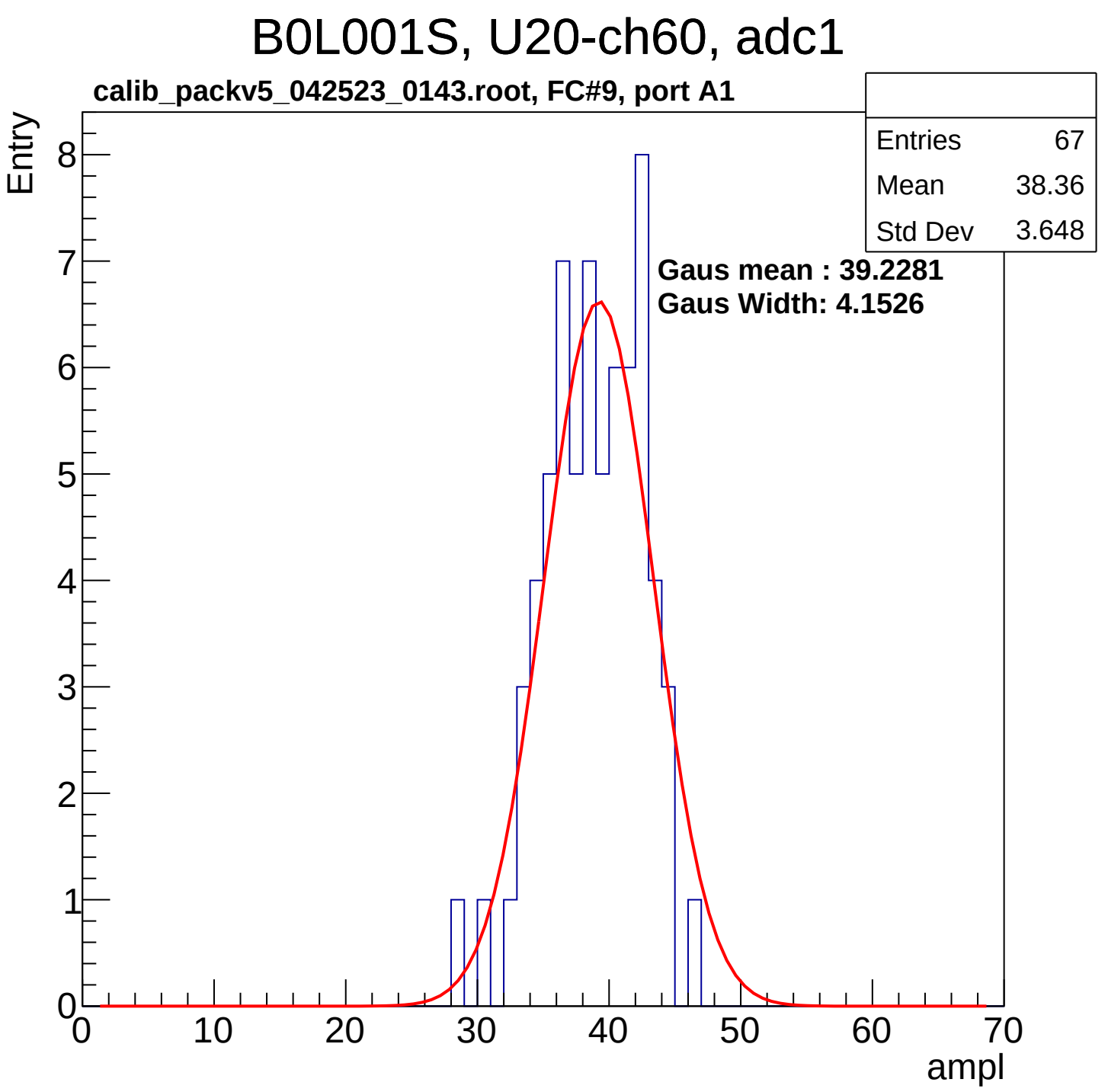
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.96
Std Dev 5.092
Gaus mean : 32.1075
Gaus Width: 3.2256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.36
Std Dev 3.648
Gaus mean : 39.2281
Gaus Width: 4.1526
Entry
ampl
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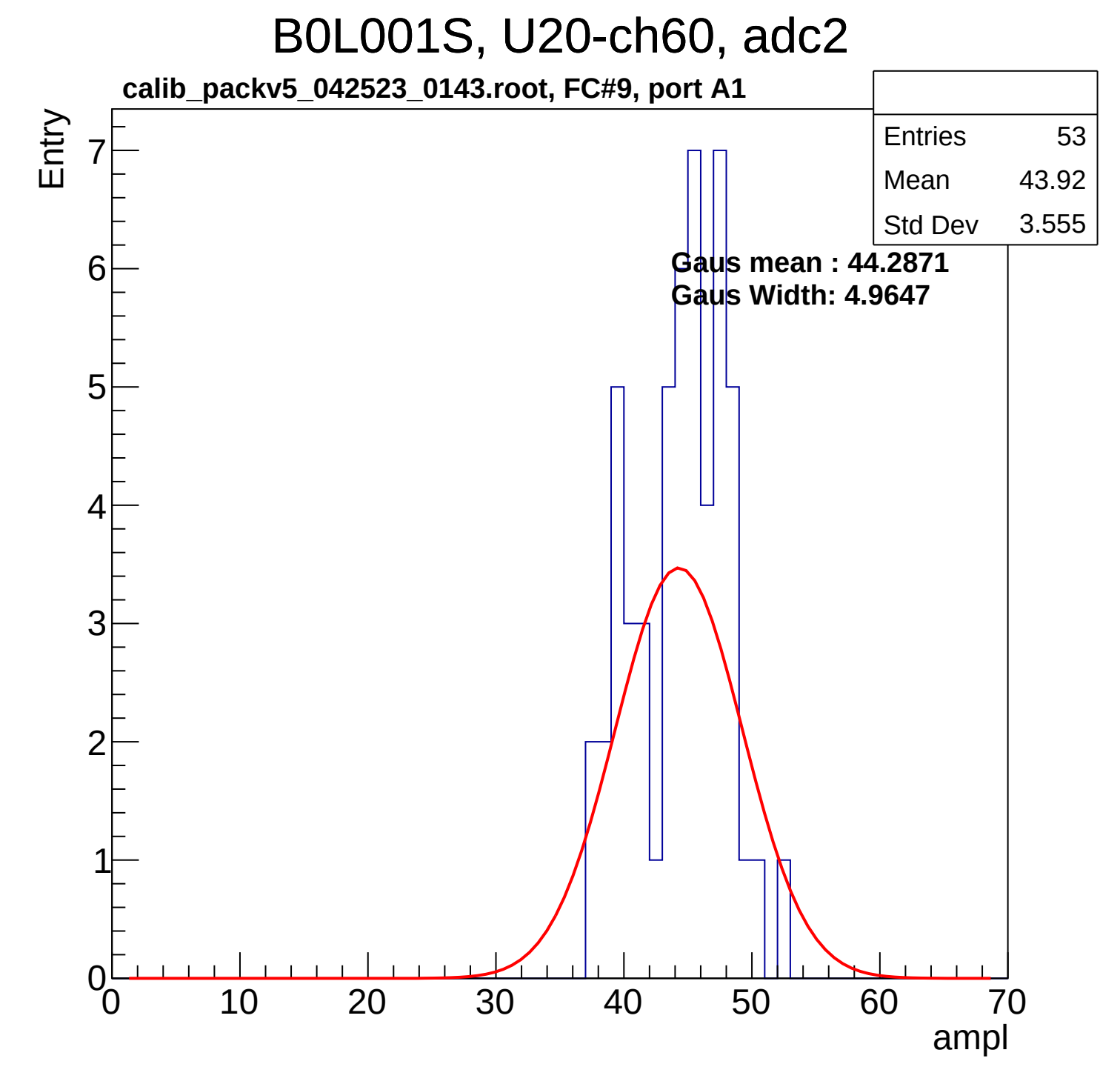
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 43.92
Std Dev 3.555
Gaus mean : 44.2871
Gaus Width: 4.9647
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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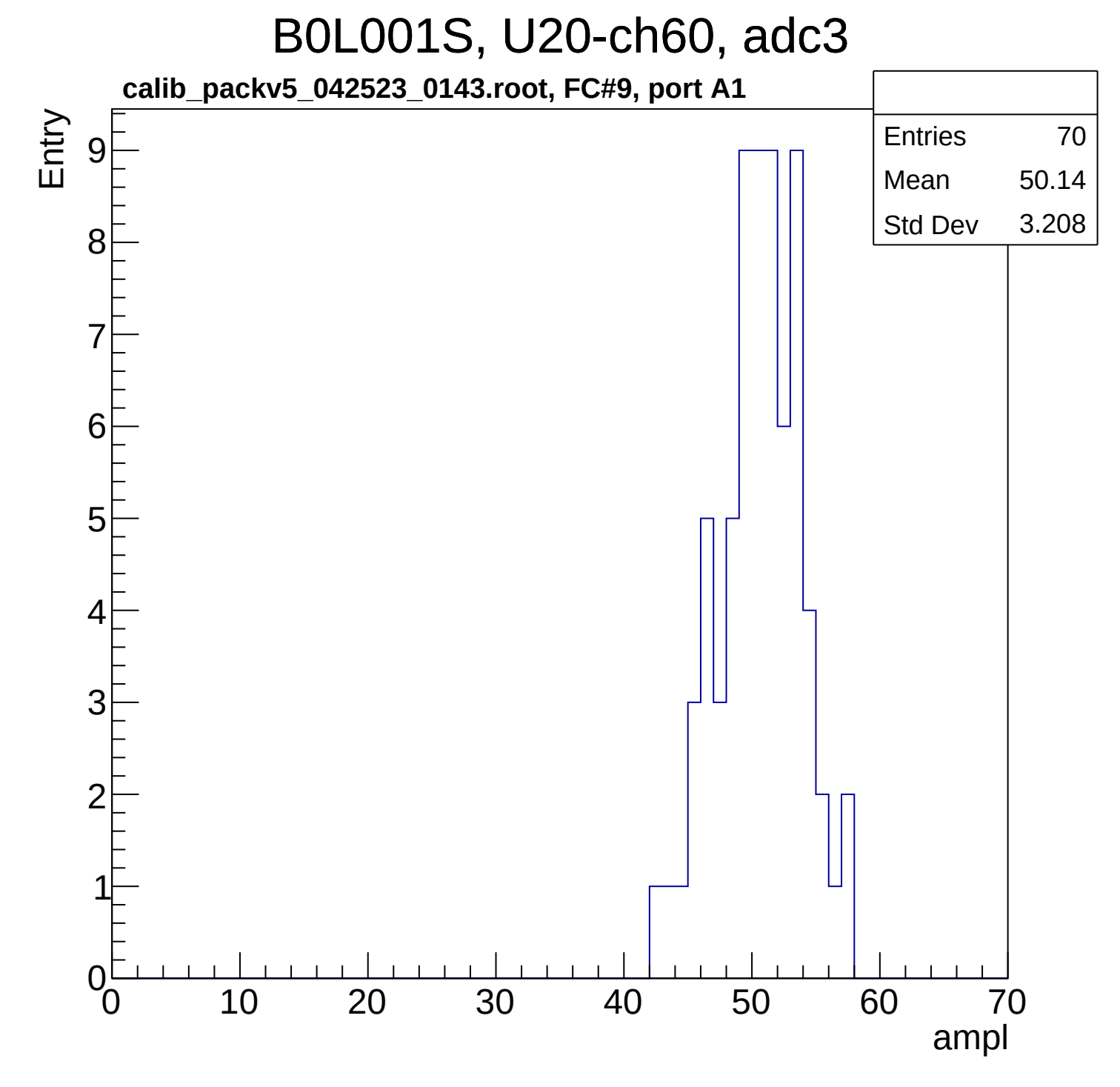
40
50
60
70


B0L001S, U20-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.14
Std Dev 3.208
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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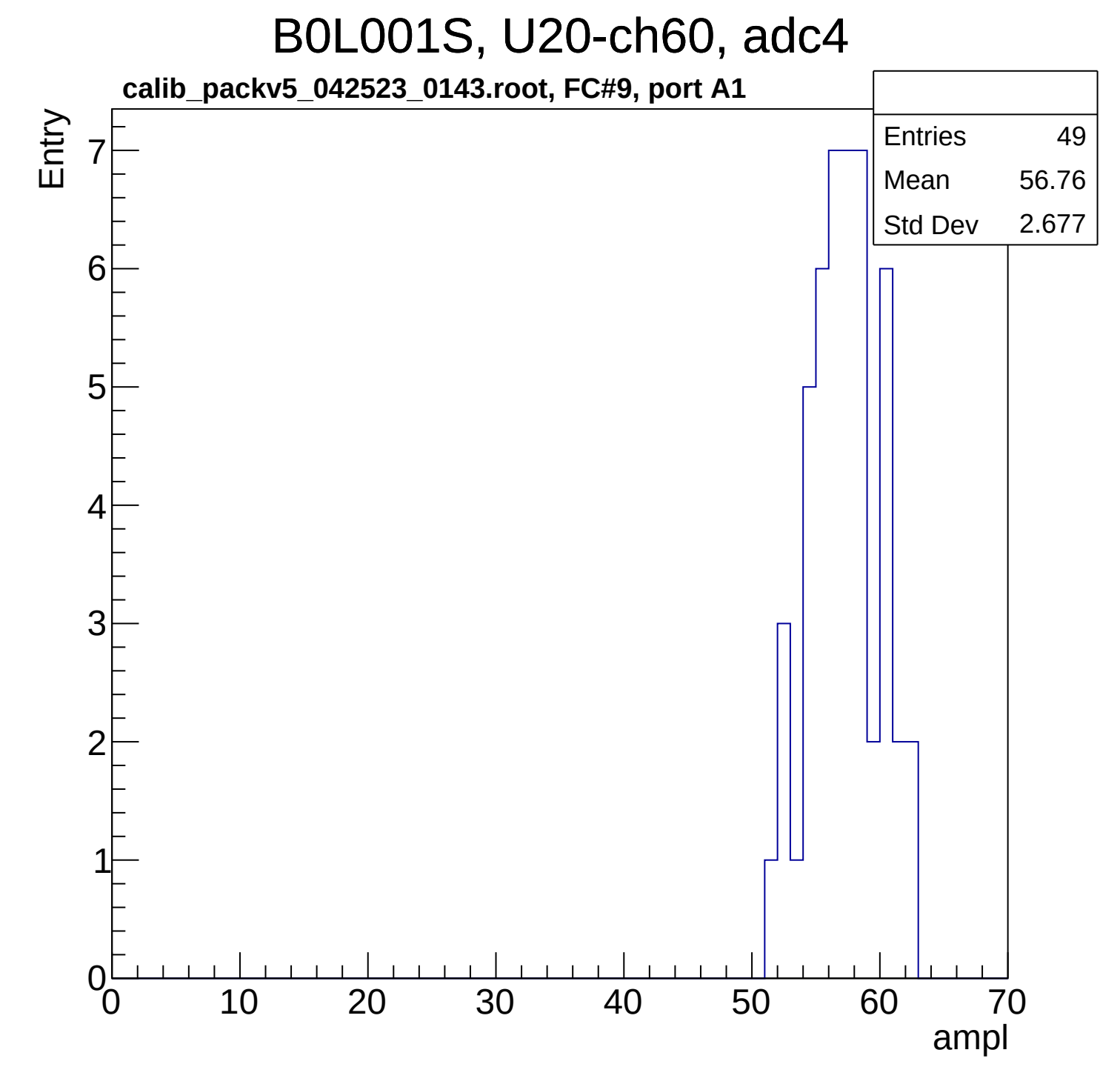
B0L001S, U20-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 56.76
Std Dev 2.677
Entry
ampl
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3
4
5
6
7
0
10
20
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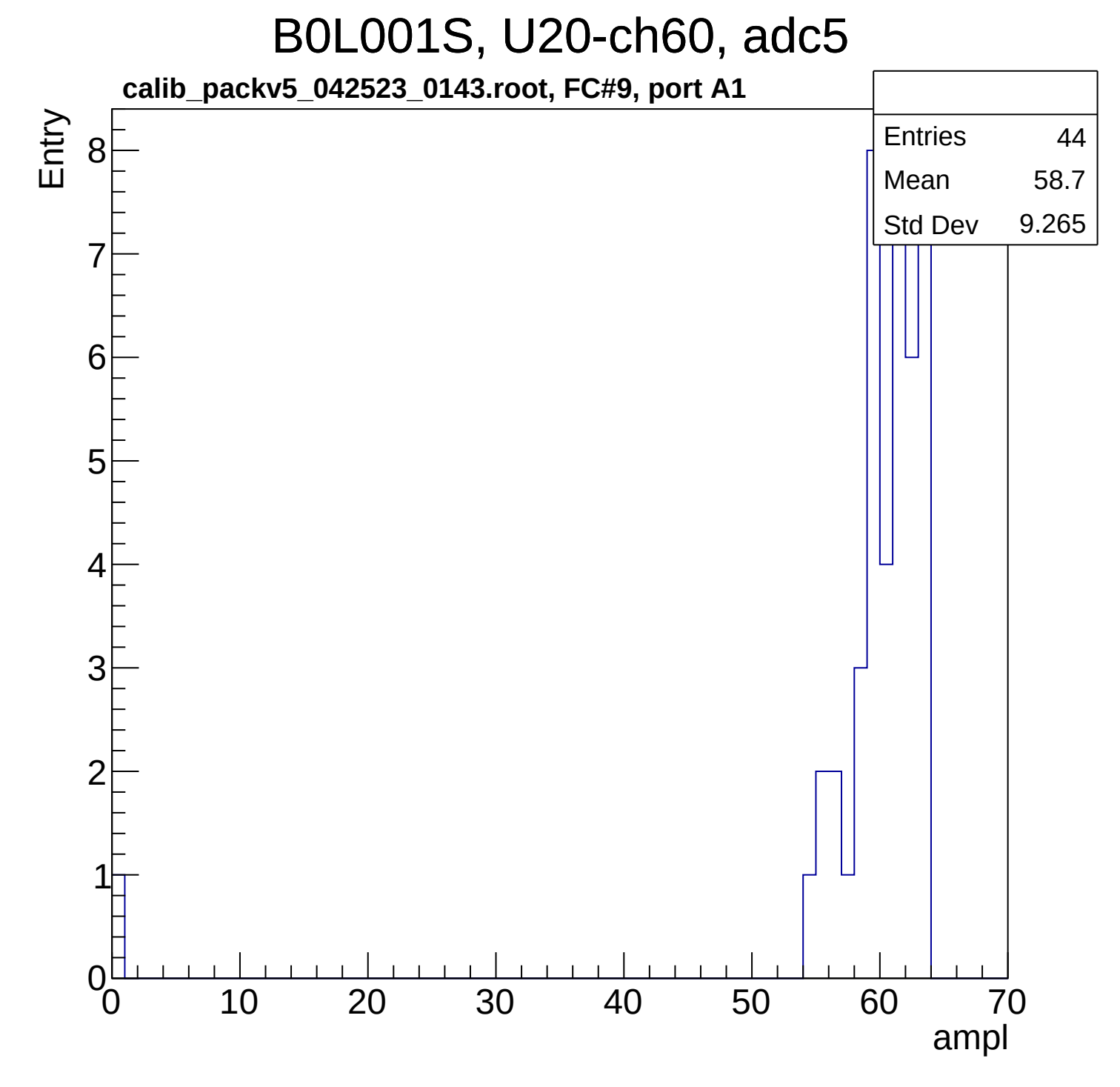
40
50
60
70


B0L001S, U20-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.7
Std Dev 9.265
Entry
ampl
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4
5
6
7
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0
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20
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40
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60
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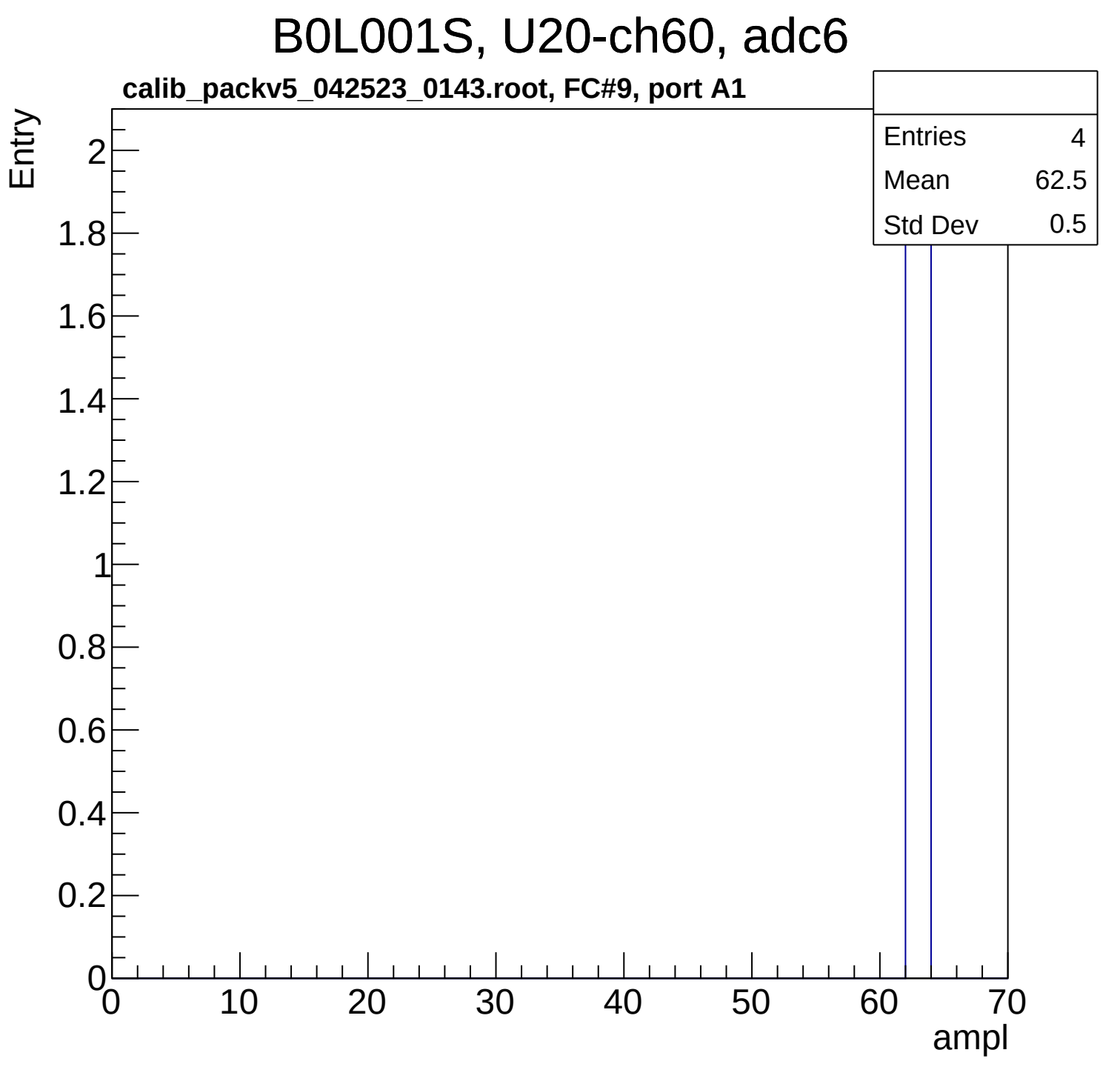
B0L001S, U20-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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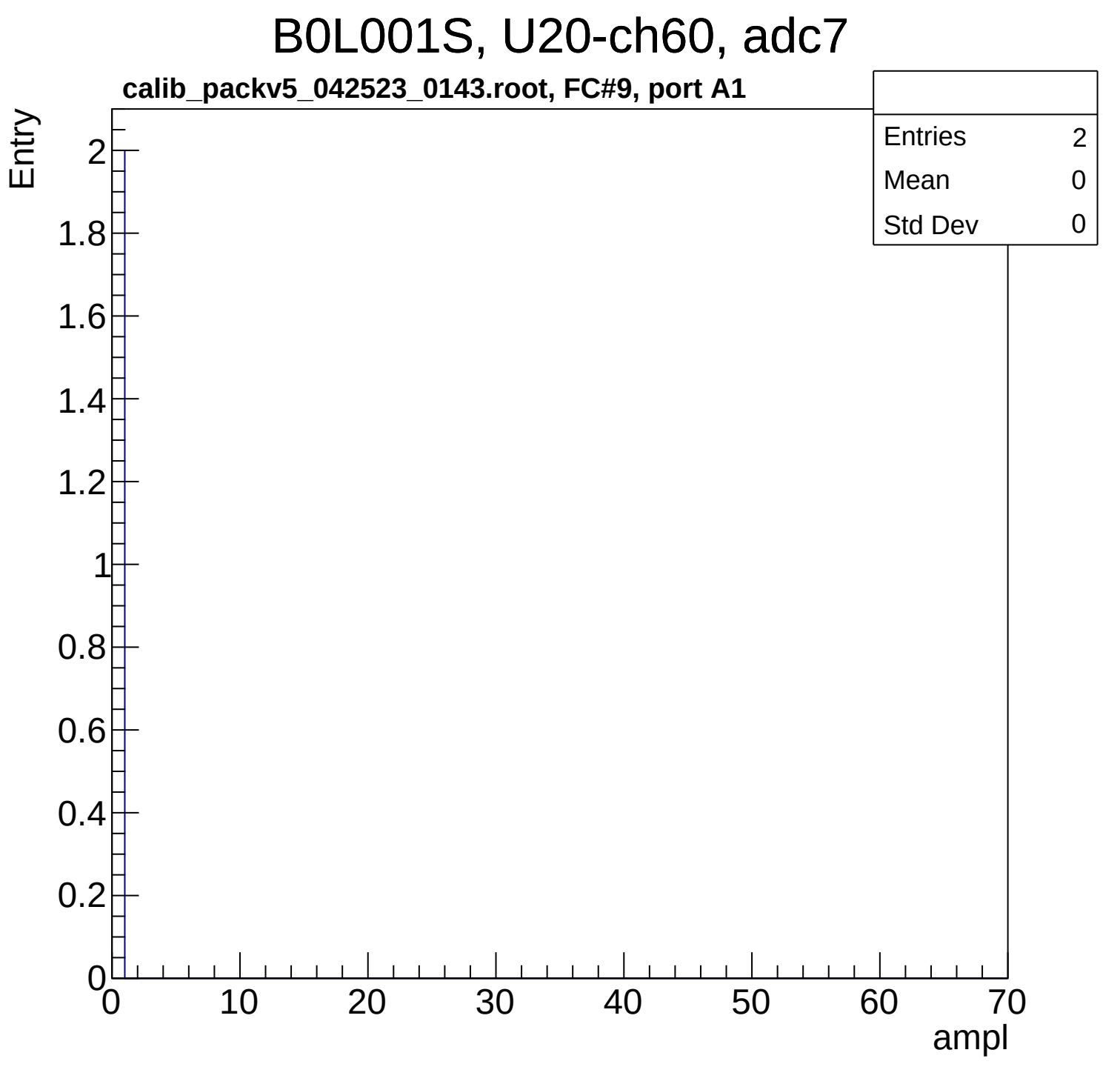
40
50
60
70


B0L001S, U20-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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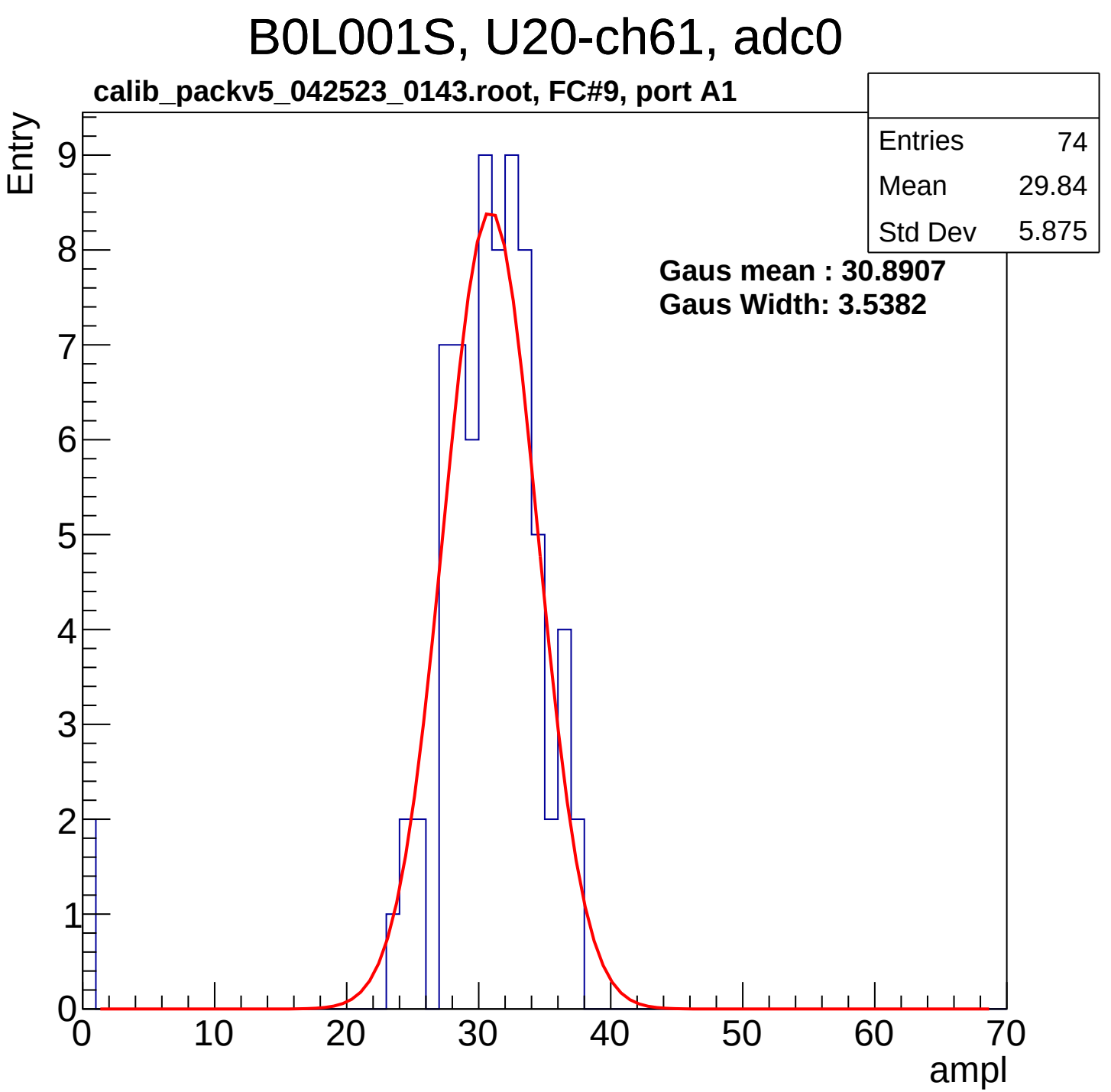
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.84
Std Dev 5.875
Gaus mean : 30.8907
Gaus Width: 3.5382
Entry
ampl
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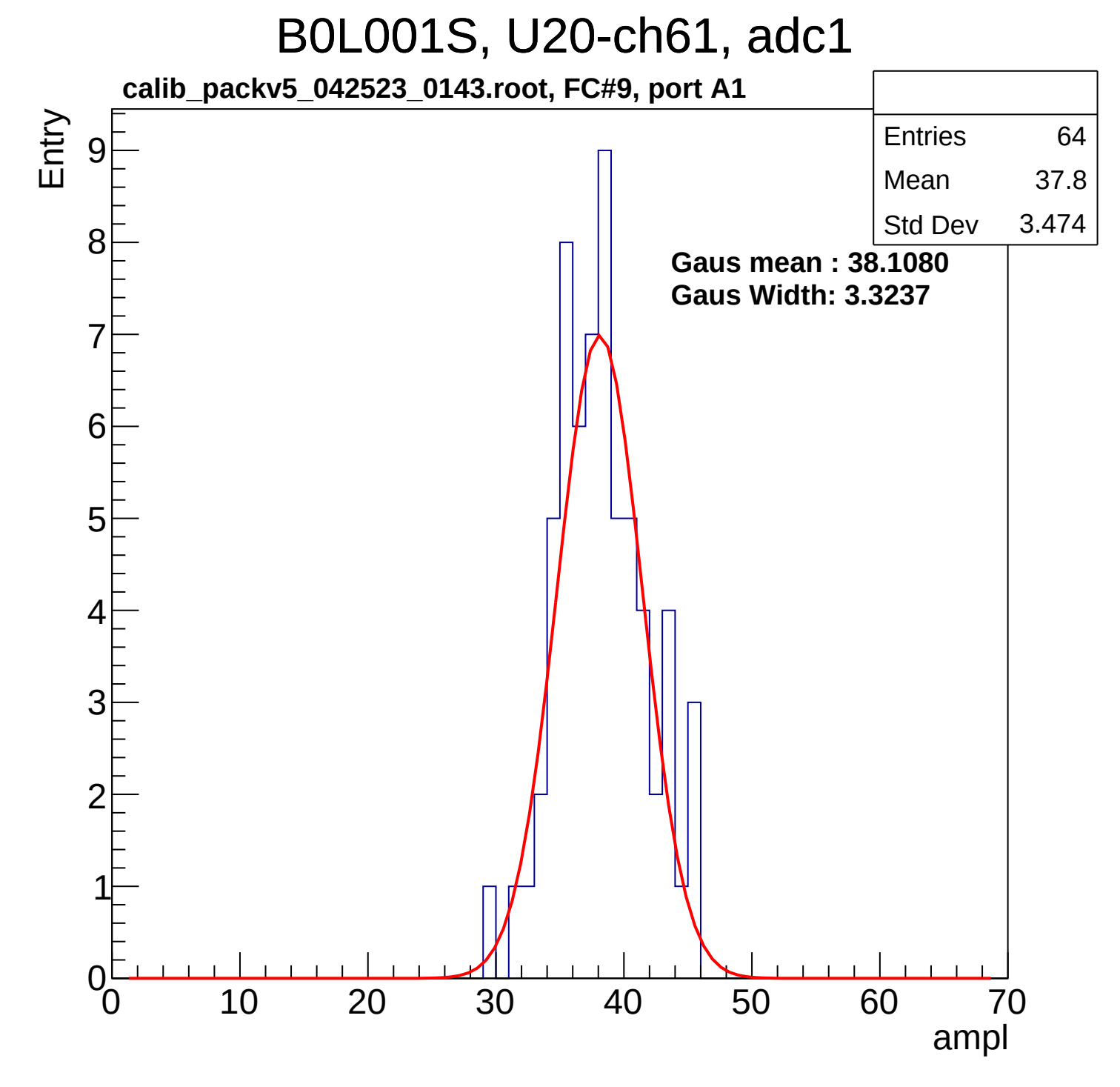
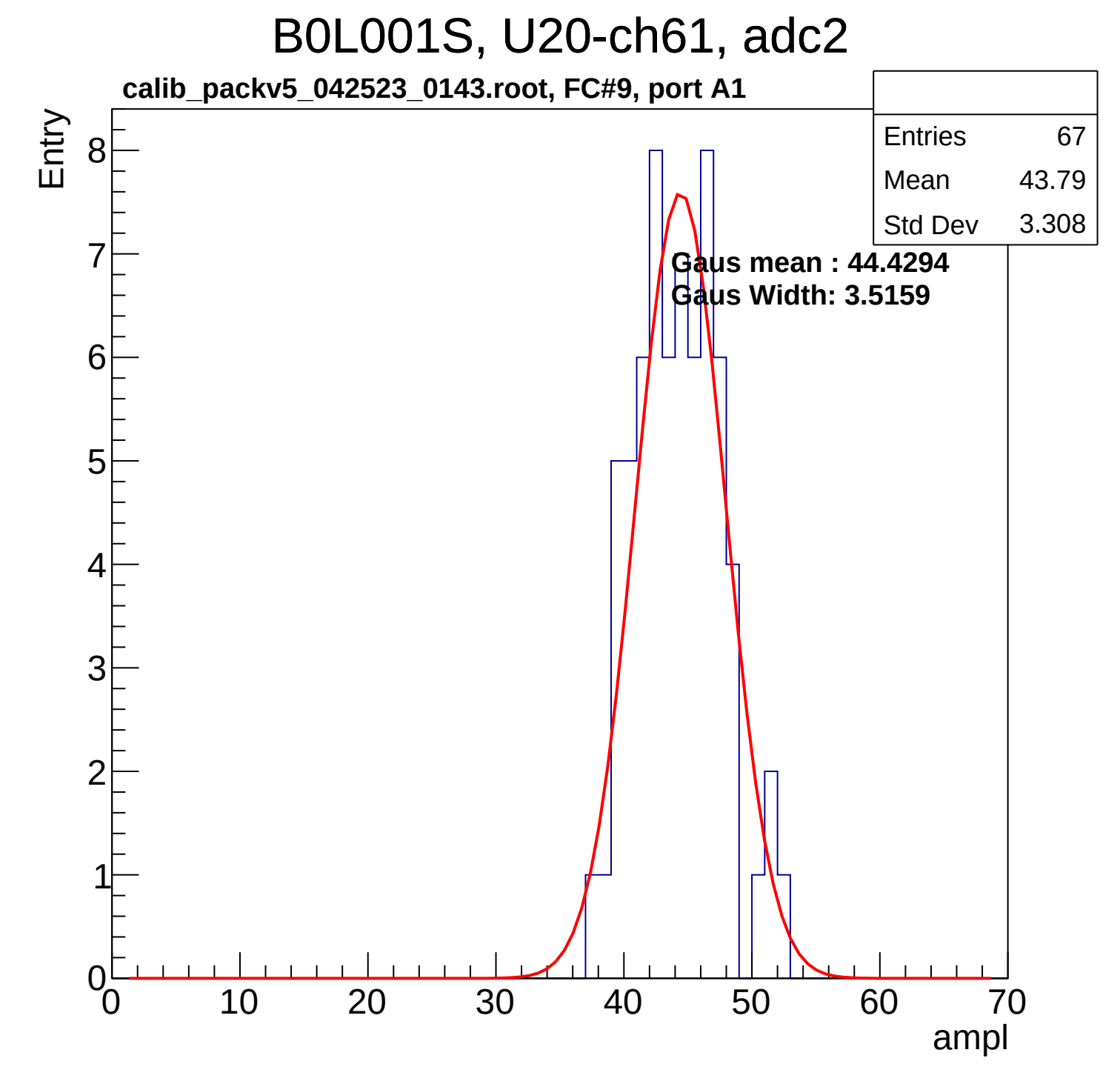
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.8
Std Dev 3.474
Gaus mean : 38.1080
Gaus Width: 3.3237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.79
Std Dev 3.308
Gaus mean : 44.4294
Gaus Width: 3.5159
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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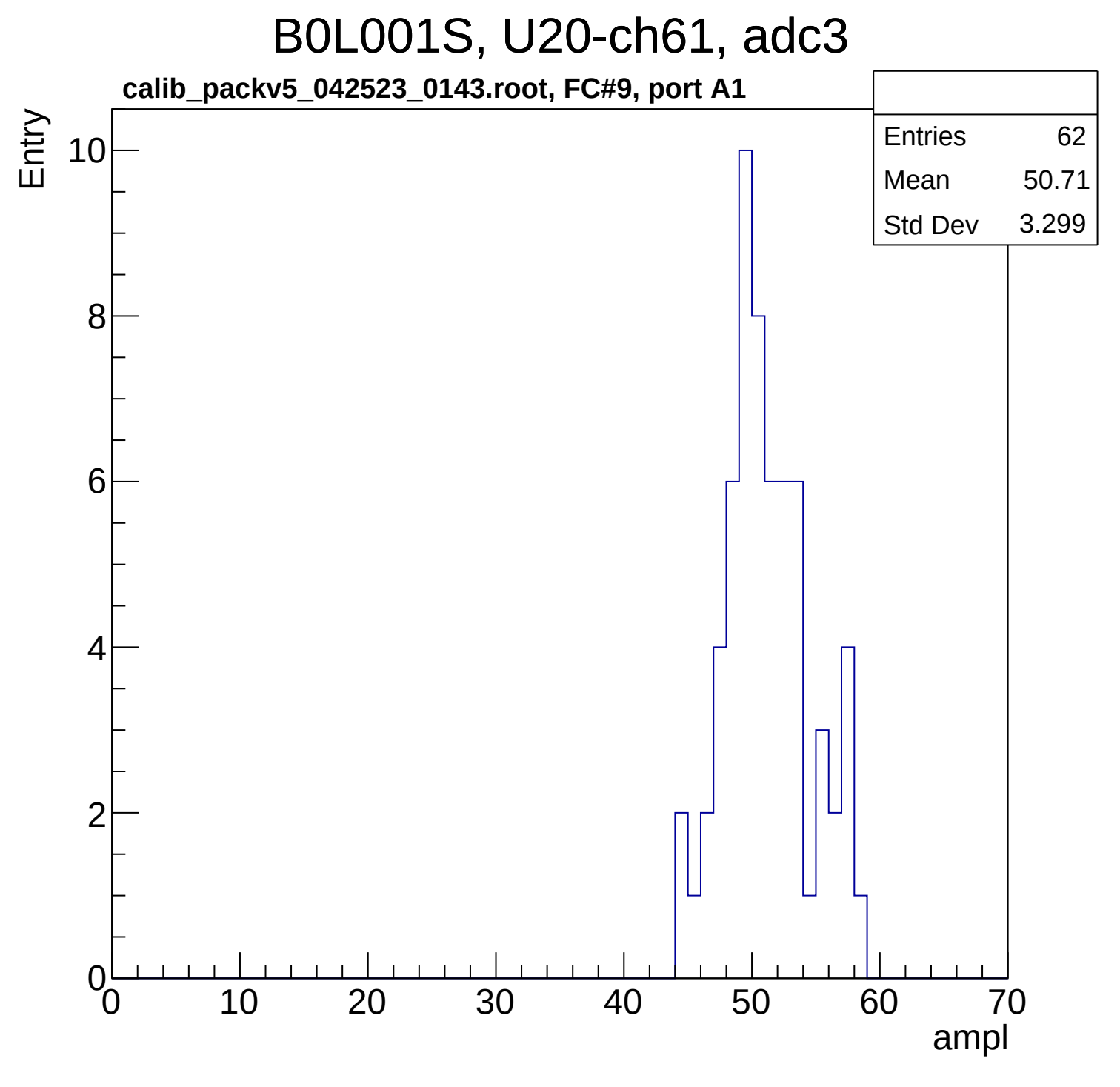
40
50
60
70


B0L001S, U20-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.71
Std Dev 3.299
Entry
ampl
0
2
4
6
8
10
0
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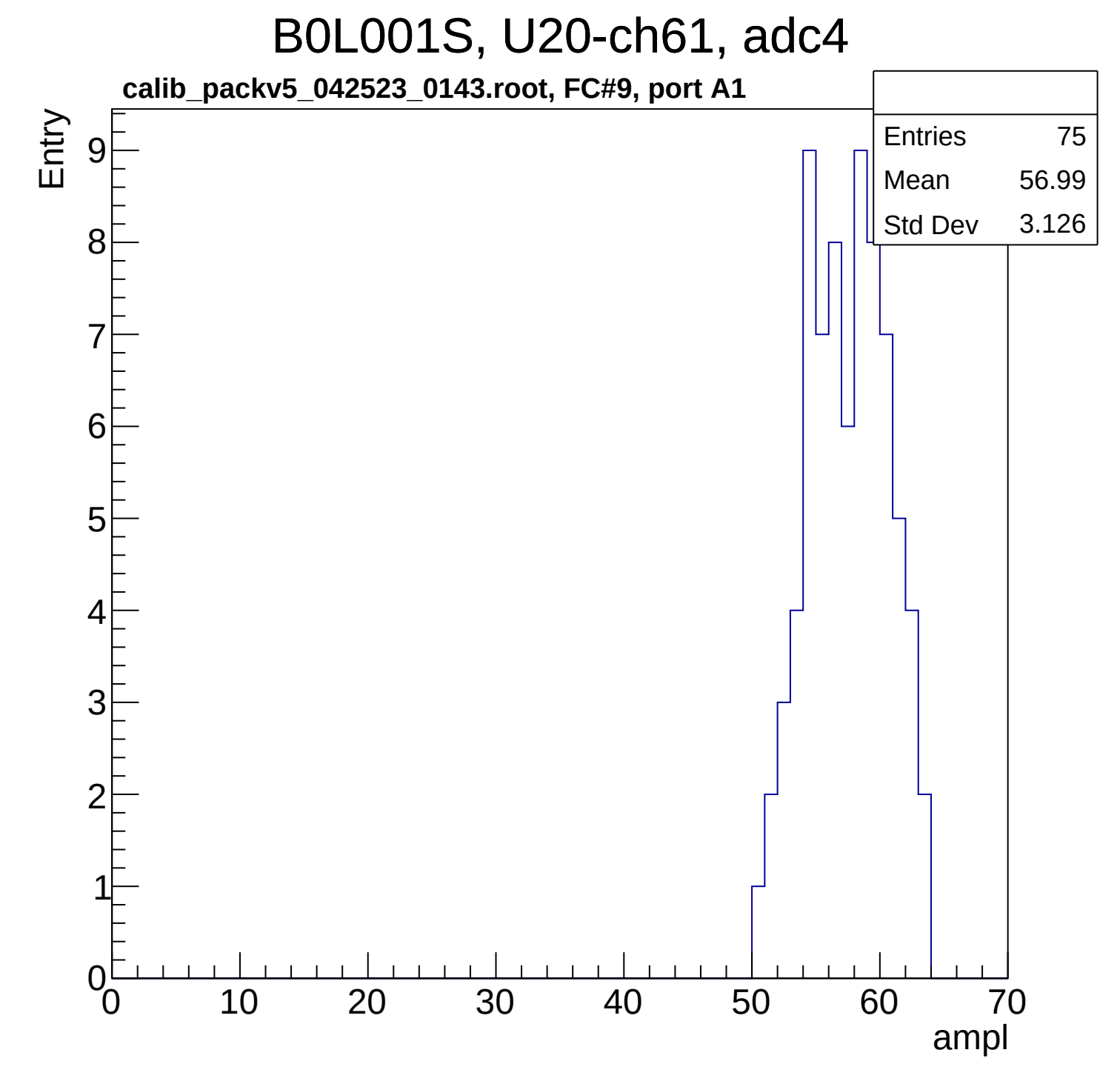
20
30
40
50
60
70


B0L001S, U20-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 56.99
Std Dev 3.126
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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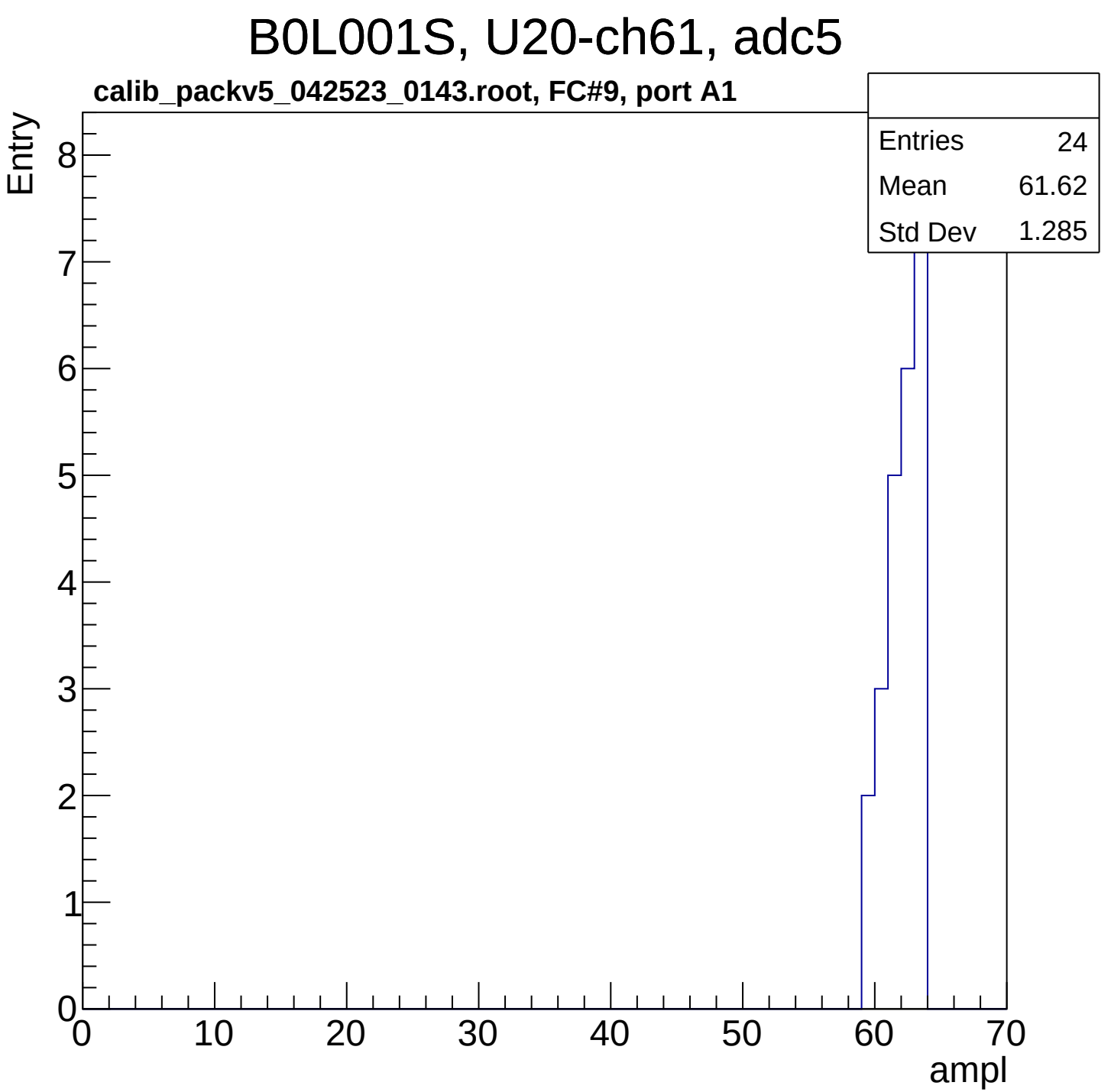
B0L001S, U20-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 61.62
Std Dev 1.285
Entry
ampl
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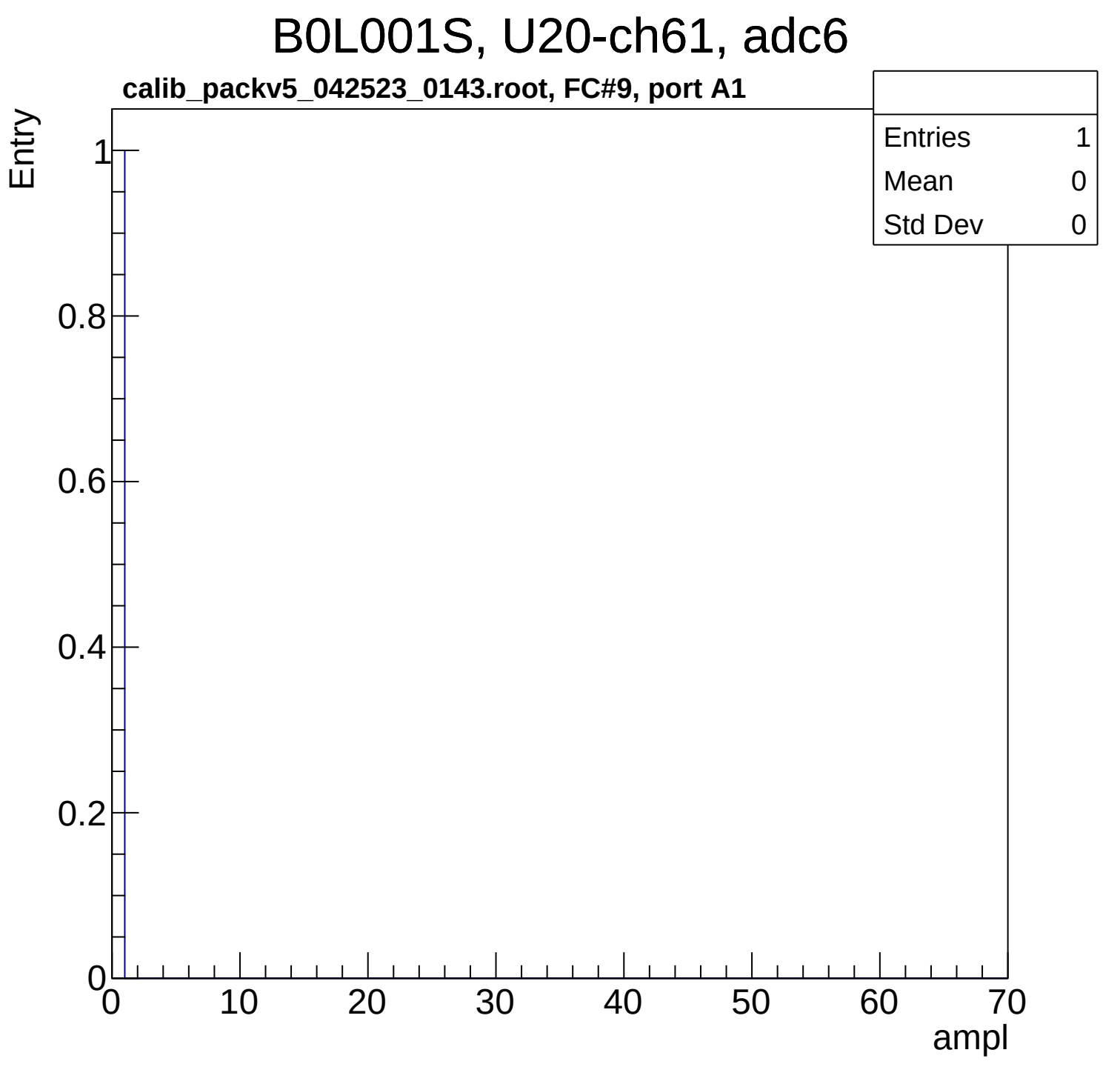
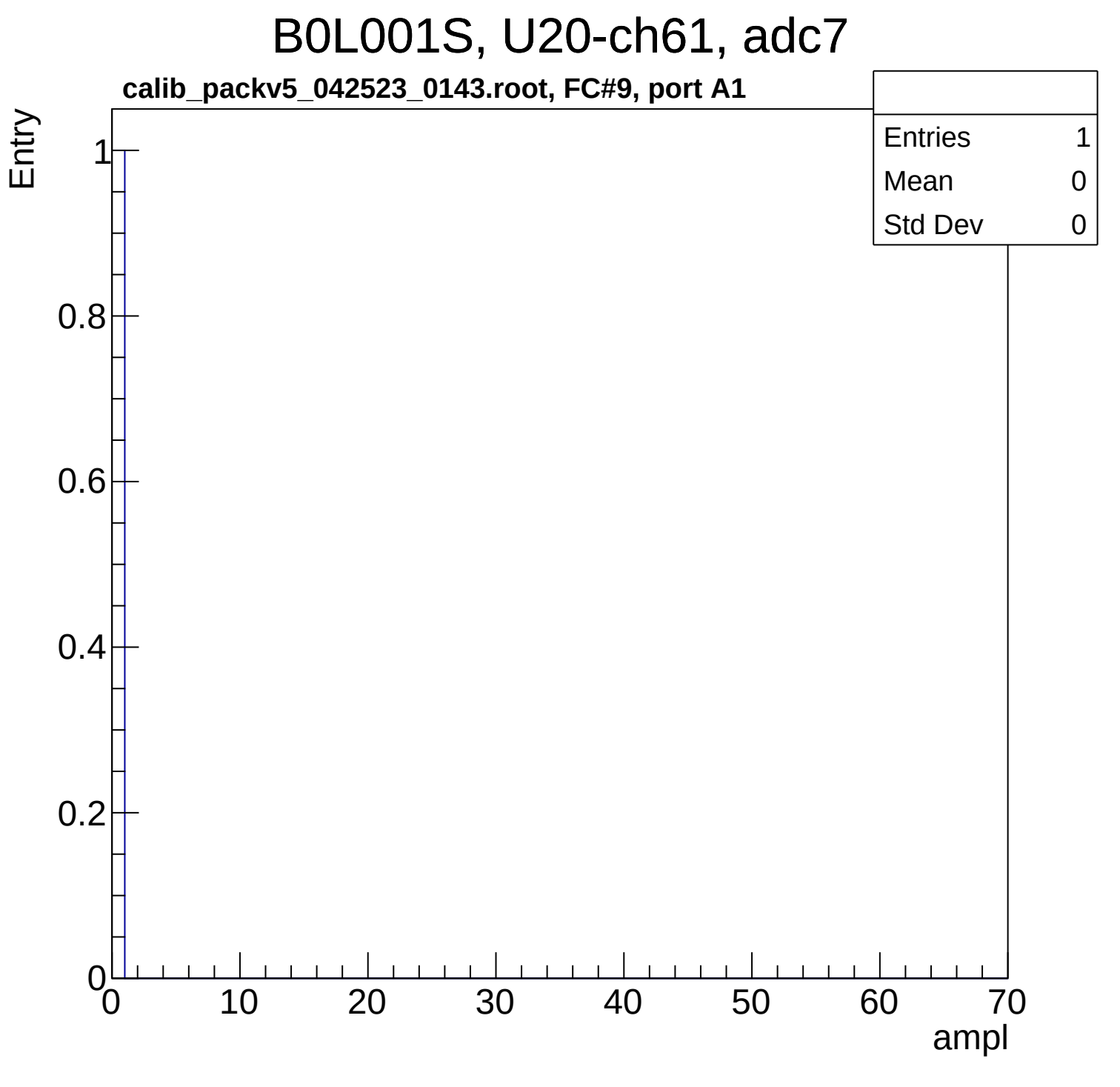
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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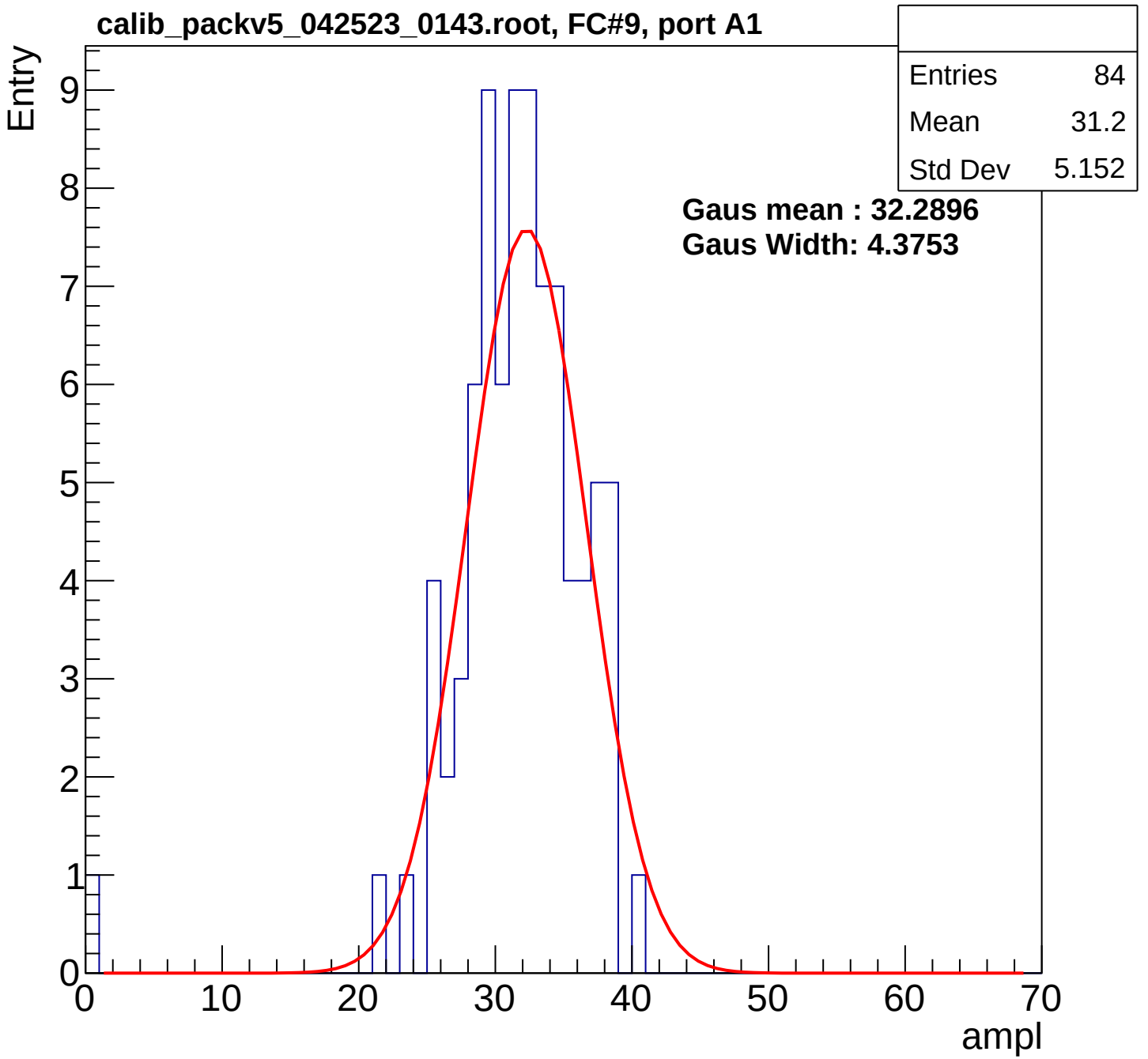
70
ampl


B0L001S, U20-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 31.2
Std Dev 5.152
Gaus mean : 32.2896
Gaus Width: 4.3753
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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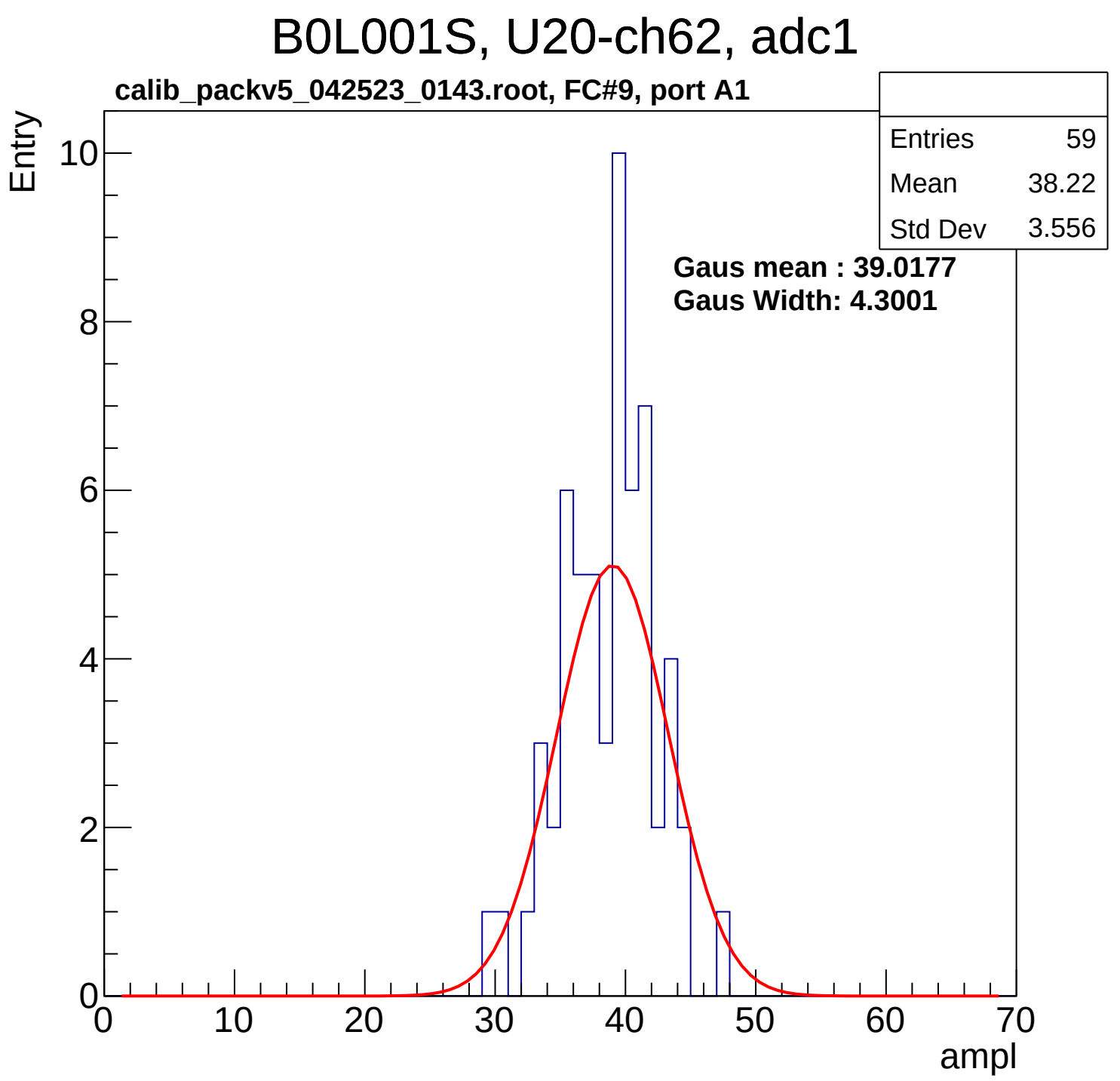
B0L001S, U20-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.22
Std Dev 3.556
Gaus mean : 39.0177
Gaus Width: 4.3001
Entry
ampl
0
2
4
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8
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0
10
20
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40
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60
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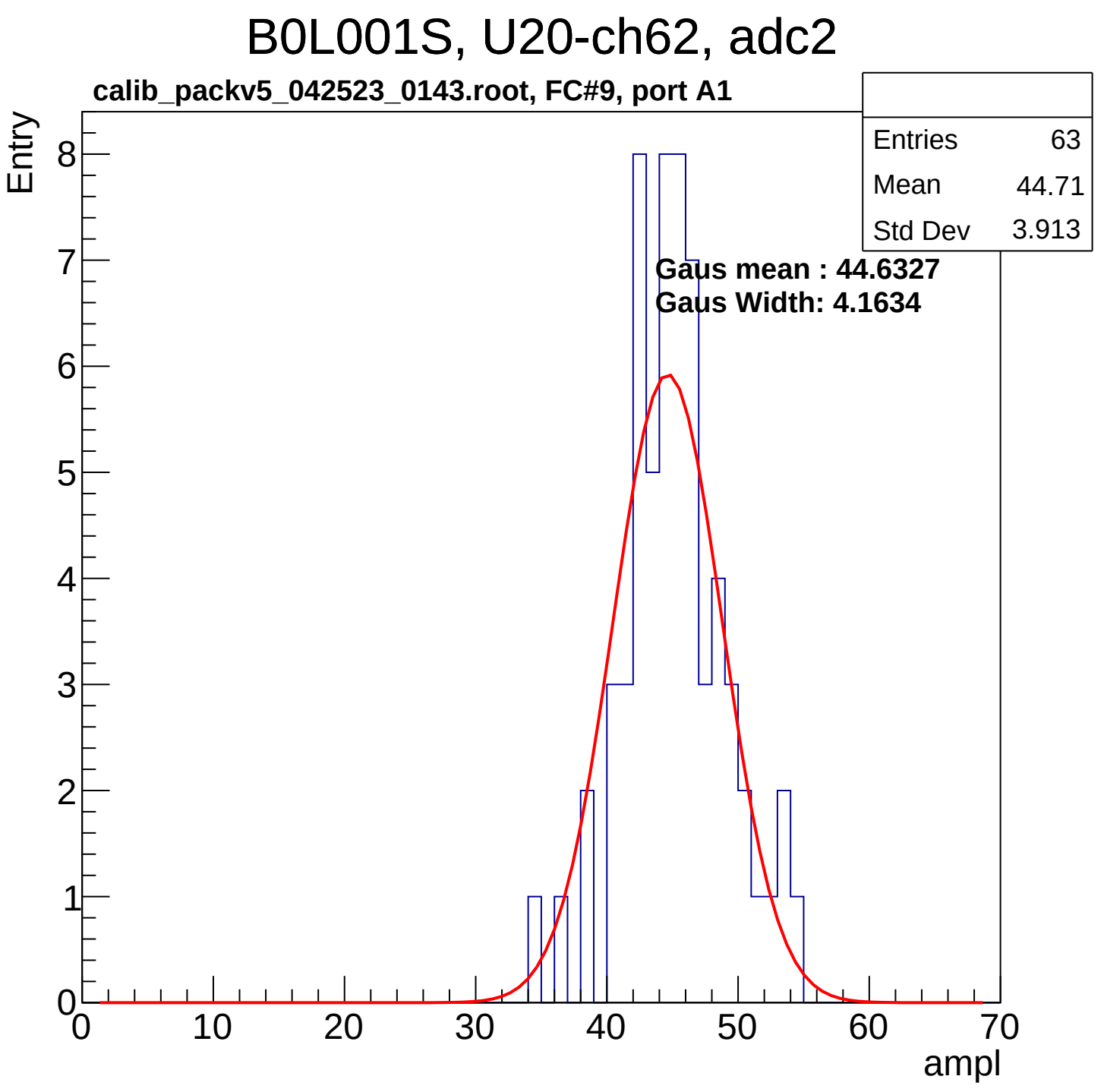
B0L001S, U20-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.71
Std Dev 3.913
Gaus mean : 44.6327
Gaus Width: 4.1634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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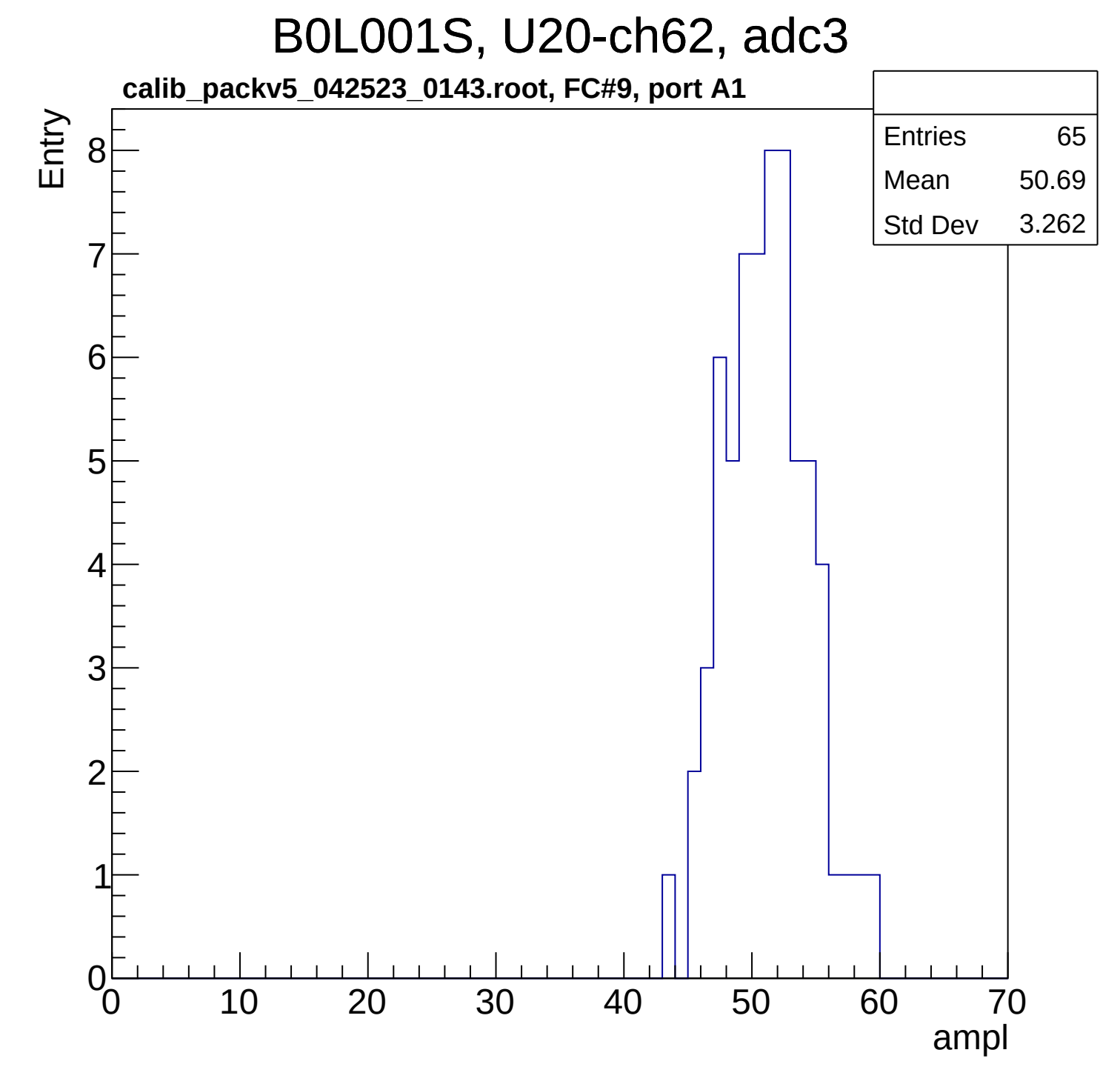
B0L001S, U20-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.69
Std Dev 3.262
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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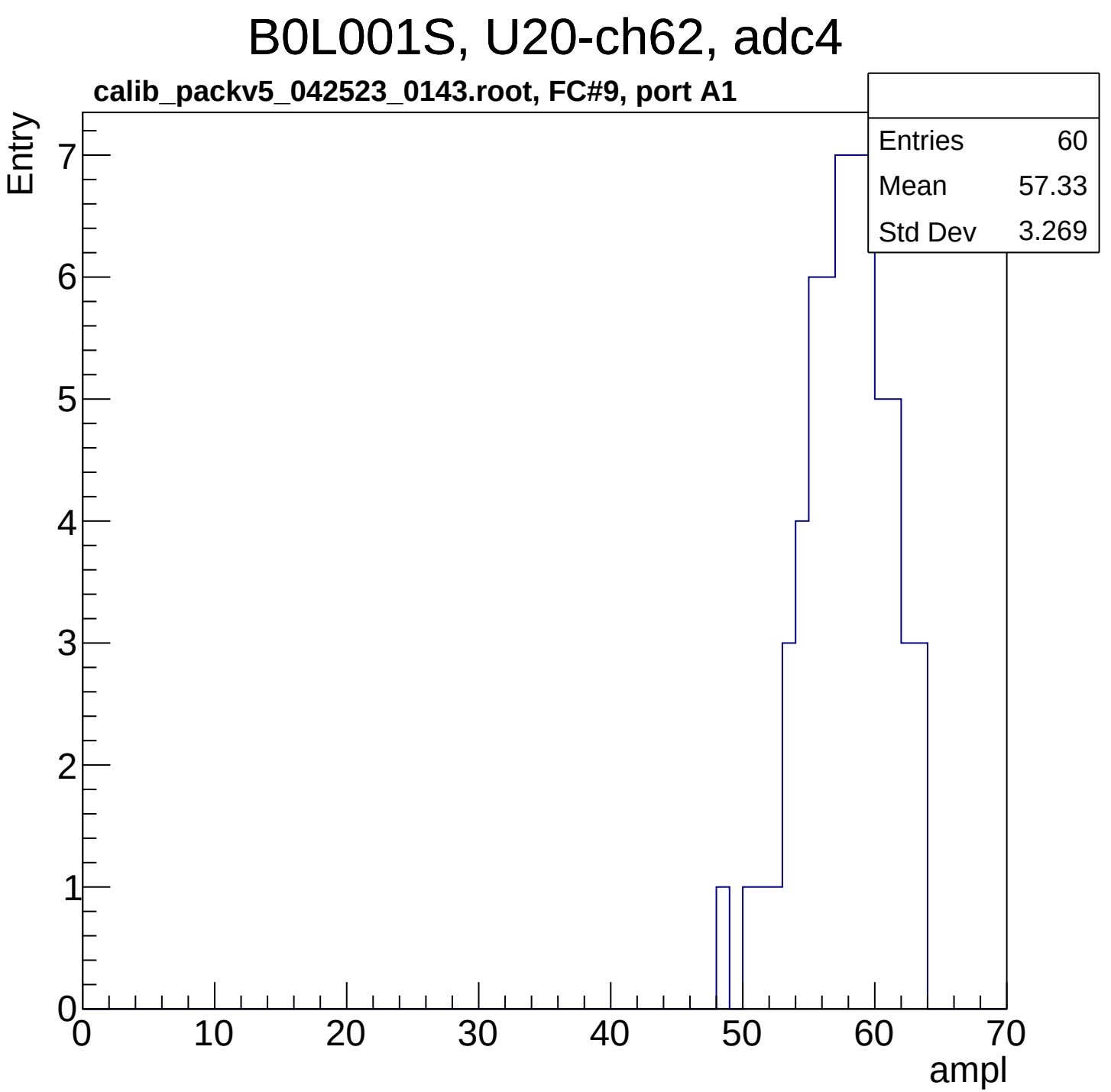
B0L001S, U20-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.33
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
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0
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60
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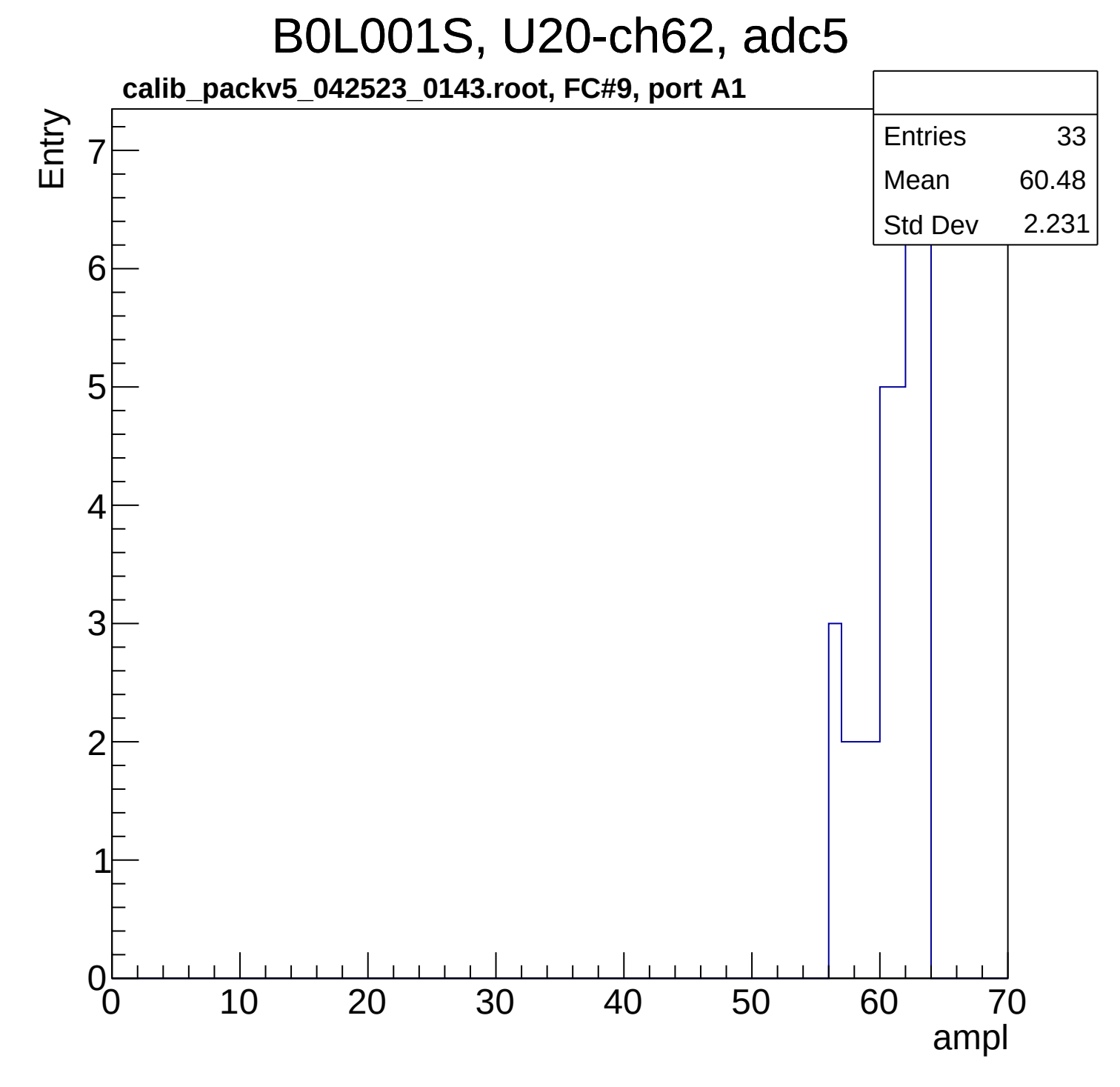
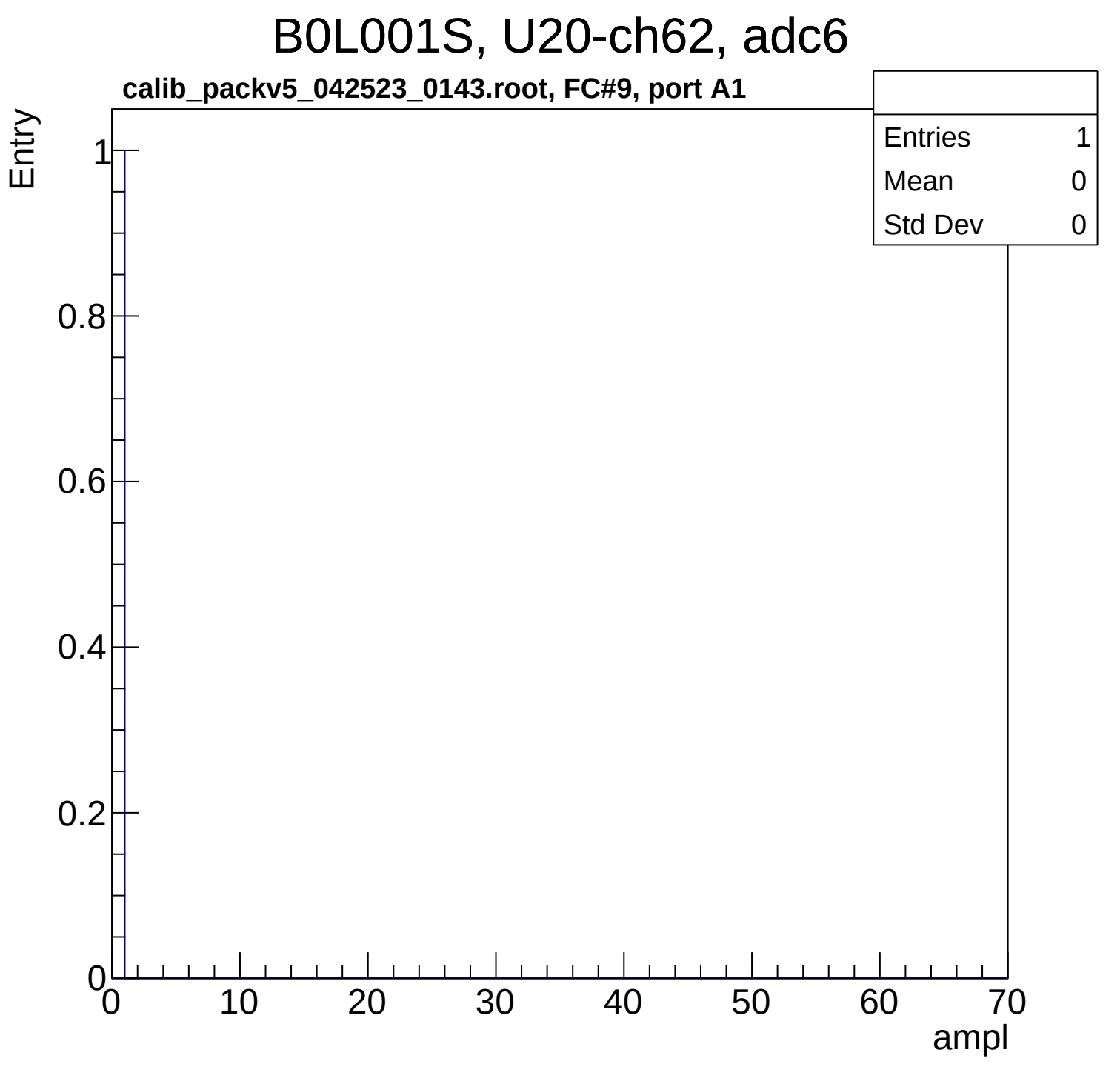
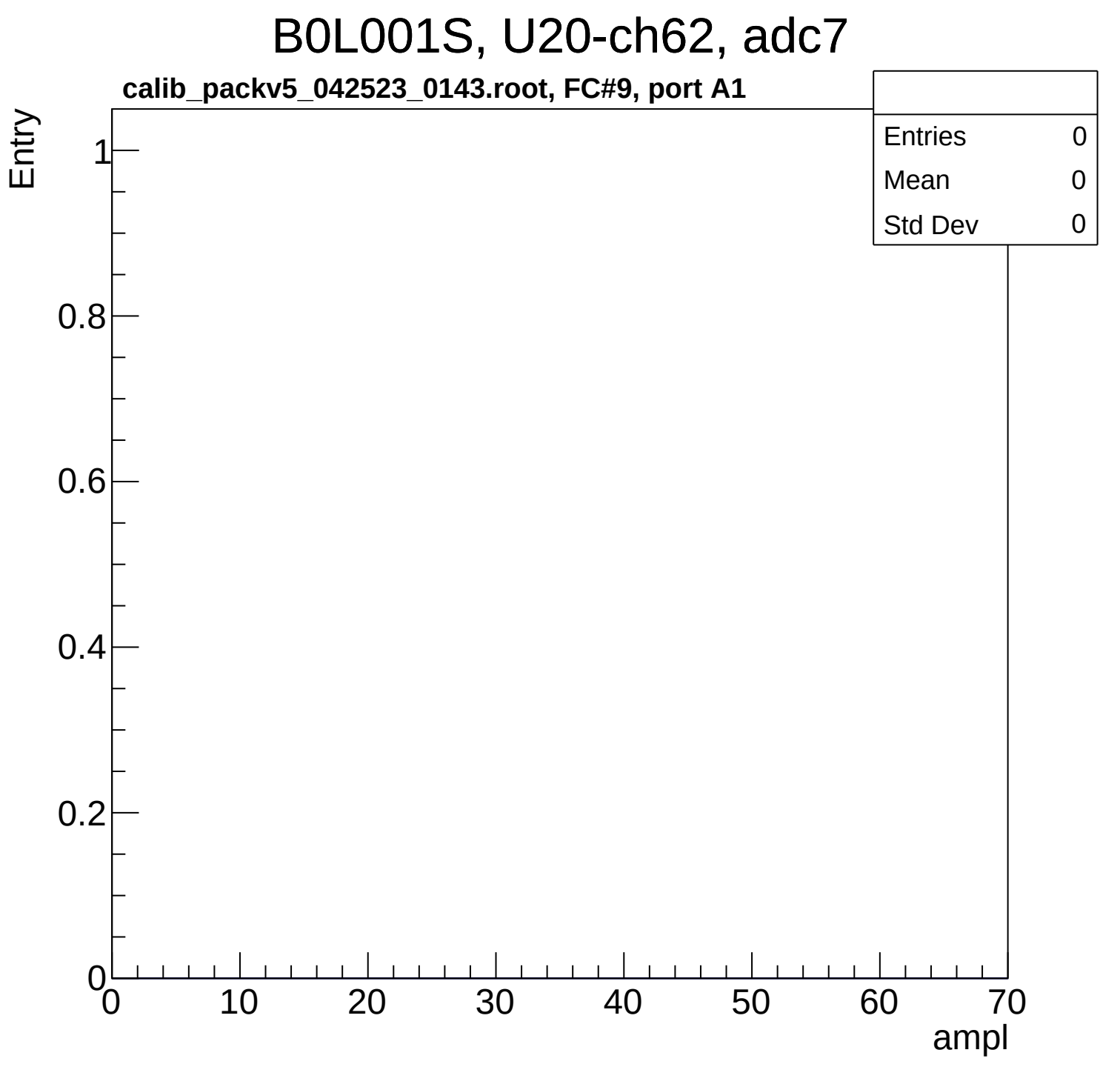
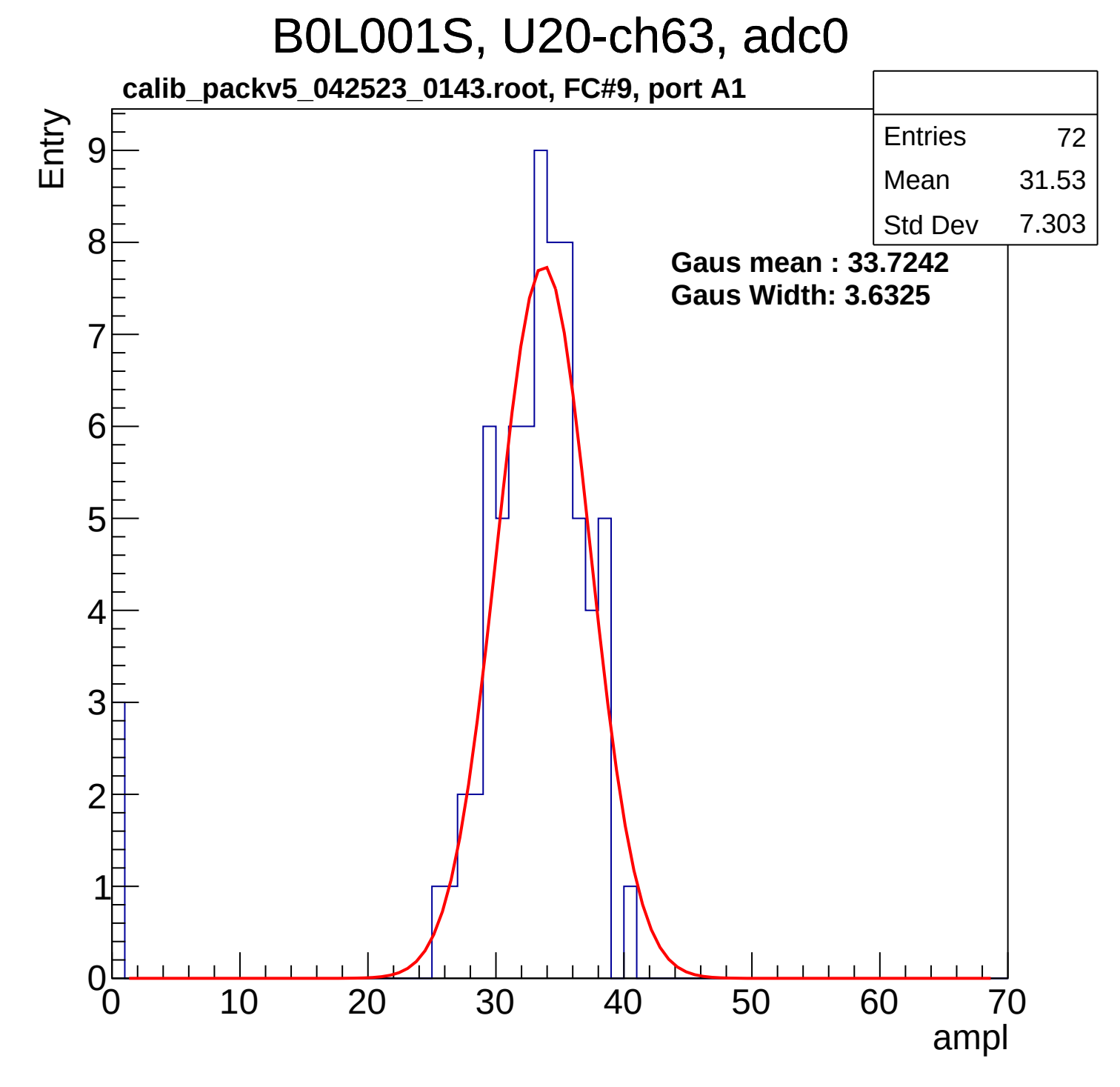
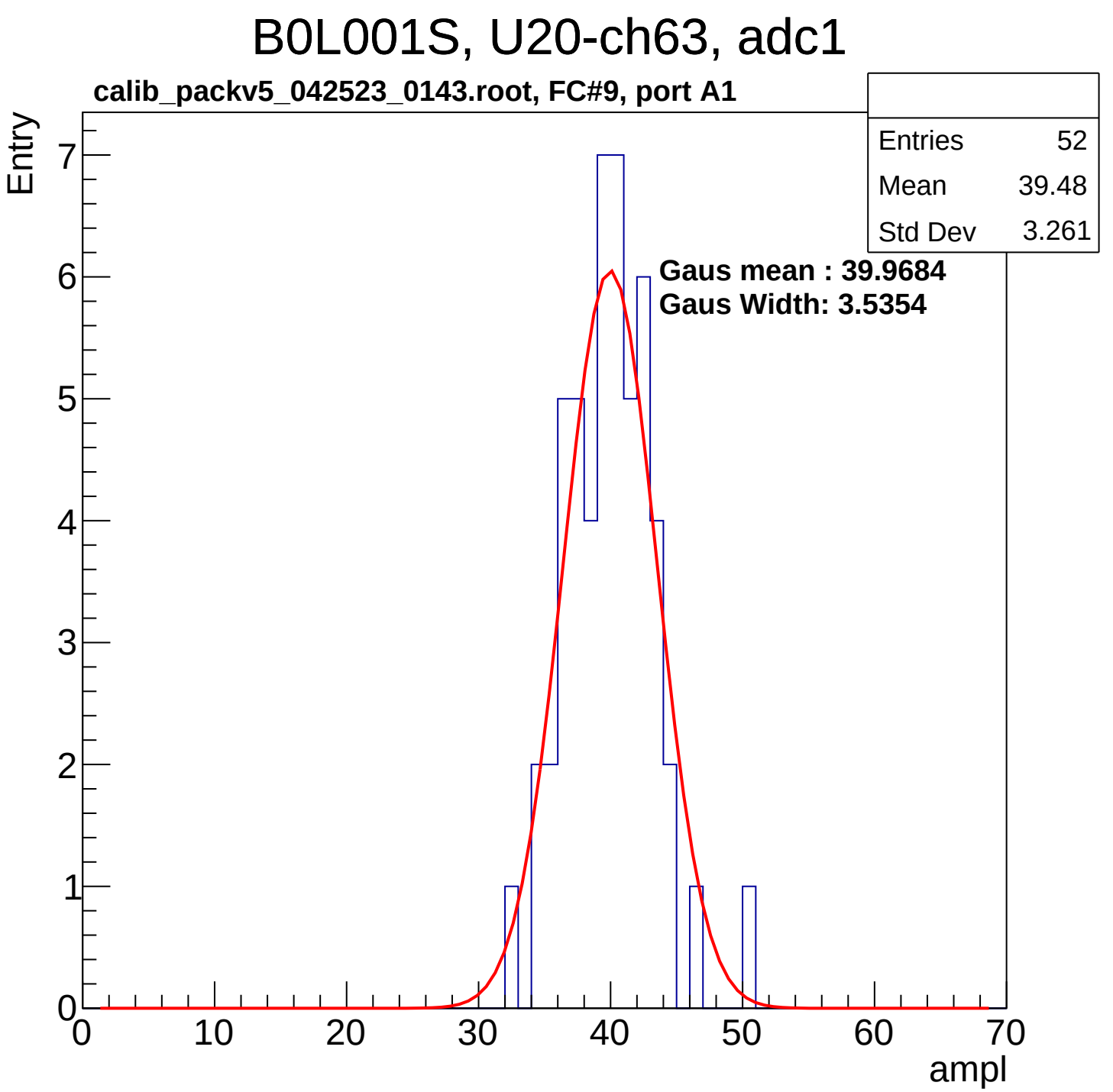
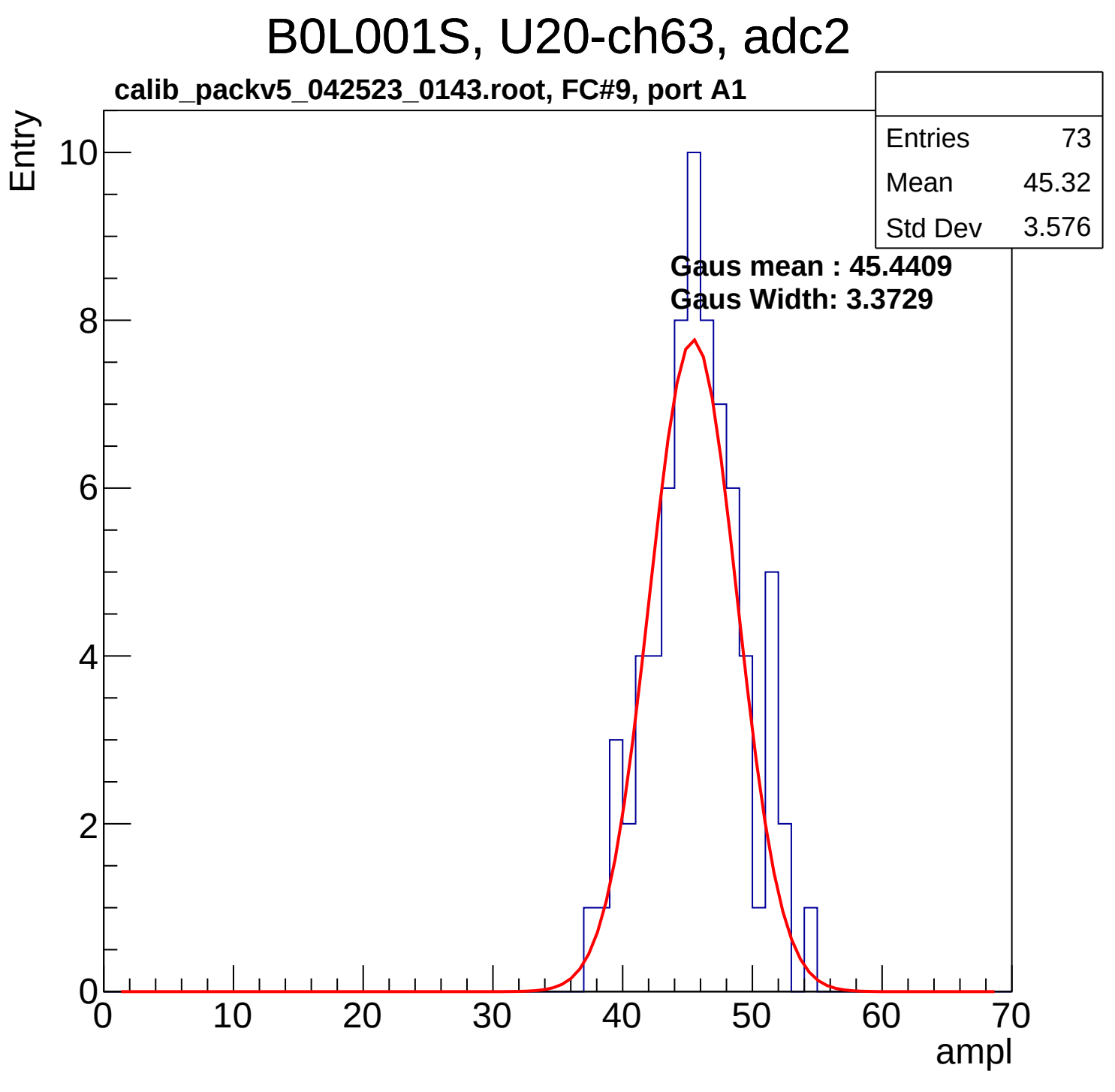

B0L001S, U20-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 60.48
Std Dev 2.231
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

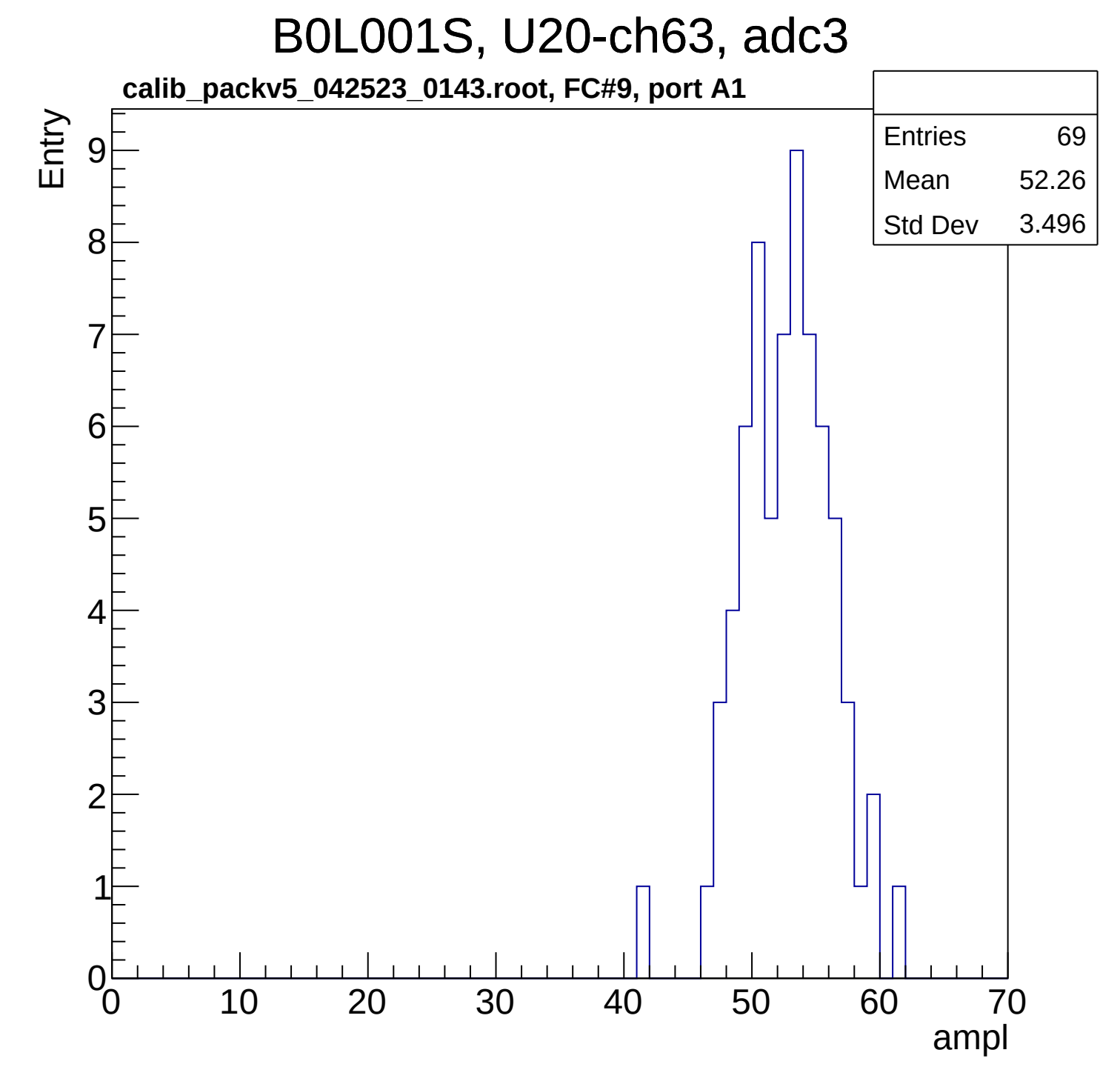

B0L001S, U20-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 31.53
Std Dev 7.303
Gaus mean : 33.7242
Gaus Width: 3.6325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 39.48
Std Dev 3.261
Gaus mean : 39.9684
Gaus Width: 3.5354
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.32
Std Dev 3.576
Gaus mean : 45.4409
Gaus Width: 3.3729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 52.26
Std Dev 3.496
Entry
ampl
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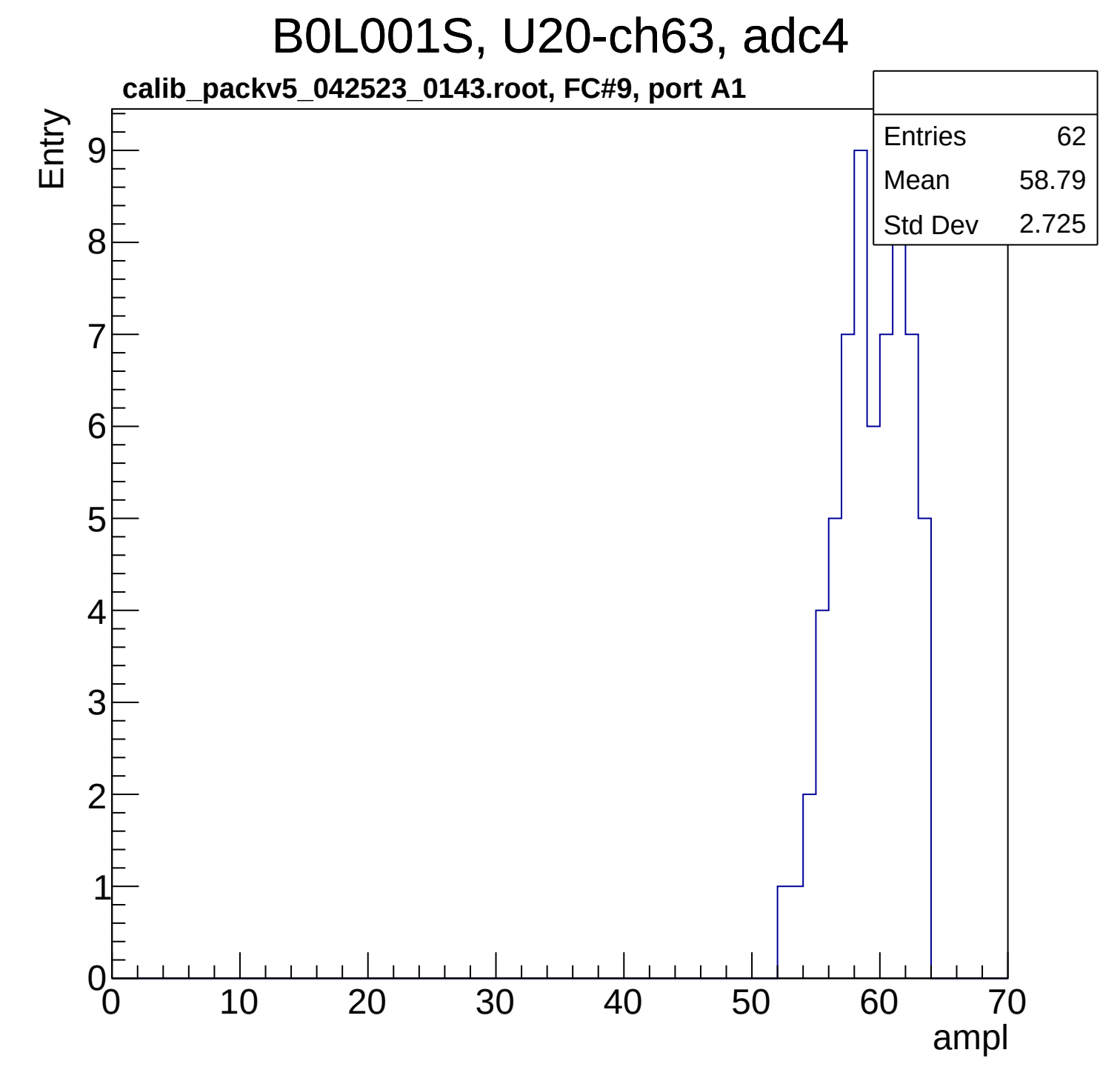
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 58.79
Std Dev 2.725
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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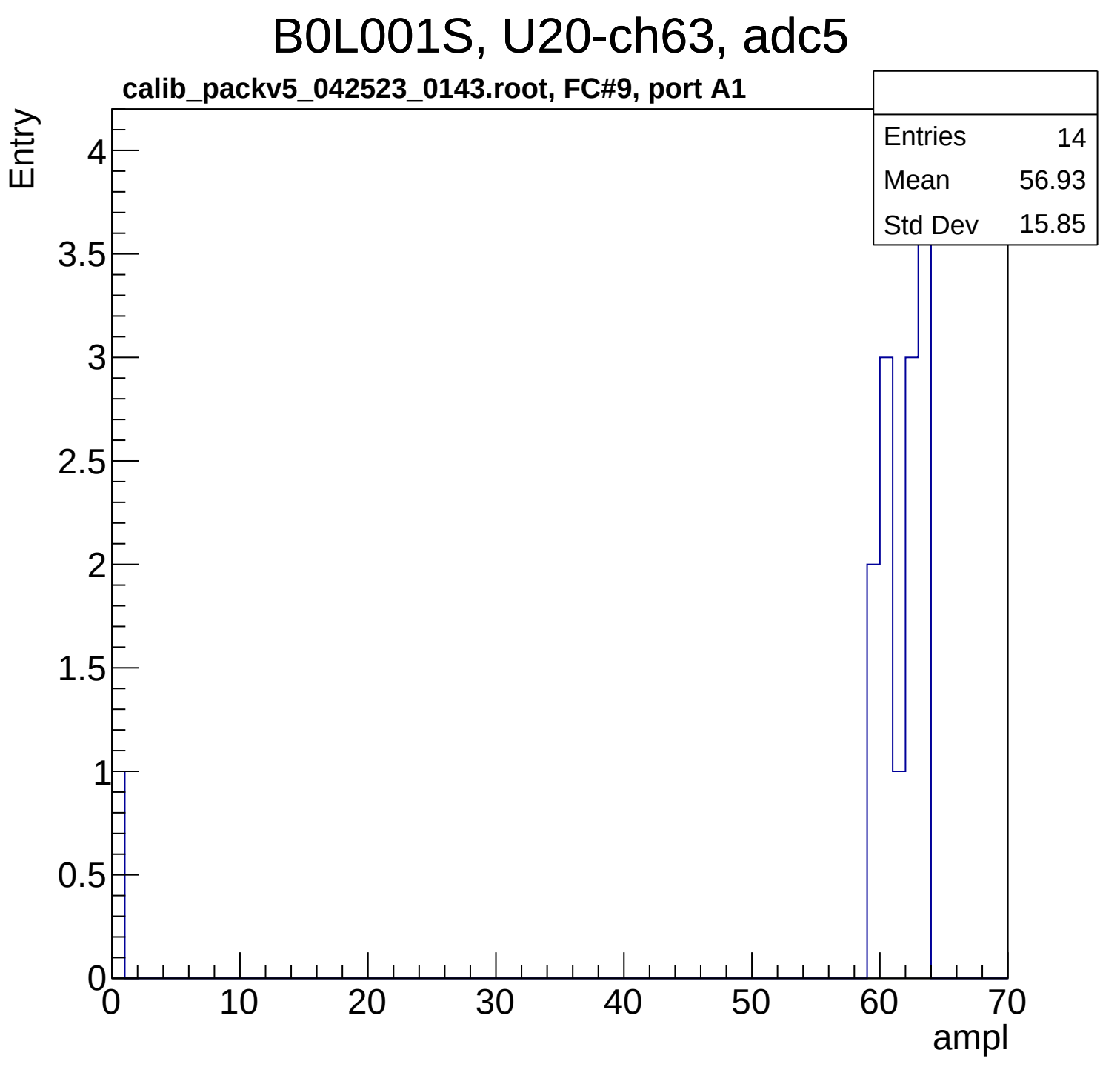
B0L001S, U20-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 56.93
Std Dev 15.85
Entry
ampl
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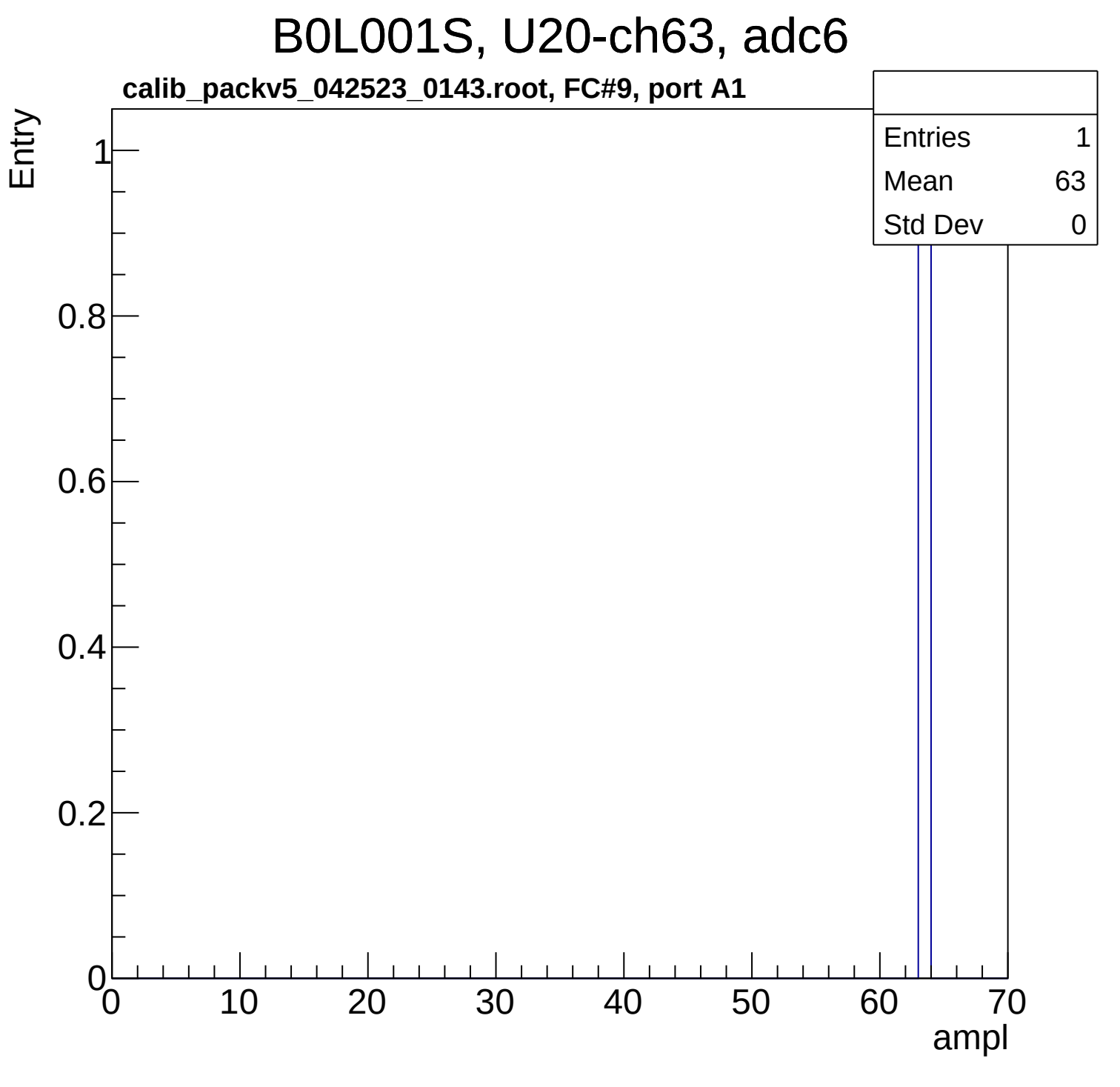
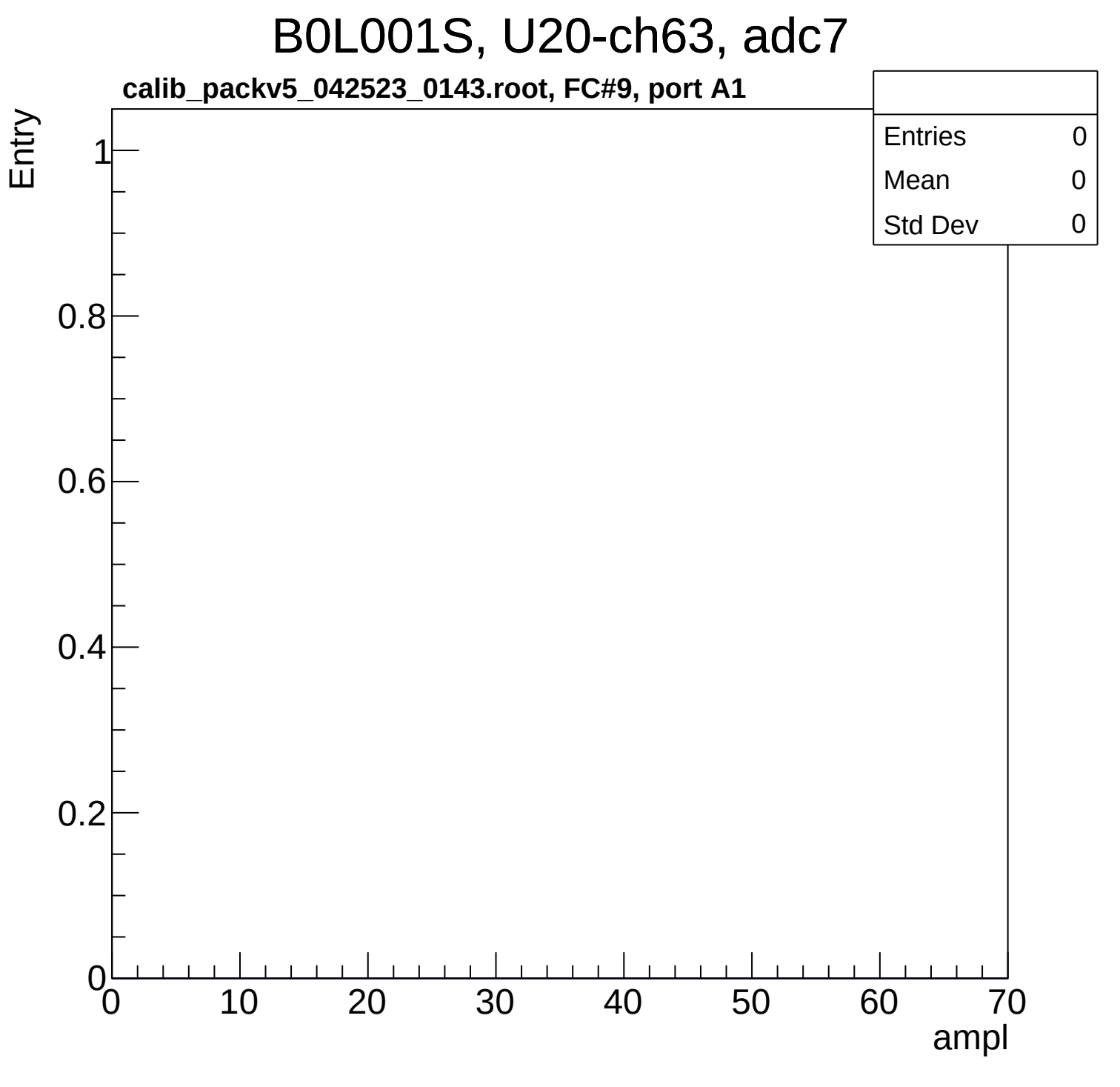
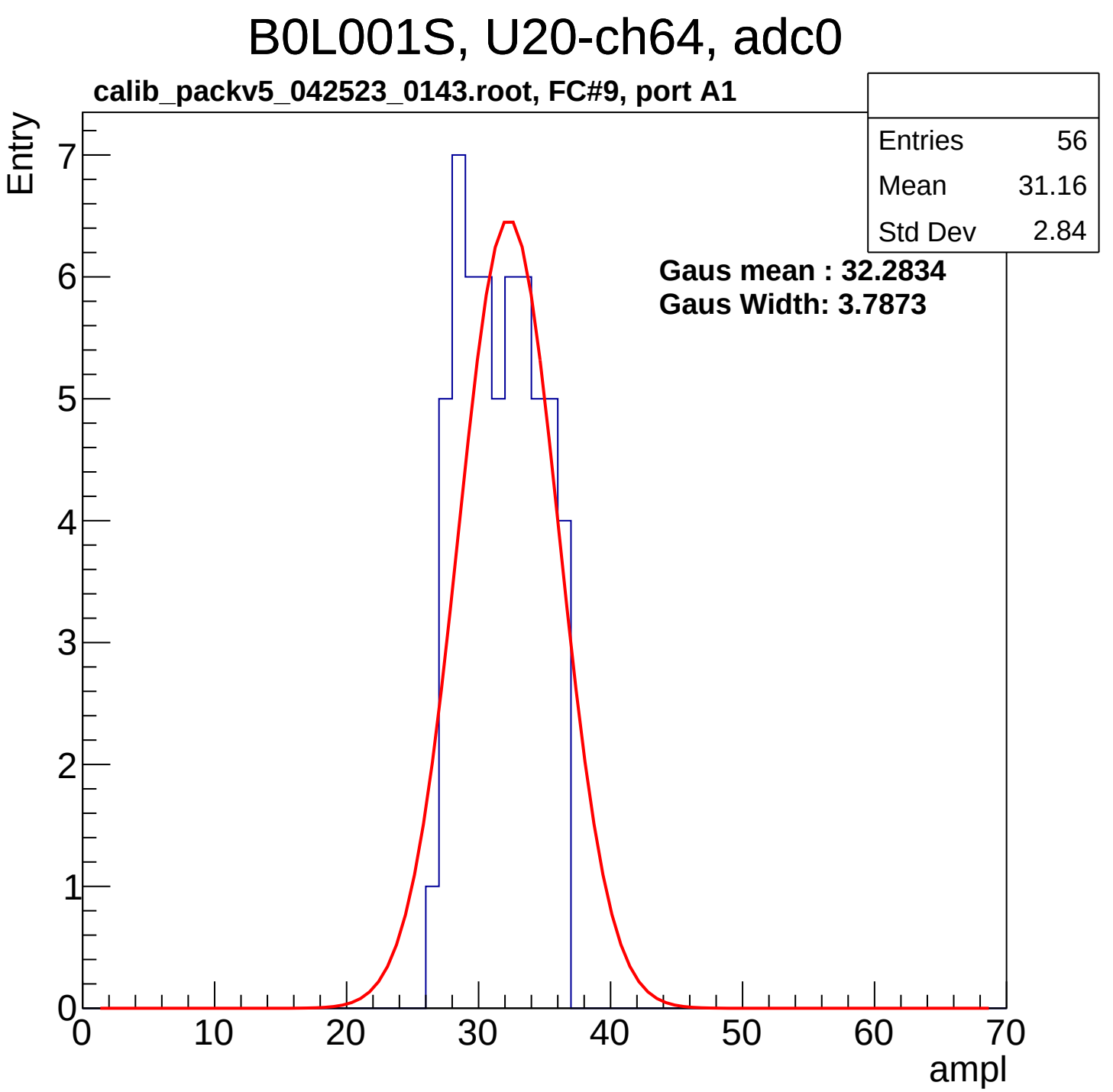
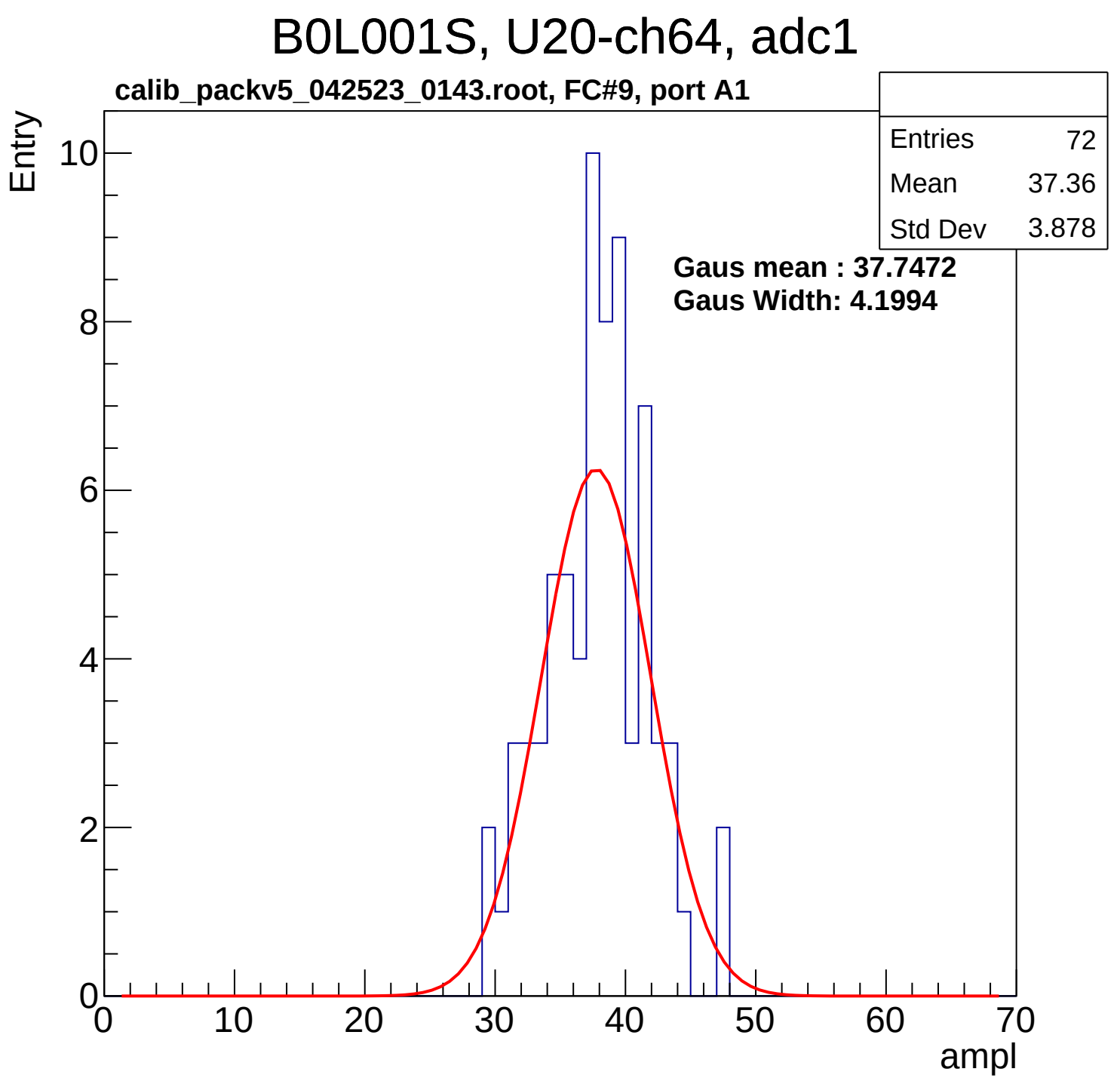
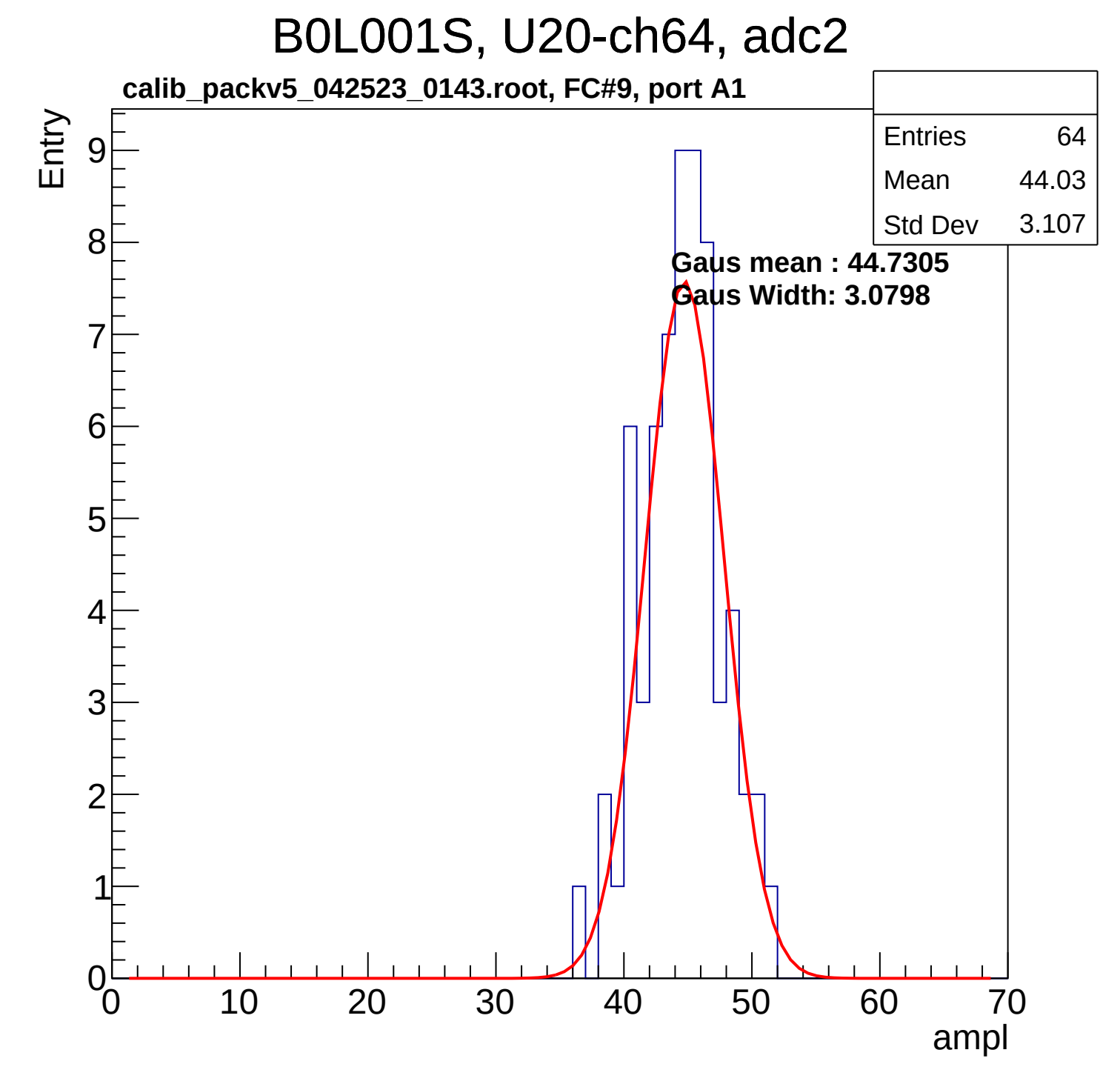
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U20-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 31.16
Std Dev 2.84
Gaus mean : 32.2834
Gaus Width: 3.7873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.36
Std Dev 3.878
Gaus mean : 37.7472
Gaus Width: 4.1994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 44.03
Std Dev 3.107
Gaus mean : 44.7305
Gaus Width: 3.0798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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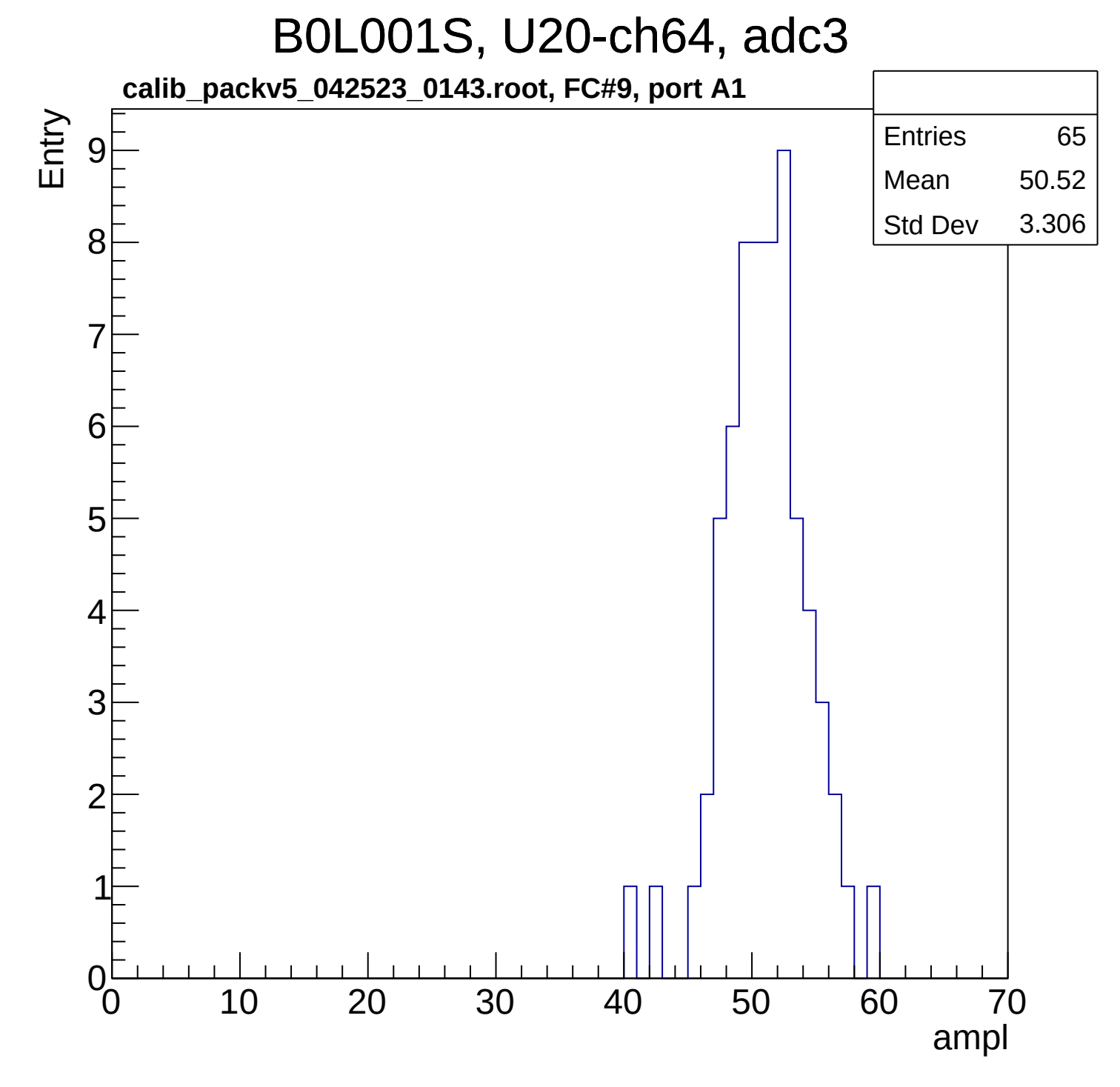
40
50
60
70


B0L001S, U20-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.52
Std Dev 3.306
Entry
ampl
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2
3
4
5
6
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0
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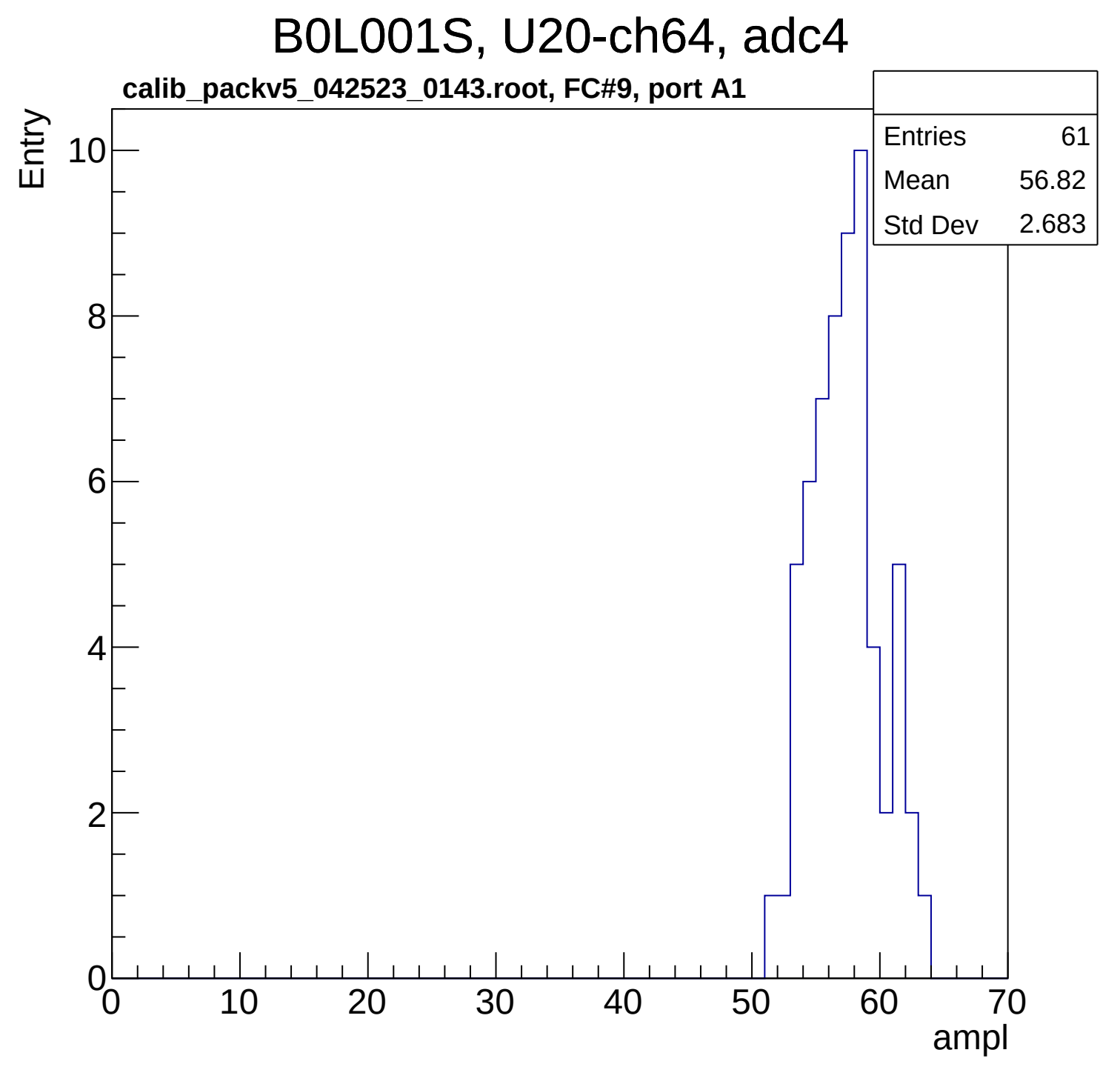
B0L001S, U20-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.82
Std Dev 2.683
Entry
ampl
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6
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0
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40
50
60
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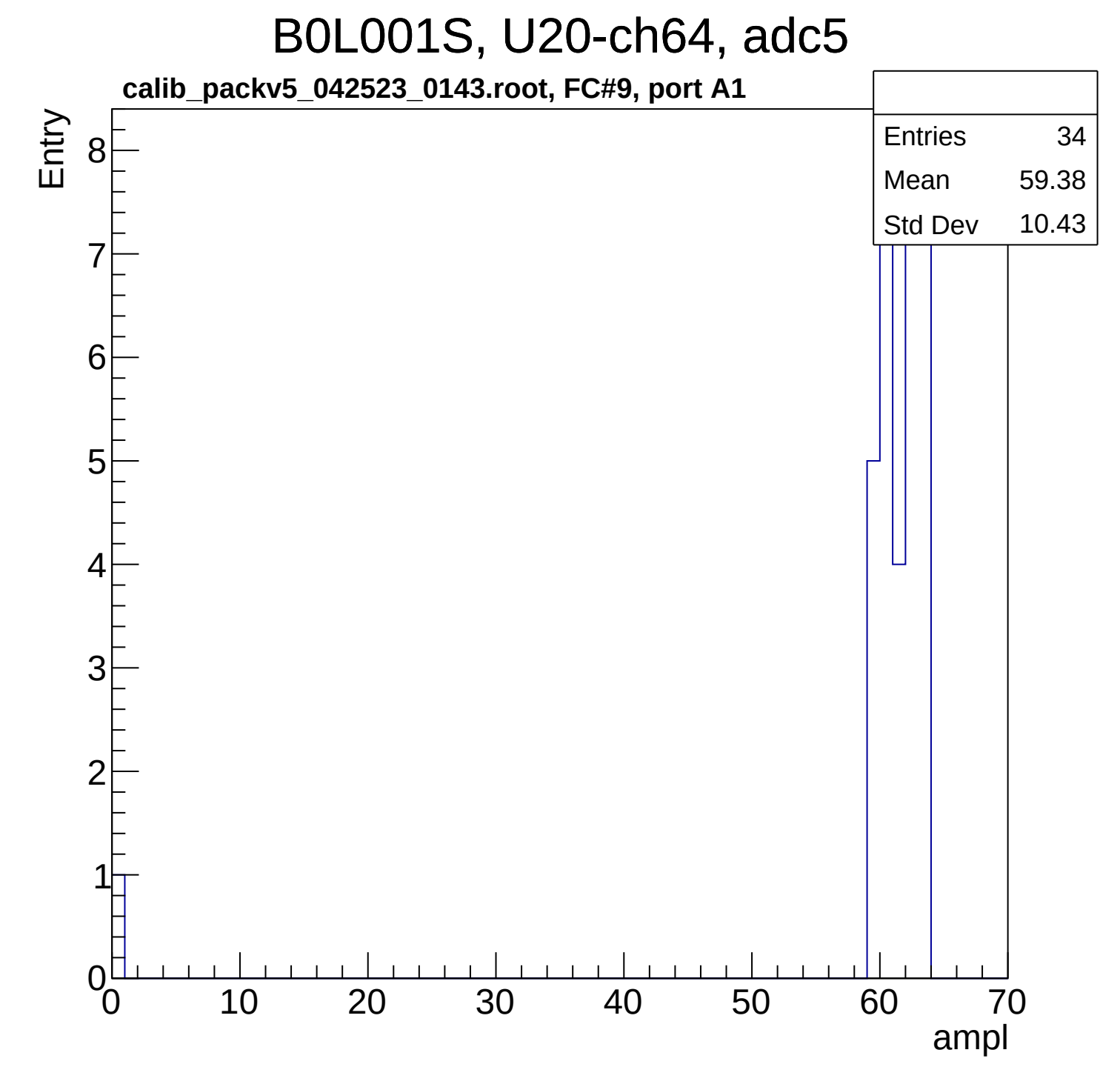
B0L001S, U20-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.38
Std Dev 10.43
Entry
ampl
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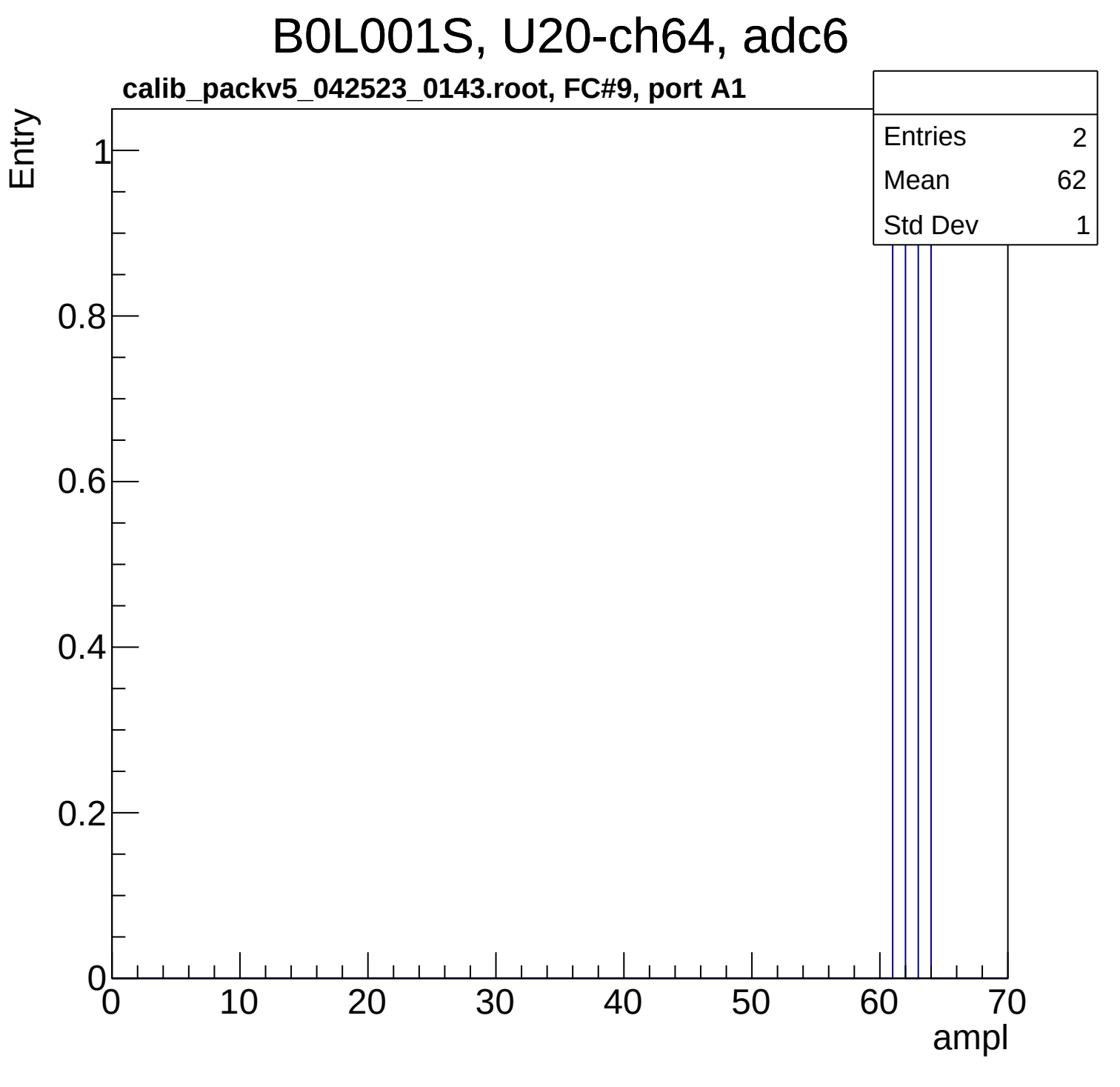
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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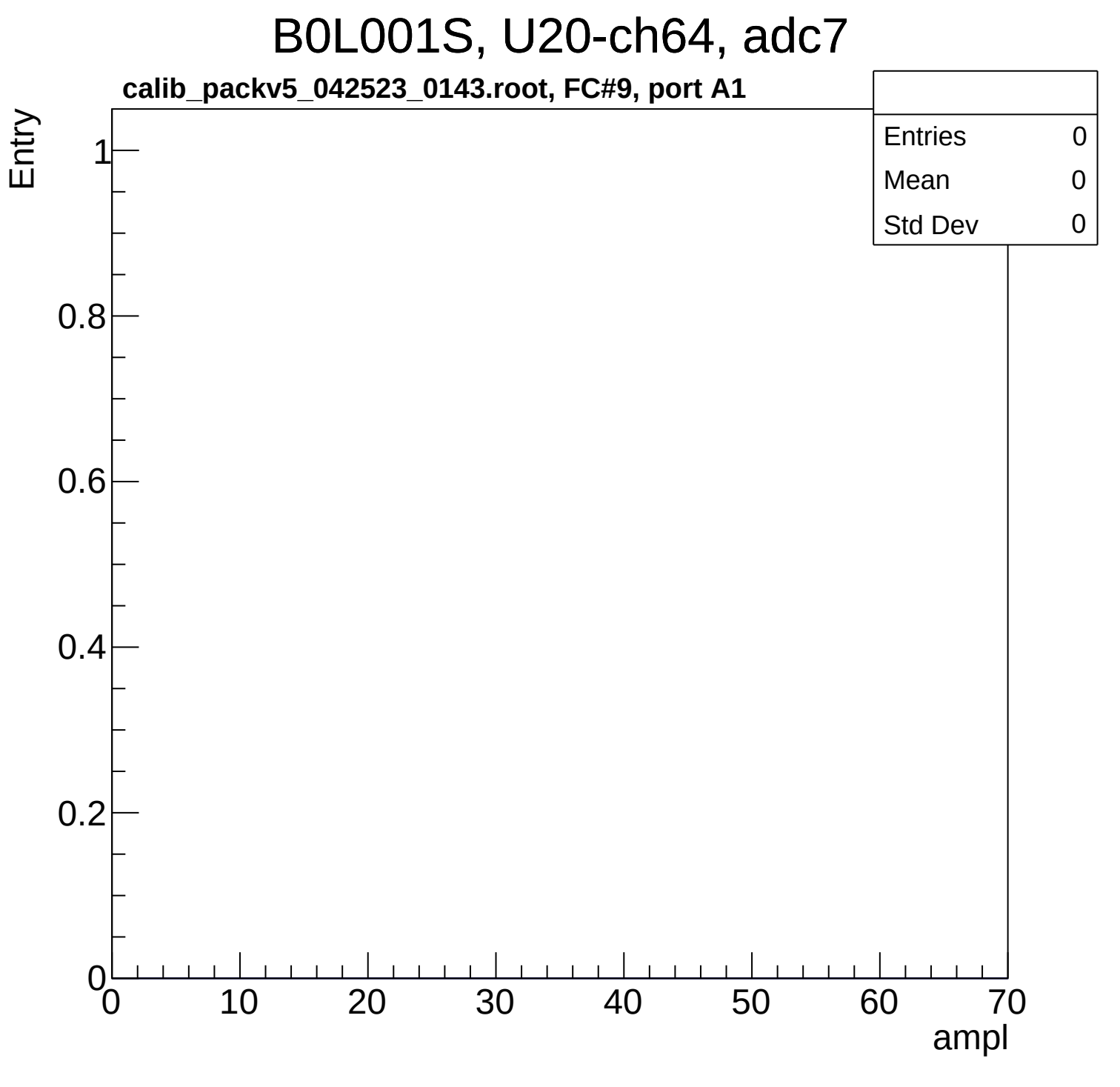
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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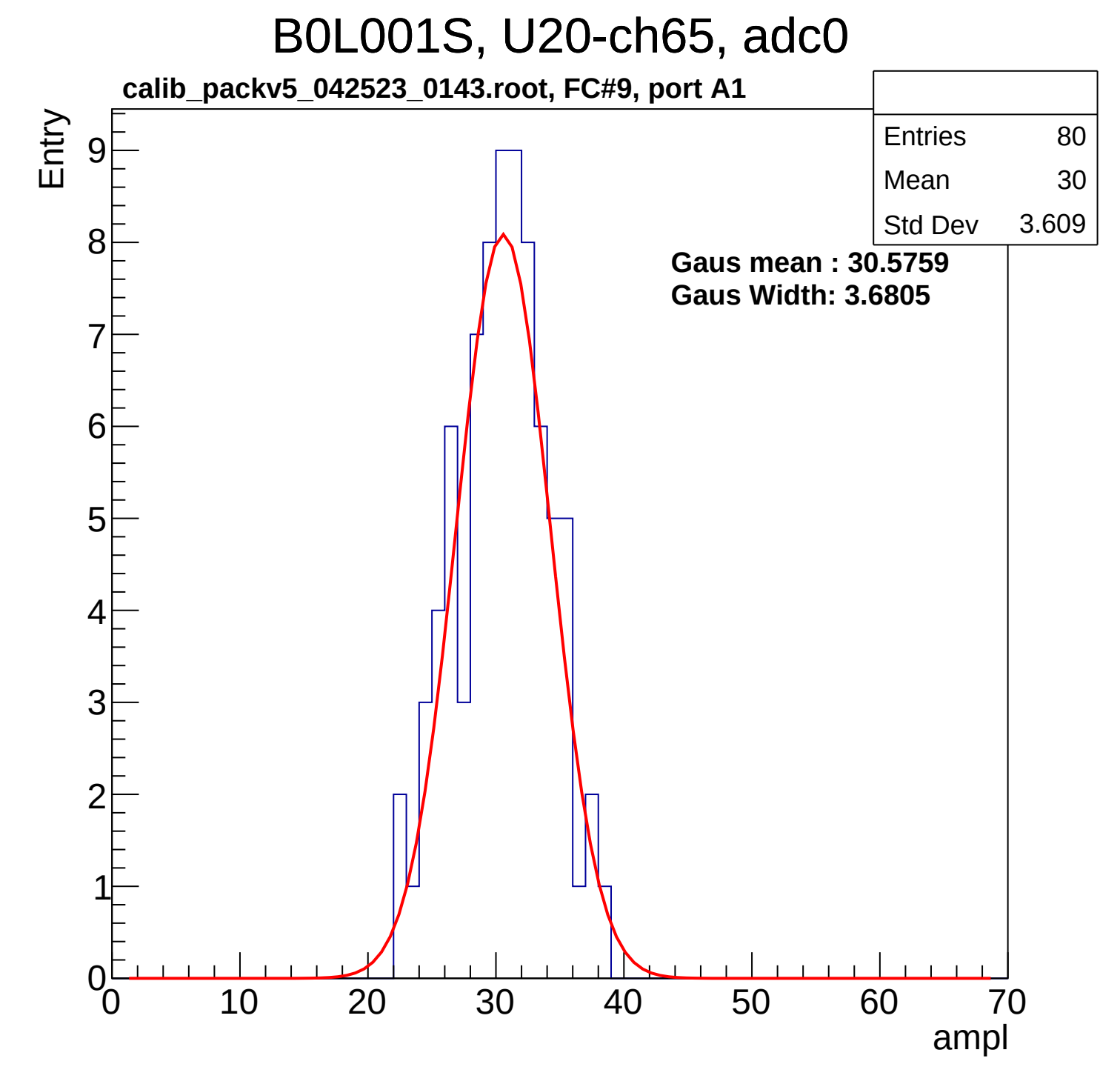
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30
Std Dev 3.609
Gaus mean : 30.5759
Gaus Width: 3.6805
Entry
ampl
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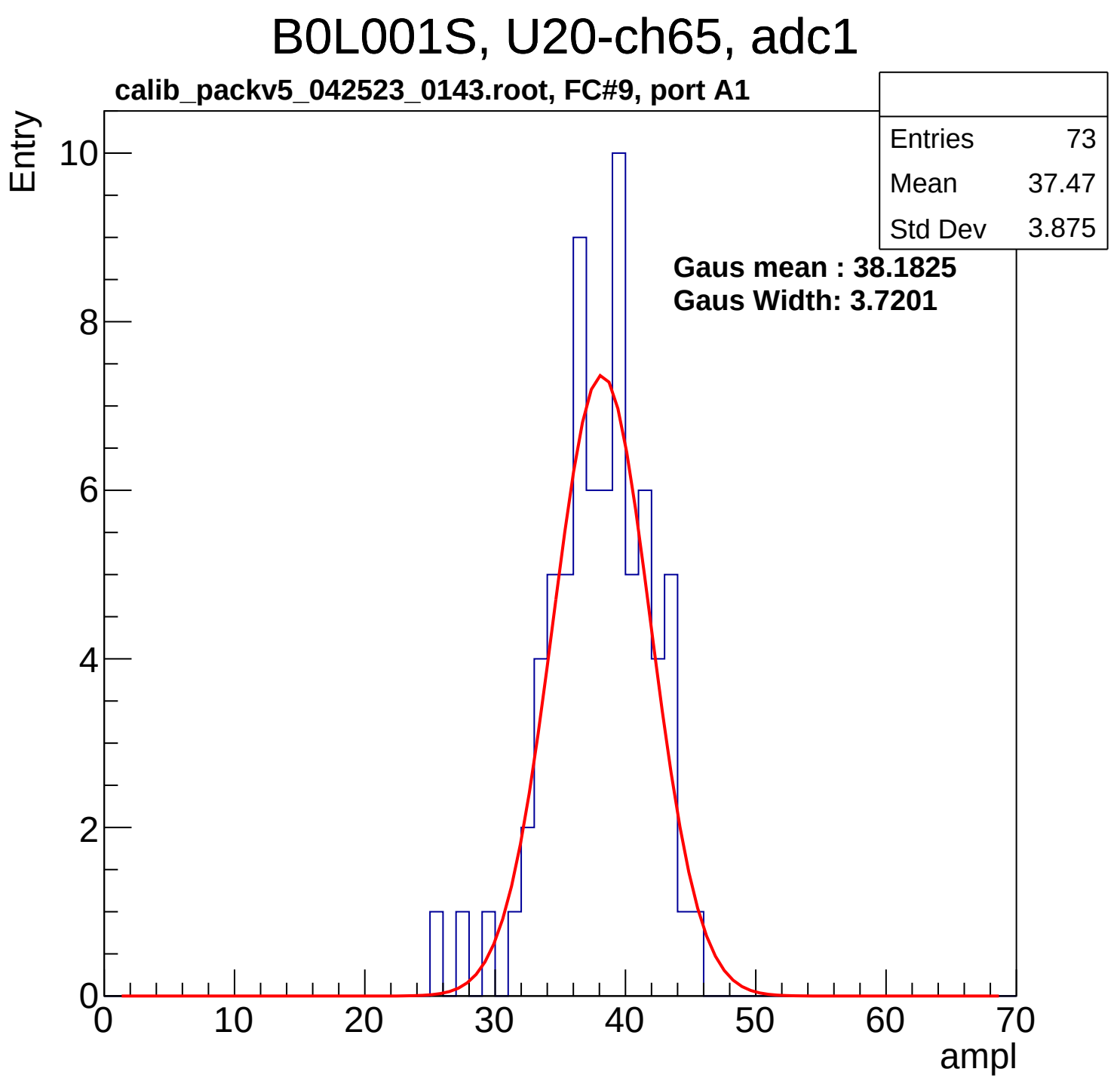
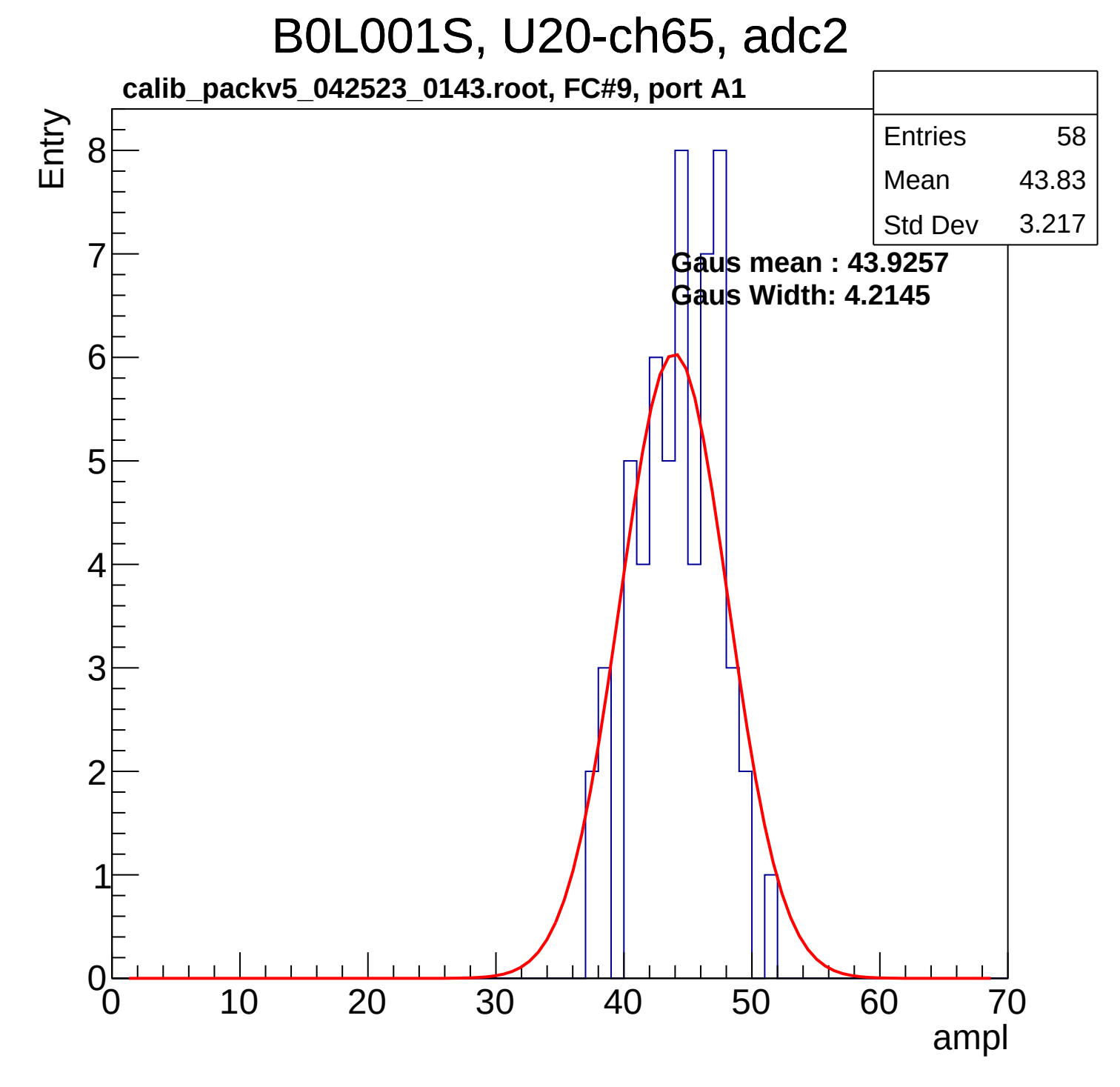
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.47
Std Dev 3.875
Gaus mean : 38.1825
Gaus Width: 3.7201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.83
Std Dev 3.217
Gaus mean : 43.9257
Gaus Width: 4.2145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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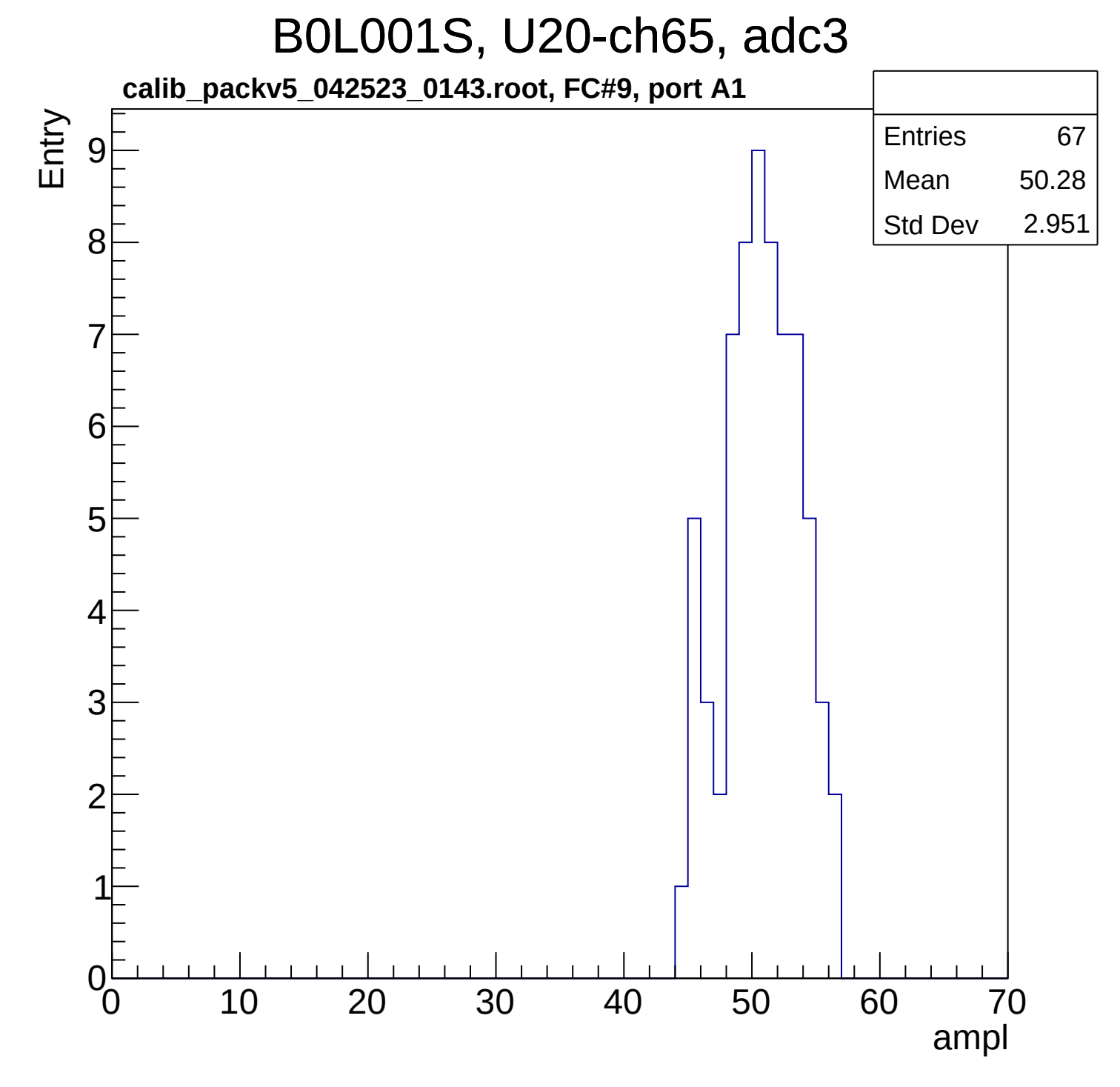
B0L001S, U20-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.28
Std Dev 2.951
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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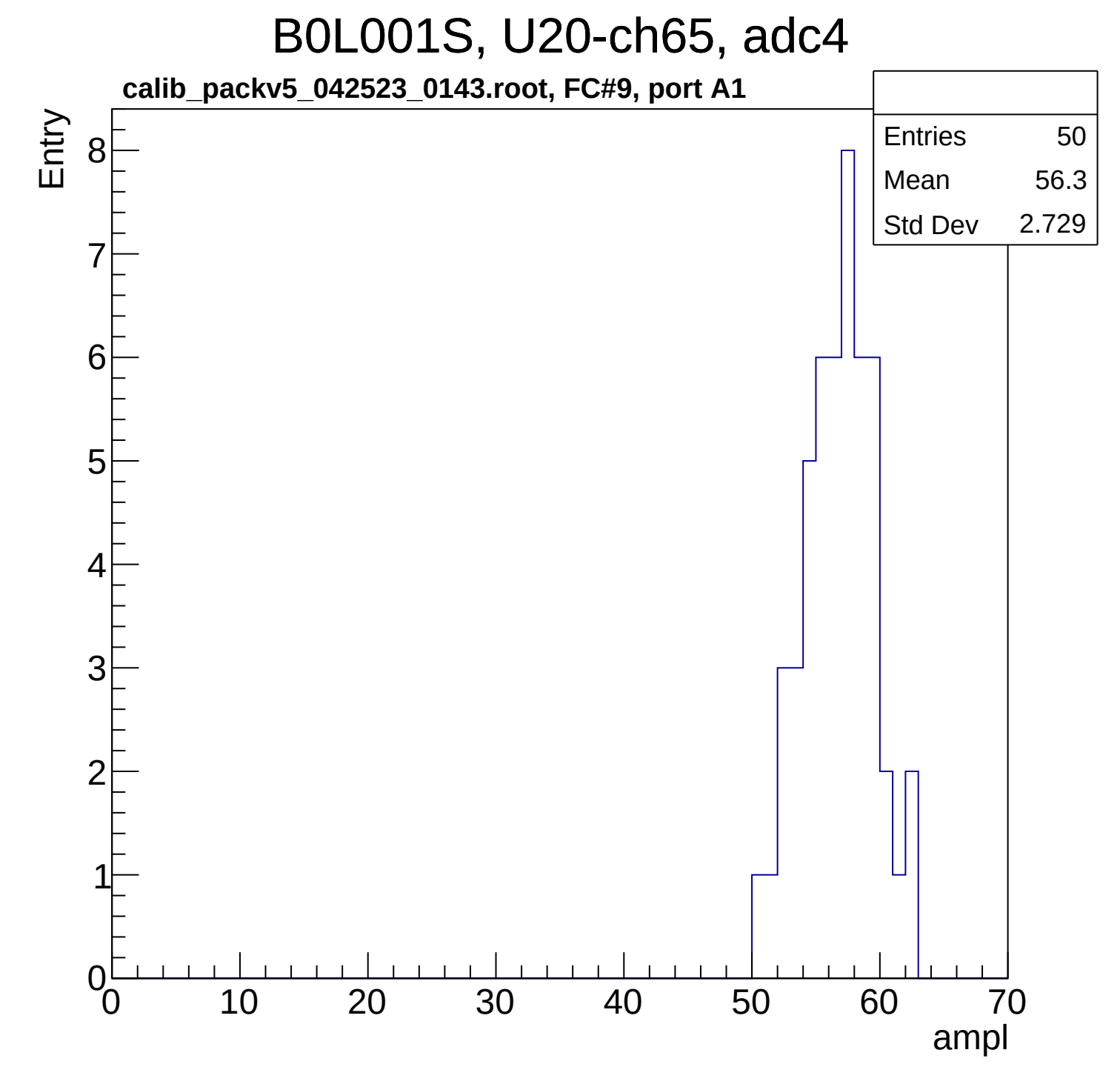
B0L001S, U20-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.3
Std Dev 2.729
Entry
ampl
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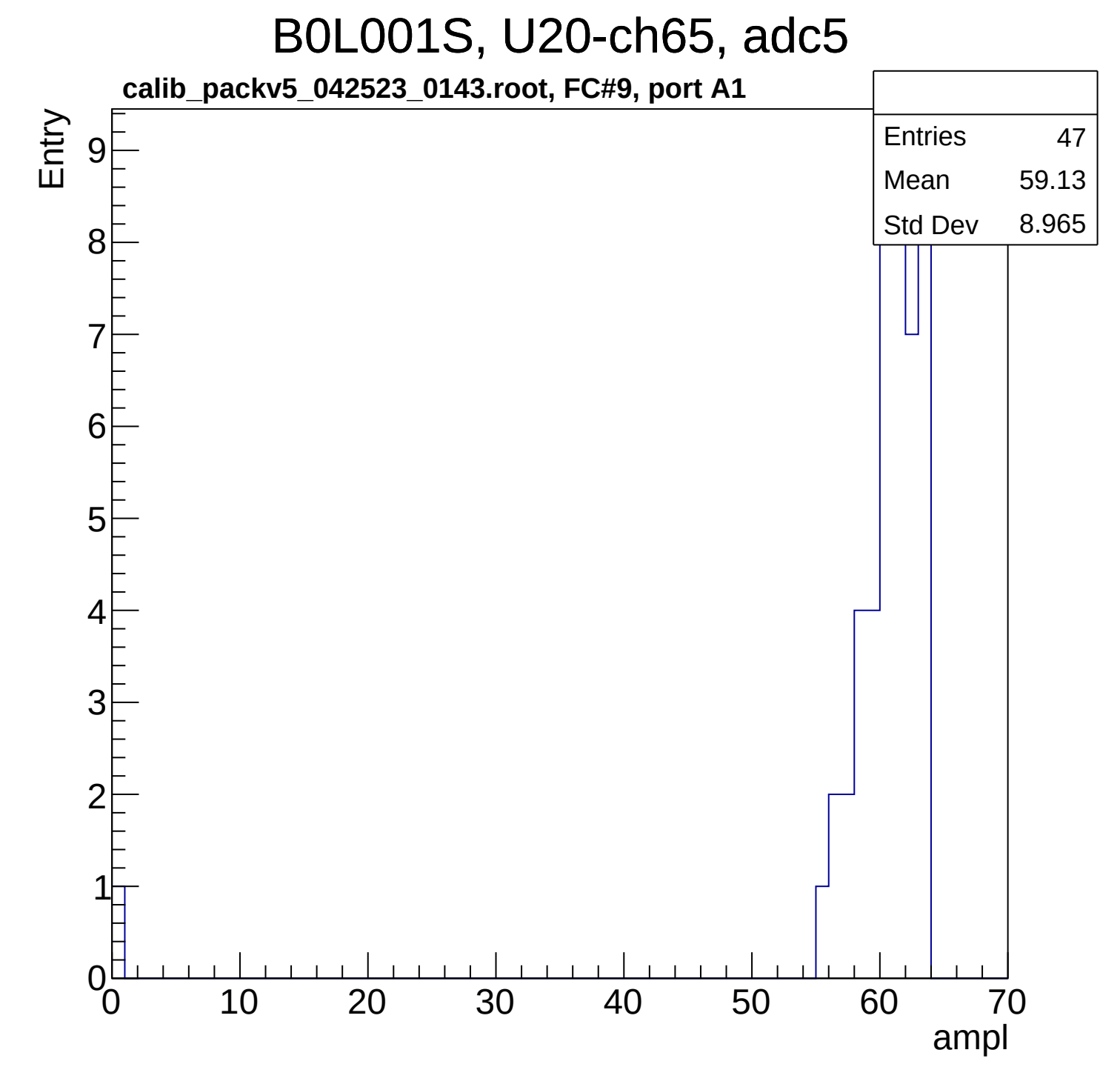
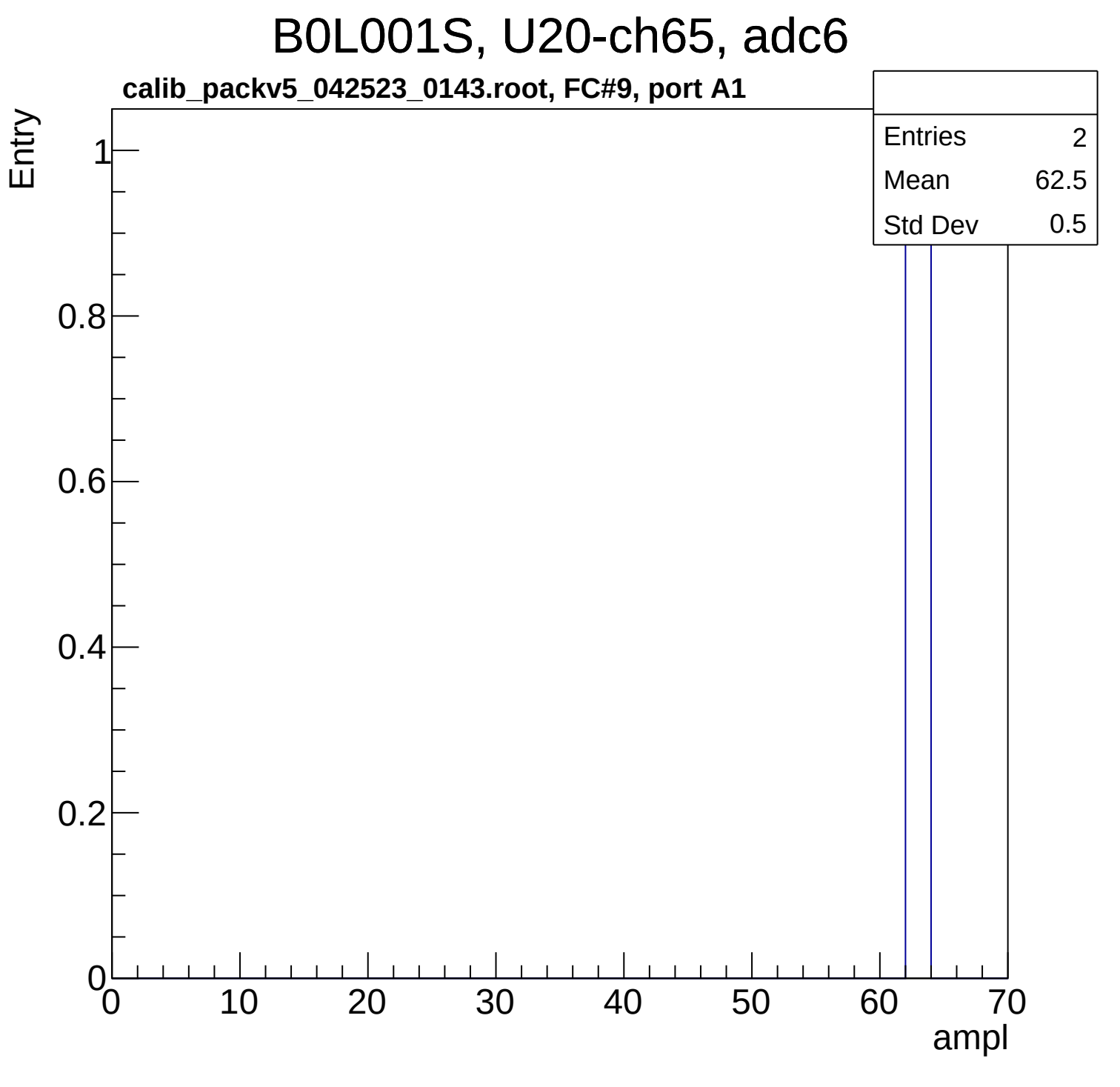
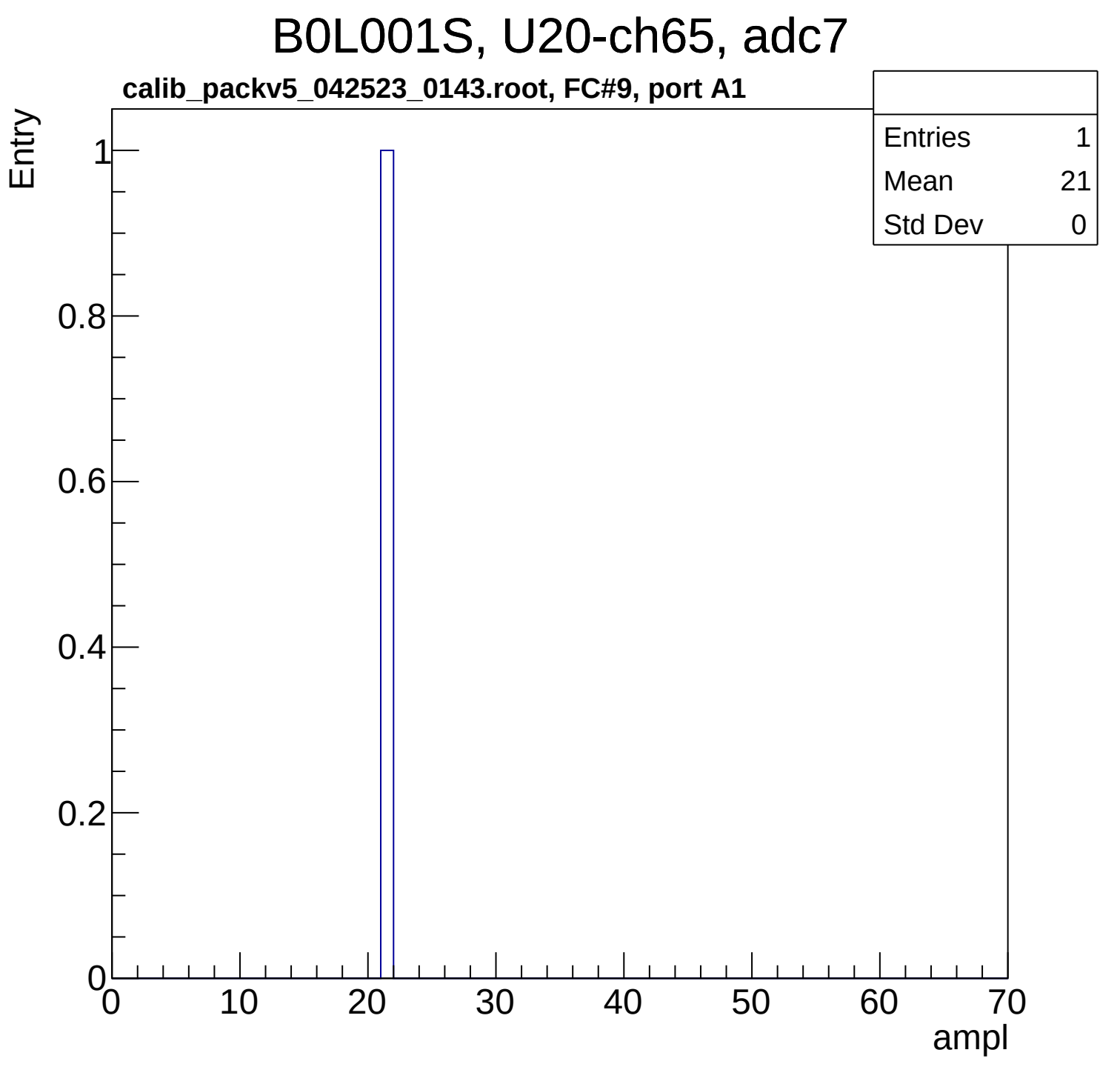
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.13
Std Dev 8.965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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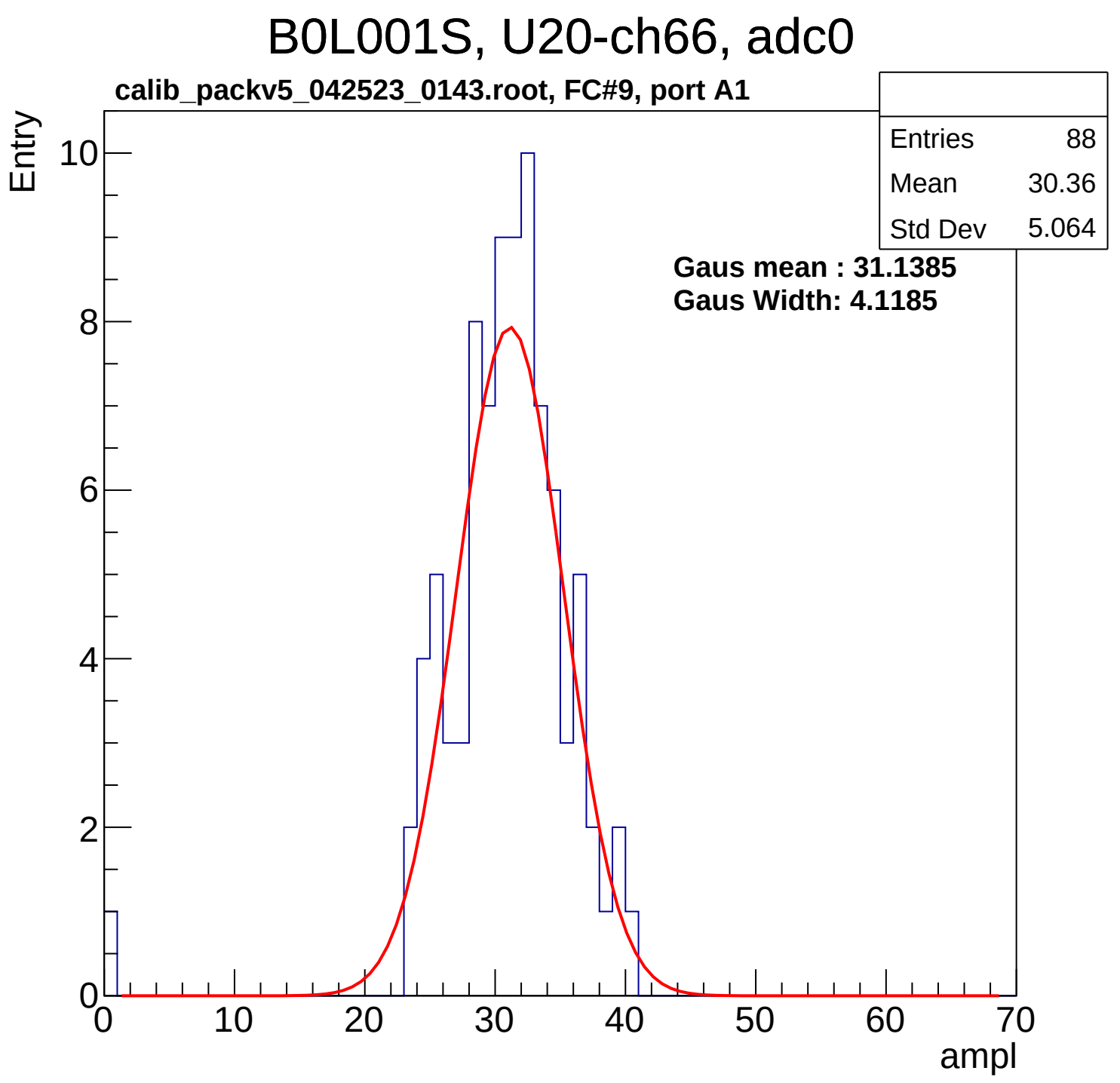
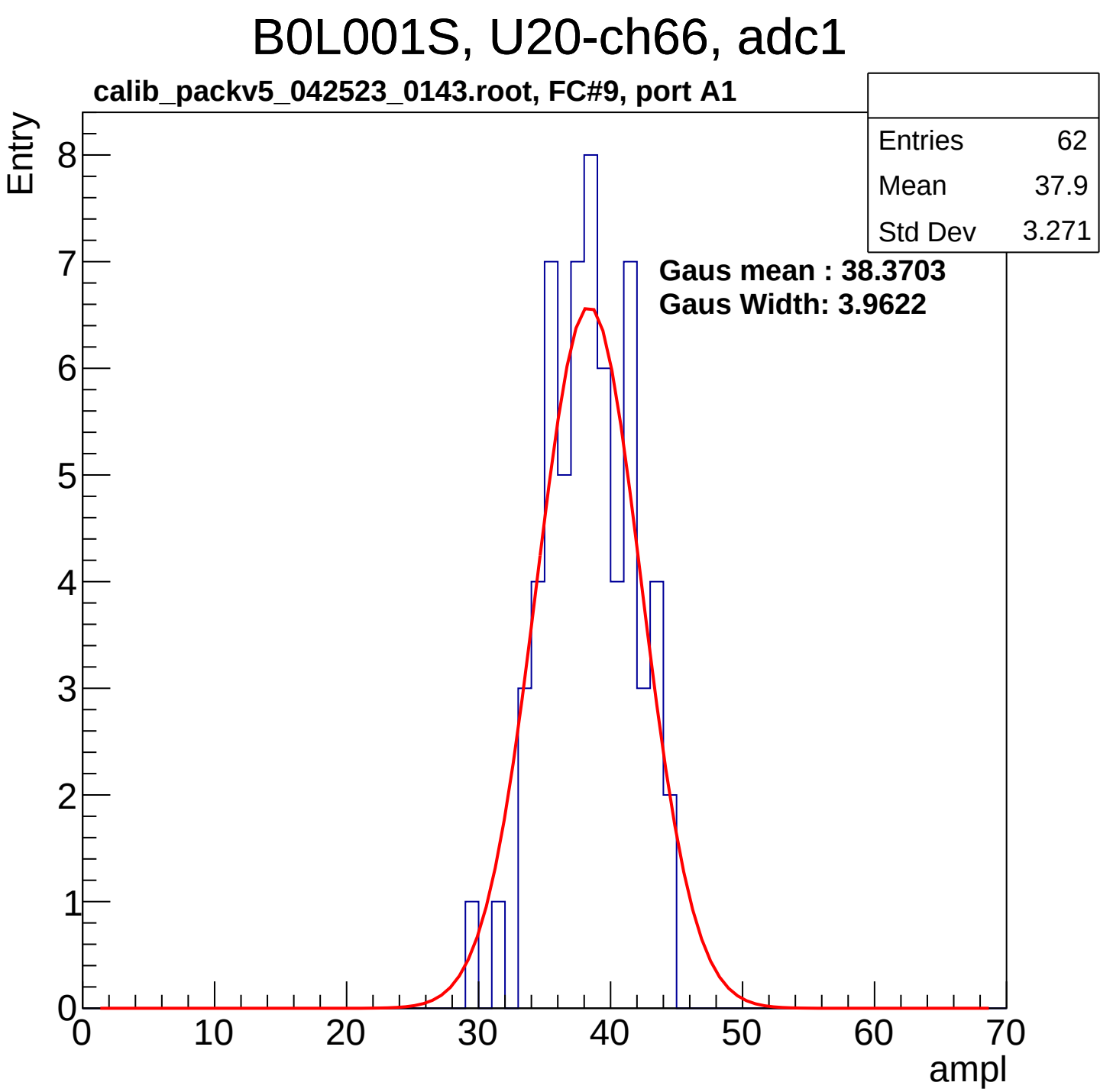
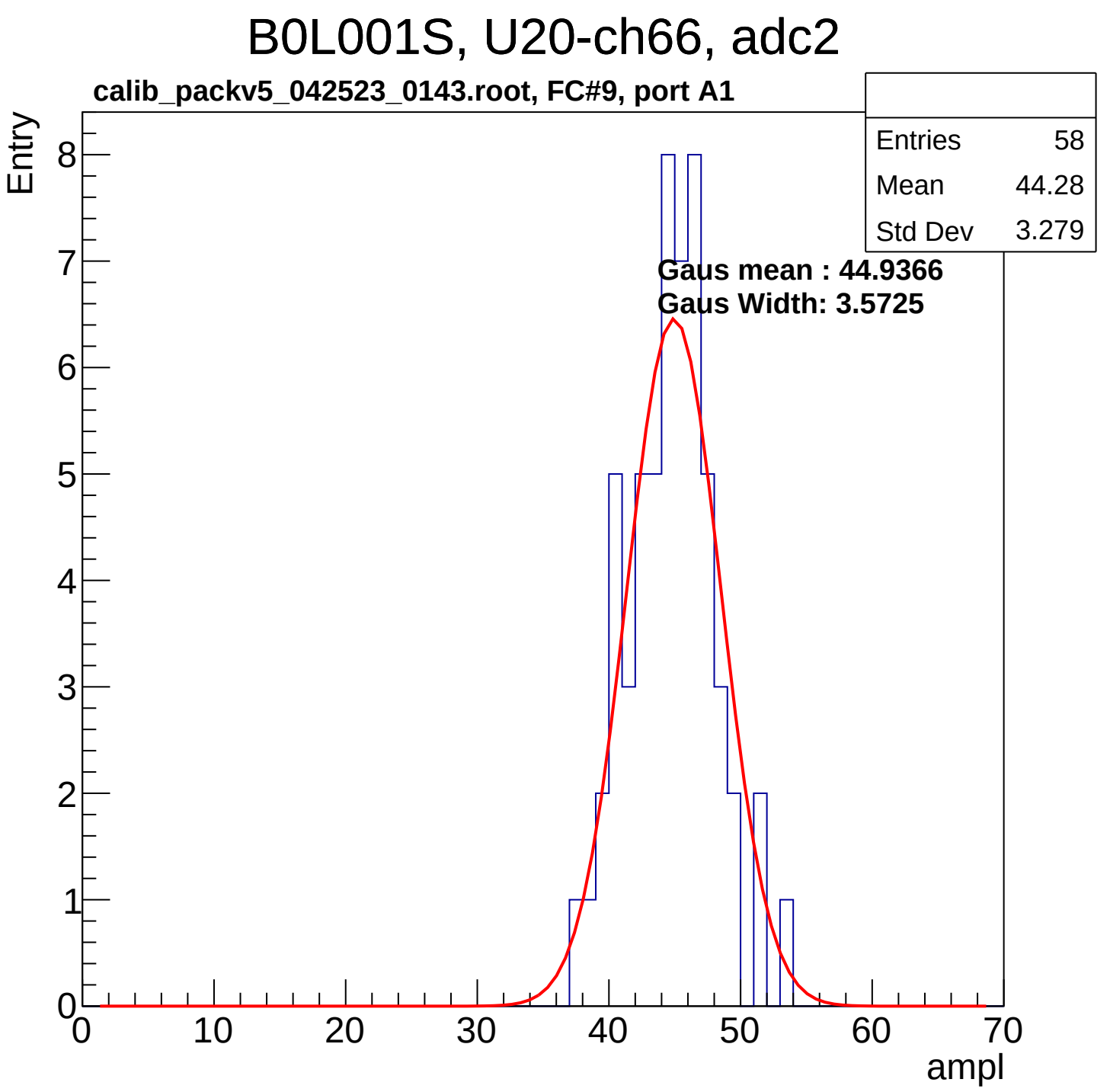
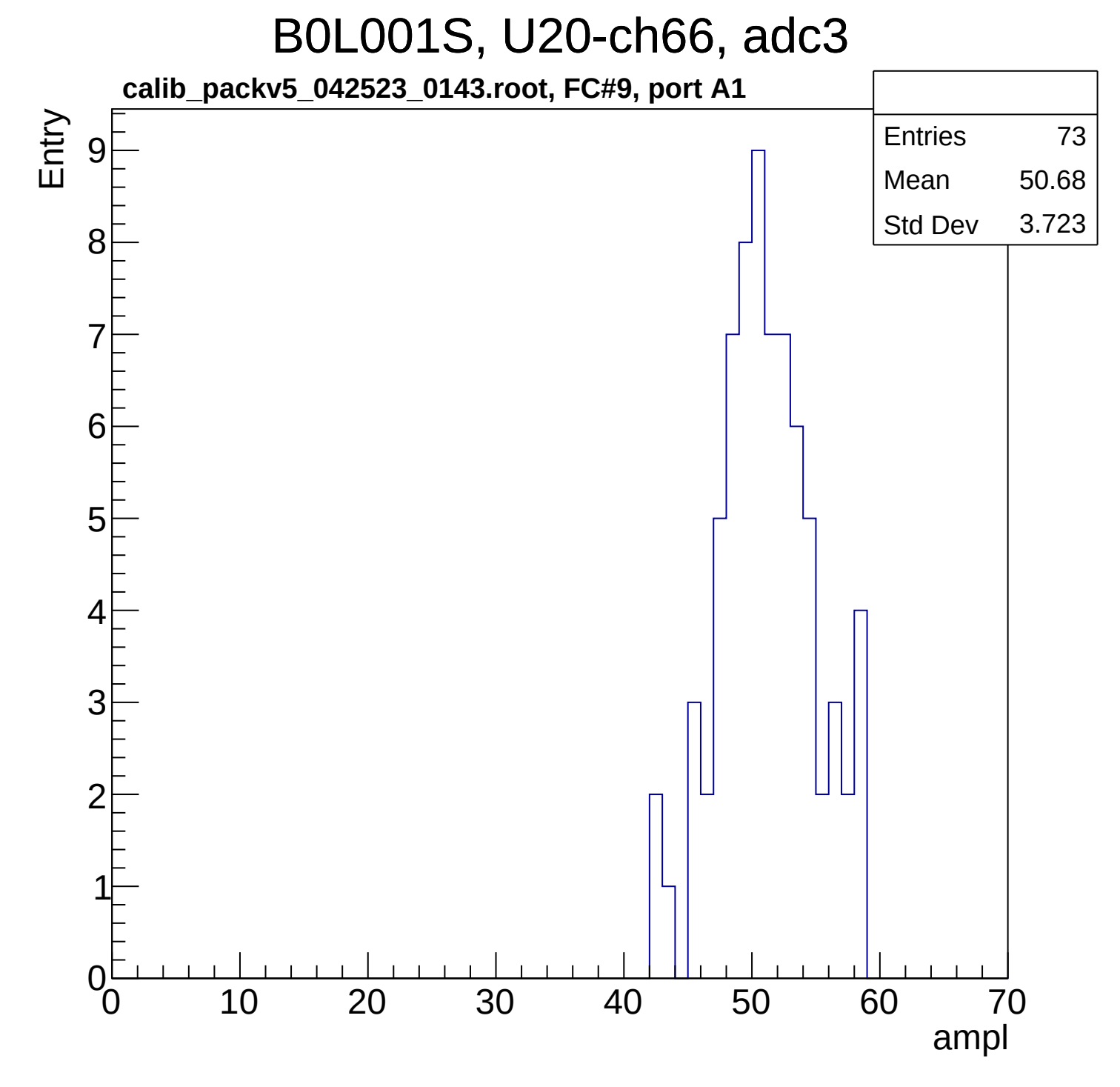
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 30.36
Std Dev 5.064
Gaus mean : 31.1385
Gaus Width: 4.1185
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.9
Std Dev 3.271
Gaus mean : 38.3703
Gaus Width: 3.9622
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.28
Std Dev 3.279
Gaus mean : 44.9366
Gaus Width: 3.5725
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 50.68
Std Dev 3.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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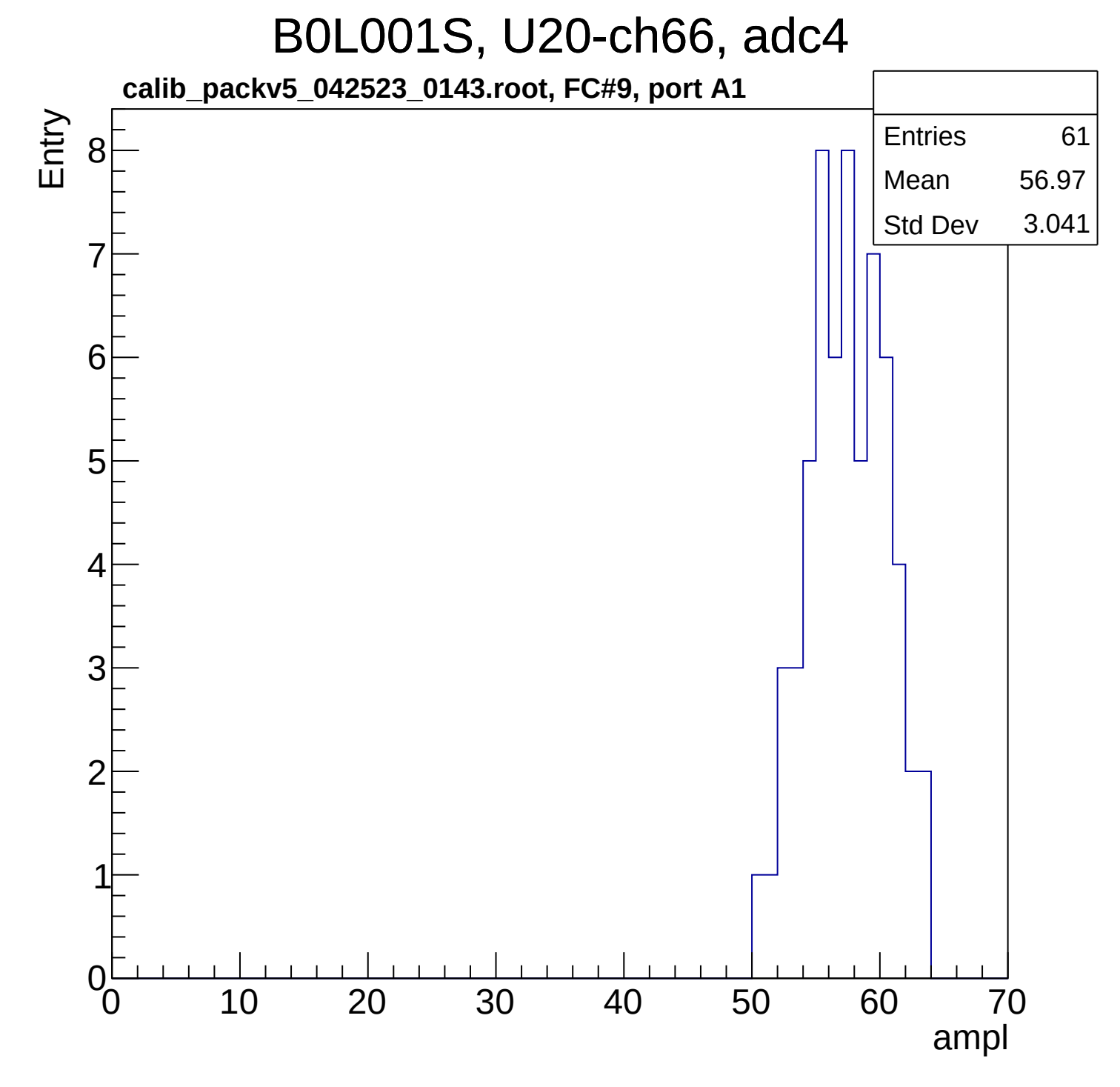
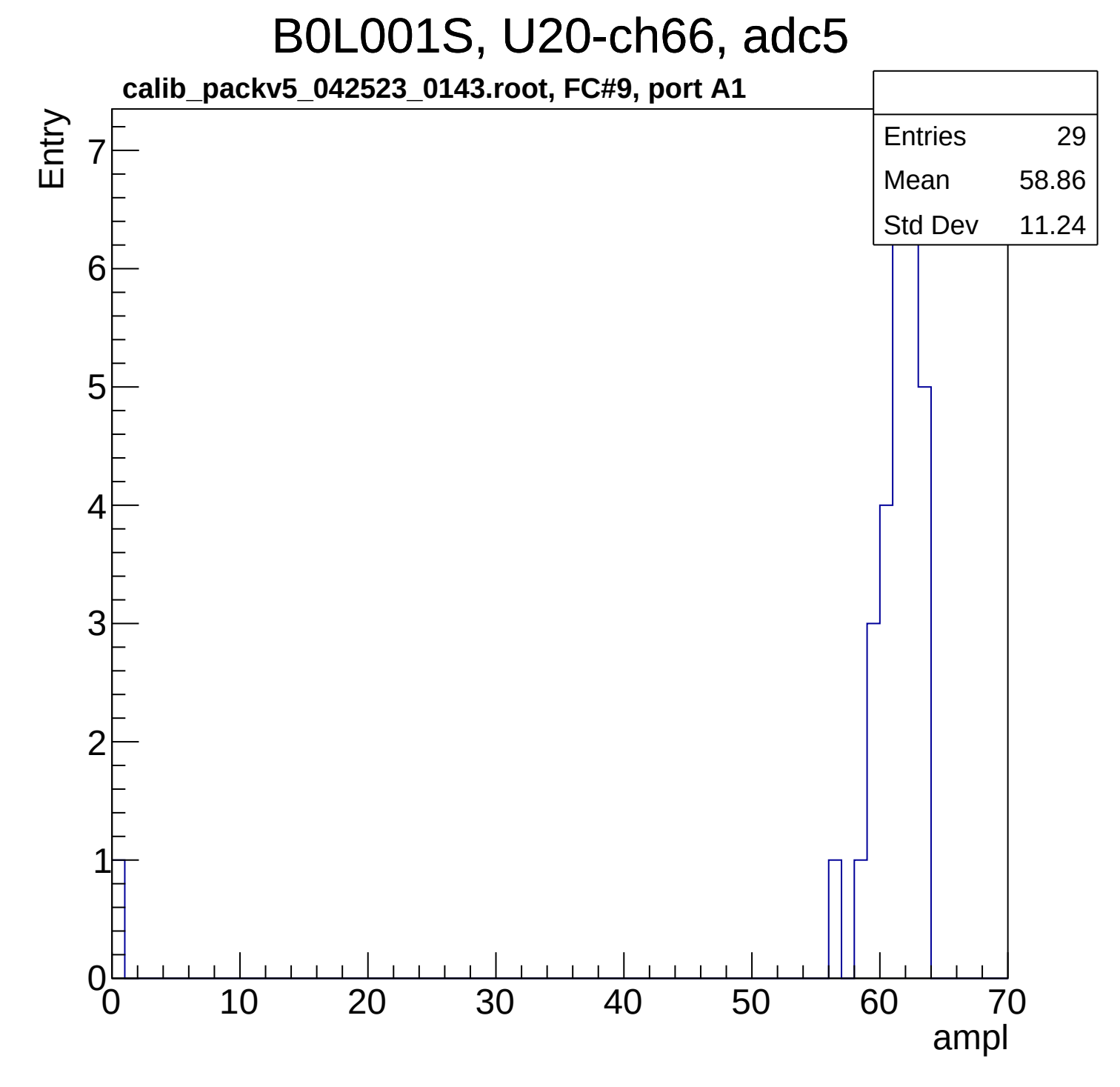
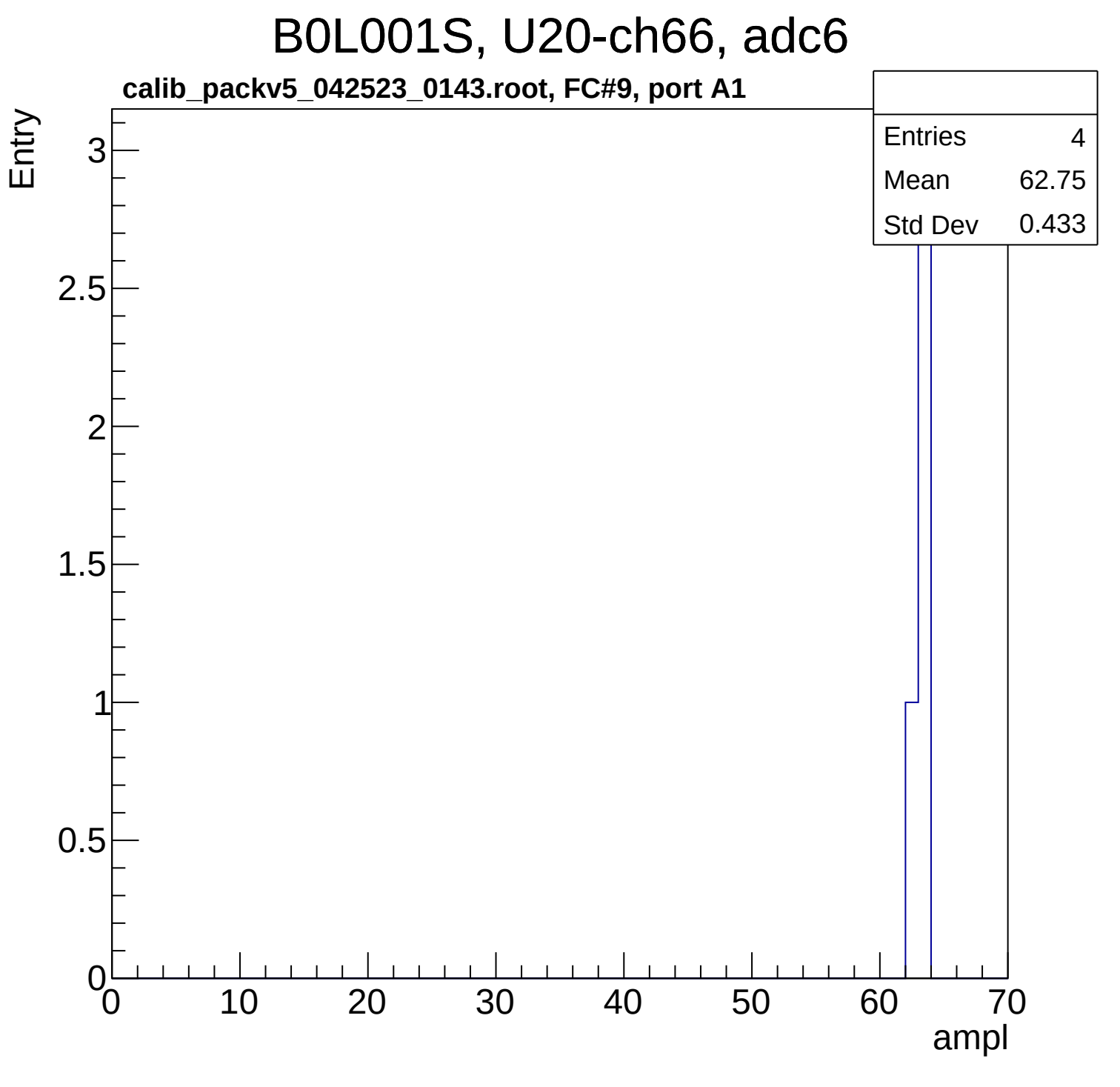

B0L001S, U20-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.97
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.86
Std Dev 11.24
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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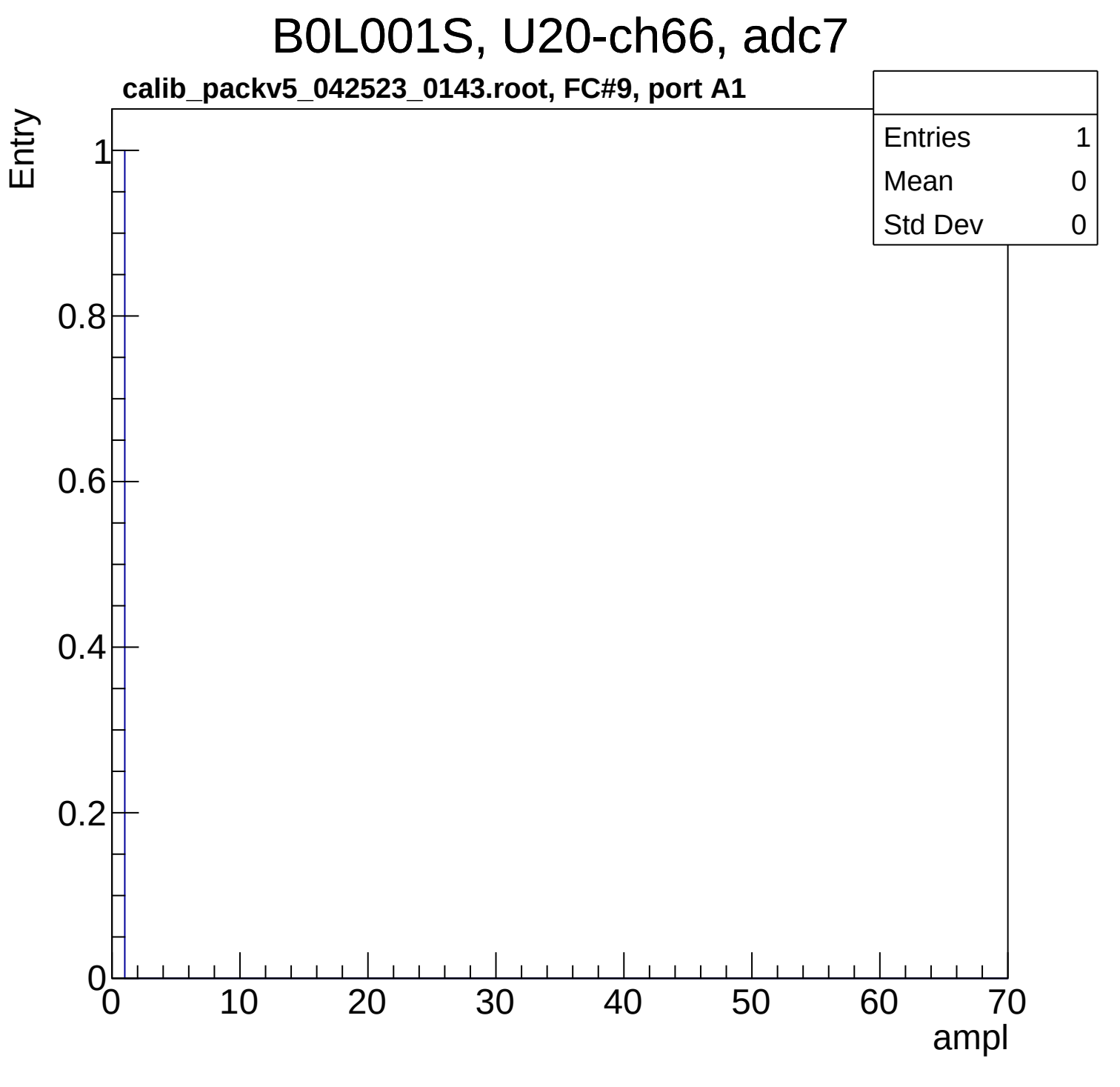
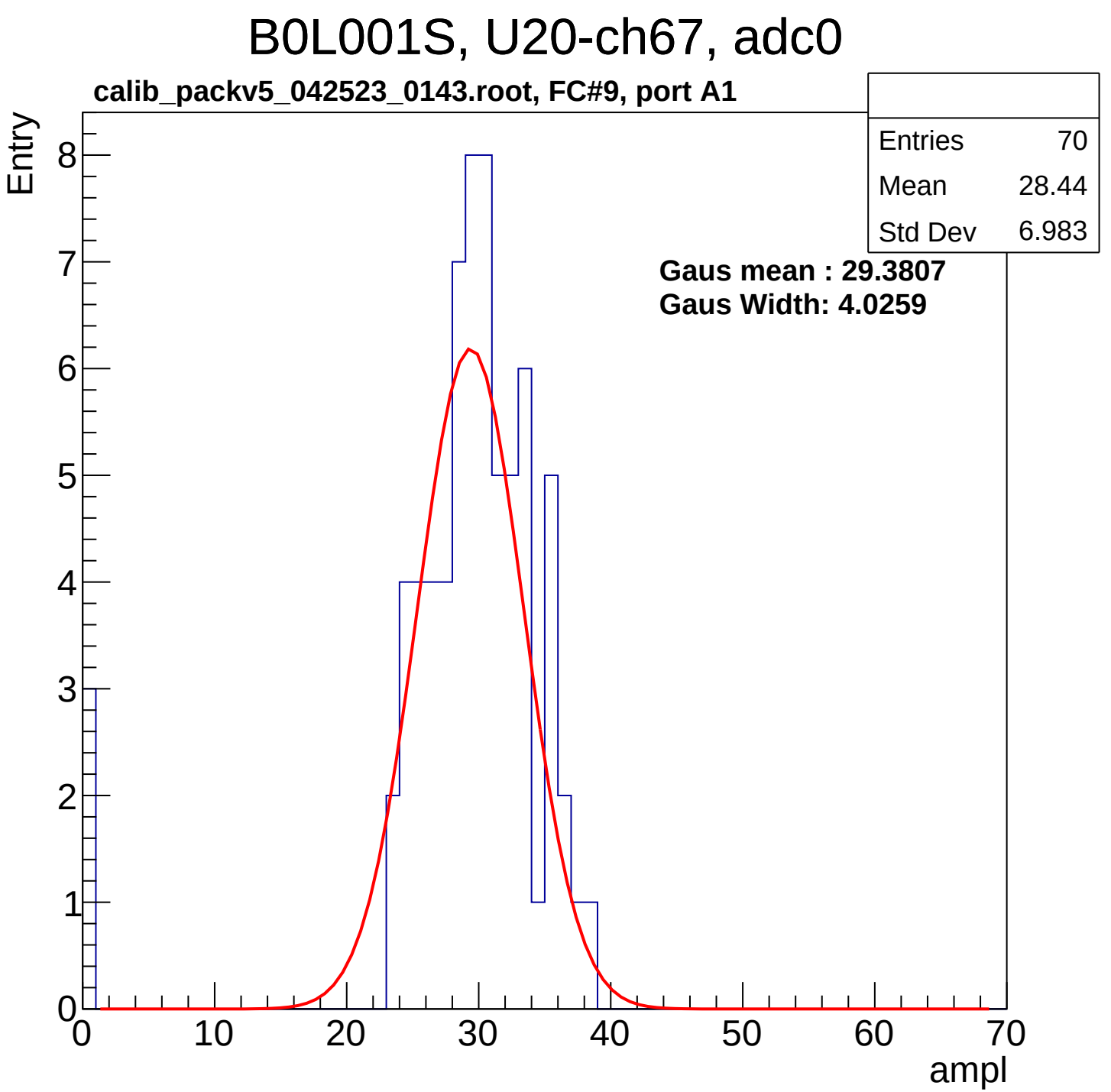
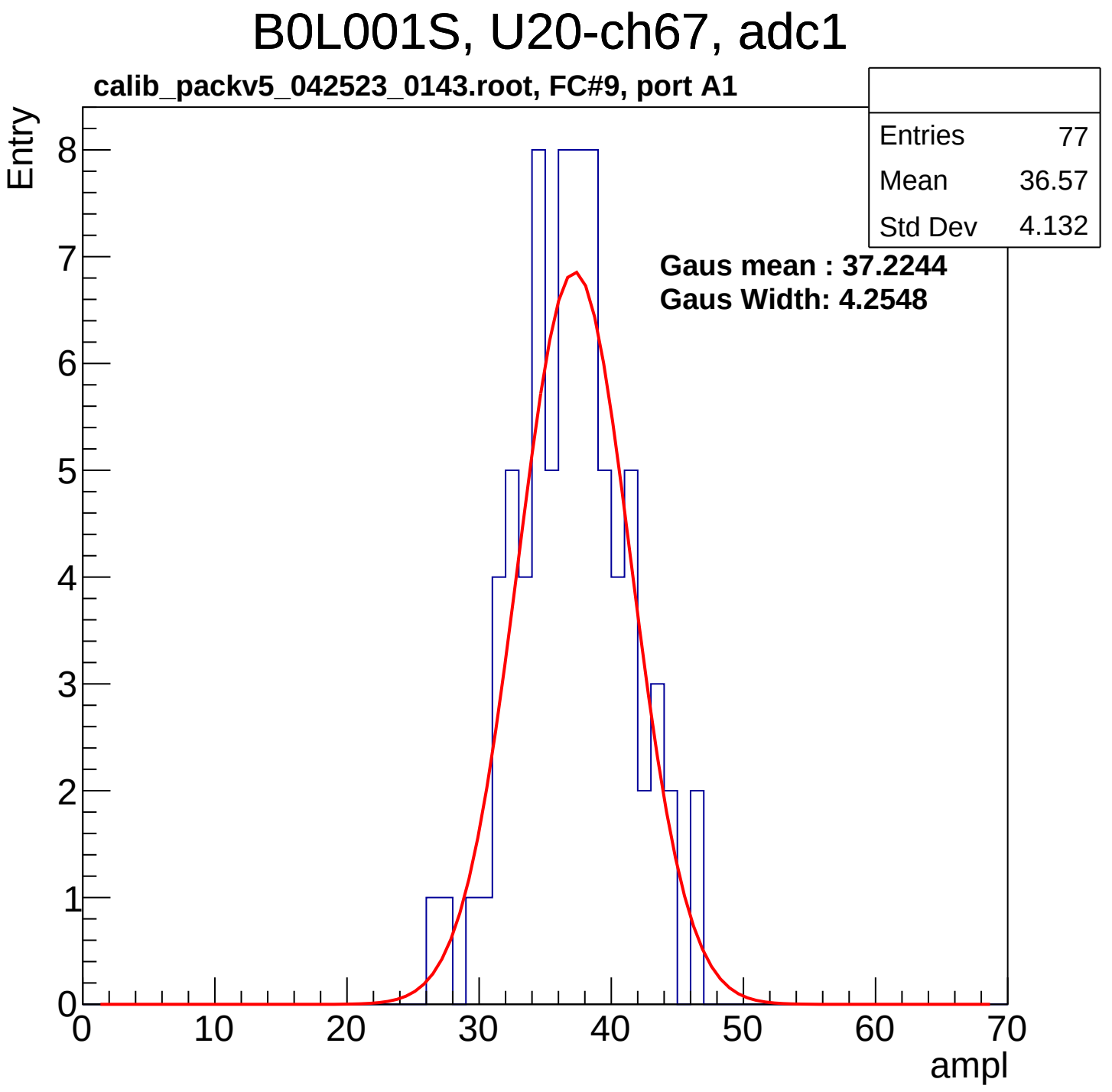
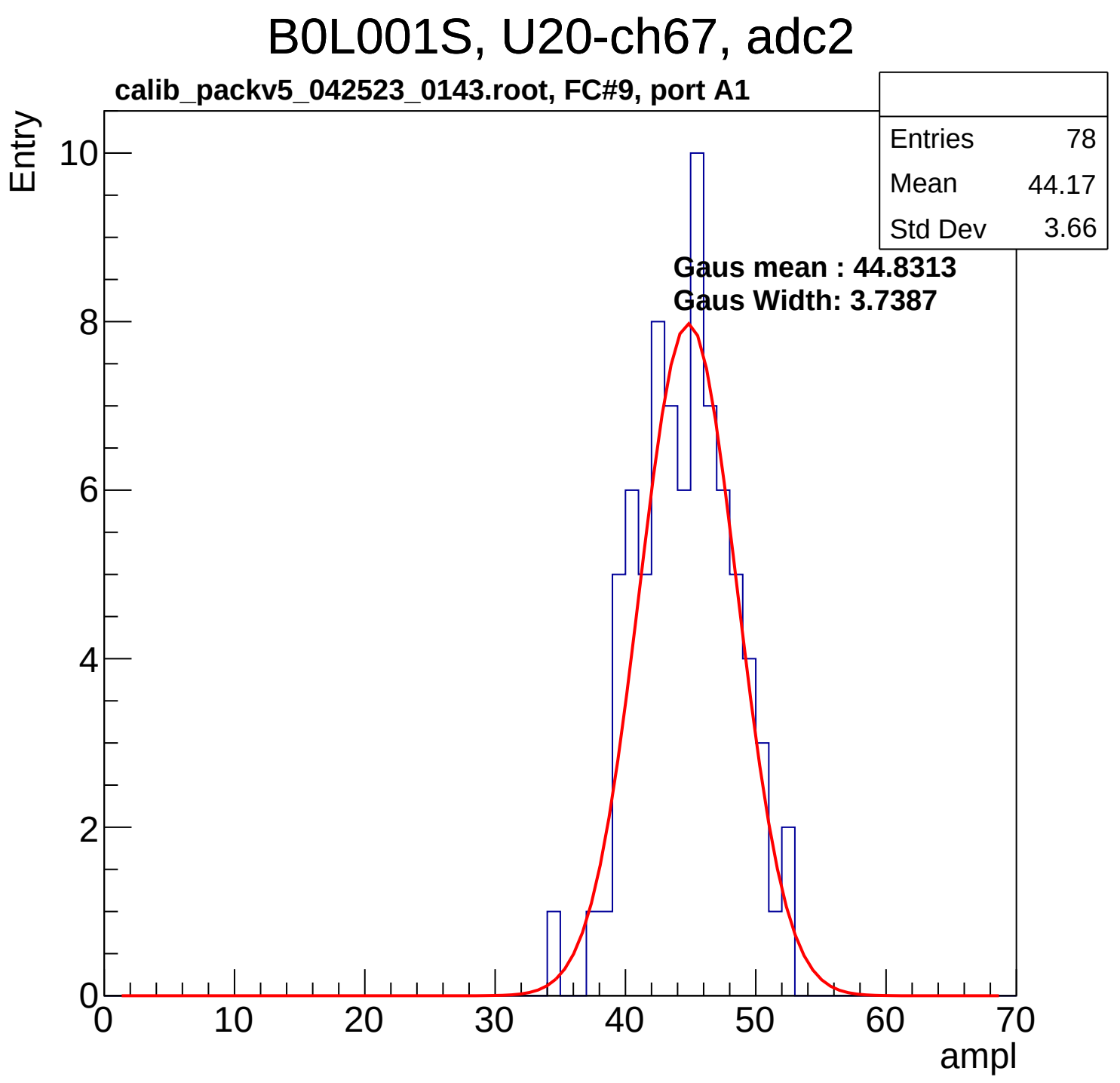
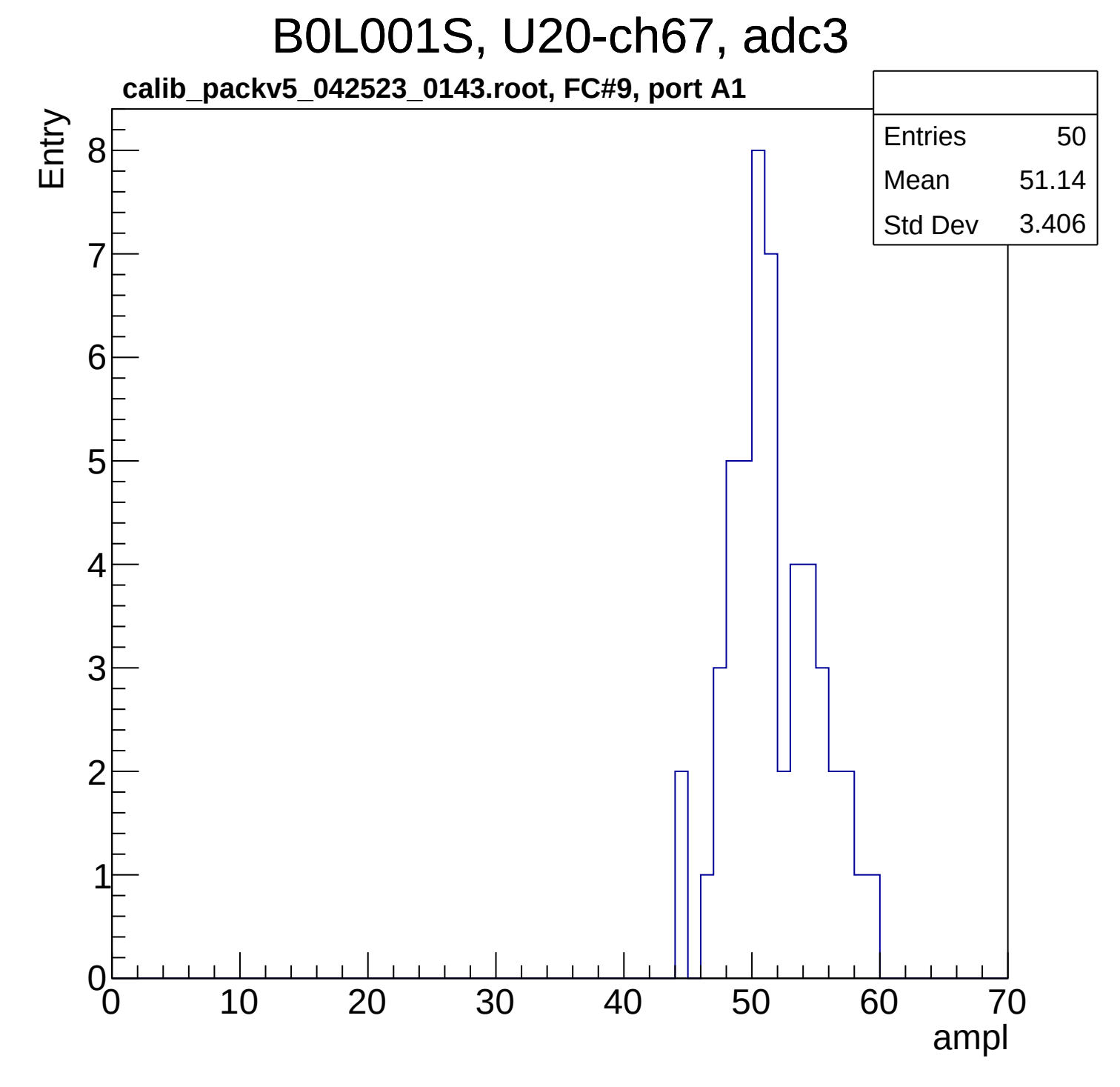
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 28.44
Std Dev 6.983
Gaus mean : 29.3807
Gaus Width: 4.0259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.57
Std Dev 4.132
Gaus mean : 37.2244
Gaus Width: 4.2548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.17
Std Dev 3.66
Gaus mean : 44.8313
Gaus Width: 3.7387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 51.14
Std Dev 3.406
Entry
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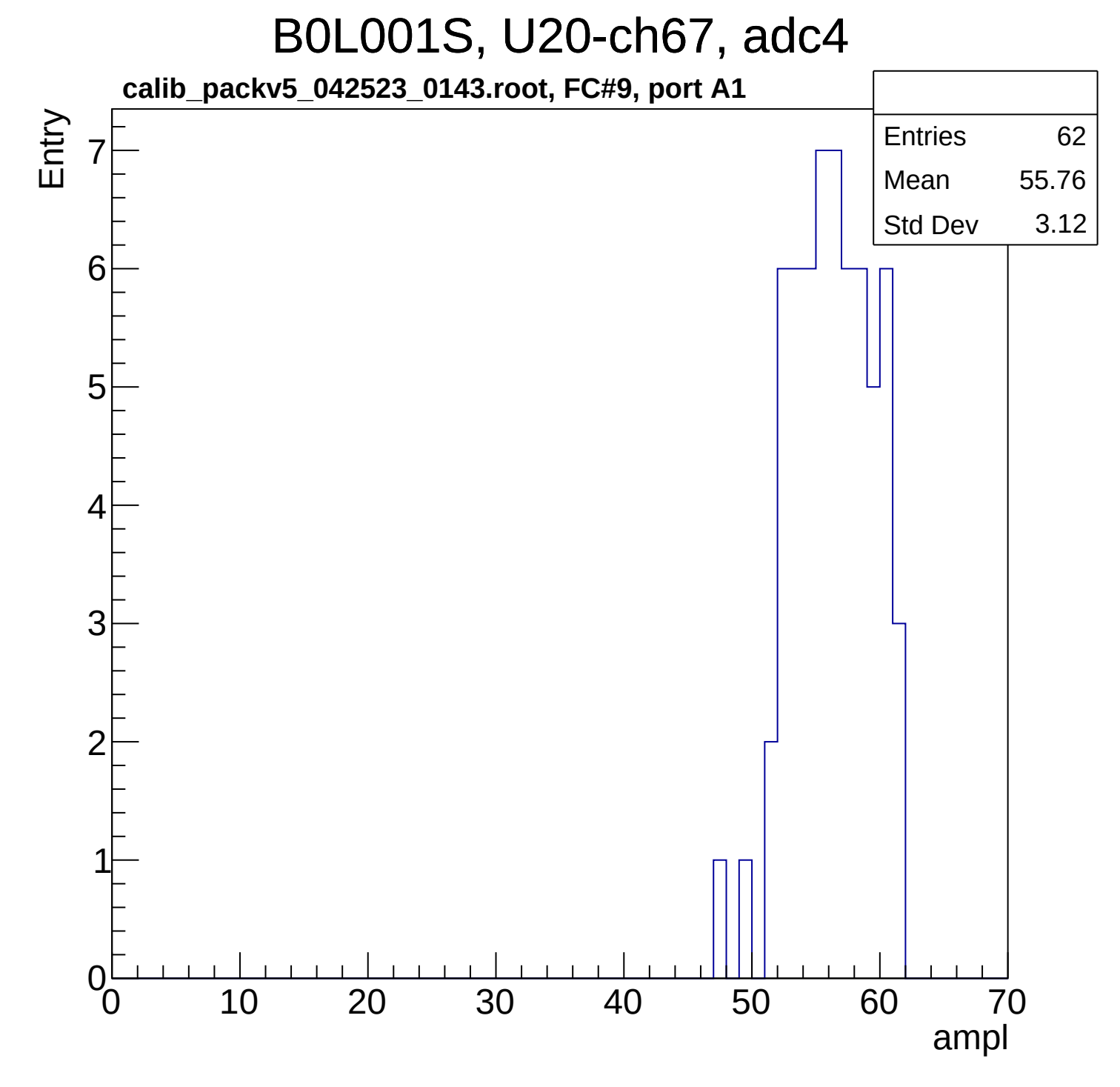
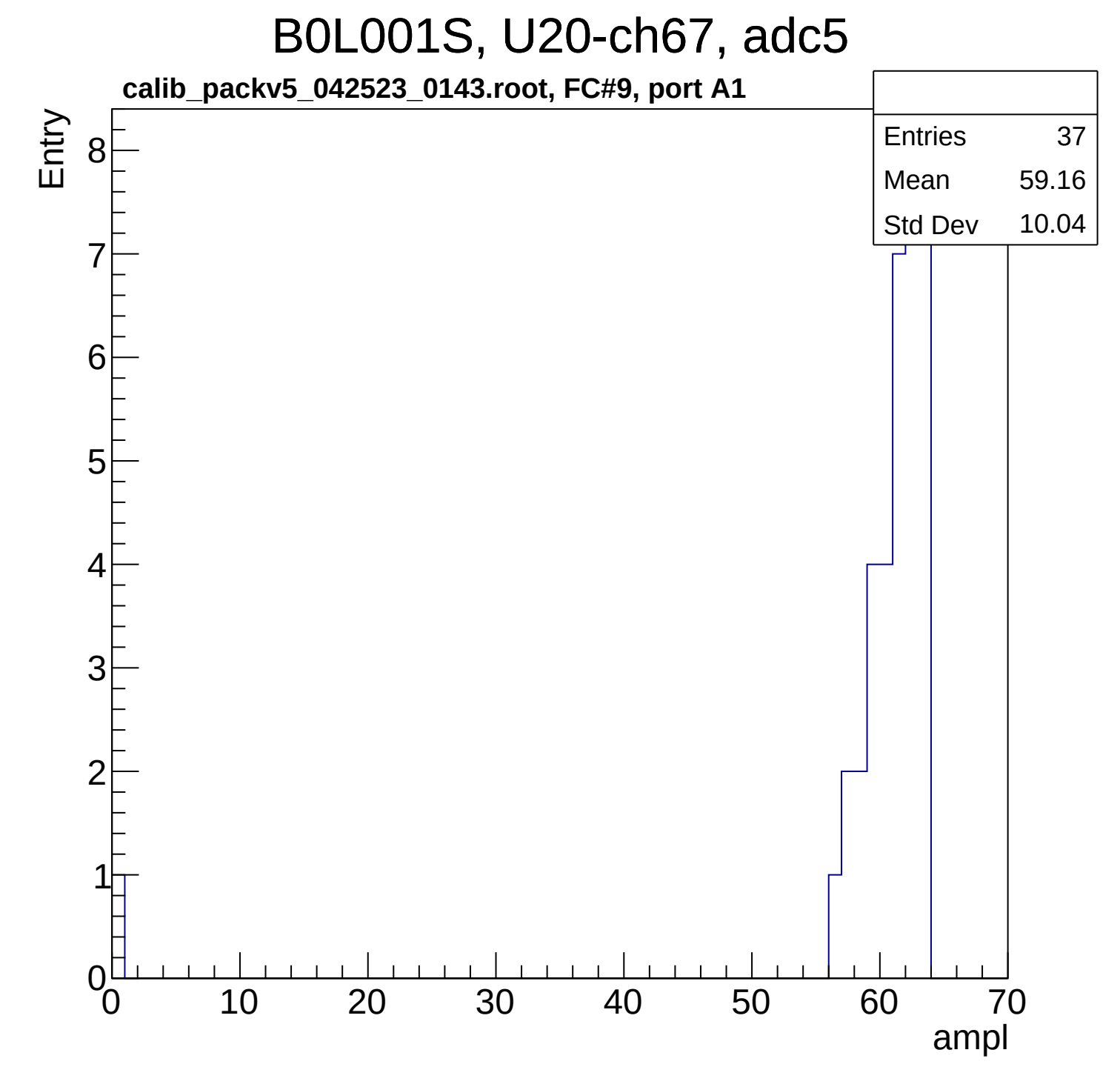
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.76
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.16
Std Dev 10.04
Entry
ampl
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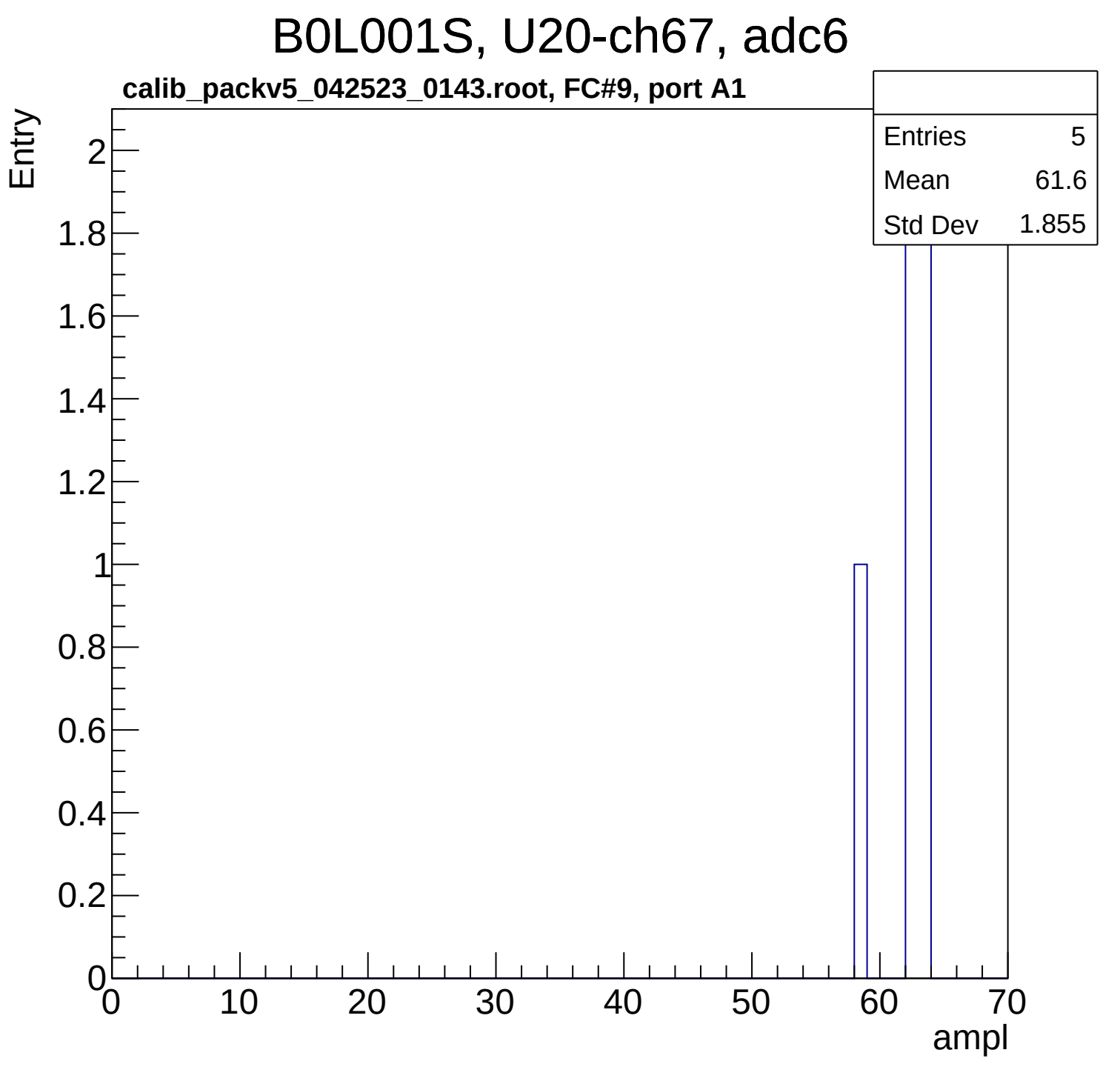
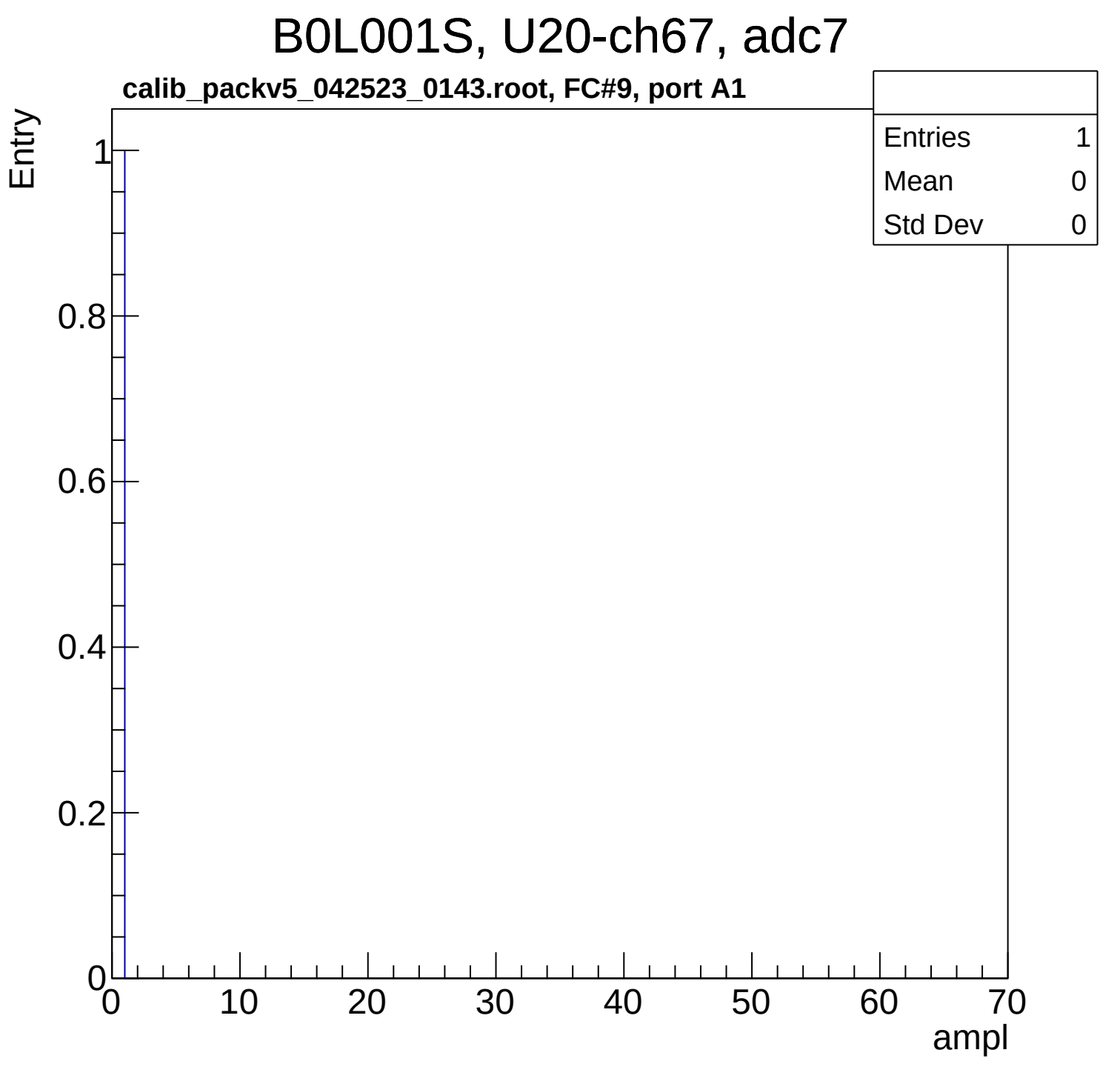
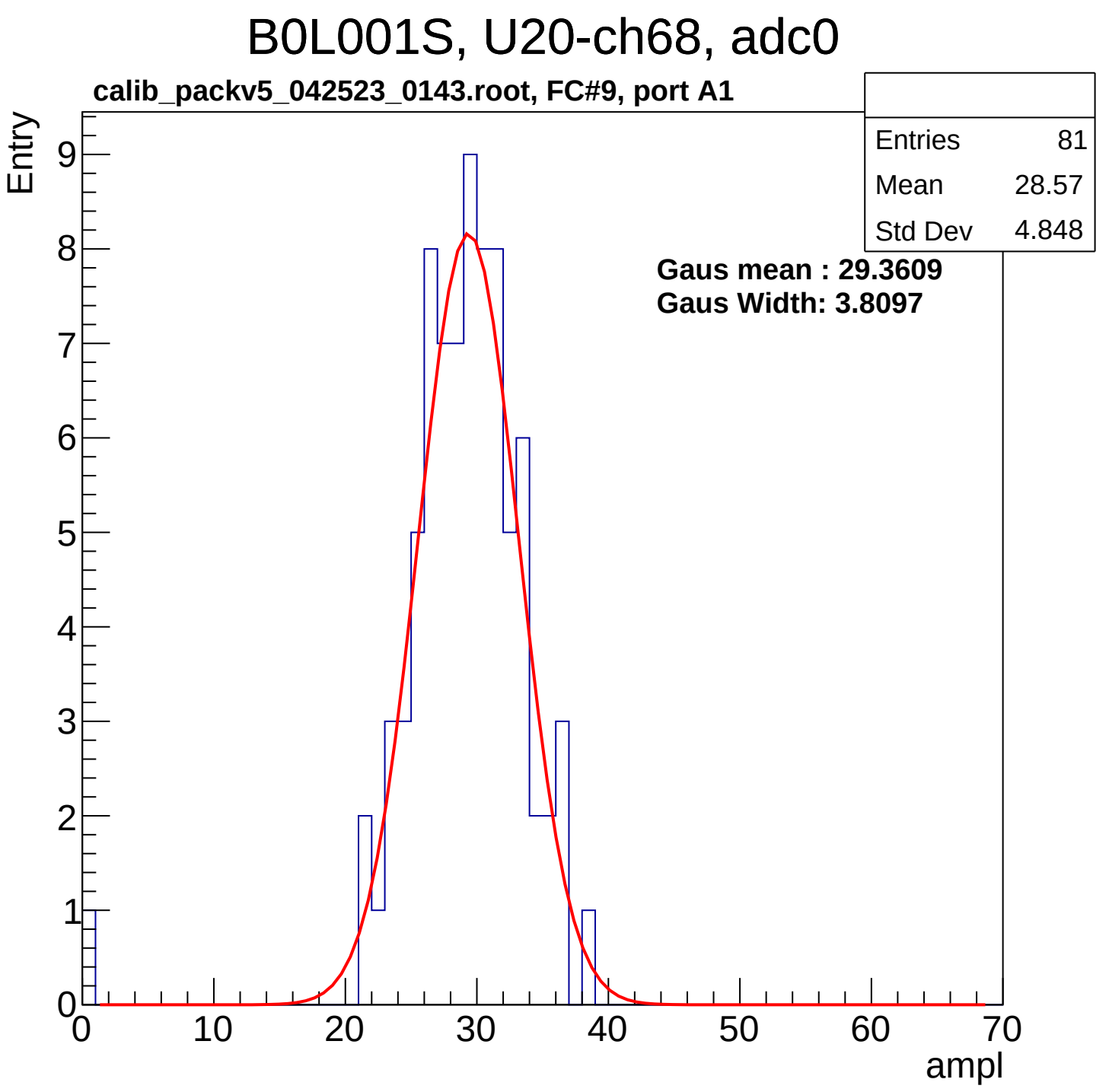
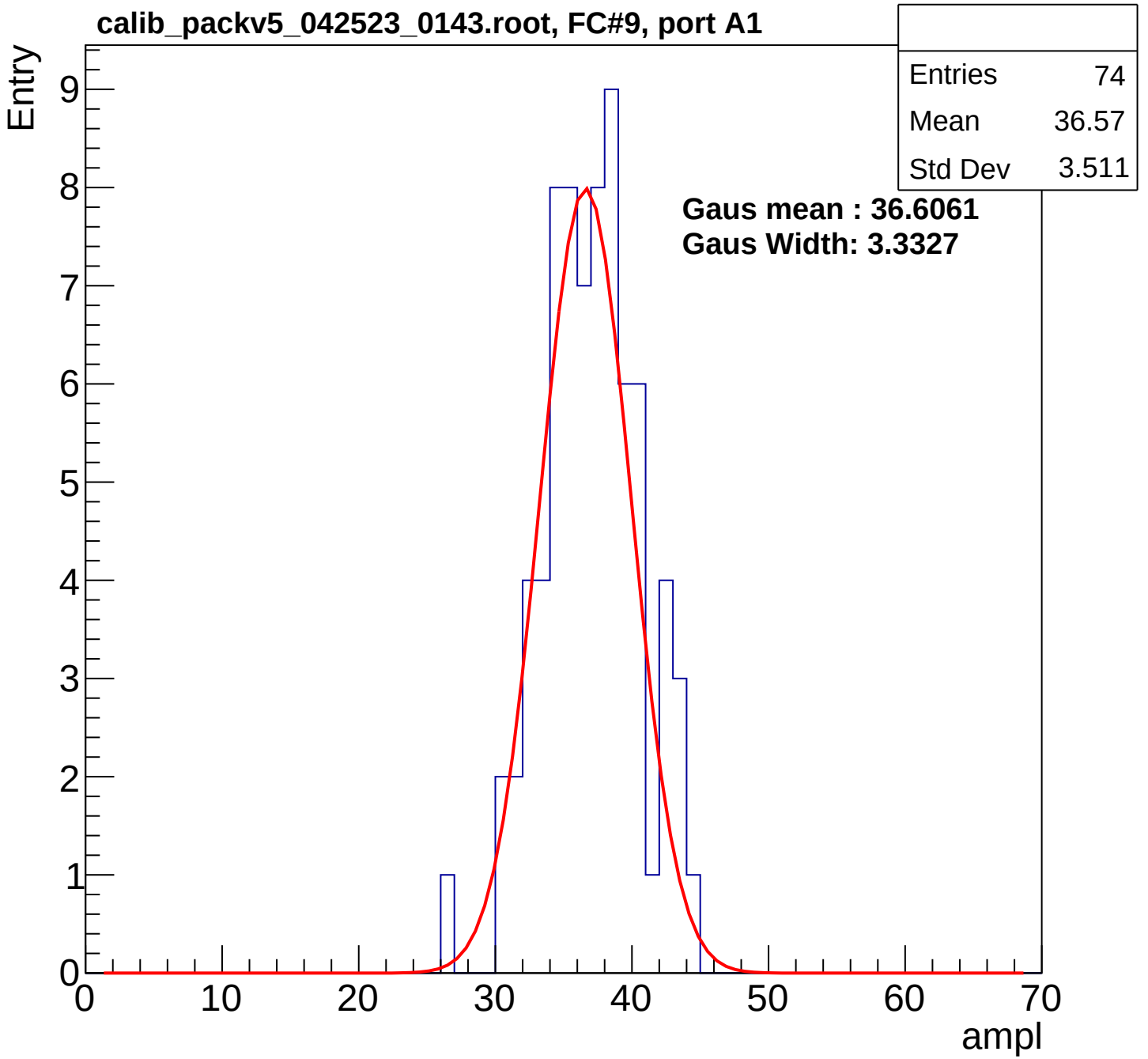
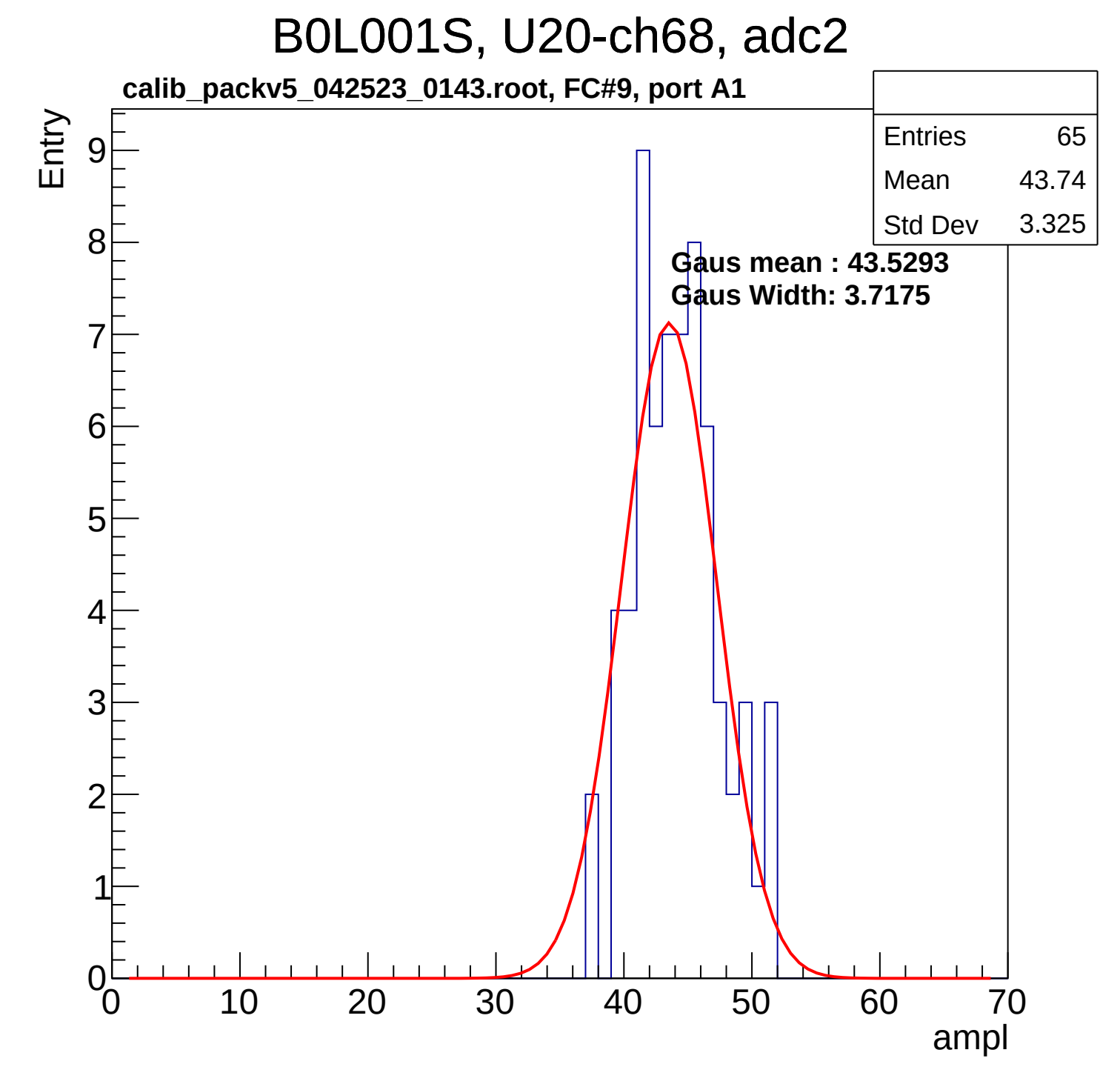
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

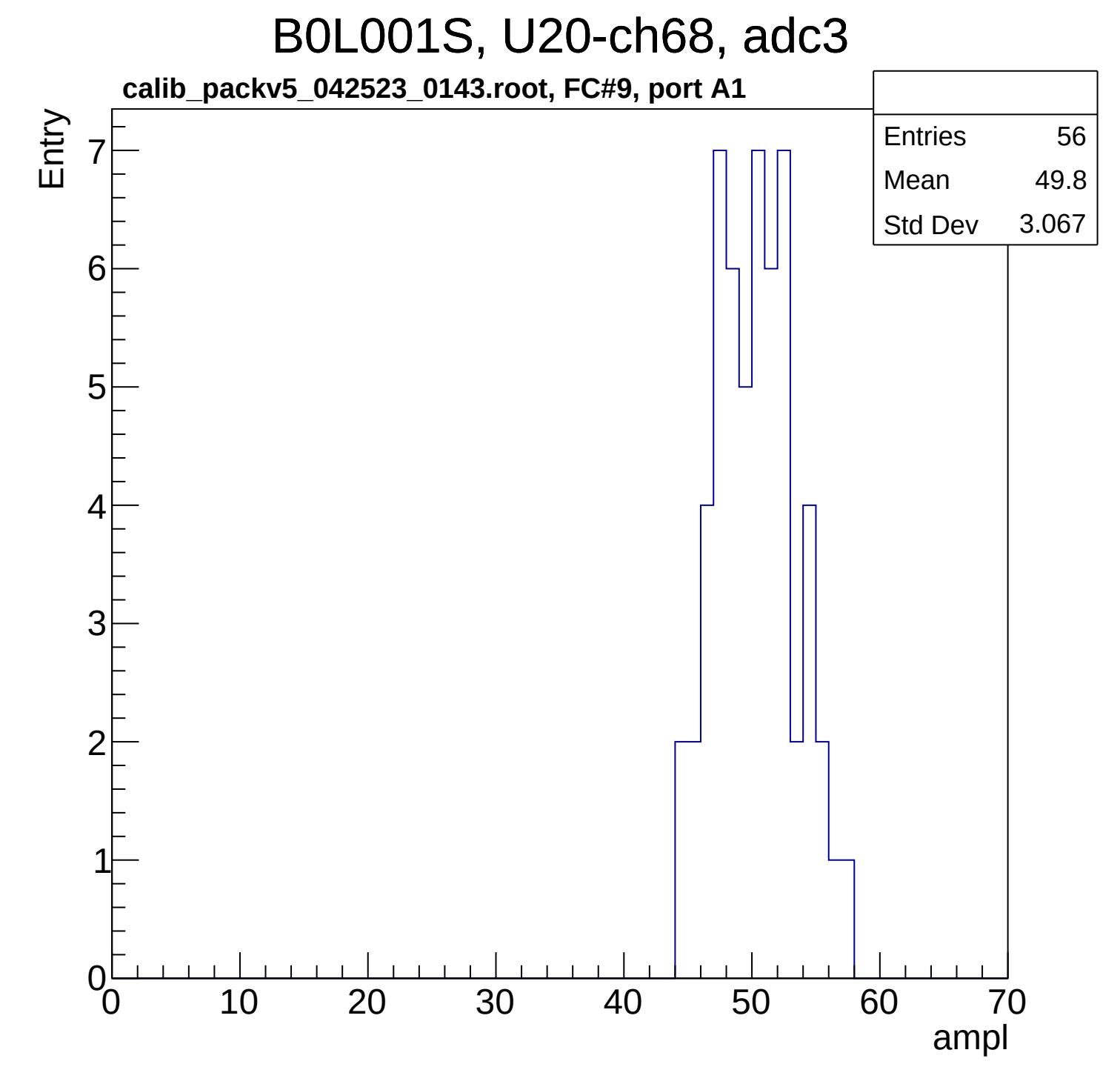

B0L001S, U20-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.6
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 28.57
Std Dev 4.848
Gaus mean : 29.3609
Gaus Width: 3.8097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.57
Std Dev 3.511
Gaus mean : 36.6061
Gaus Width: 3.3327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.74
Std Dev 3.325
Gaus mean : 43.5293
Gaus Width: 3.7175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.8
Std Dev 3.067
Entry
ampl
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3
4
5
6
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0
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20
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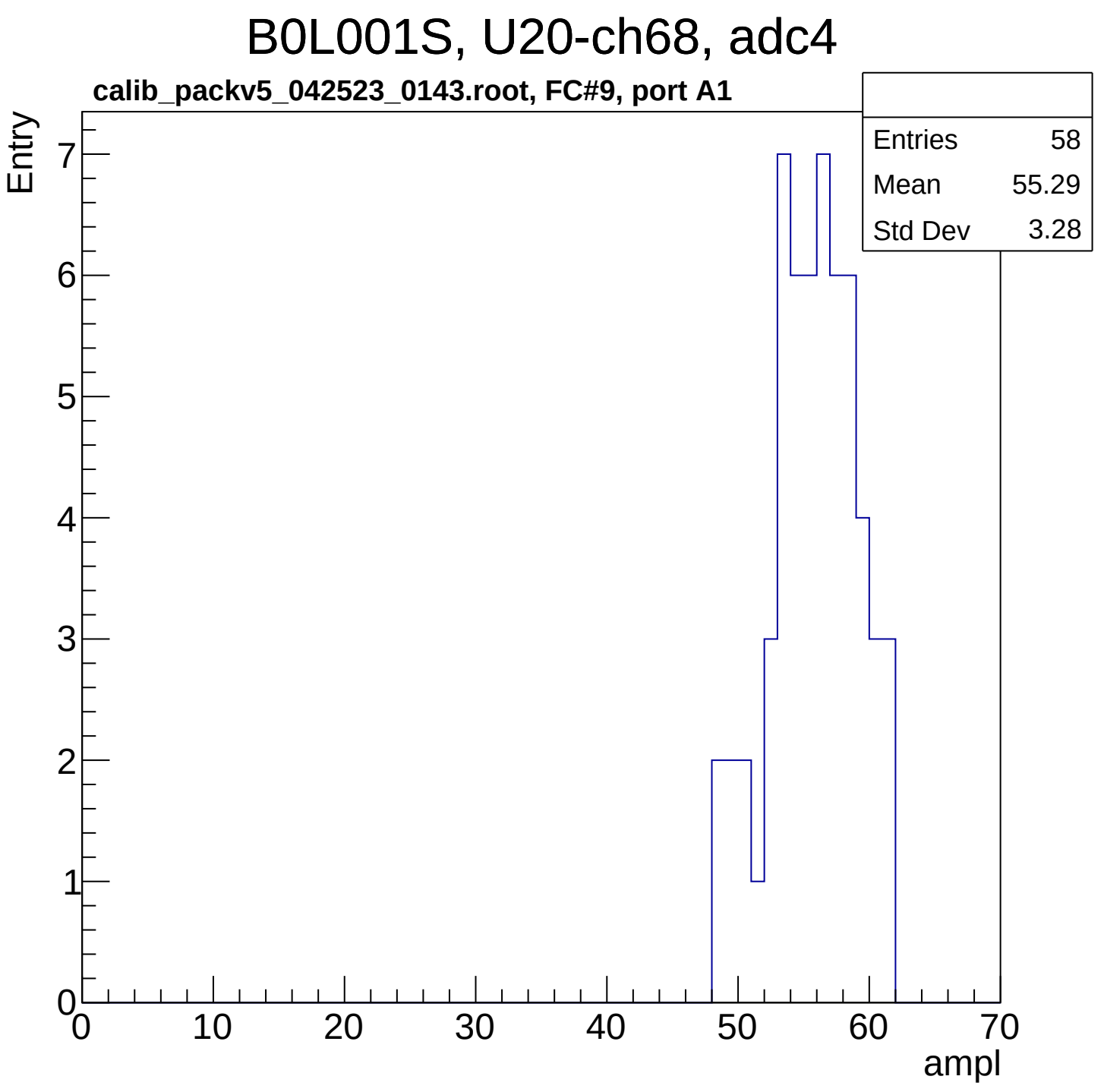
40
50
60
70


B0L001S, U20-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.29
Std Dev 3.28
Entry
ampl
0
1
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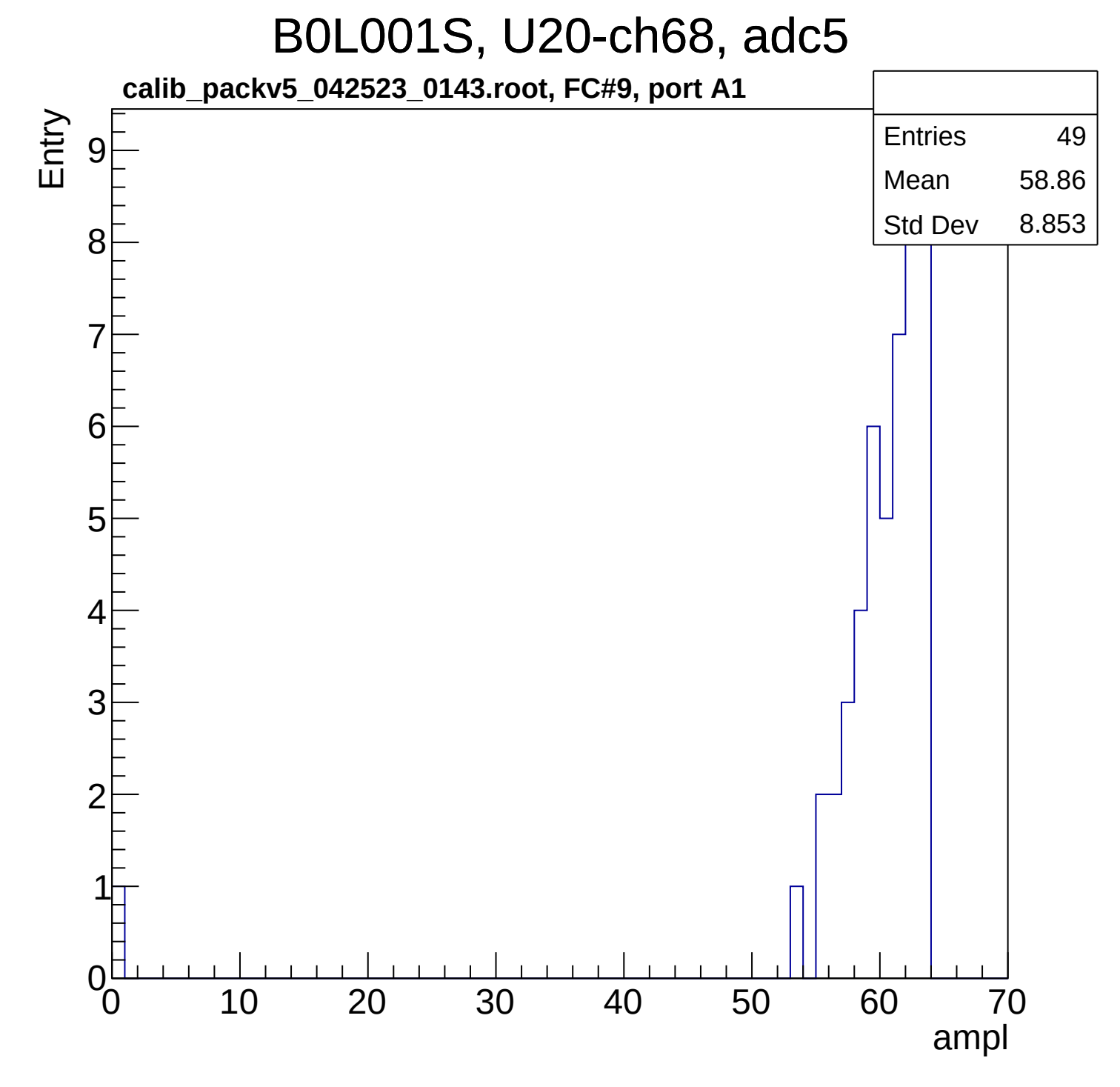
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.86
Std Dev 8.853
Entry
ampl
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3
4
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6
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0
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60
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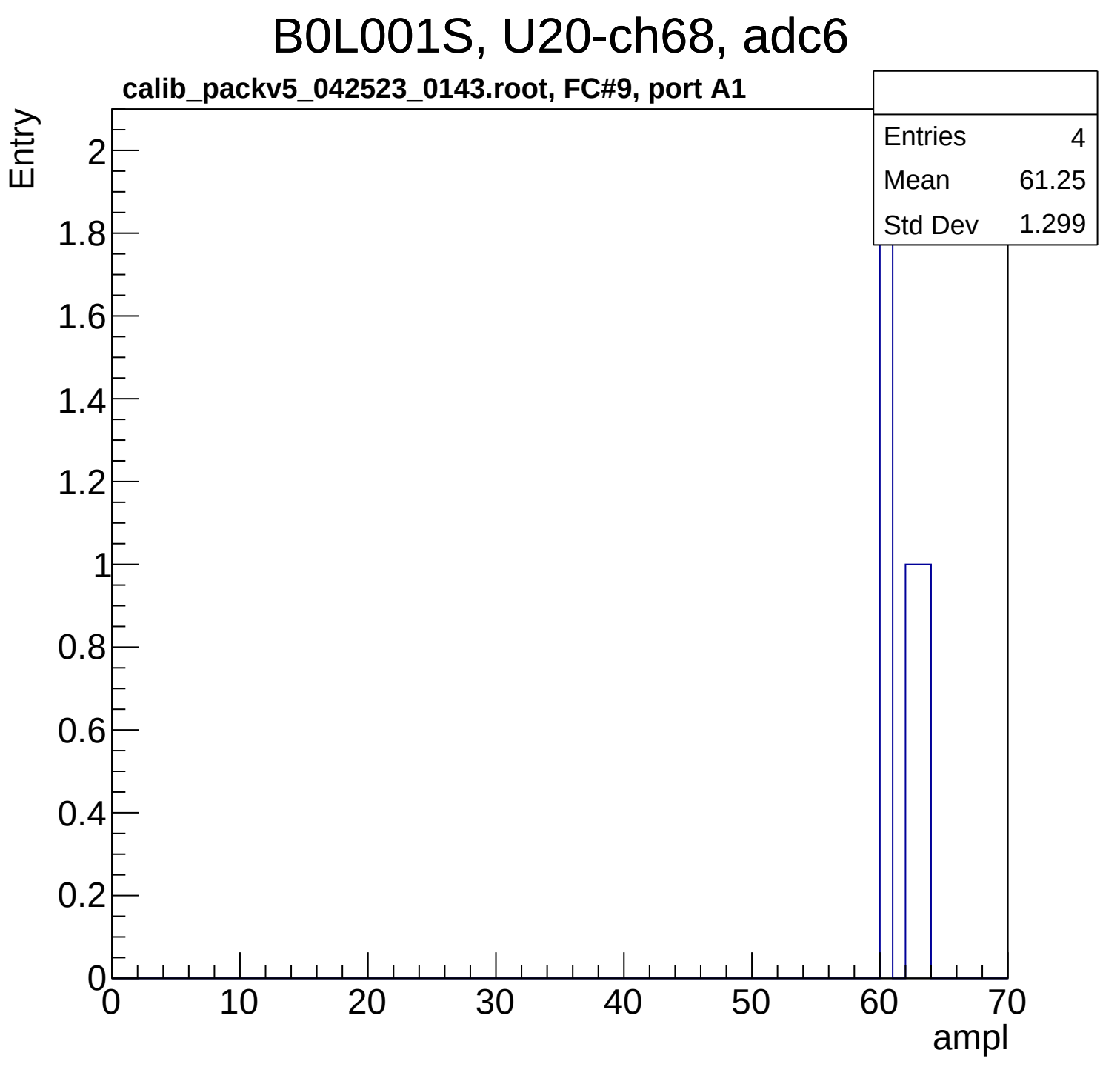
B0L001S, U20-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.25
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
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1.4
1.6
1.8
2
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40
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60
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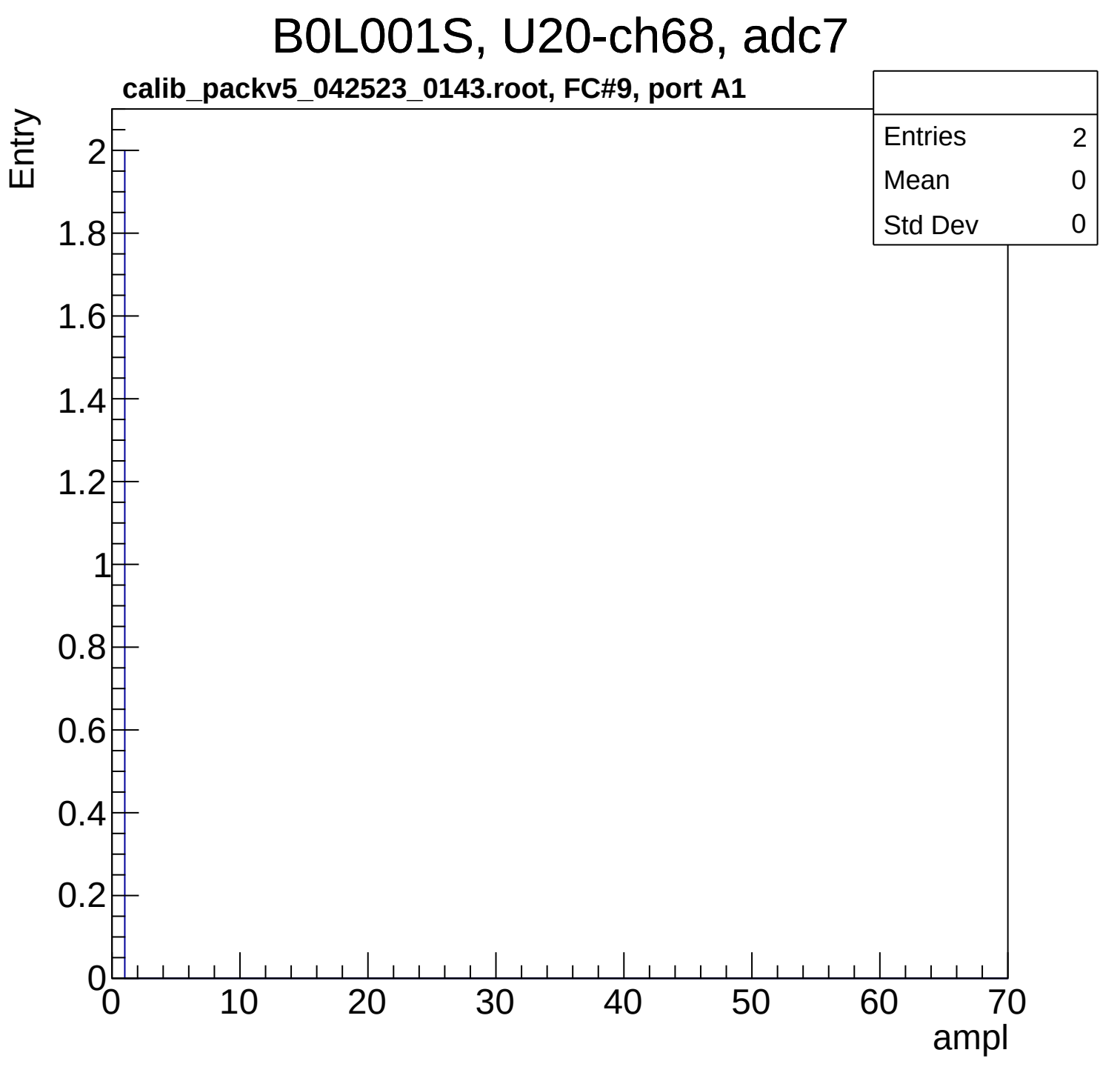
B0L001S, U20-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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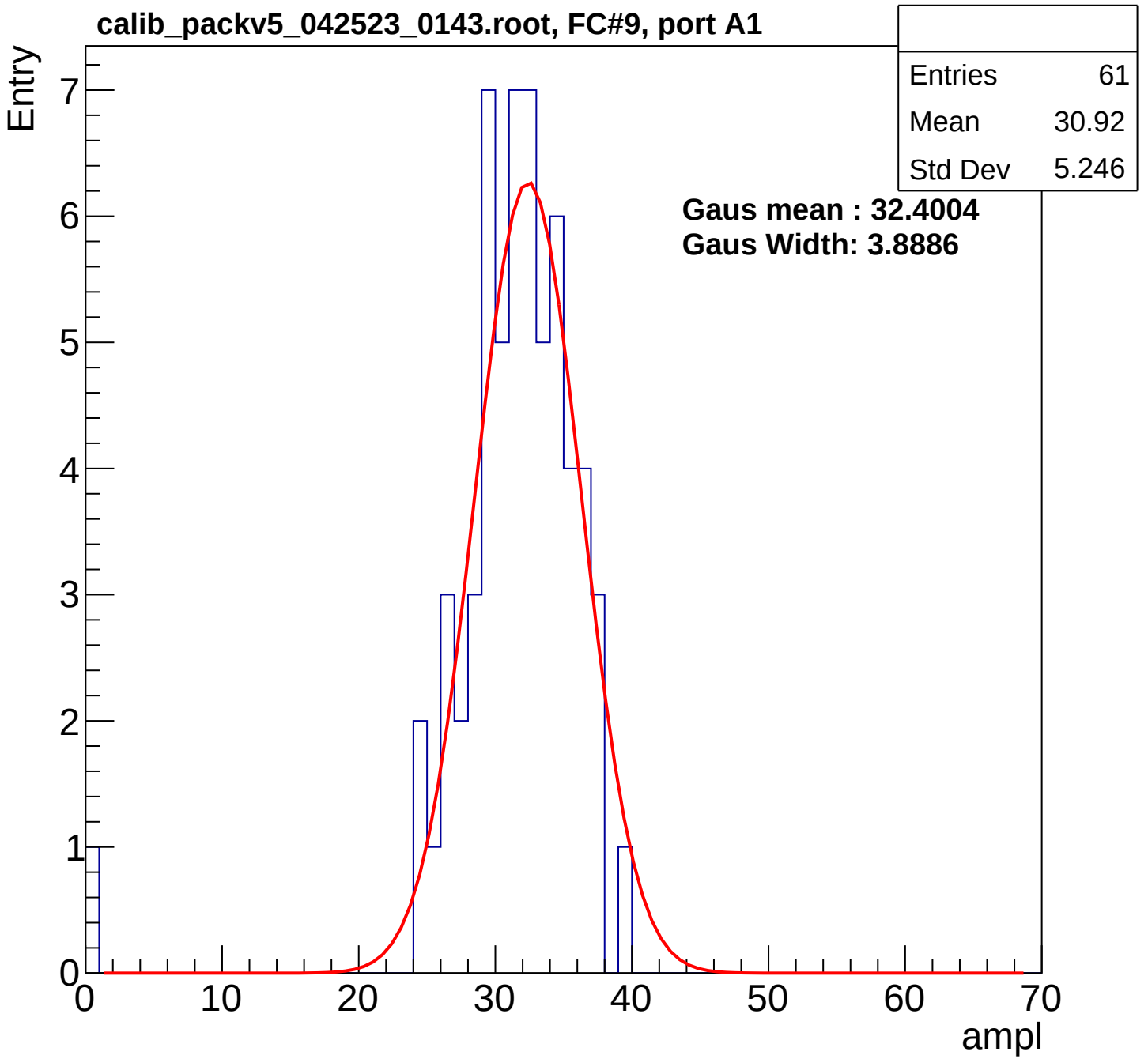
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.92
Std Dev 5.246
Gaus mean : 32.4004
Gaus Width: 3.8886
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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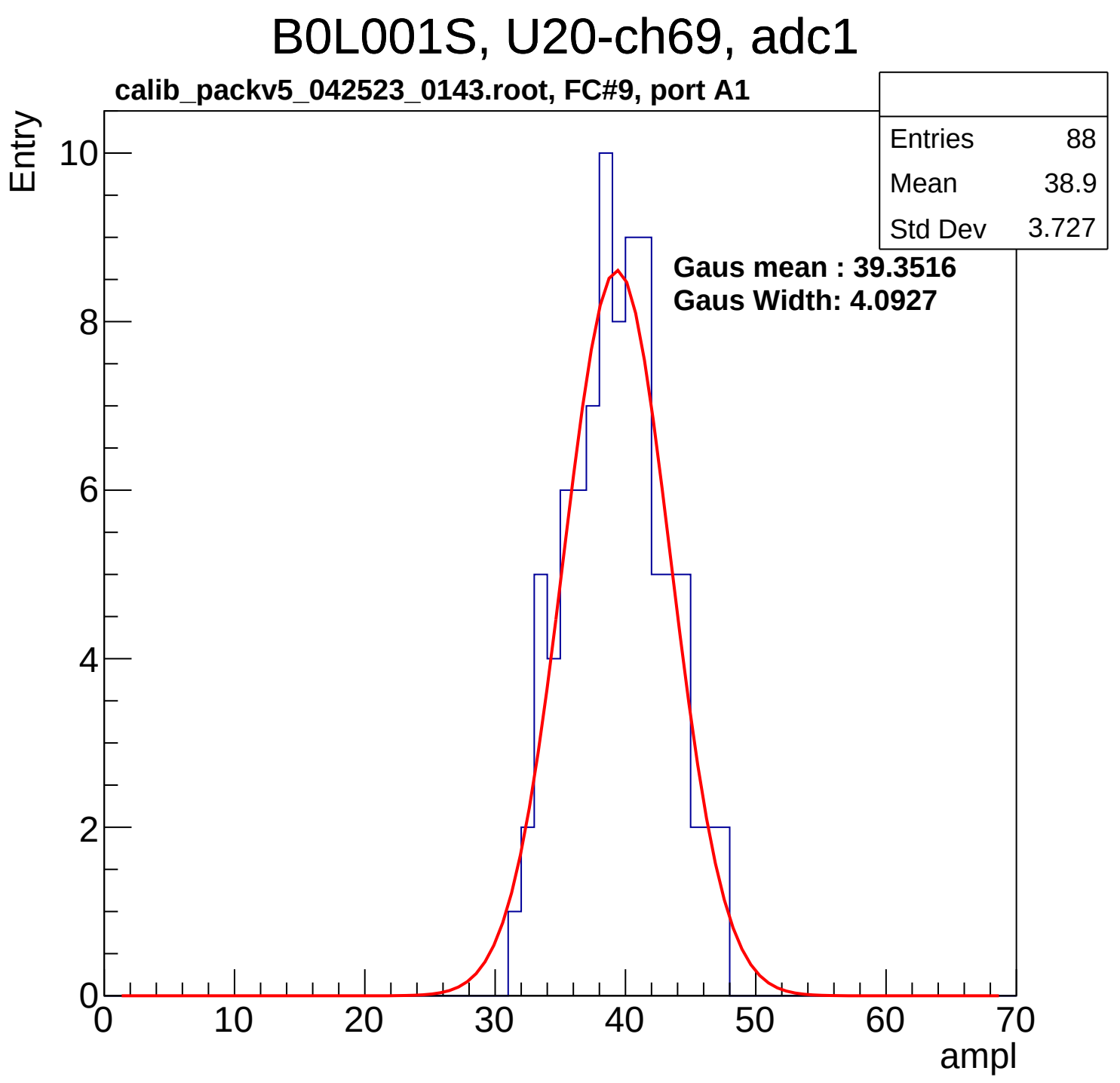
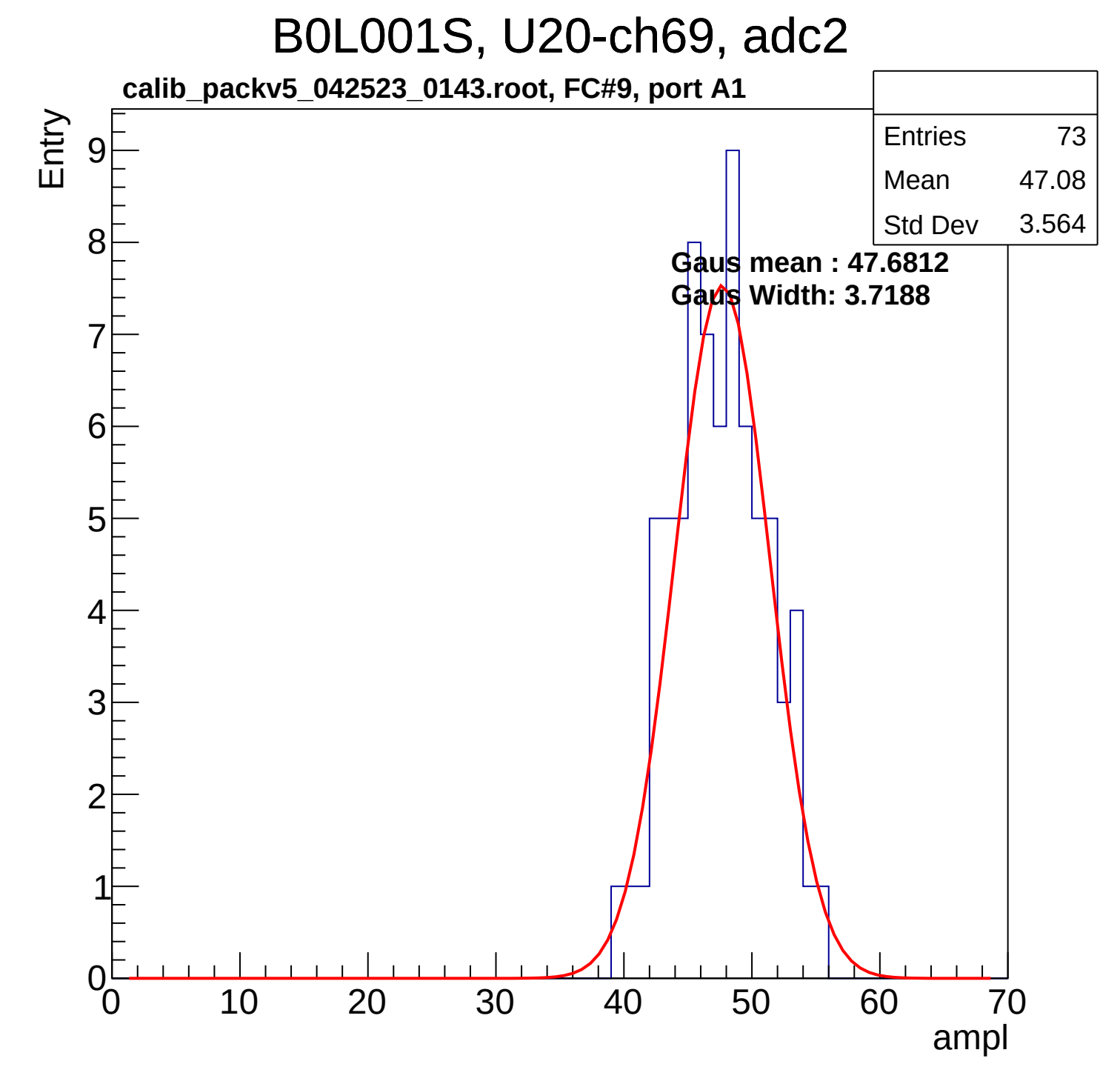
40
50
60
70


B0L001S, U20-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 38.9
Std Dev 3.727
Gaus mean : 39.3516
Gaus Width: 4.0927
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 47.08
Std Dev 3.564
Gaus mean : 47.6812
Gaus Width: 3.7188
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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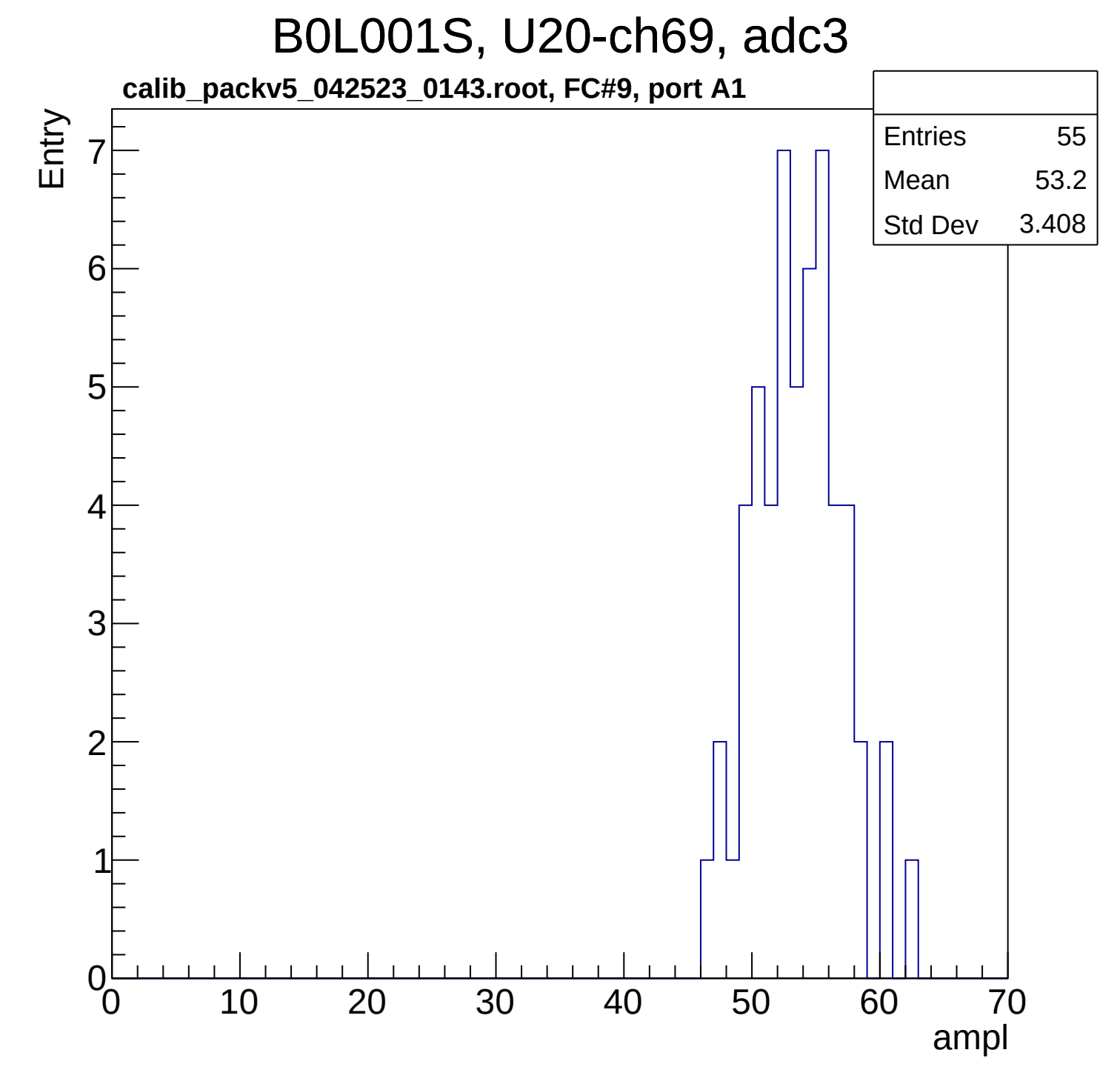
B0L001S, U20-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 53.2
Std Dev 3.408
Entry
ampl
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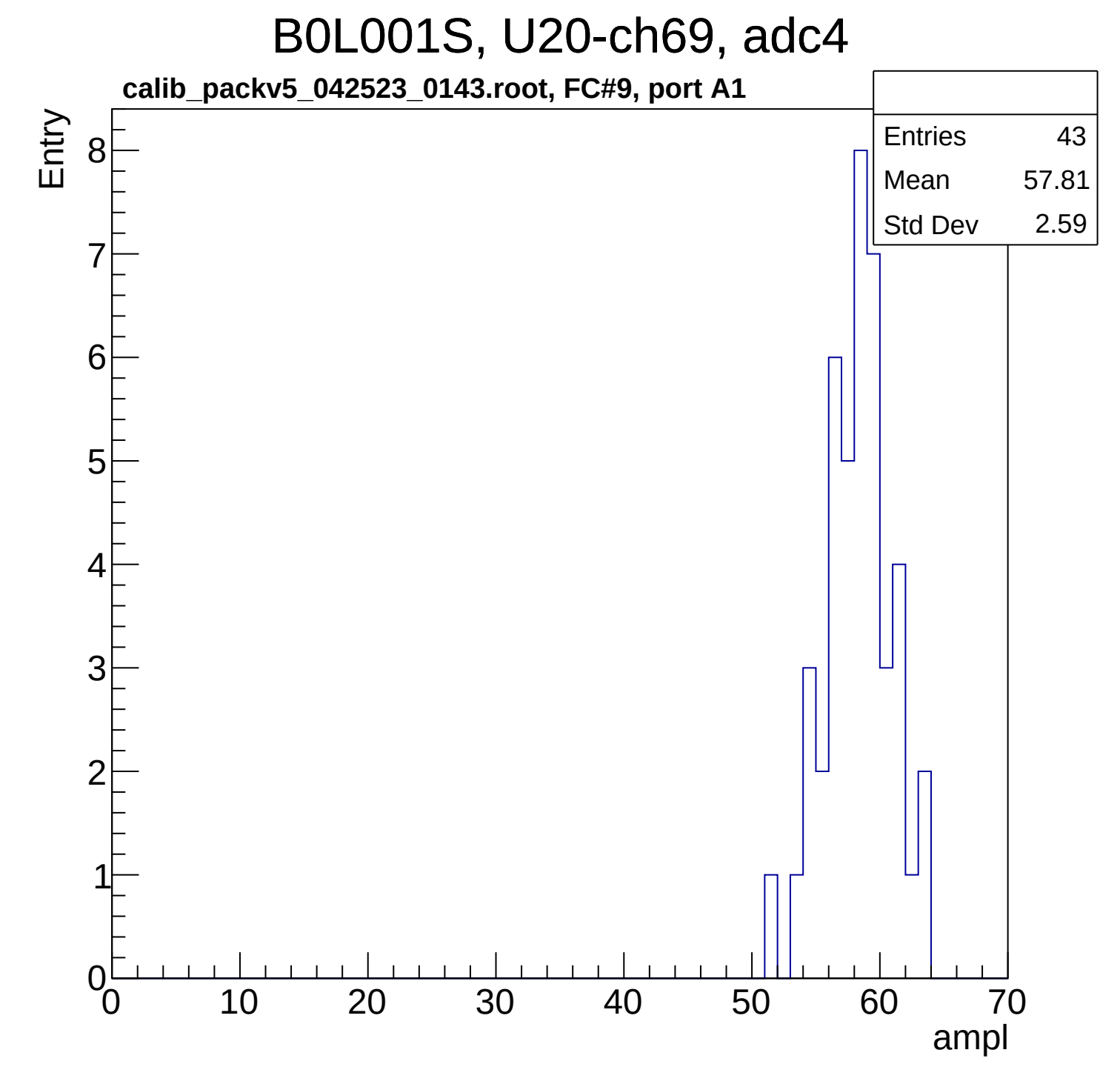
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.81
Std Dev 2.59
Entry
ampl
0
1
2
3
4
5
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7
8
0
10
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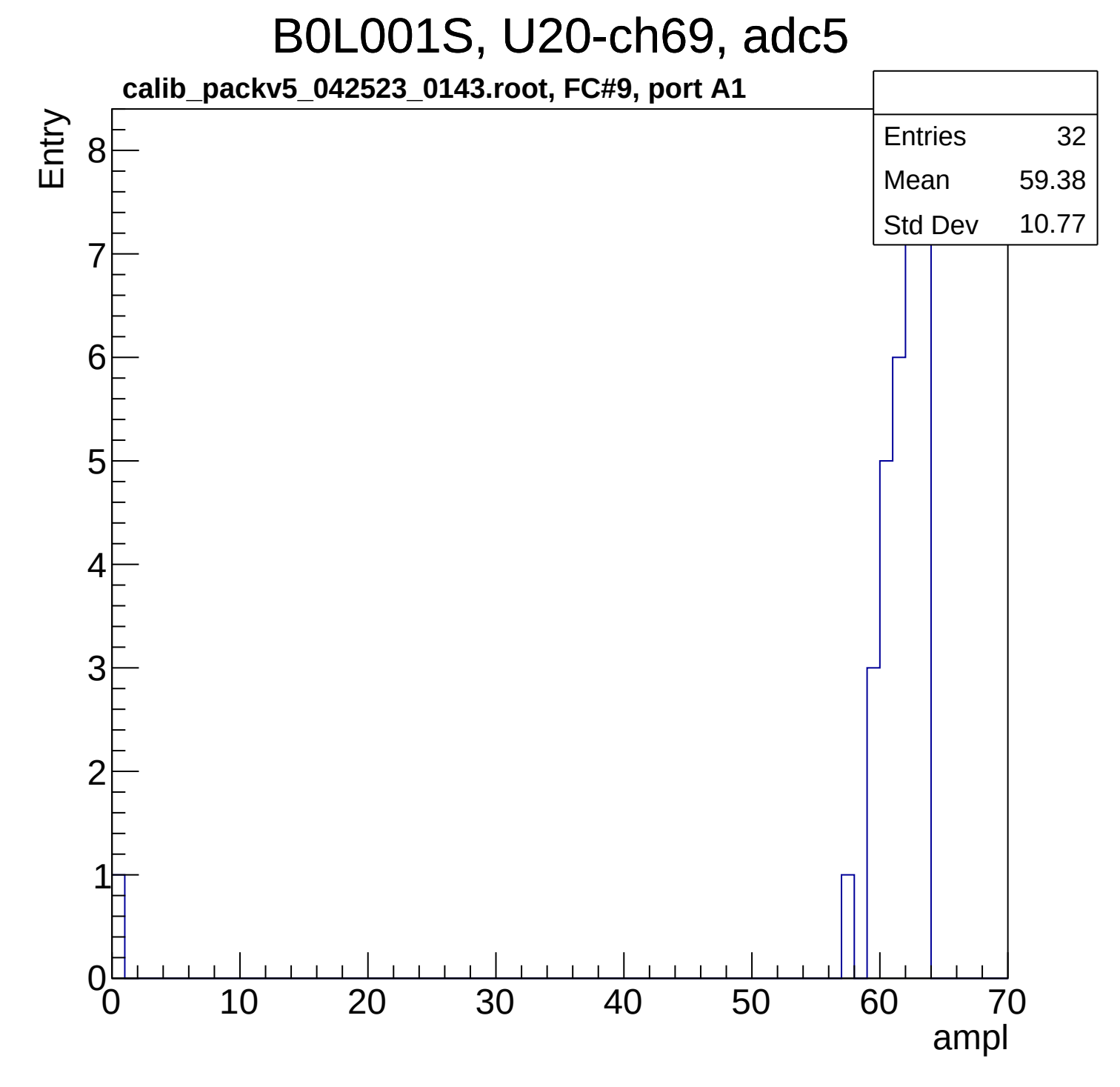
40
50
60
70


B0L001S, U20-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.38
Std Dev 10.77
Entry
ampl
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1
2
3
4
5
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0
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60
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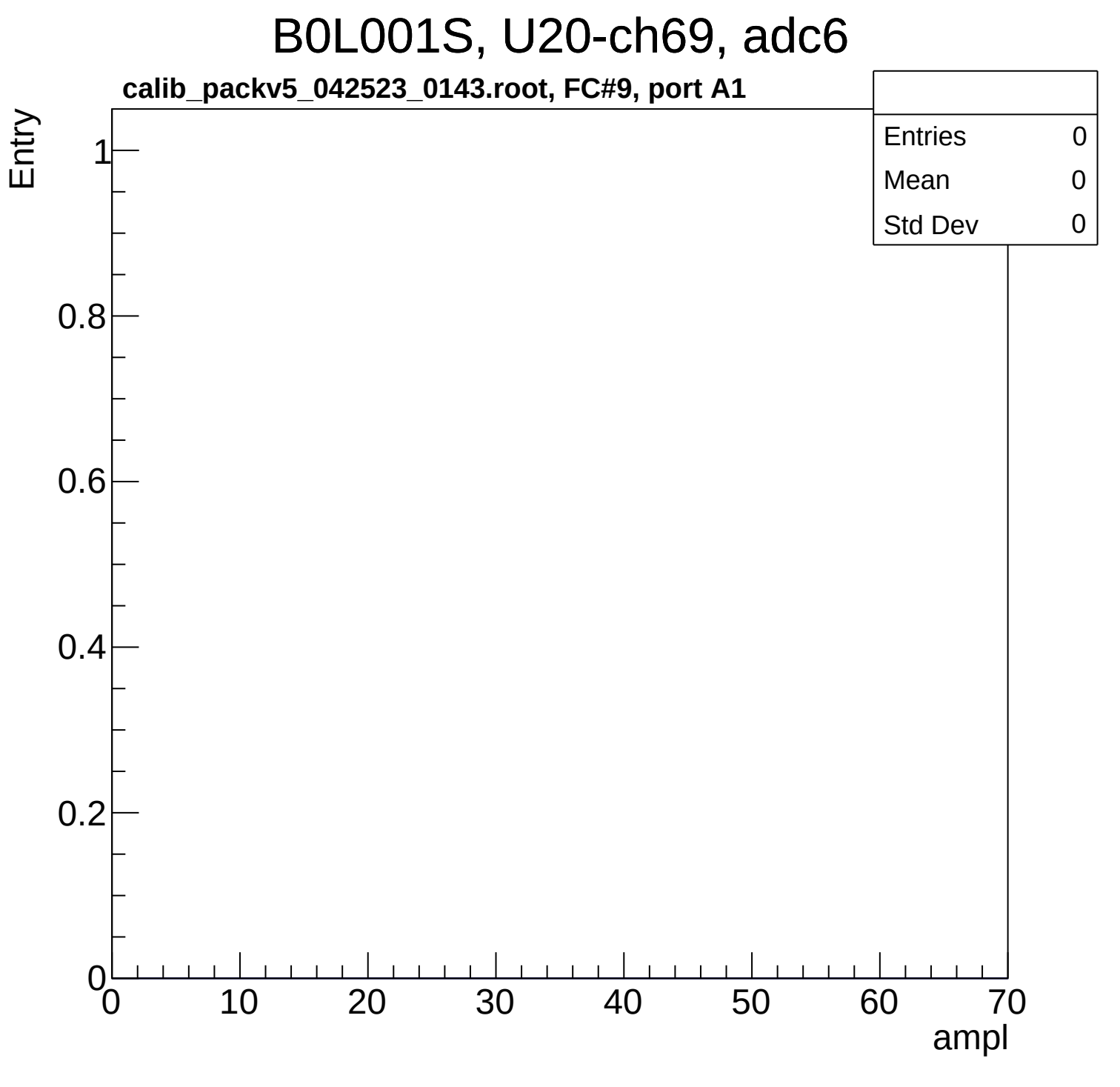
B0L001S, U20-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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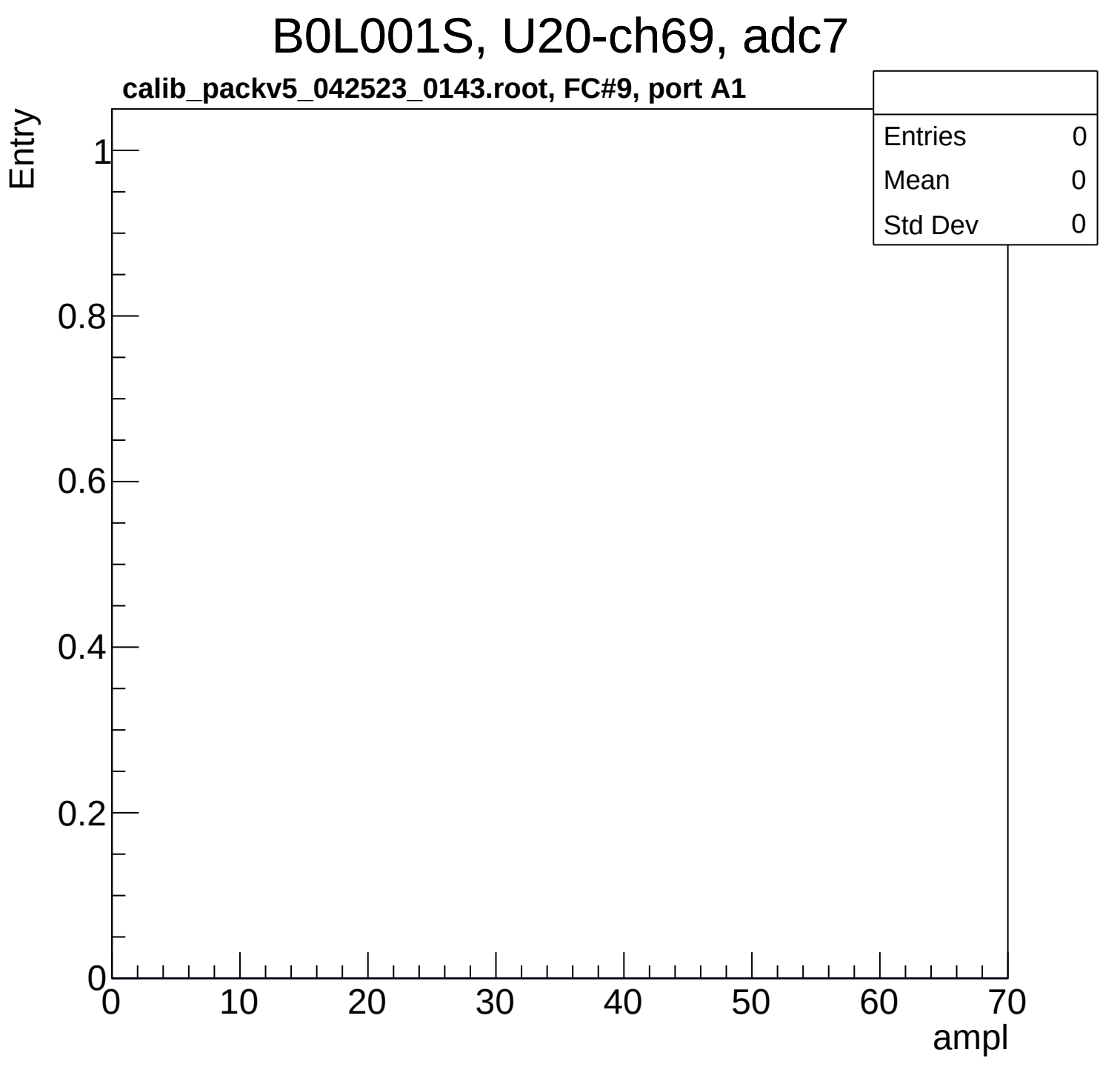
30
40
50
60
70
ampl


B0L001S, U20-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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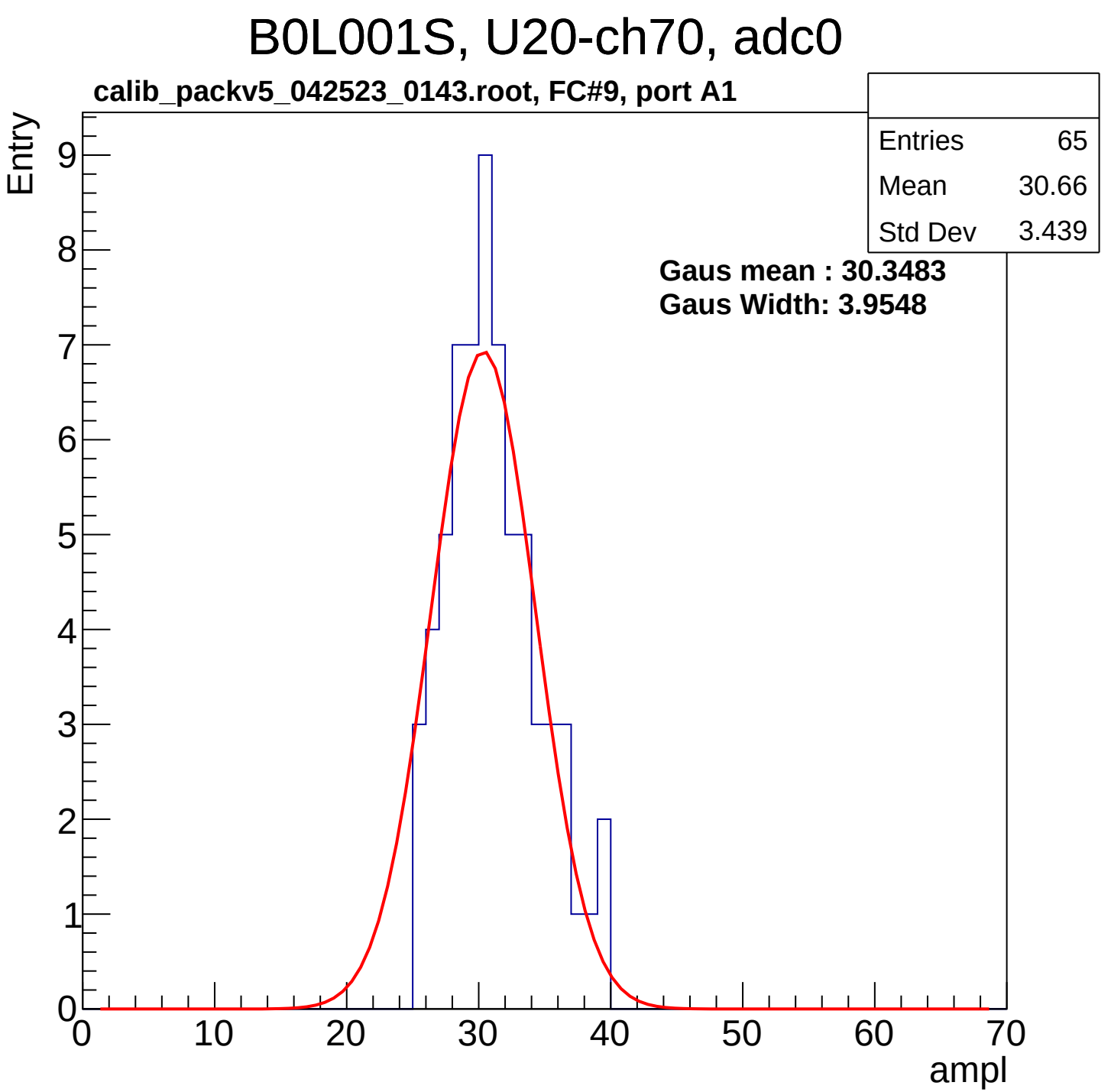
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.66
Std Dev 3.439
Gaus mean : 30.3483
Gaus Width: 3.9548
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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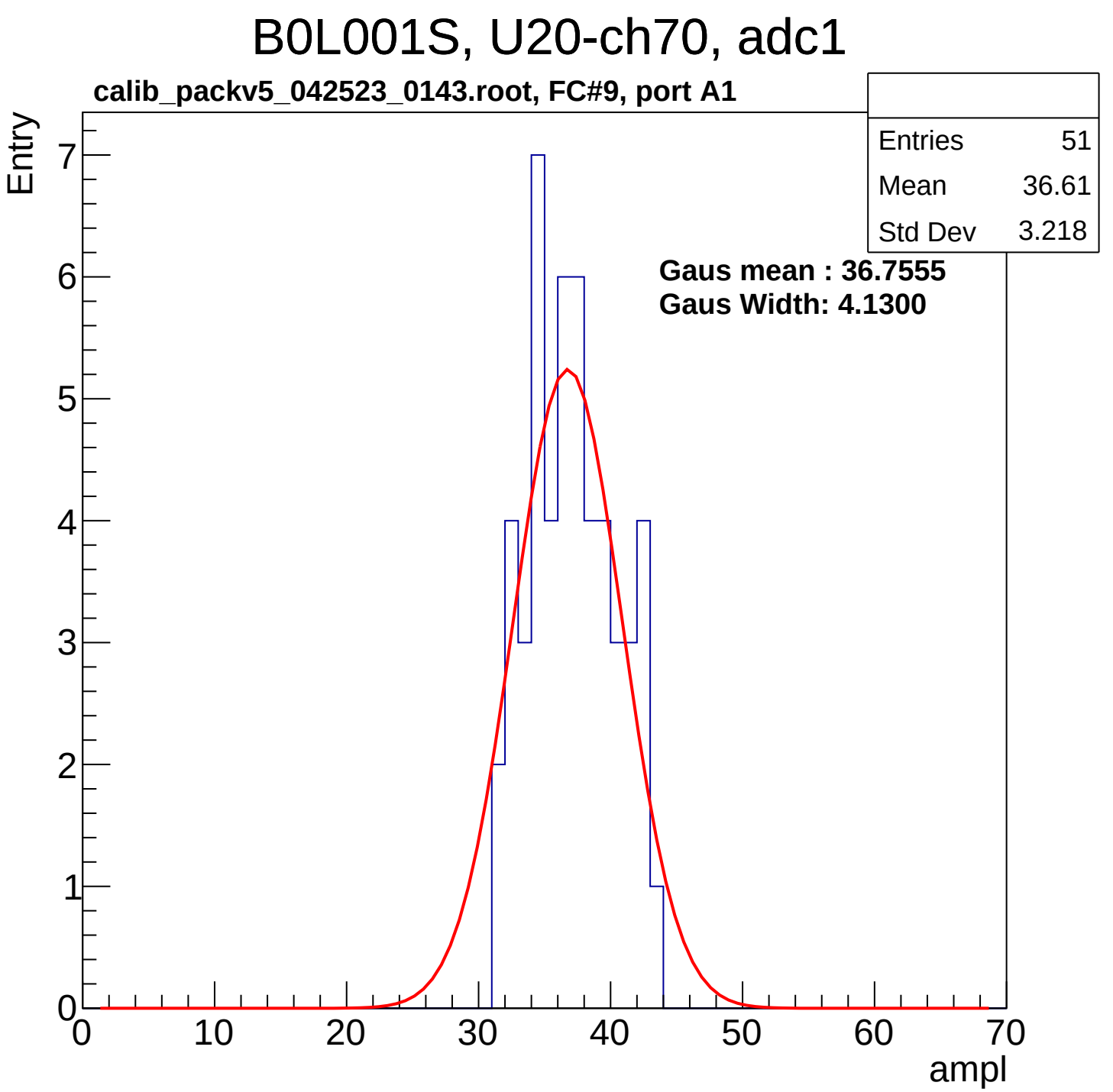
B0L001S, U20-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 36.61
Std Dev 3.218
Gaus mean : 36.7555
Gaus Width: 4.1300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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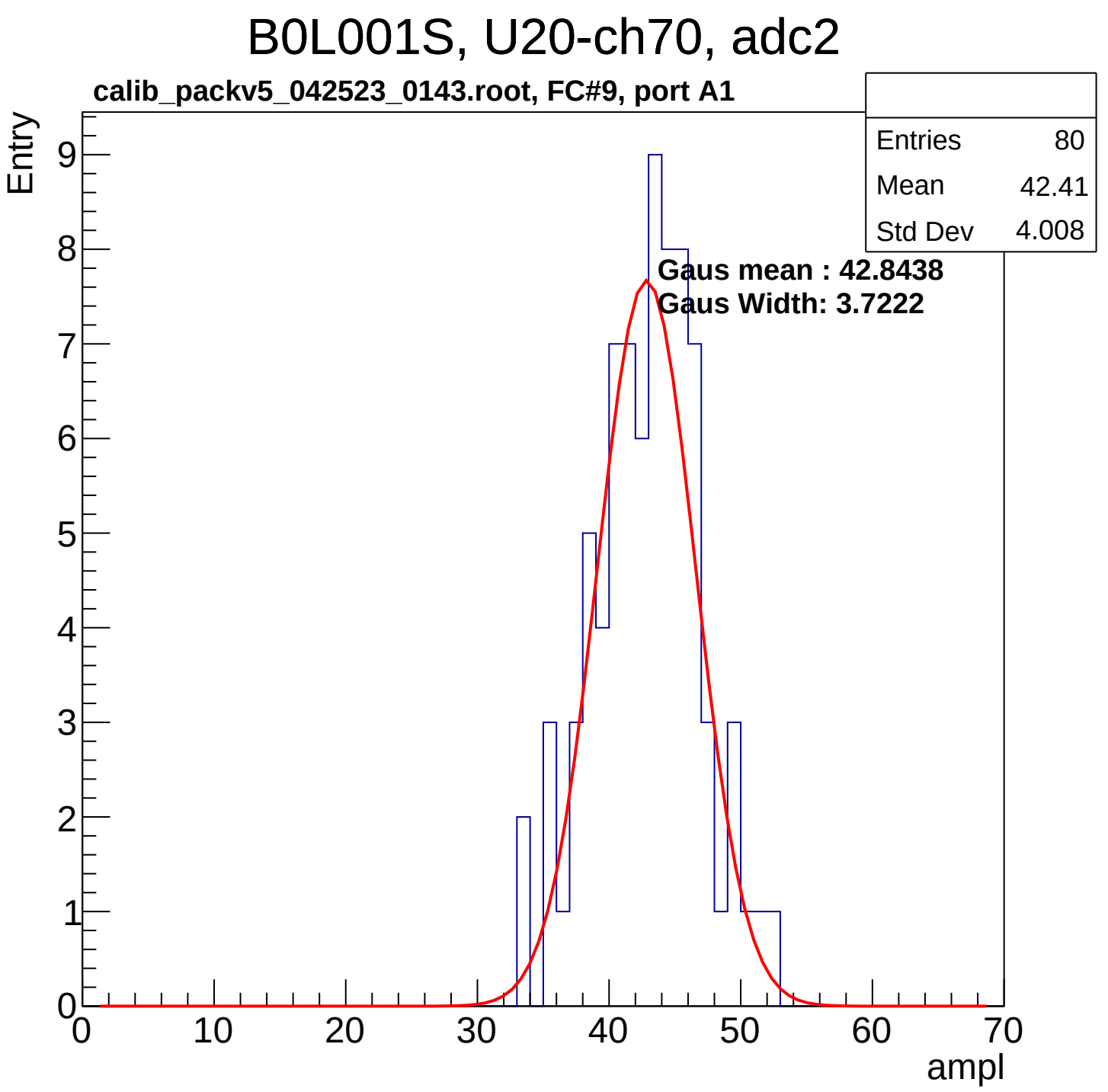
B0L001S, U20-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 42.41
Std Dev 4.008
Gaus mean : 42.8438
Gaus Width: 3.7222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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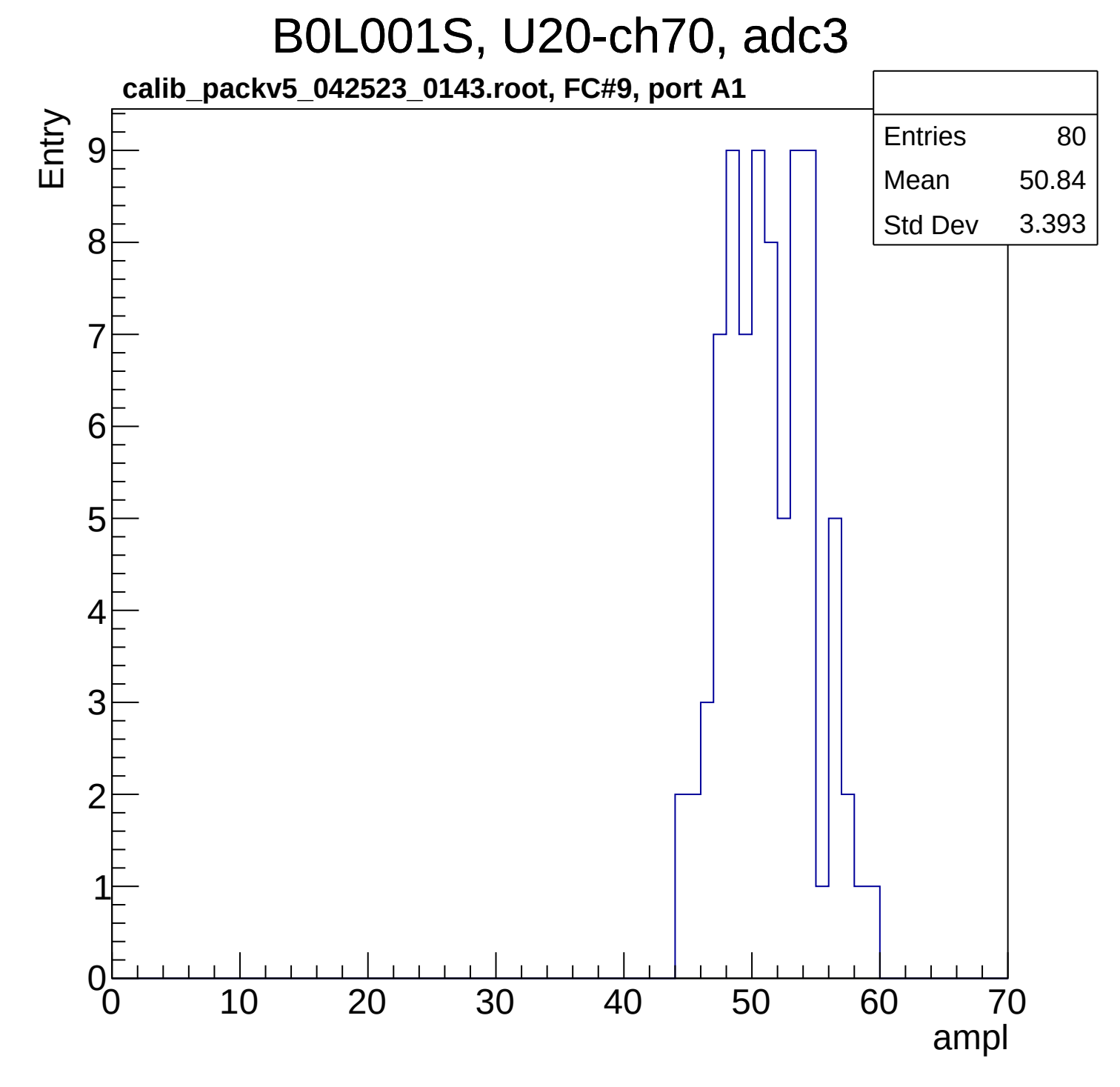
B0L001S, U20-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 50.84
Std Dev 3.393
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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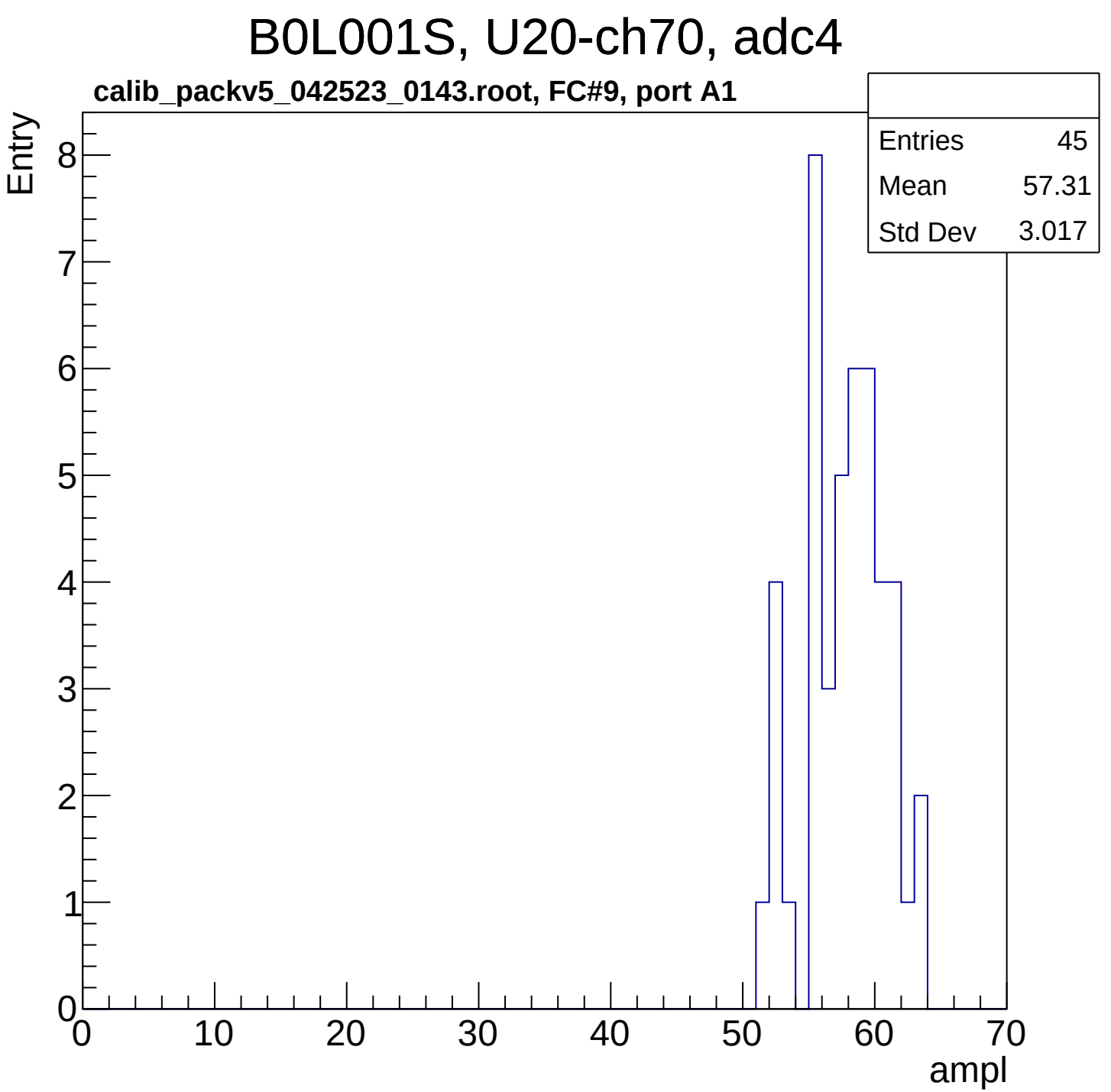
40
50
60
70


B0L001S, U20-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 57.31
Std Dev 3.017
Entry
ampl
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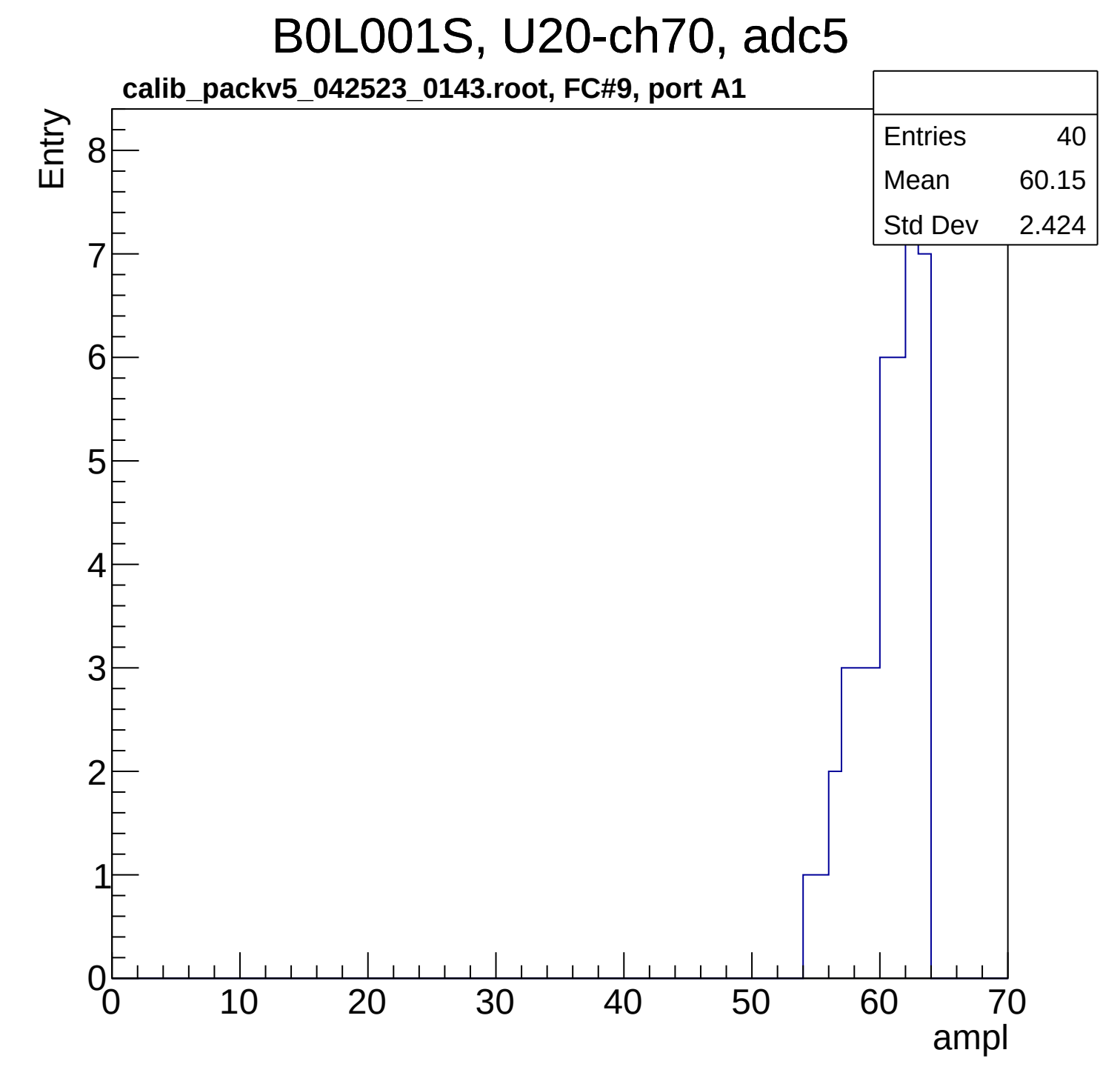
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.15
Std Dev 2.424
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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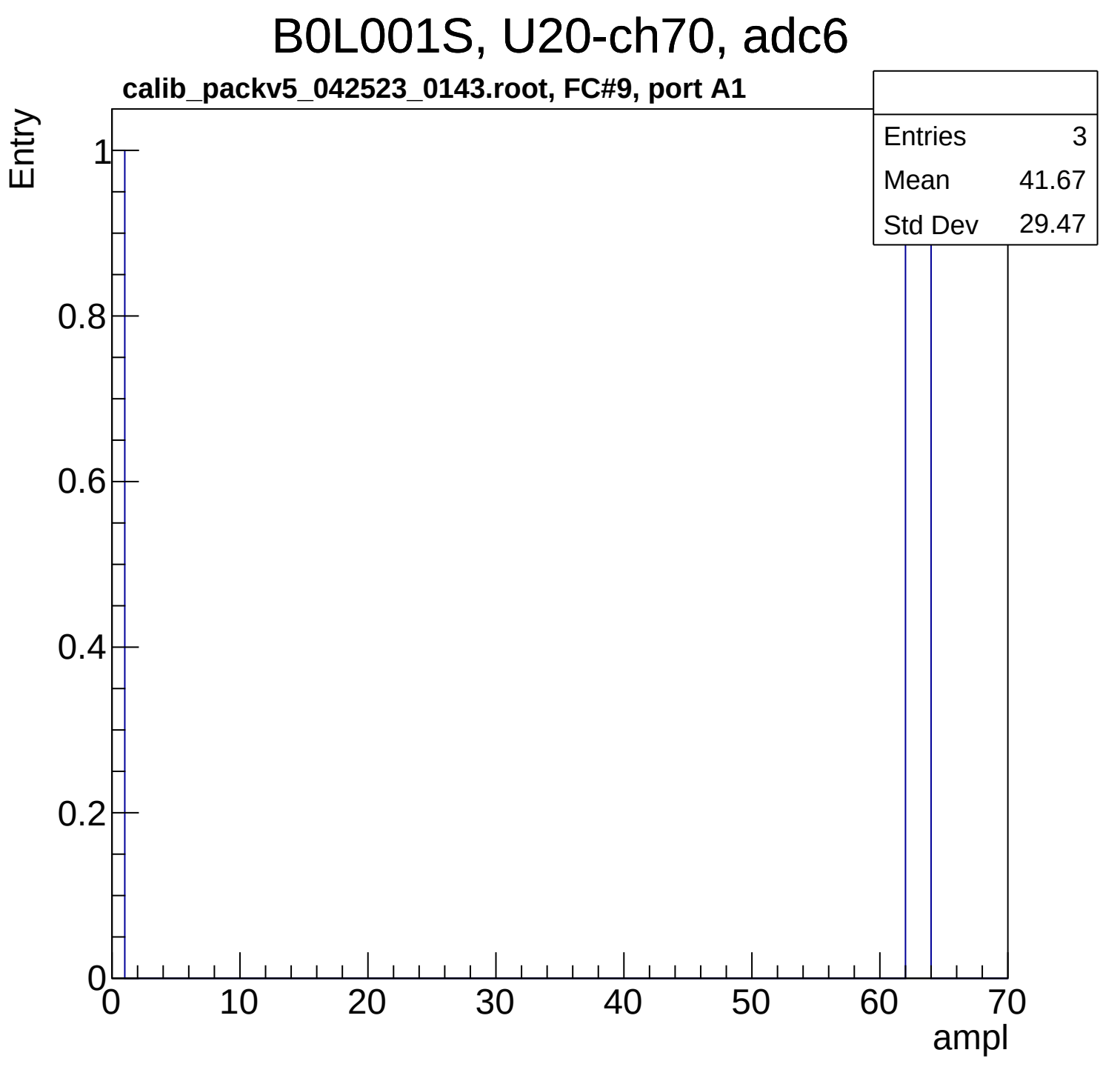
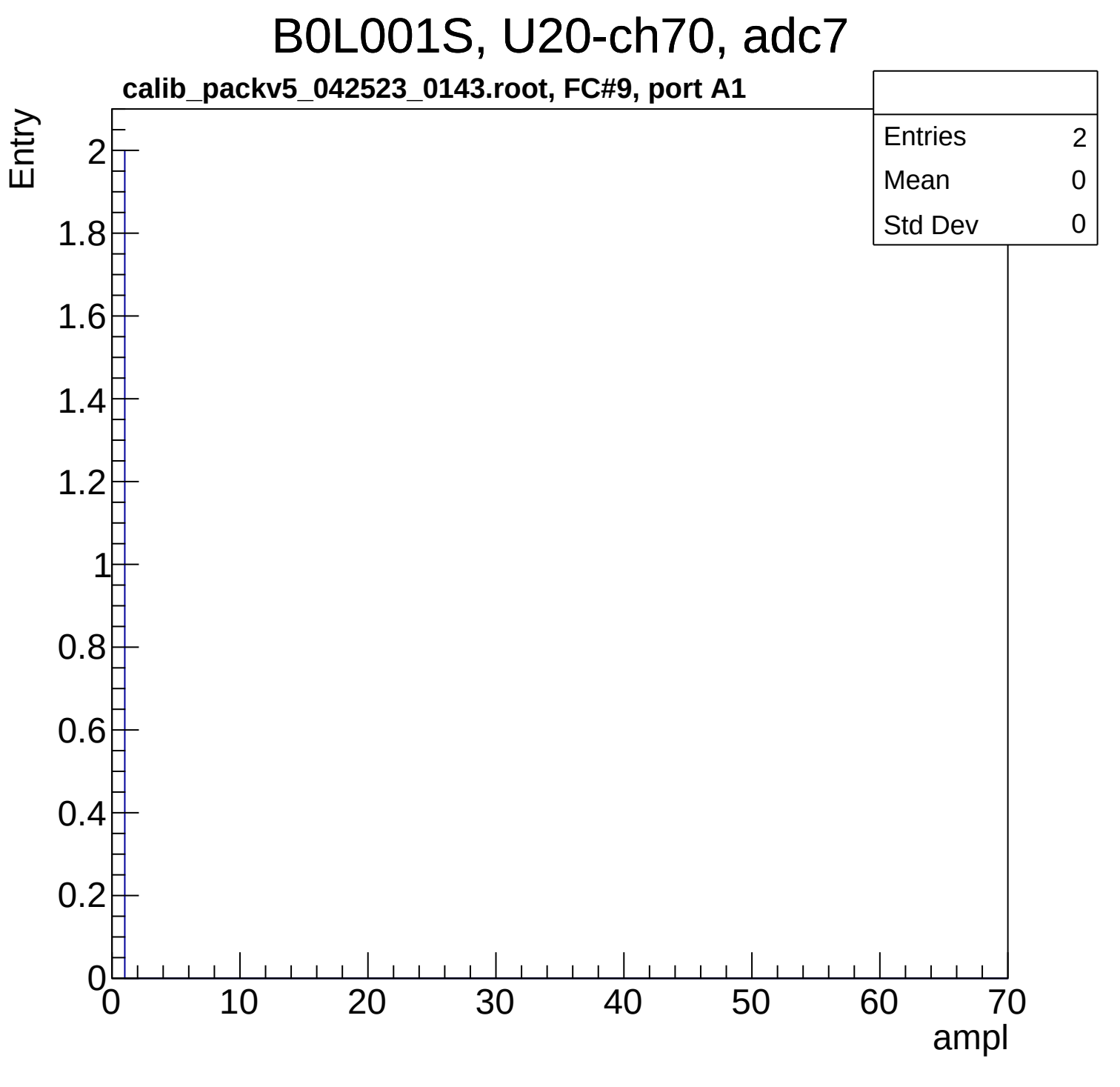
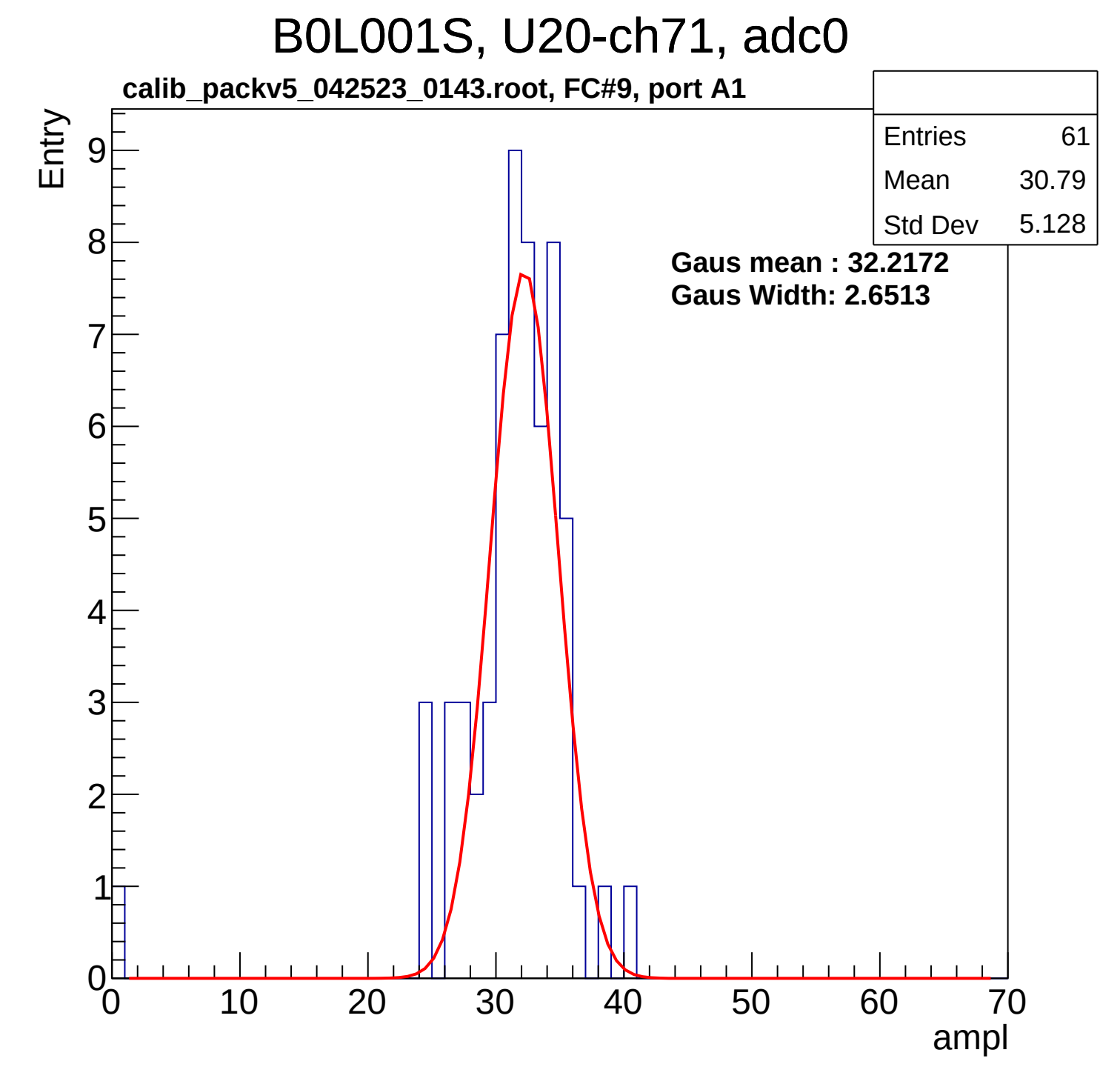

B0L001S, U20-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.79
Std Dev 5.128
Gaus mean : 32.2172
Gaus Width: 2.6513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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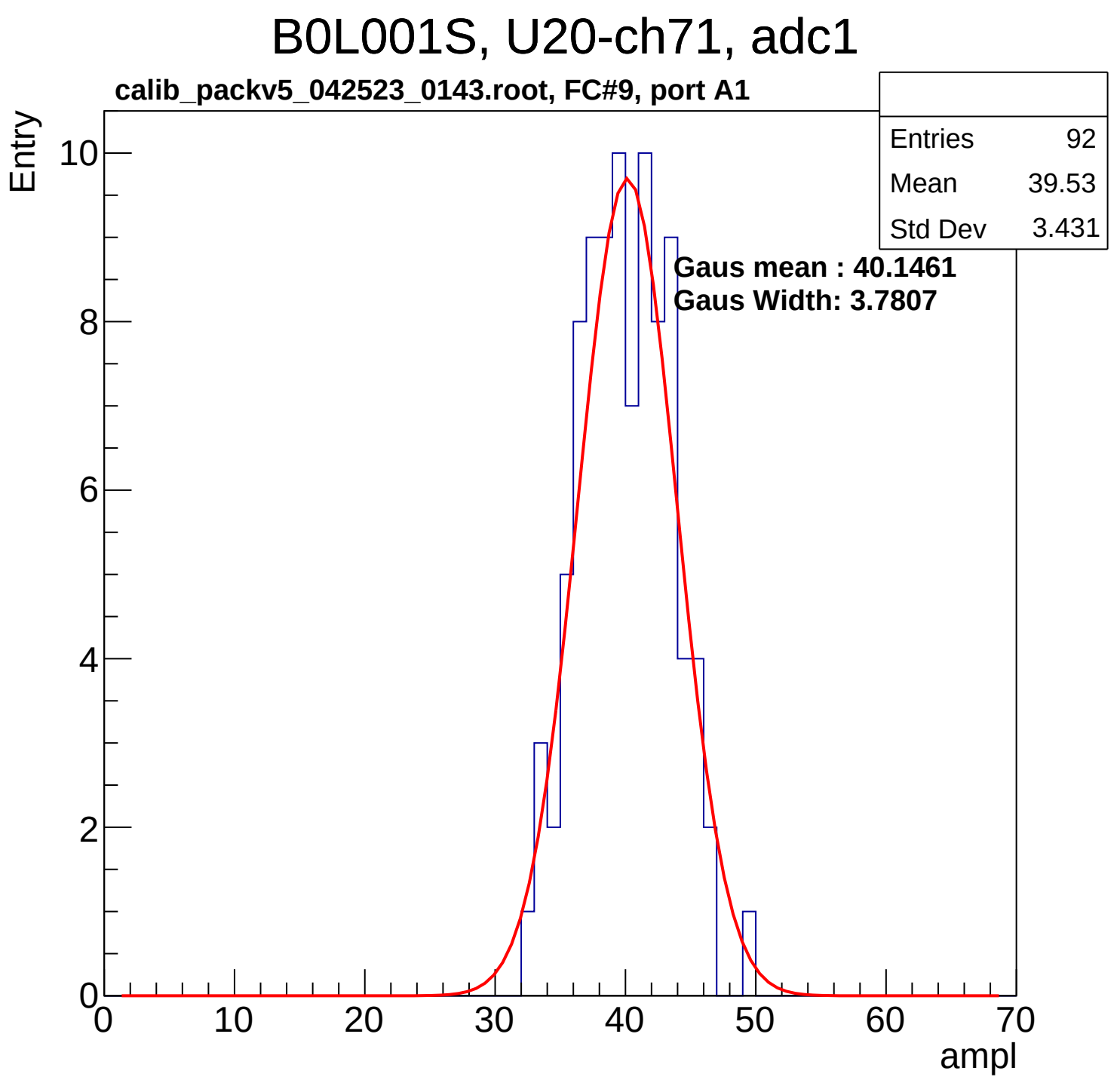
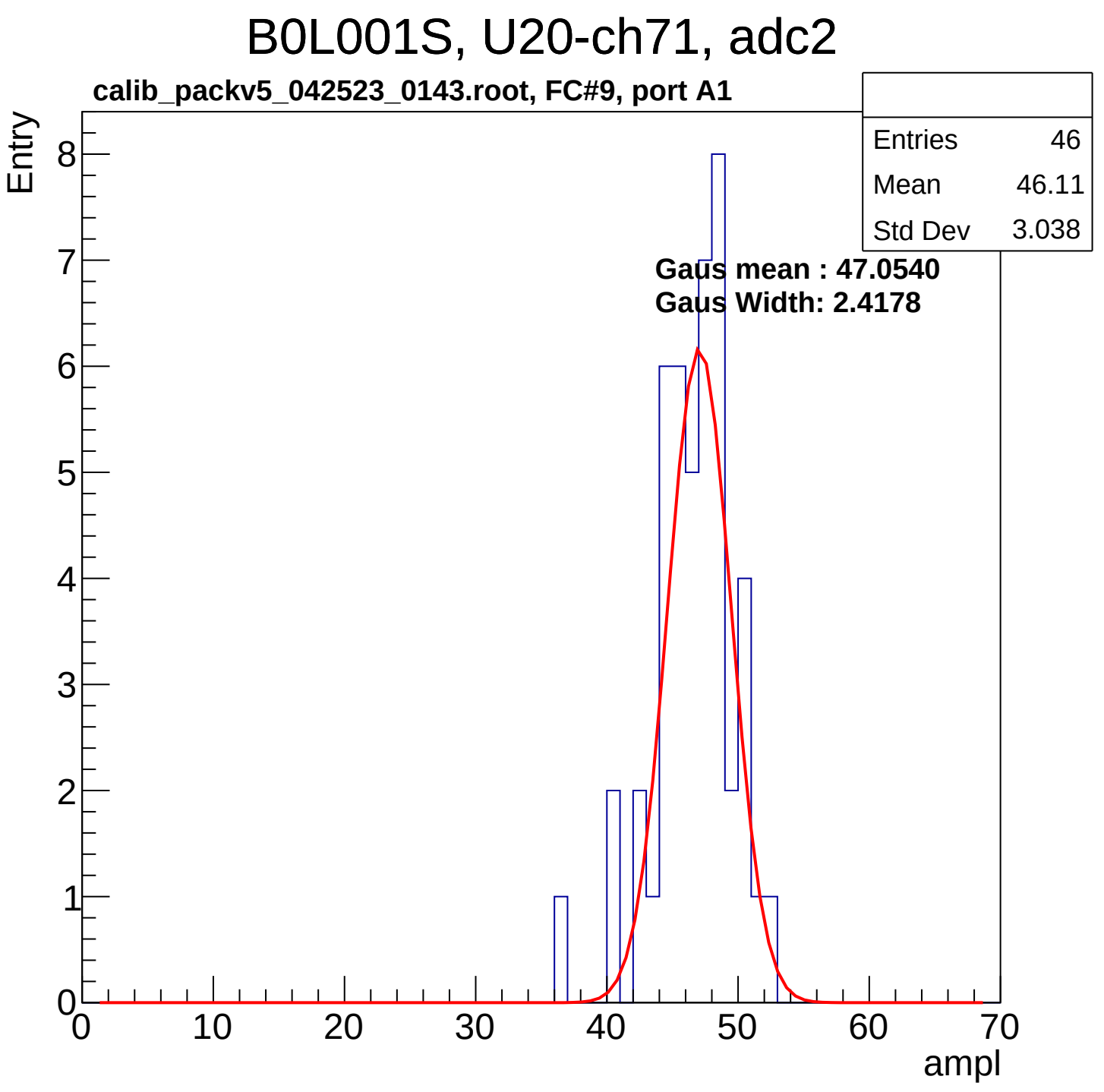

B0L001S, U20-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 92
Mean 39.53
Std Dev 3.431
Gaus mean : 40.1461
Gaus Width: 3.7807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 46.11
Std Dev 3.038
Gaus mean : 47.0540
Gaus Width: 2.4178
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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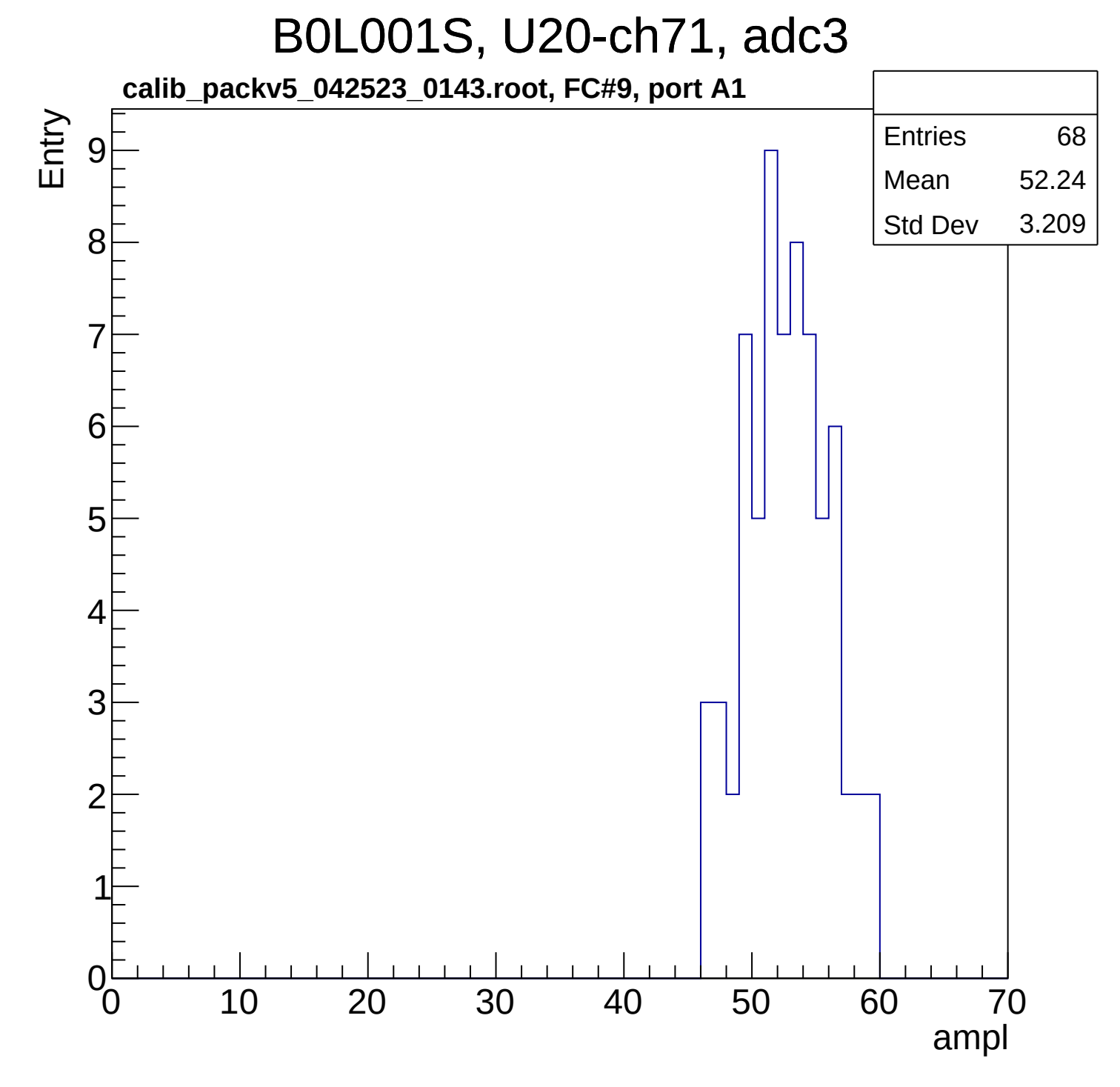
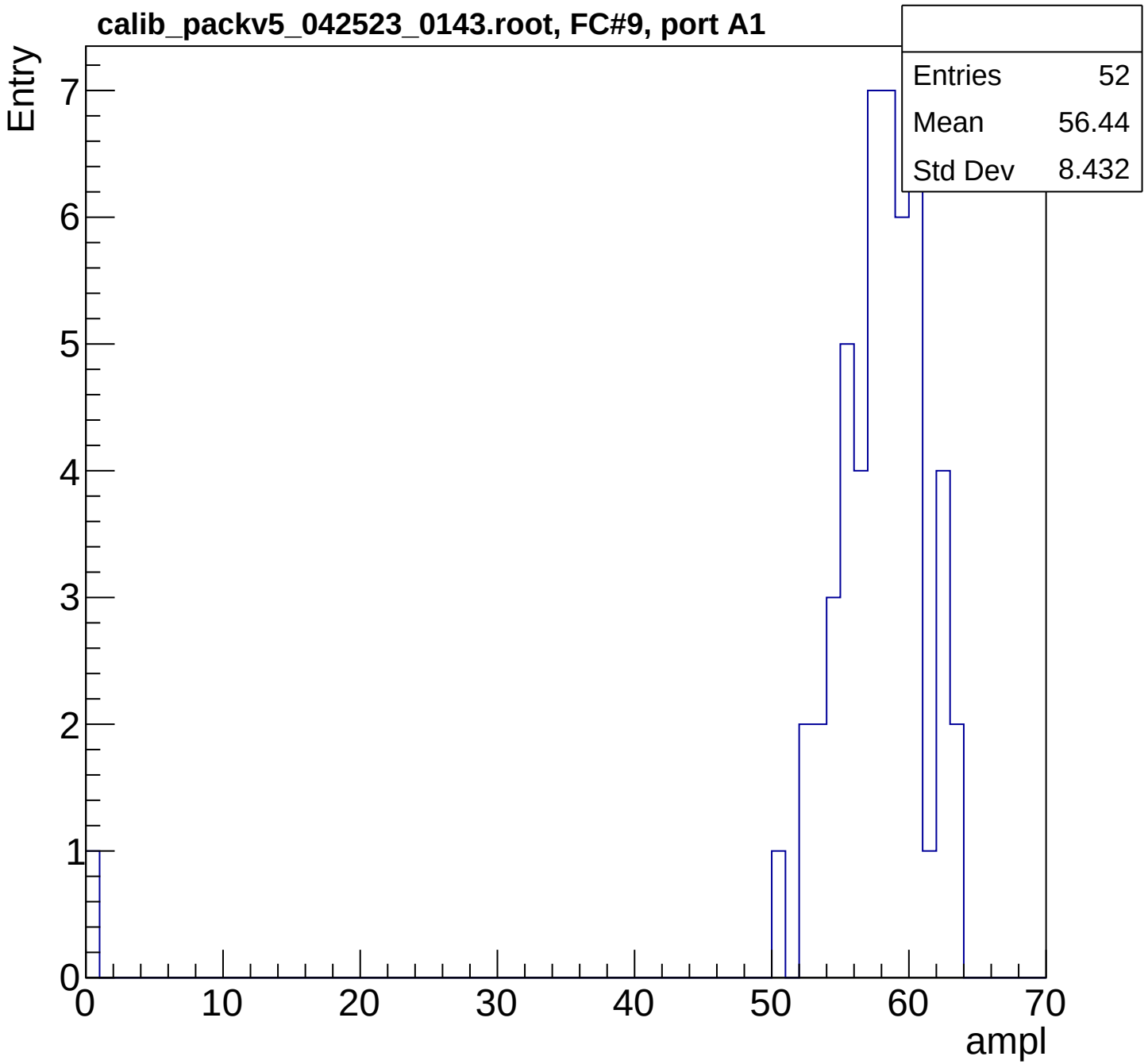
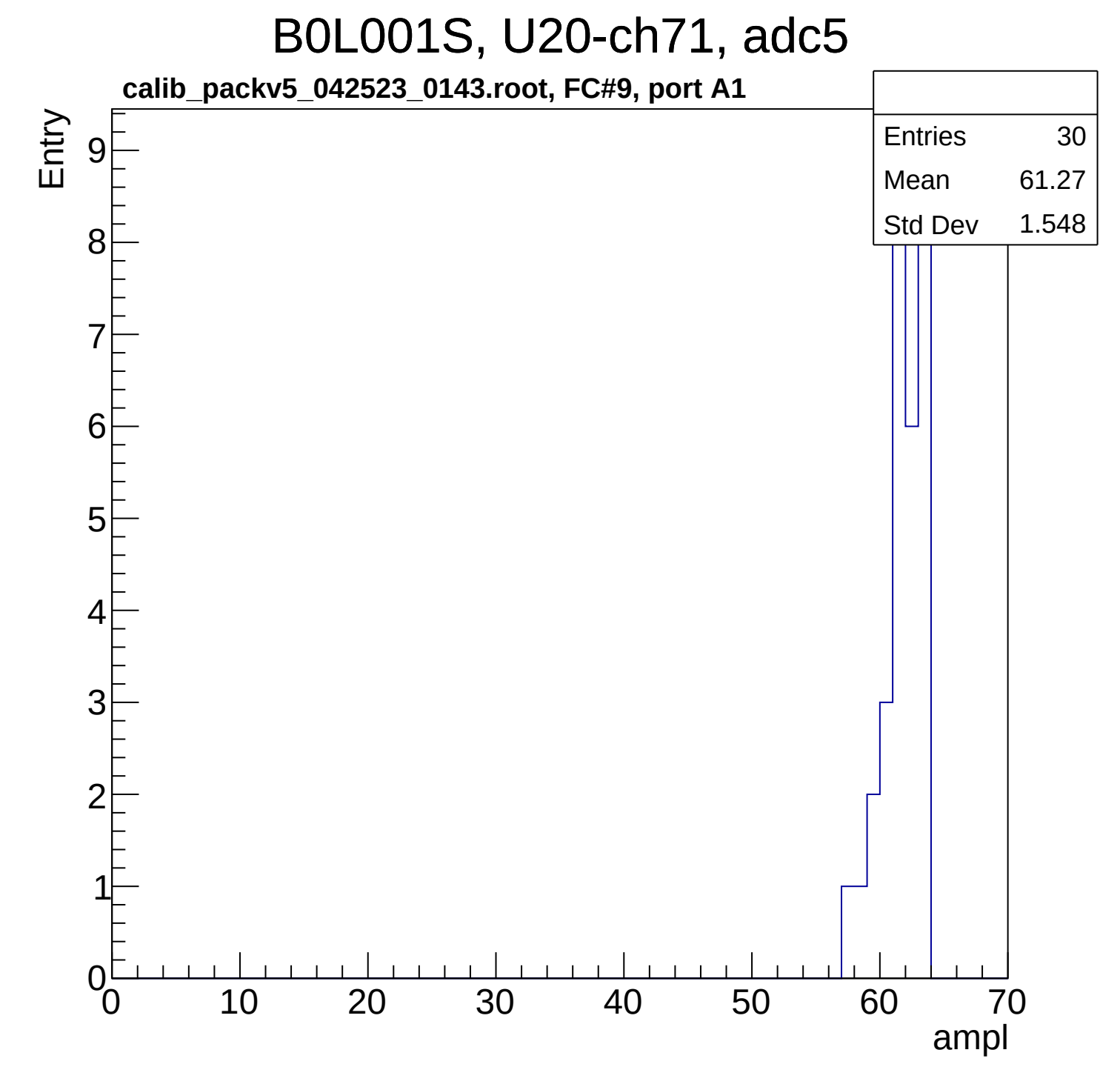

B0L001S, U20-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.24
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.44
Std Dev 8.432
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 61.27
Std Dev 1.548
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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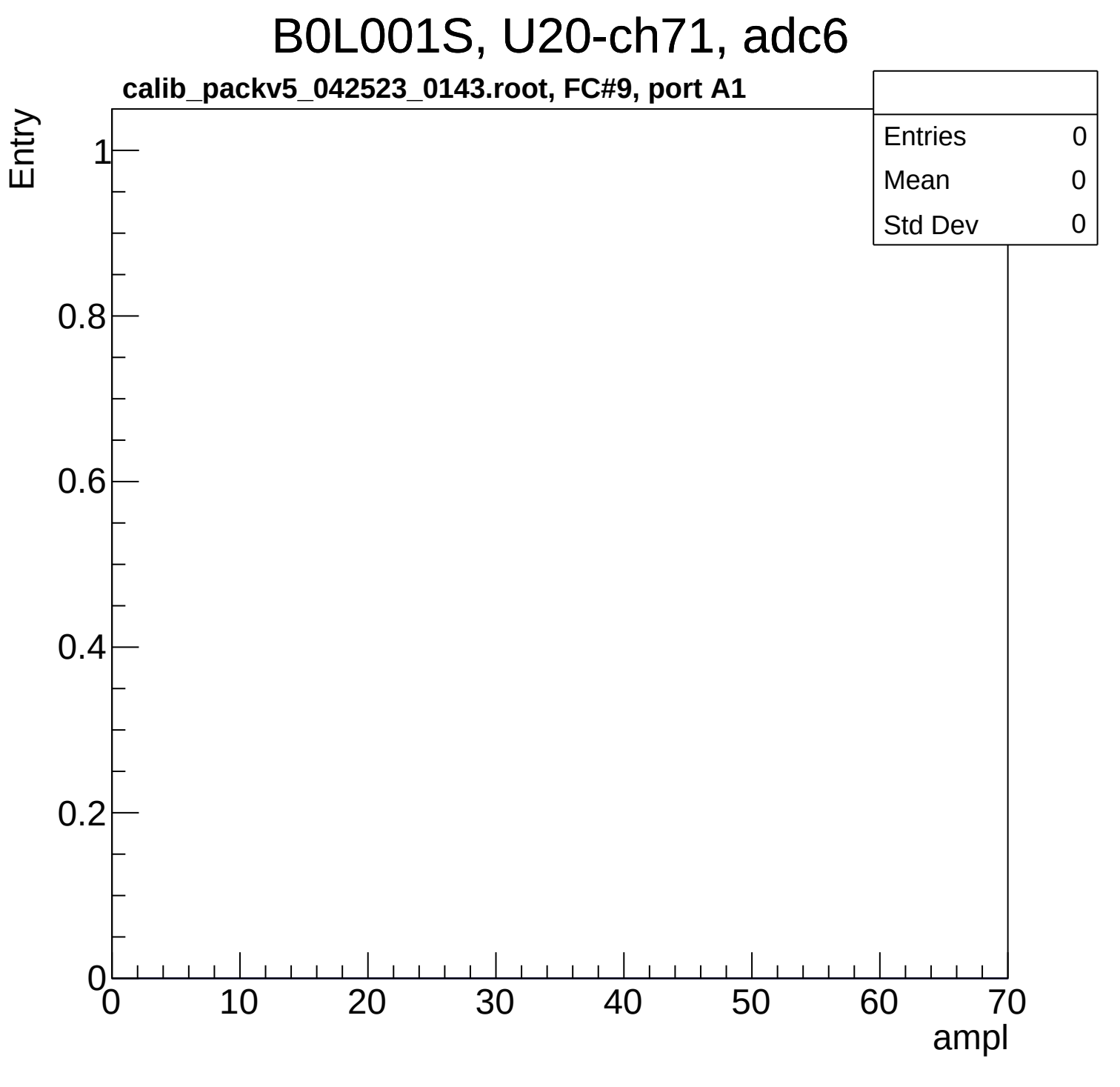
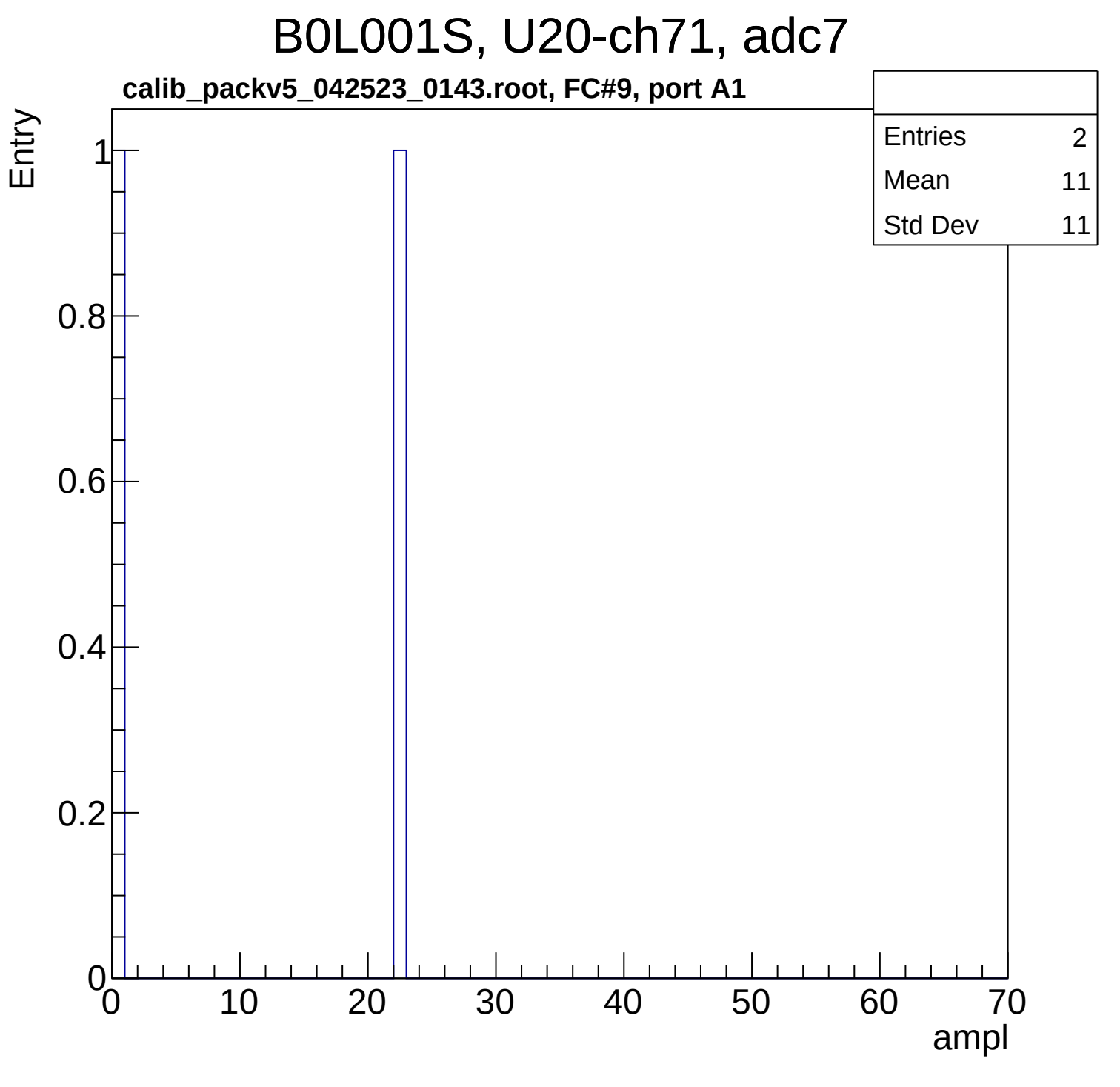
40
50
60
70


B0L001S, U20-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
1
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0.6
0.4
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0
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10
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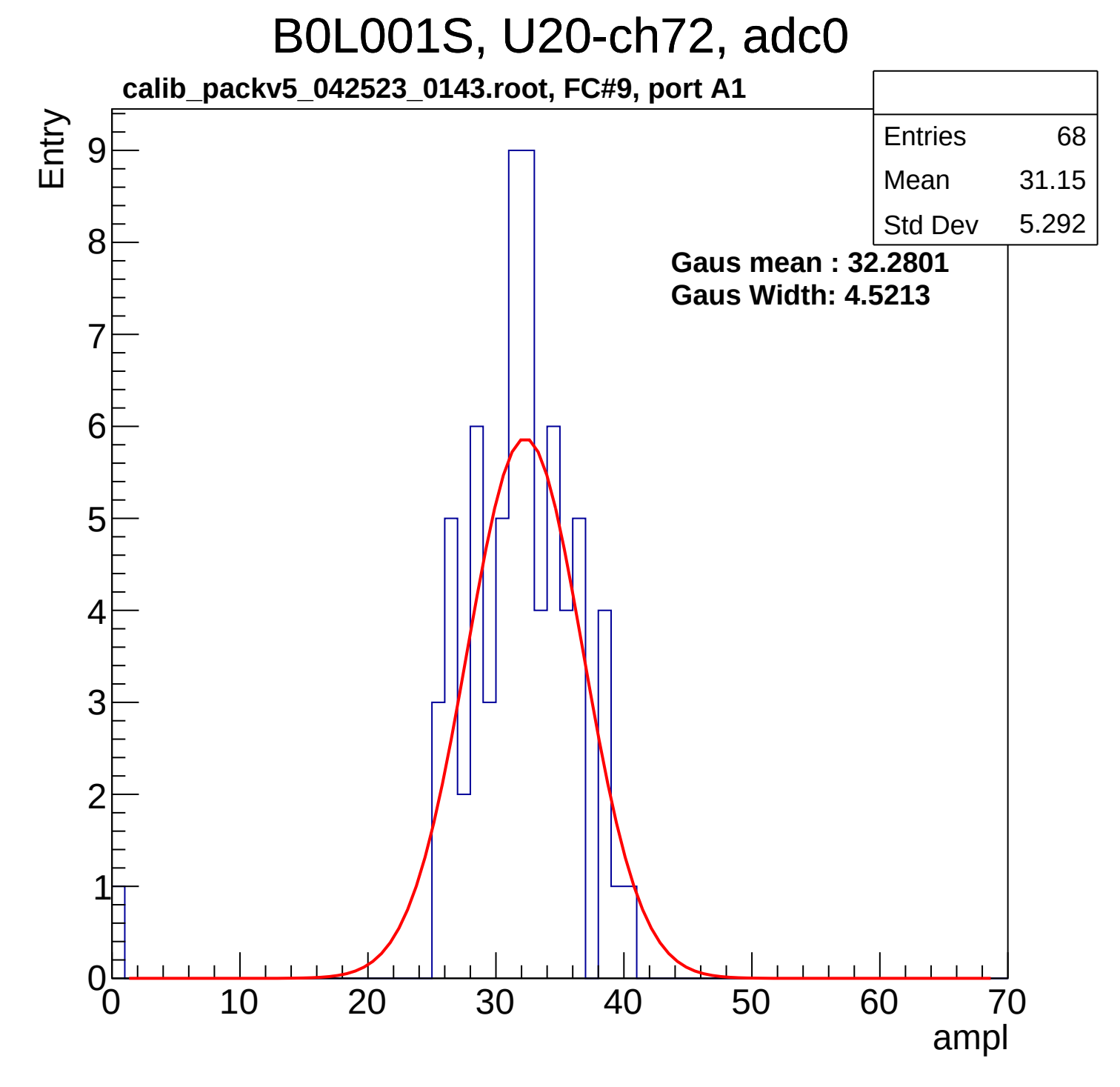
50
60
70
ampl


B0L001S, U20-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.15
Std Dev 5.292
Gaus mean : 32.2801
Gaus Width: 4.5213
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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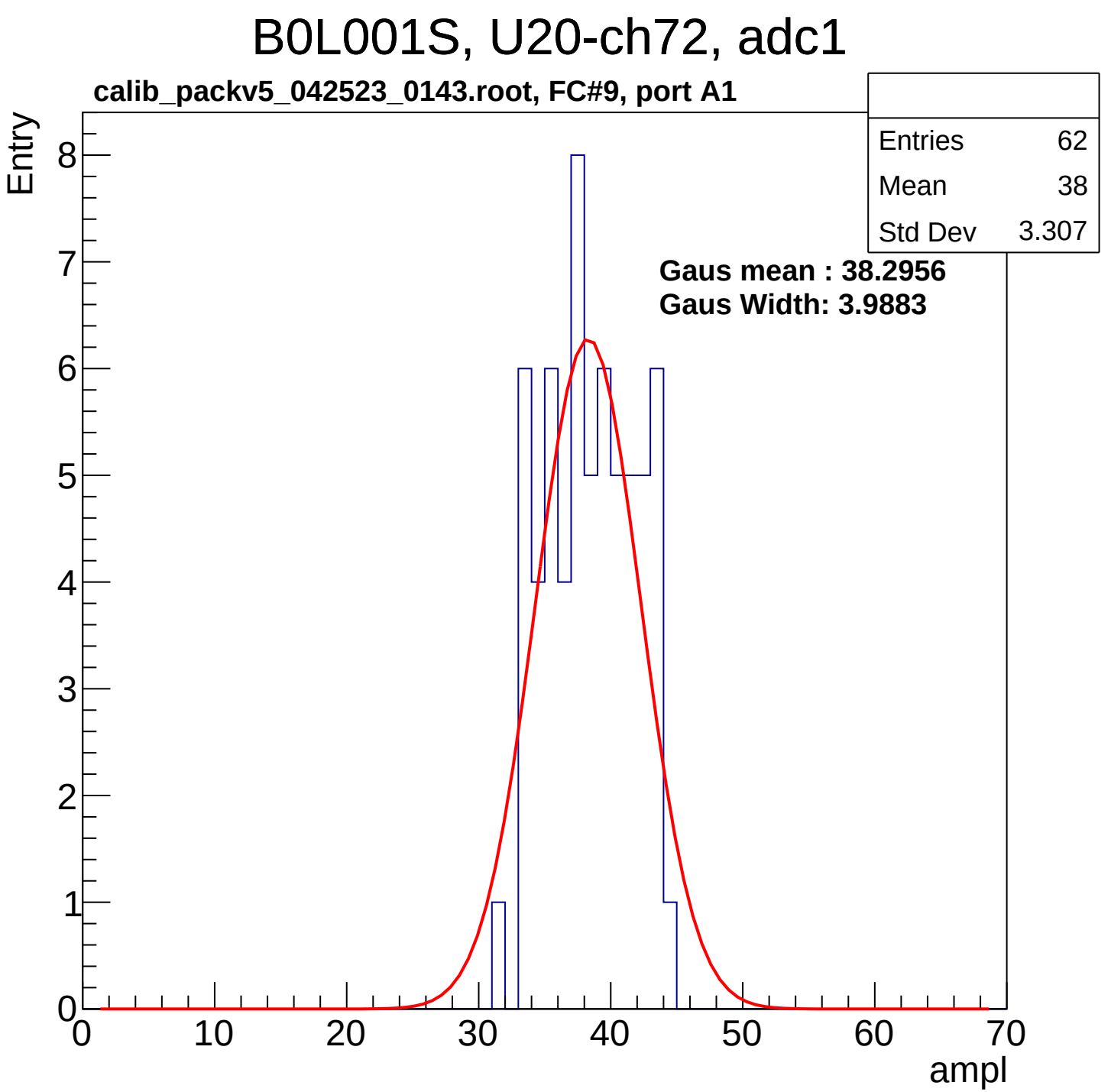
B0L001S, U20-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38
Std Dev 3.307
Gaus mean : 38.2956
Gaus Width: 3.9883
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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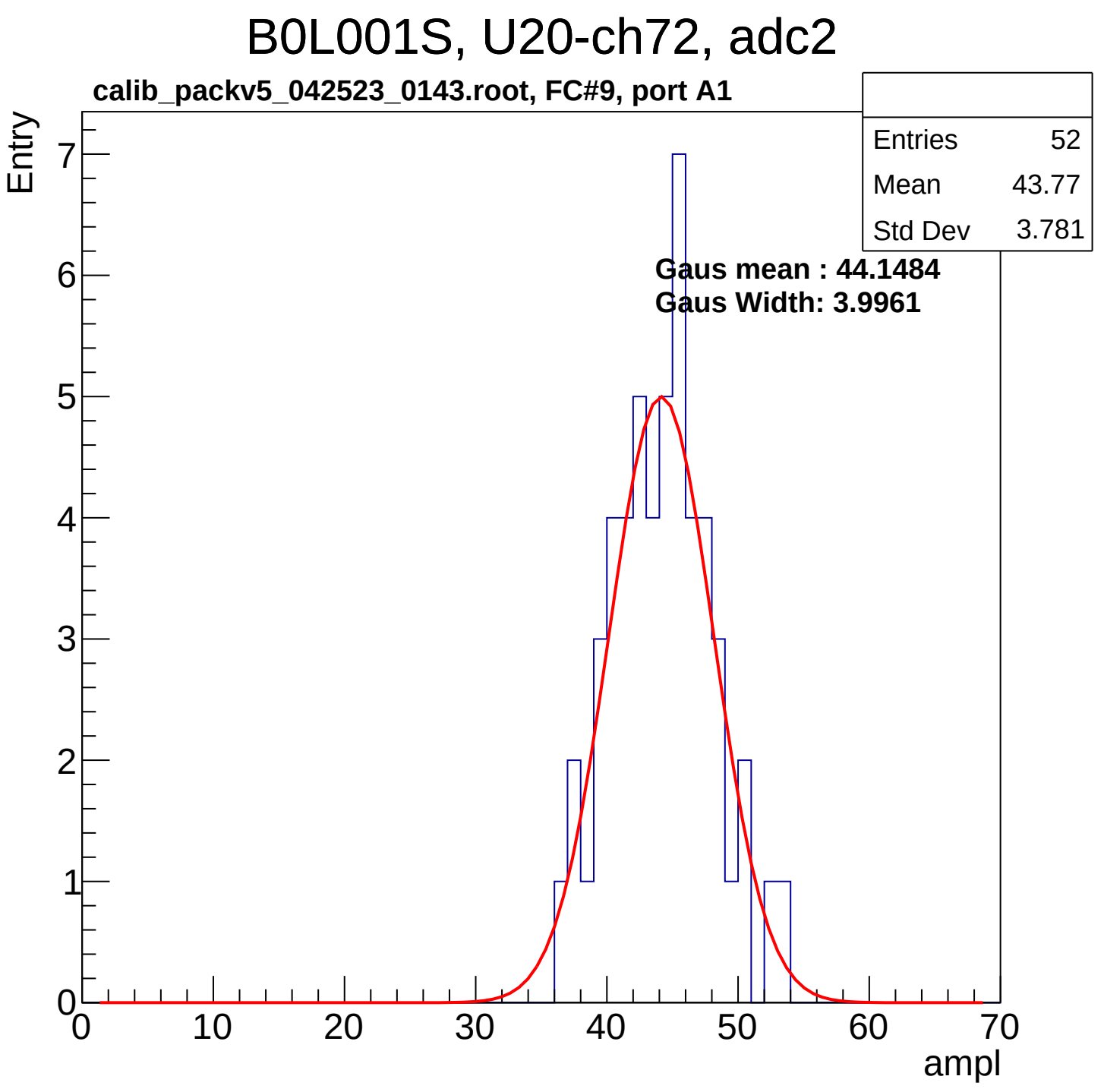
B0L001S, U20-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.77
Std Dev 3.781
Gaus mean : 44.1484
Gaus Width: 3.9961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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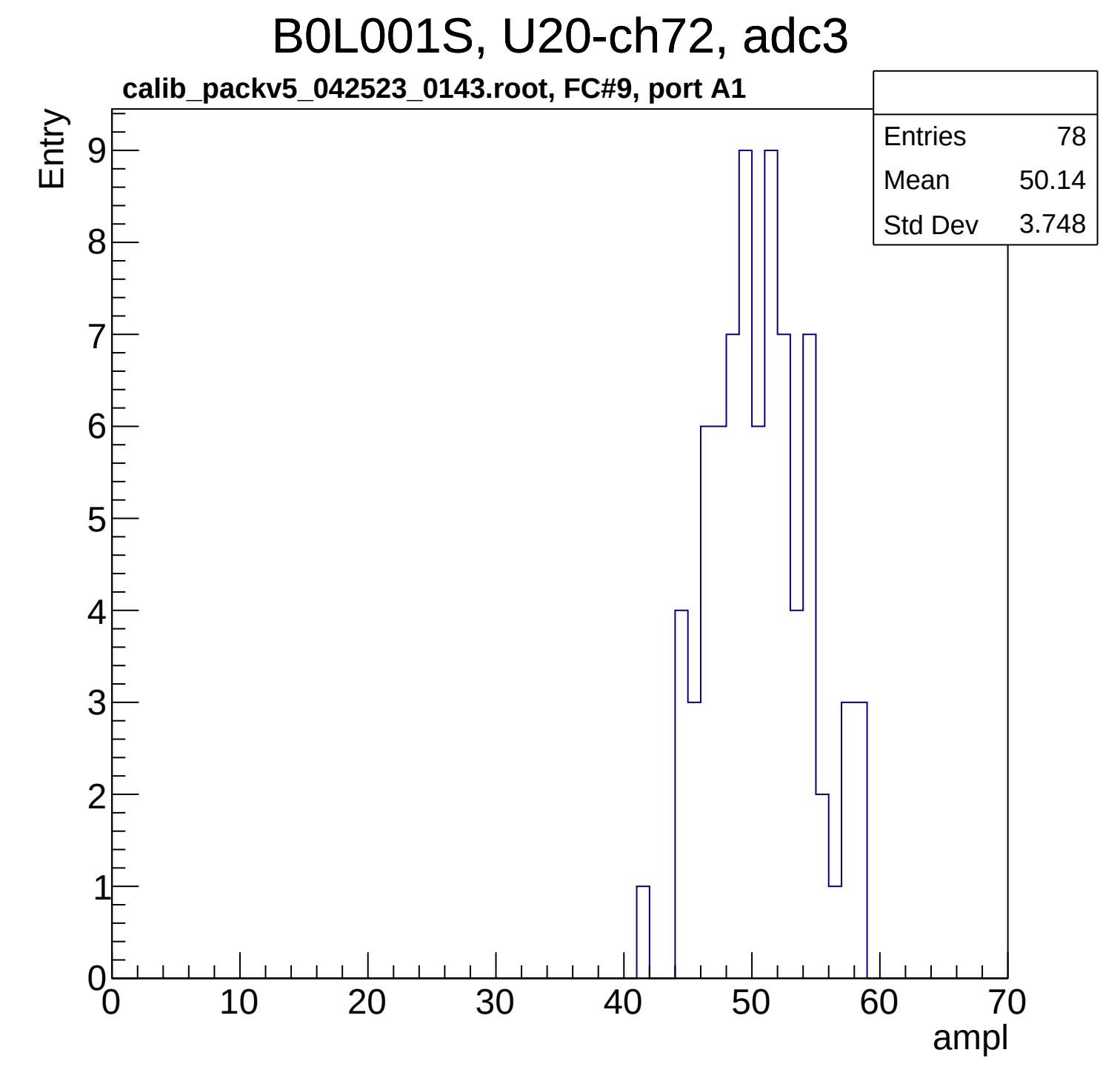
B0L001S, U20-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 50.14
Std Dev 3.748
Entry
ampl
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4
5
6
7
8
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0
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60
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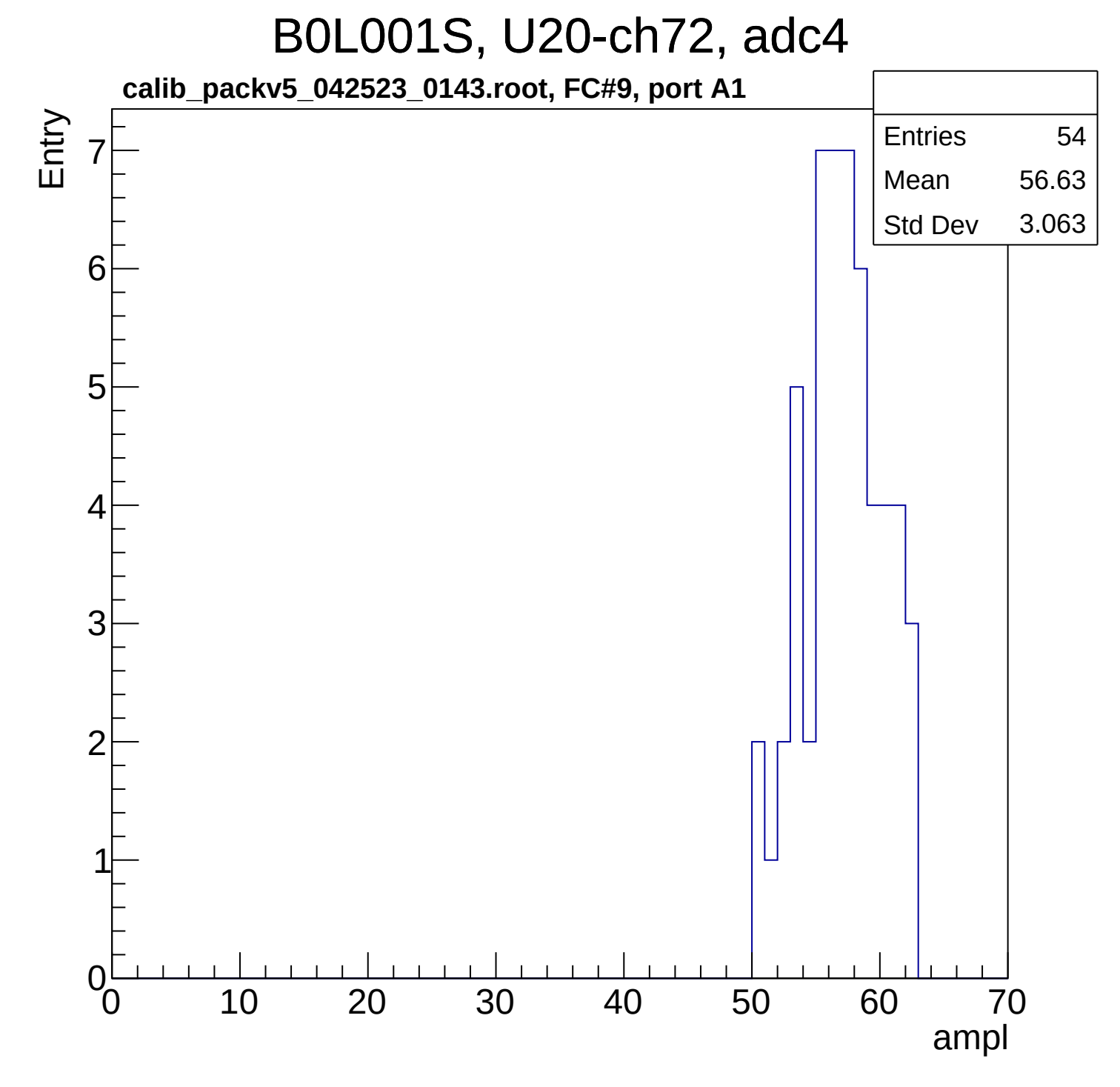
B0L001S, U20-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.63
Std Dev 3.063
Entry
ampl
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4
5
6
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0
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60
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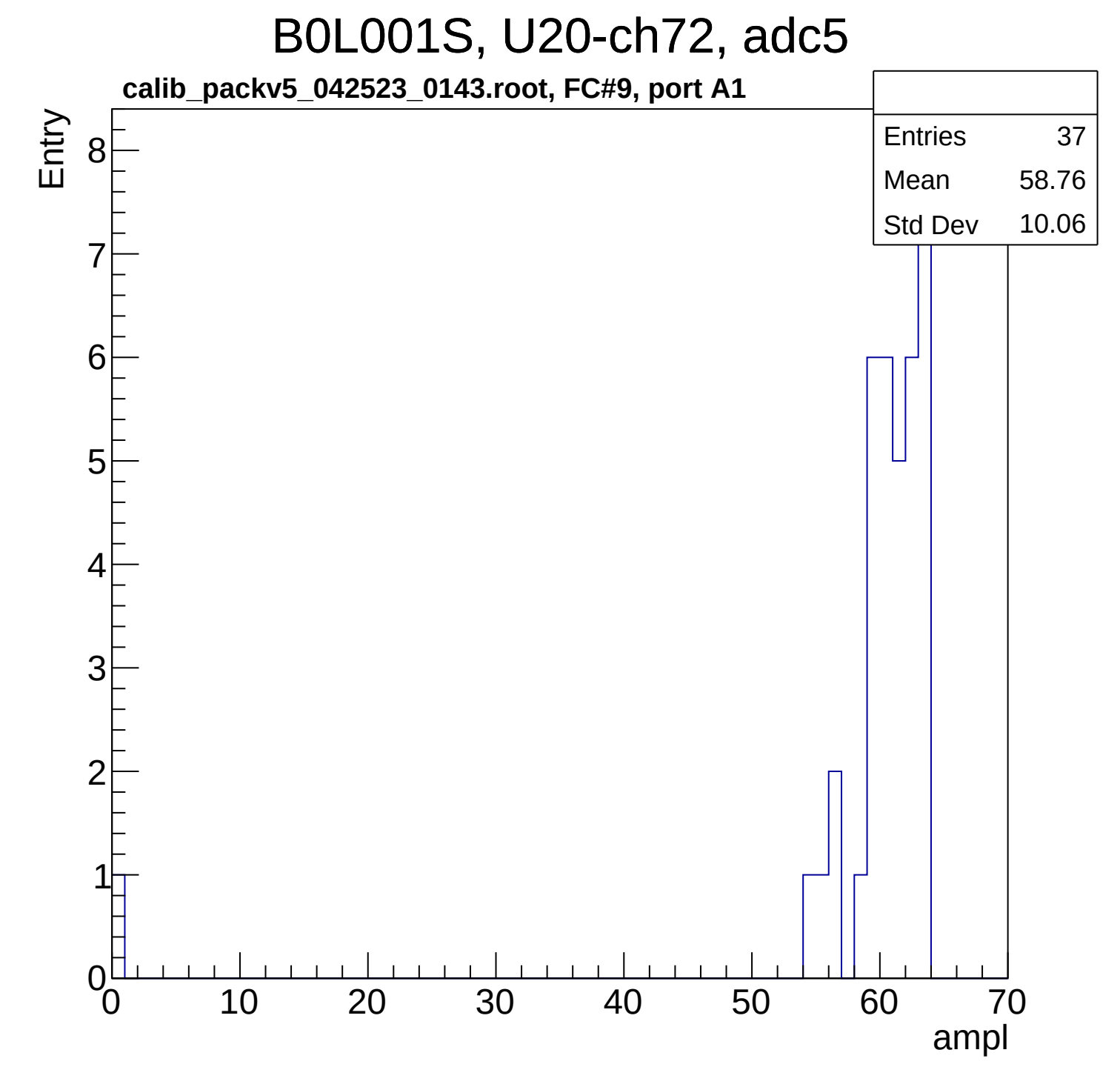
B0L001S, U20-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.76
Std Dev 10.06
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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60
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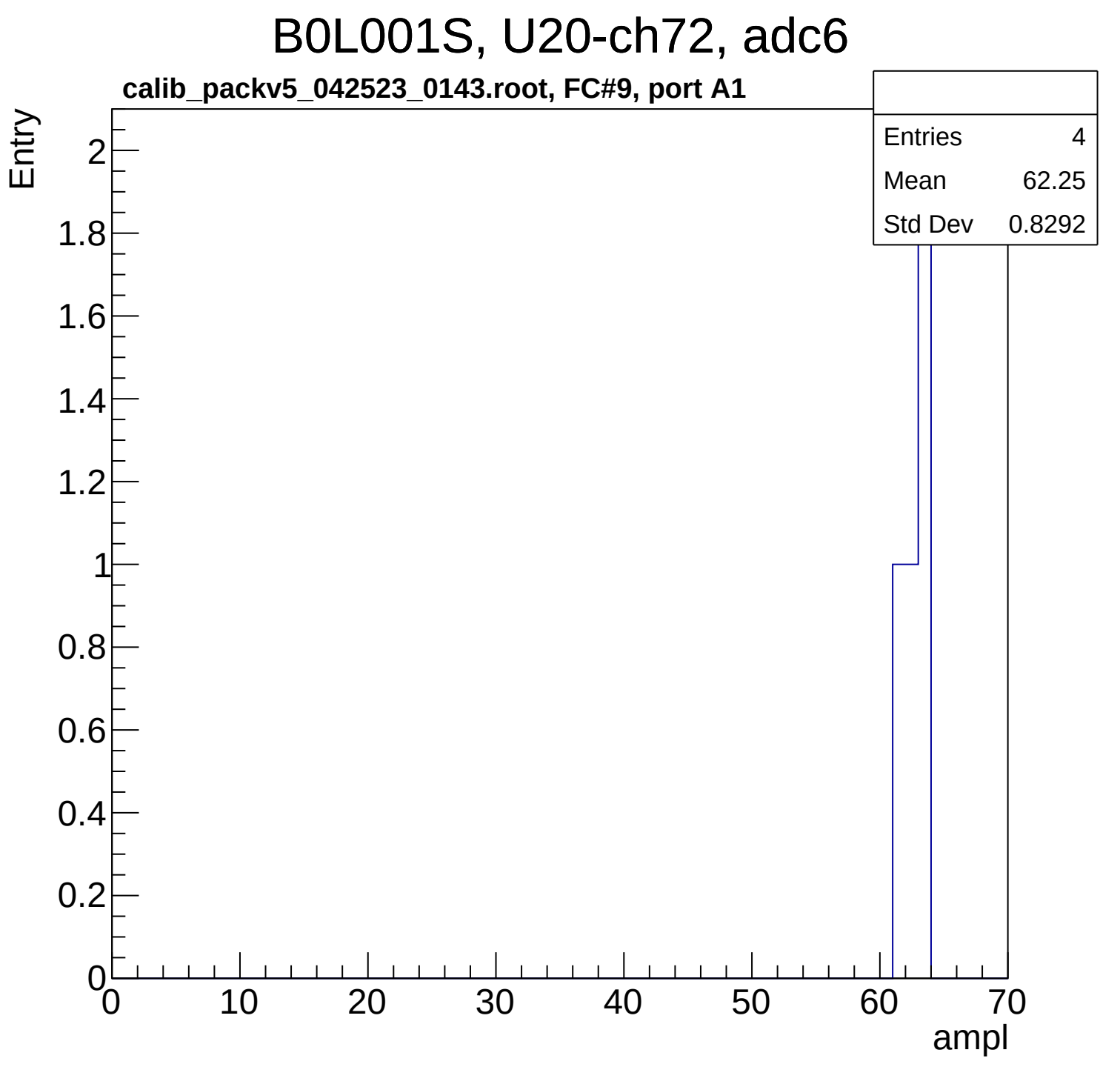
B0L001S, U20-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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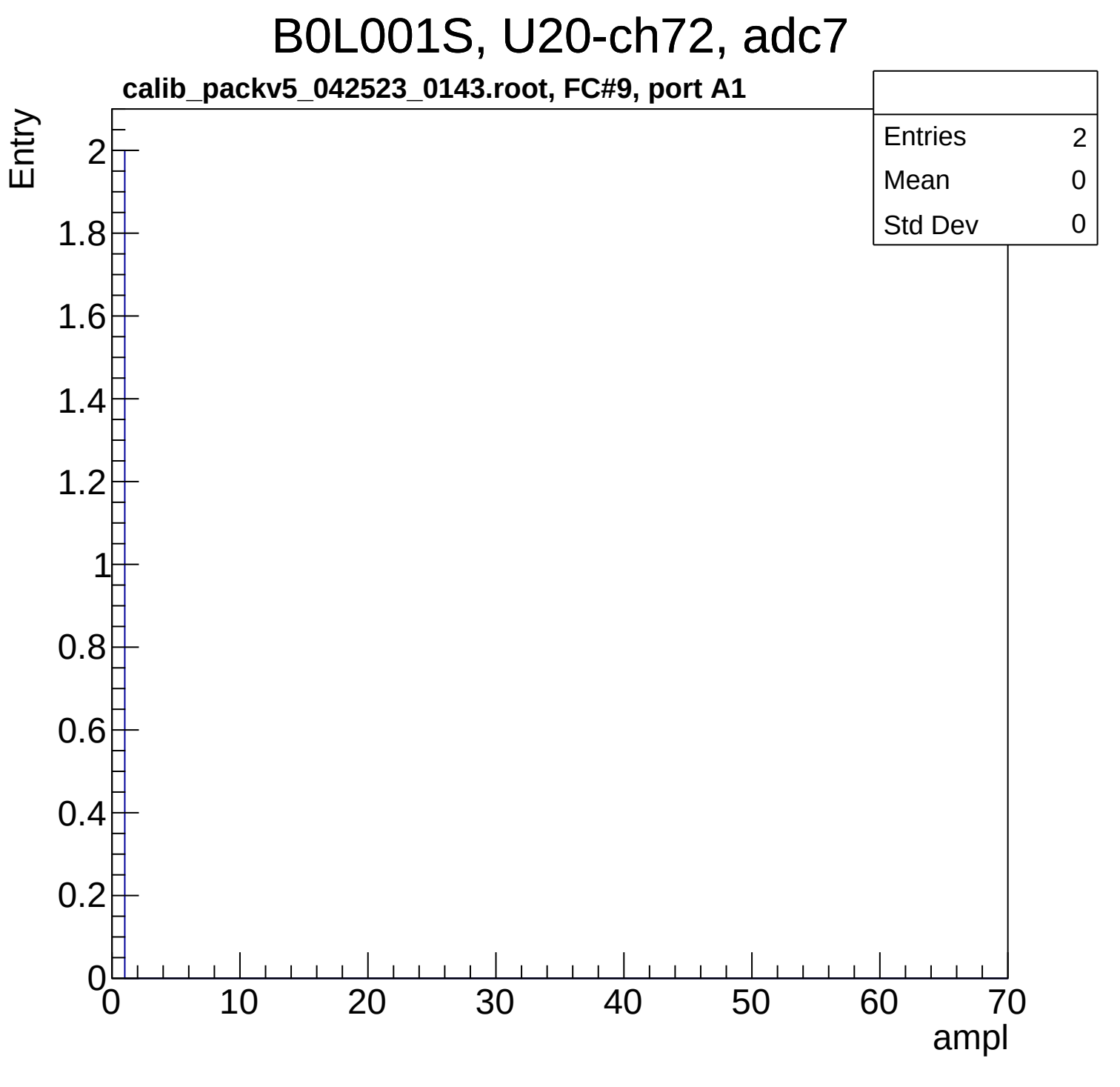
40
50
60
70


B0L001S, U20-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
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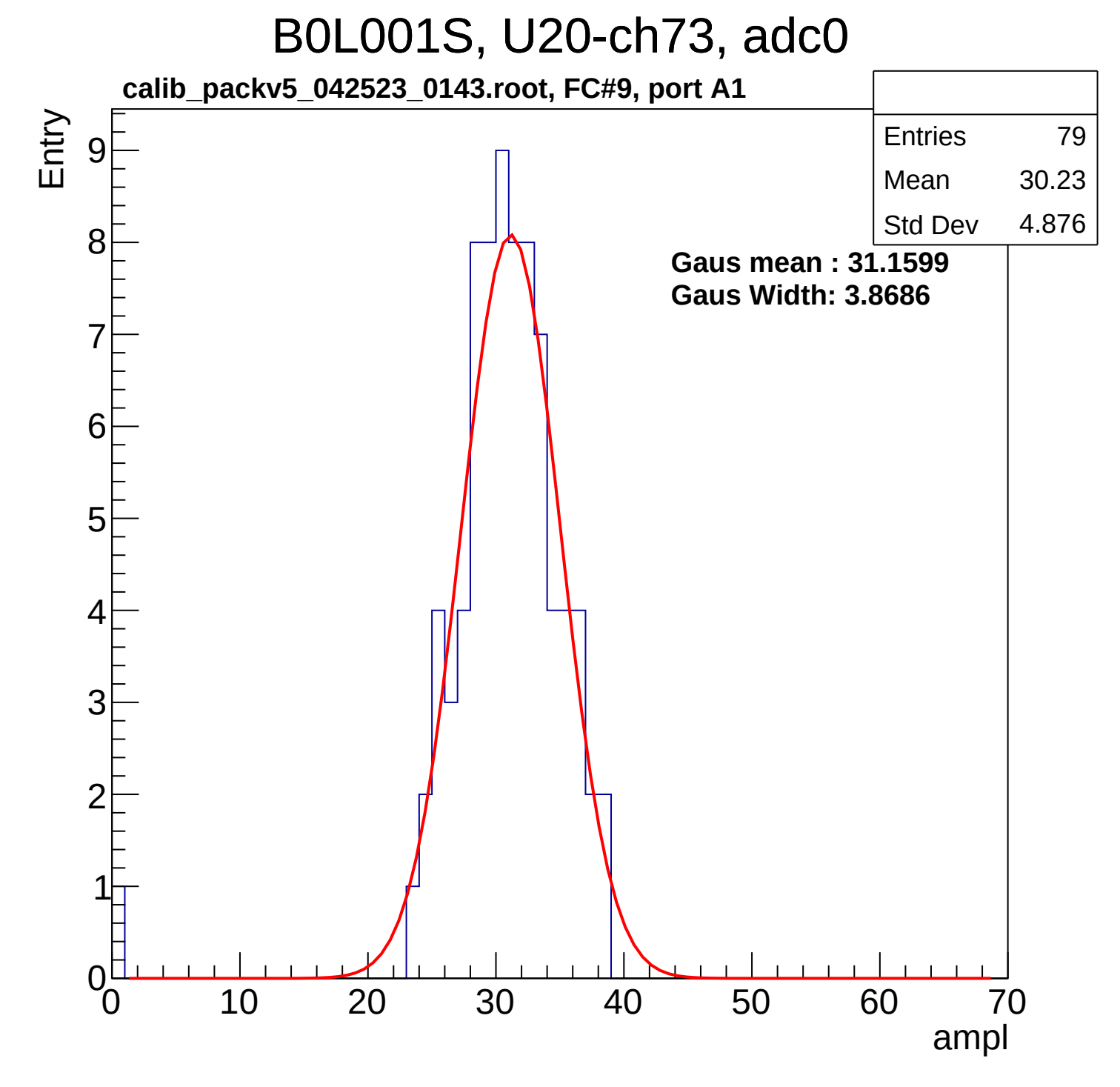
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.23
Std Dev 4.876
Gaus mean : 31.1599
Gaus Width: 3.8686
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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50
60
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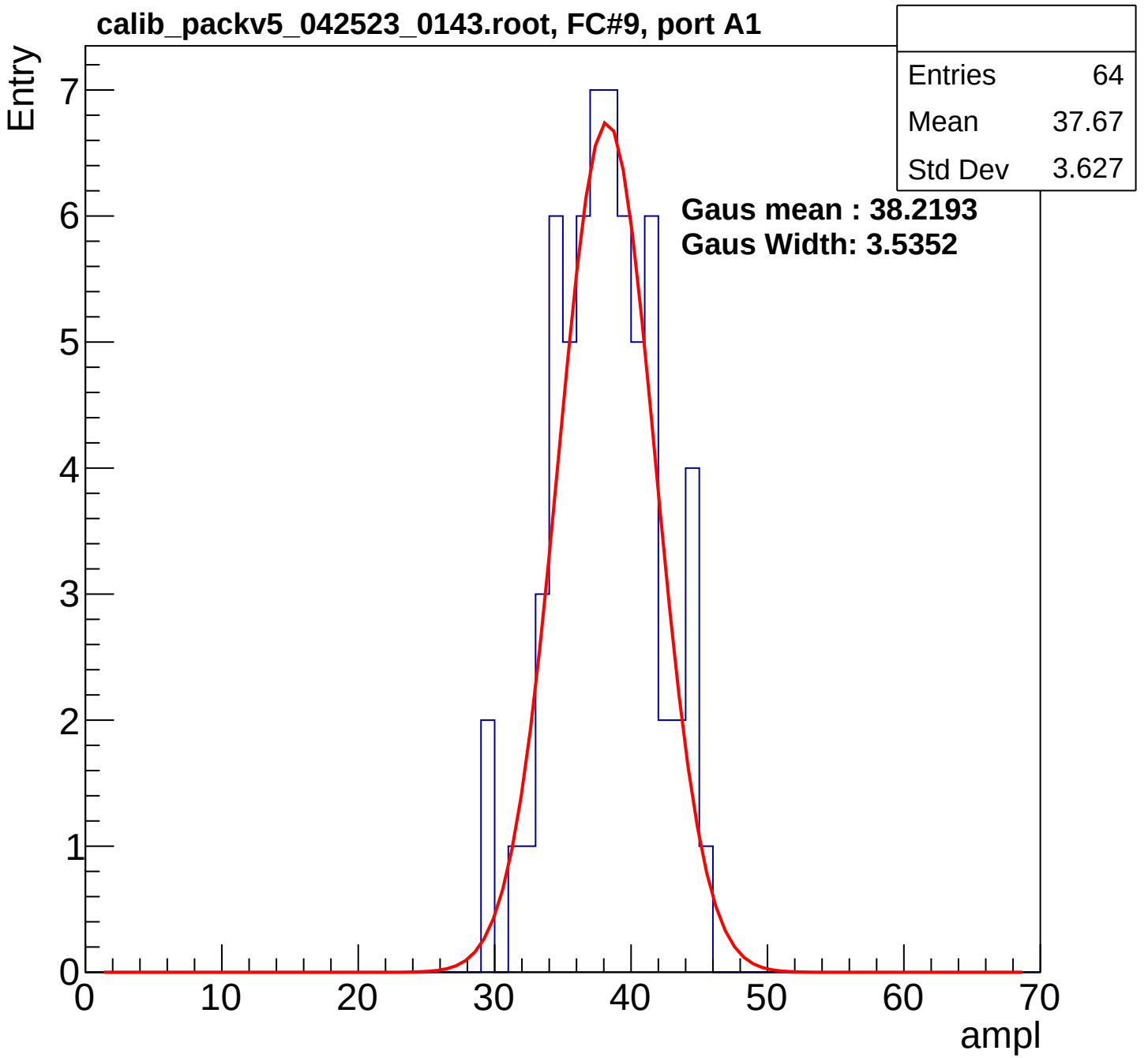
B0L001S, U20-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.67
Std Dev 3.627
Gaus mean : 38.2193
Gaus Width: 3.5352
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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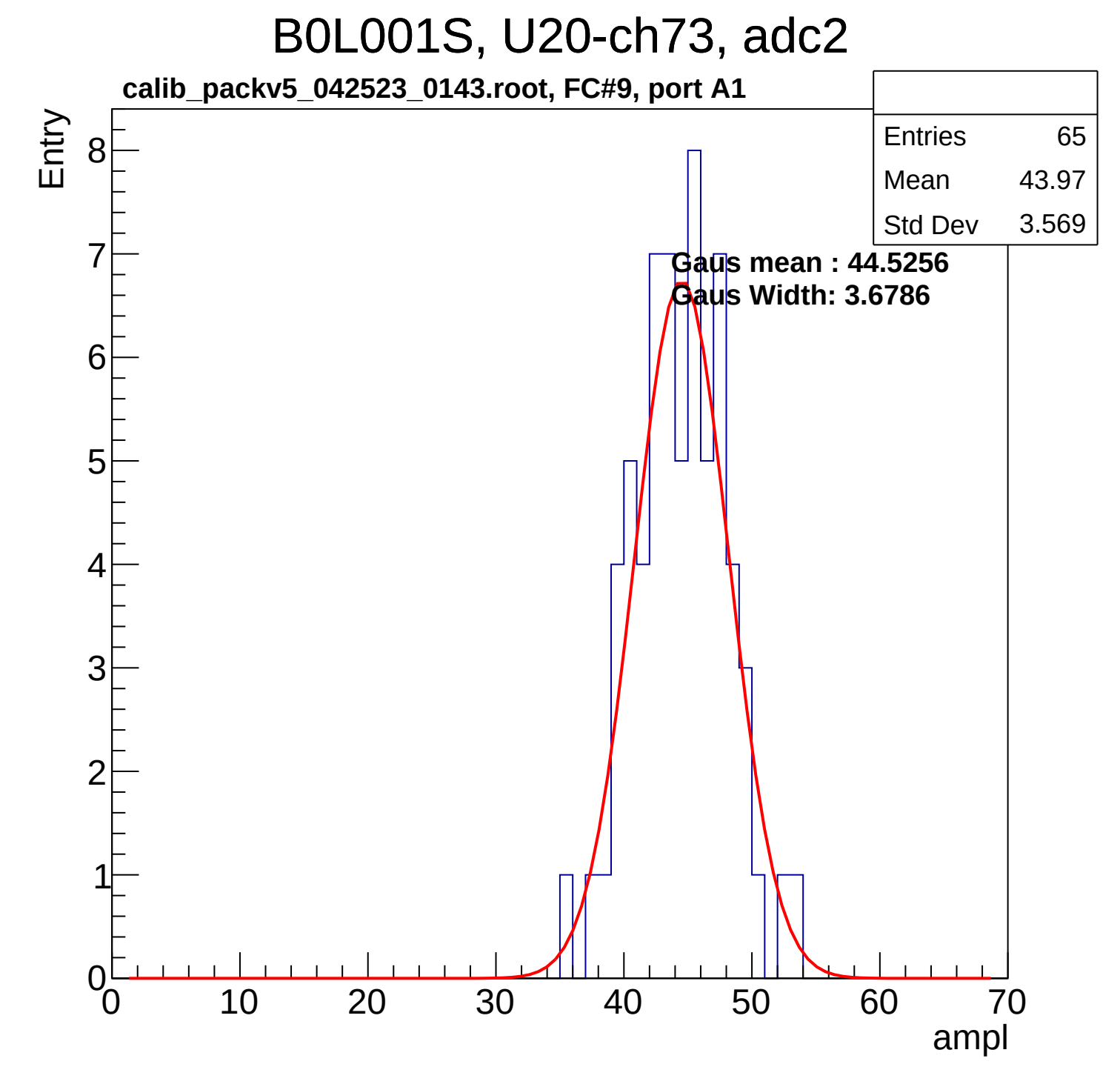
40
50
60
70


B0L001S, U20-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.97
Std Dev 3.569
Gaus mean : 44.5256
Gaus Width: 3.6786
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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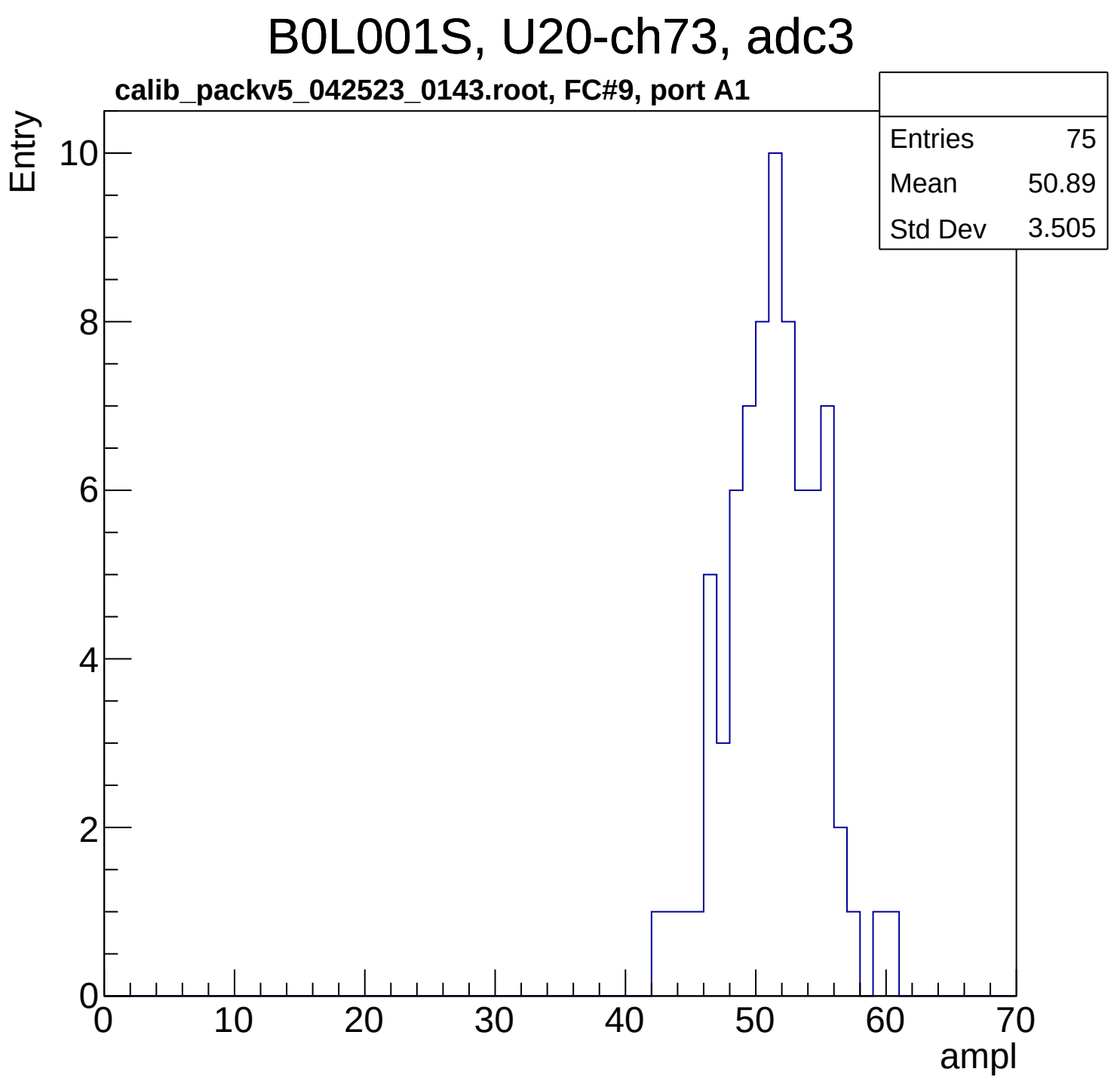
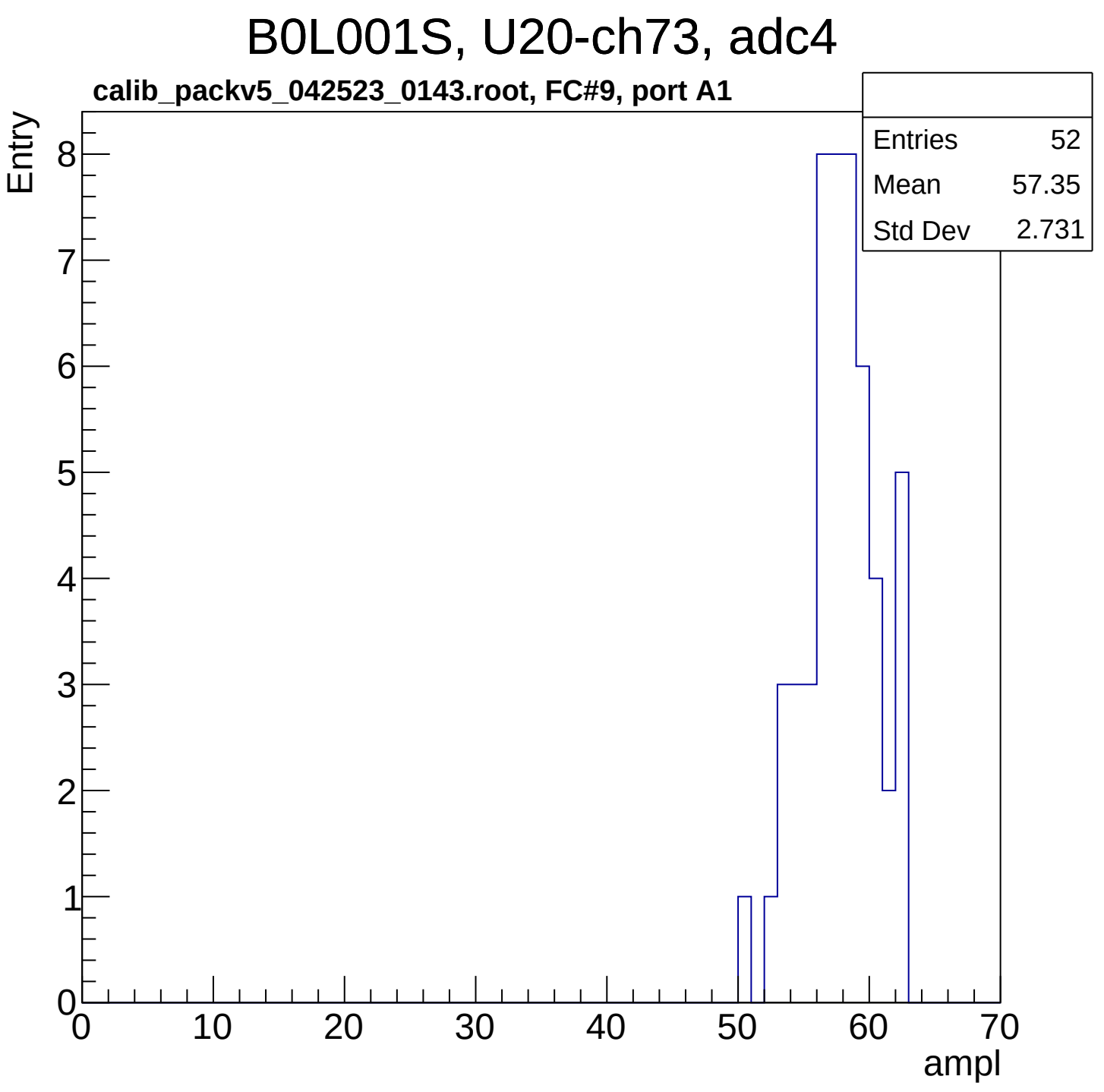
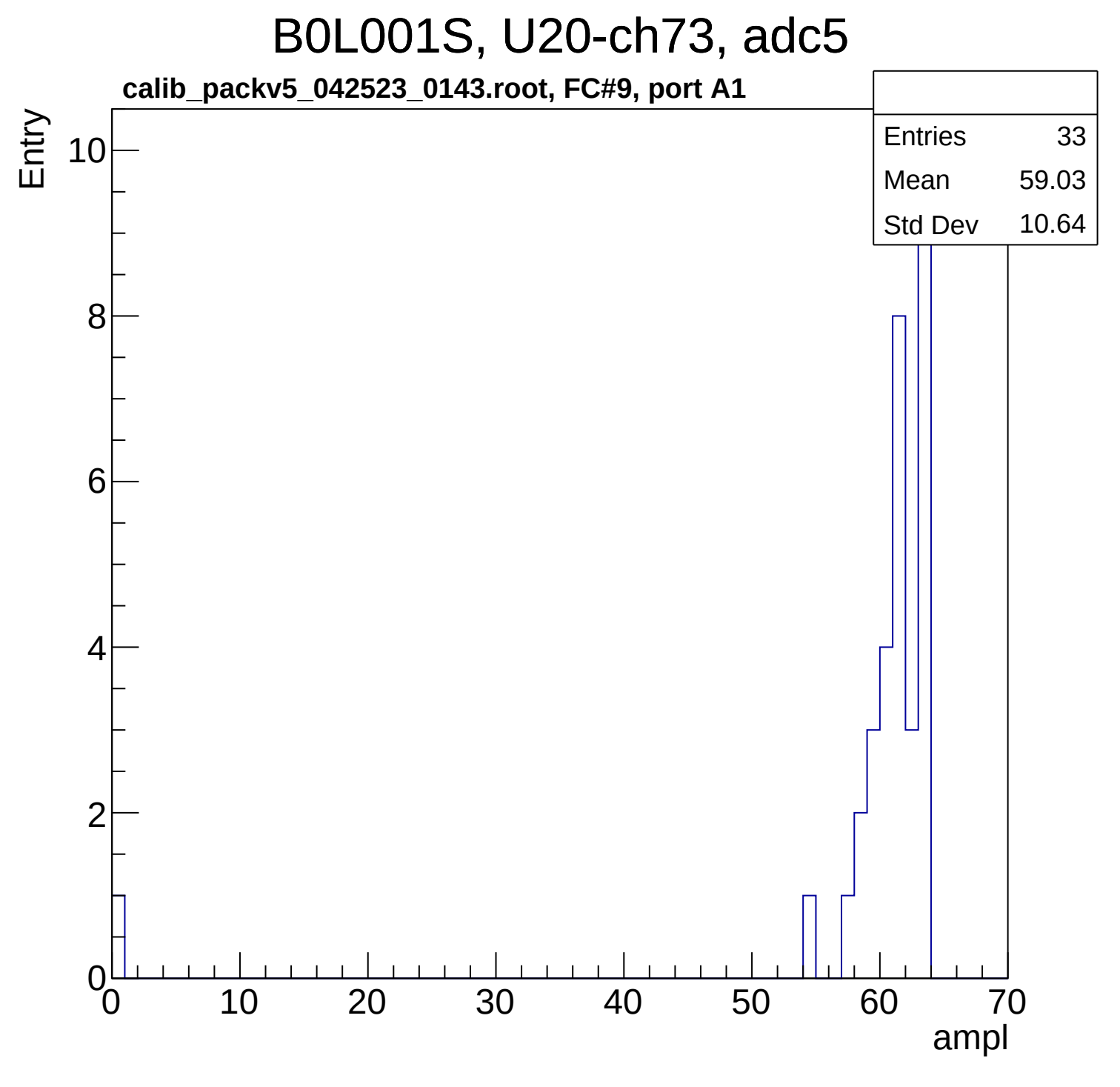
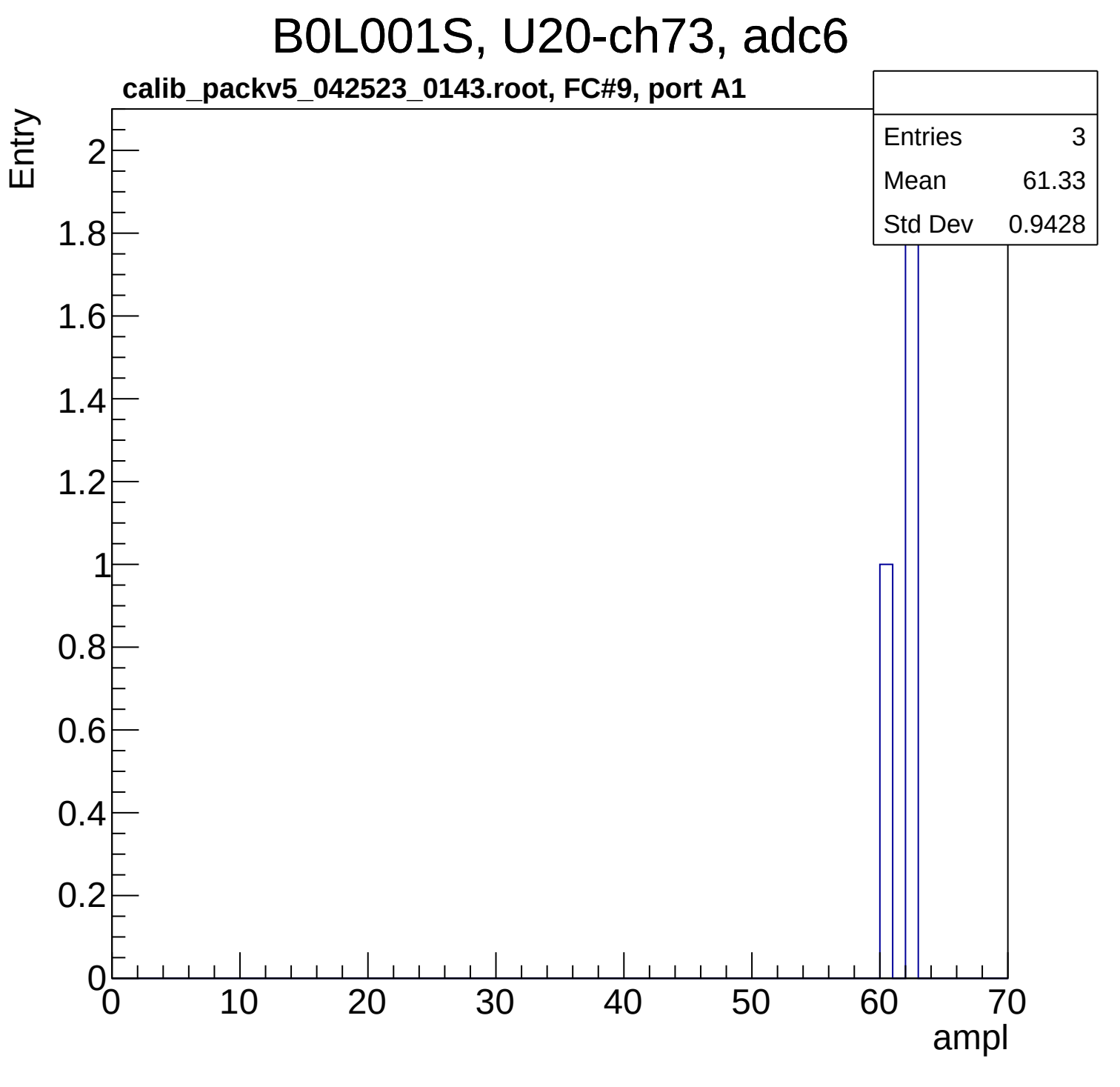

B0L001S, U20-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.89
Std Dev 3.505
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 57.35
Std Dev 2.731
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 59.03
Std Dev 10.64
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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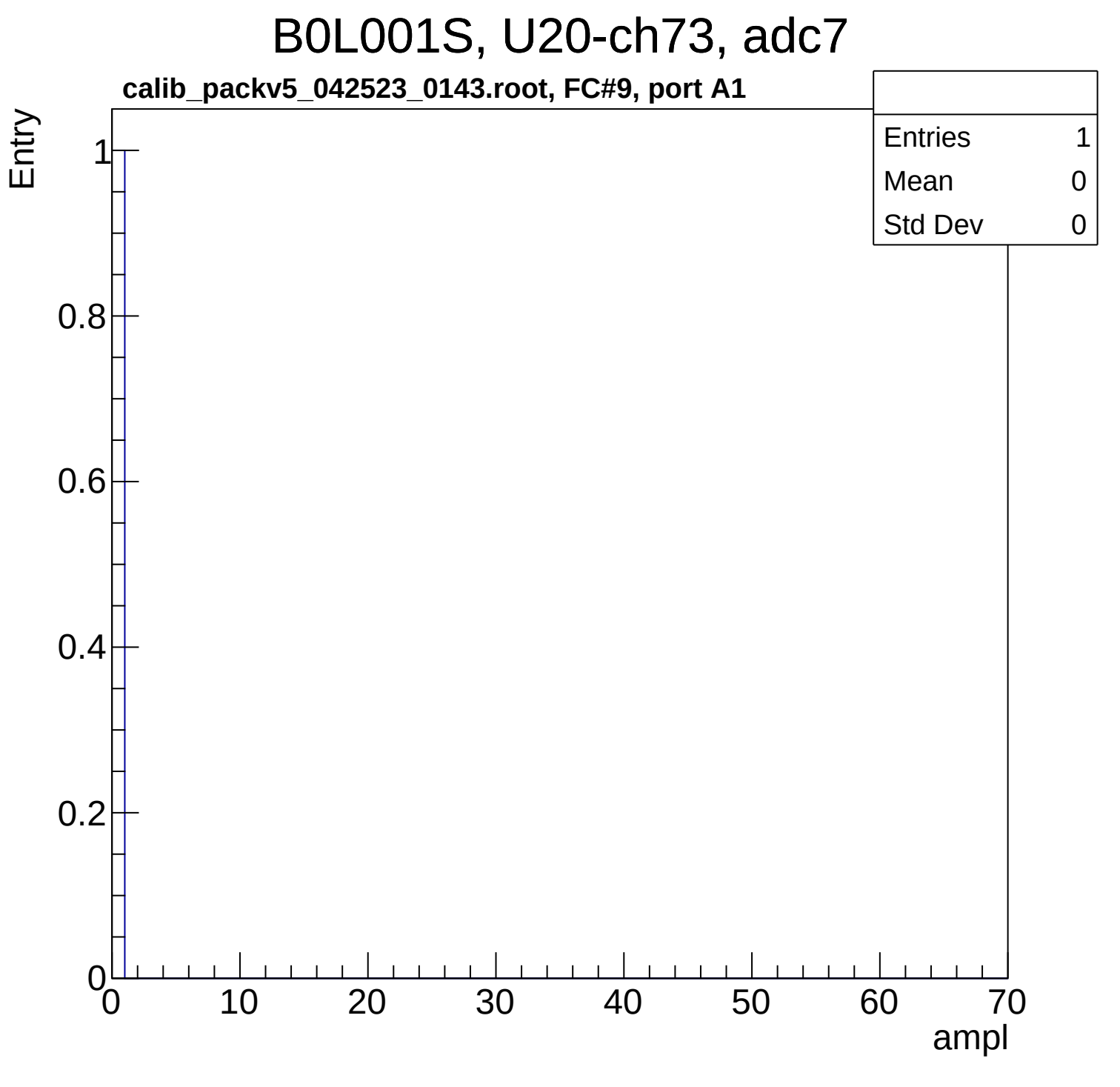
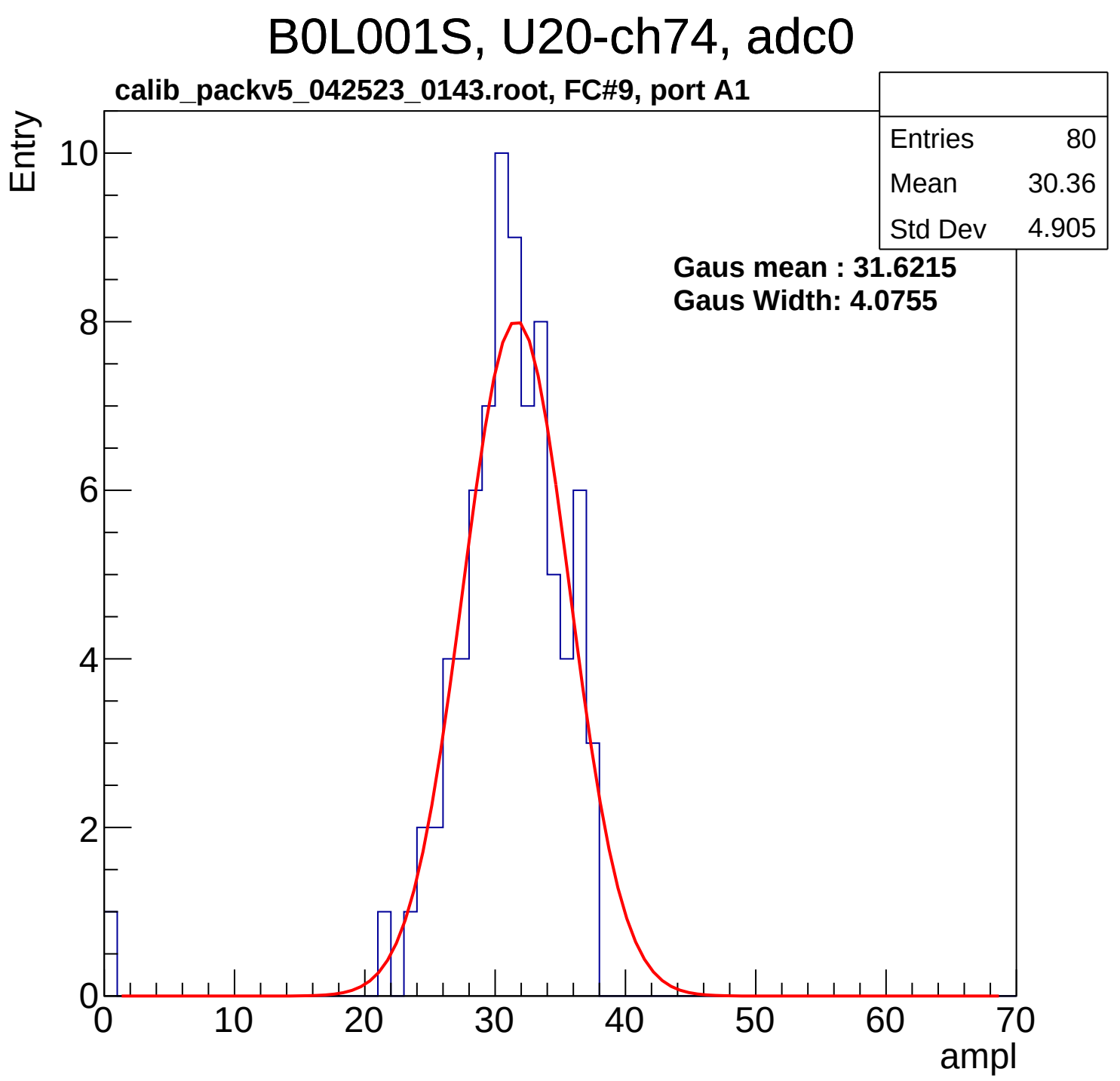
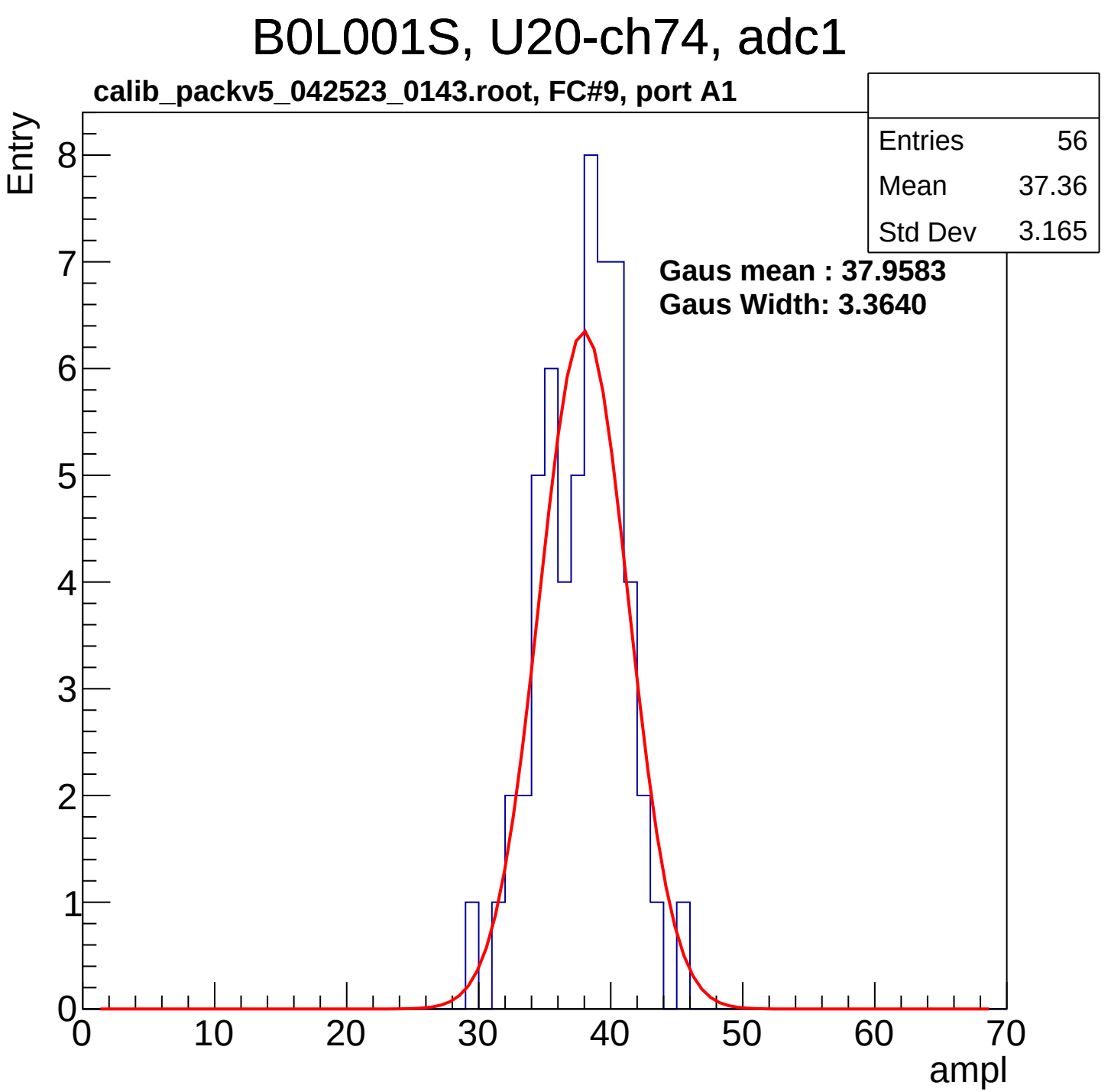
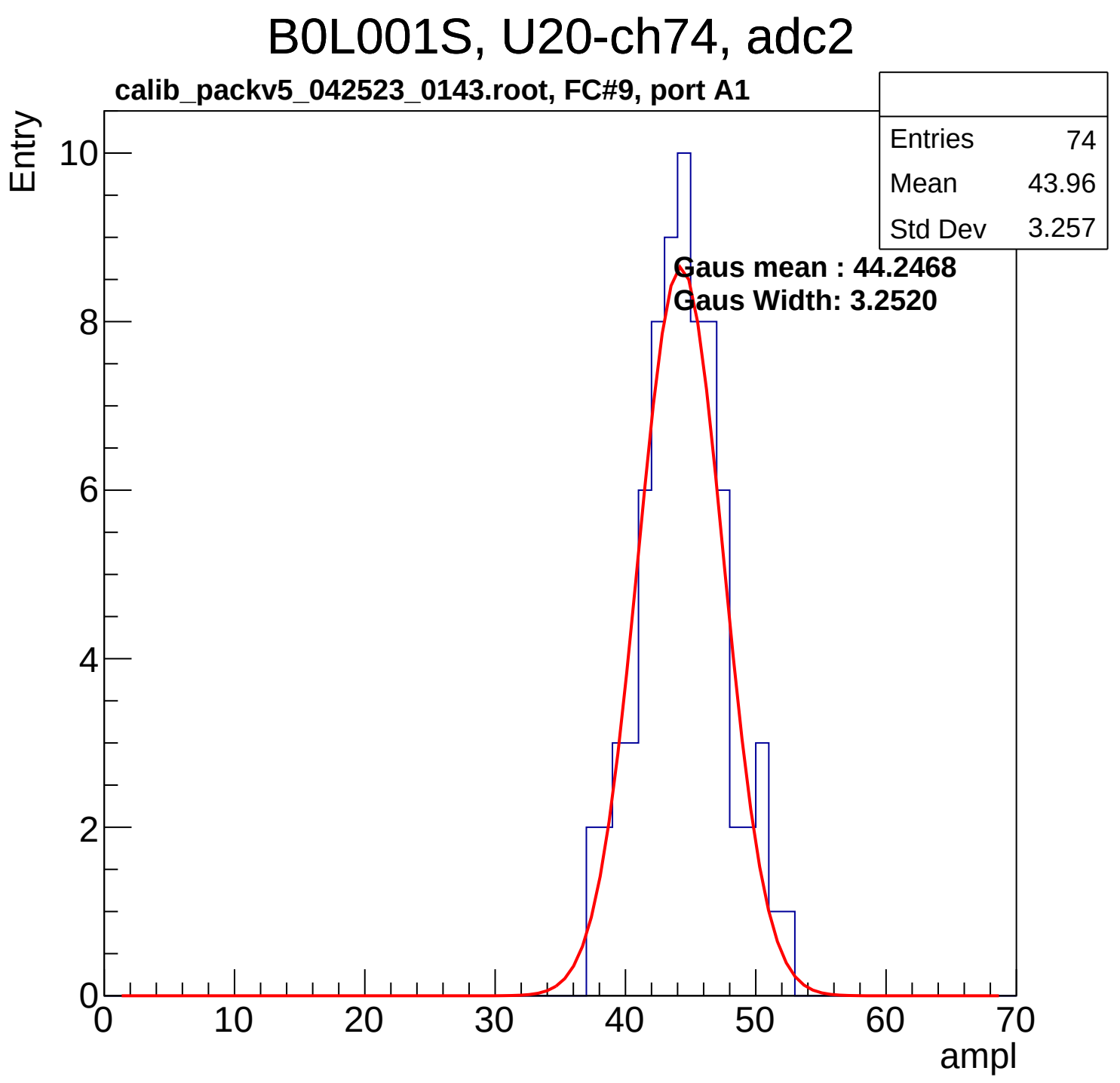

B0L001S, U20-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.36
Std Dev 4.905
Gaus mean : 31.6215
Gaus Width: 4.0755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 37.36
Std Dev 3.165
Gaus mean : 37.9583
Gaus Width: 3.3640
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.96
Std Dev 3.257
Gaus mean : 44.2468
Gaus Width: 3.2520
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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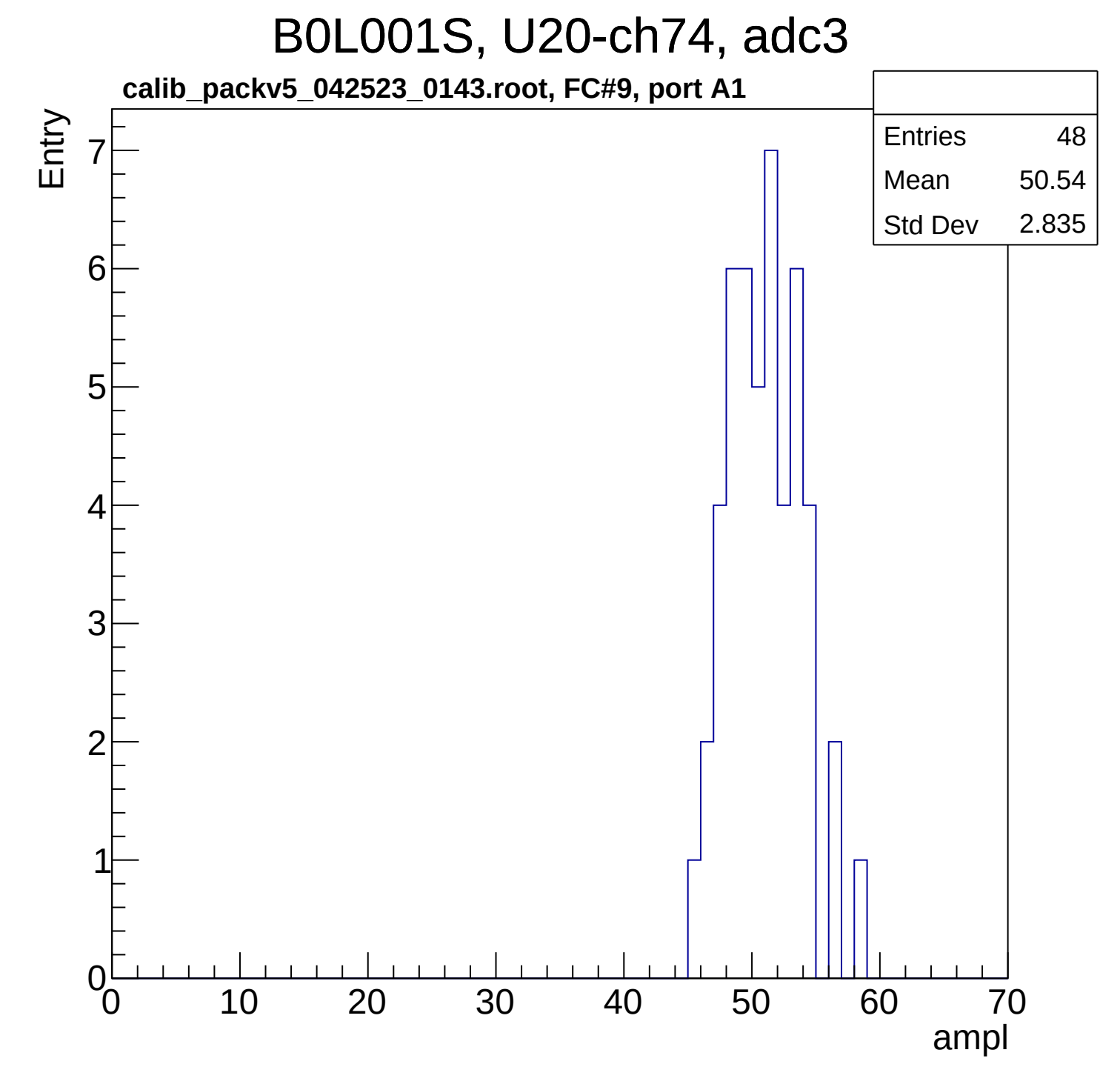
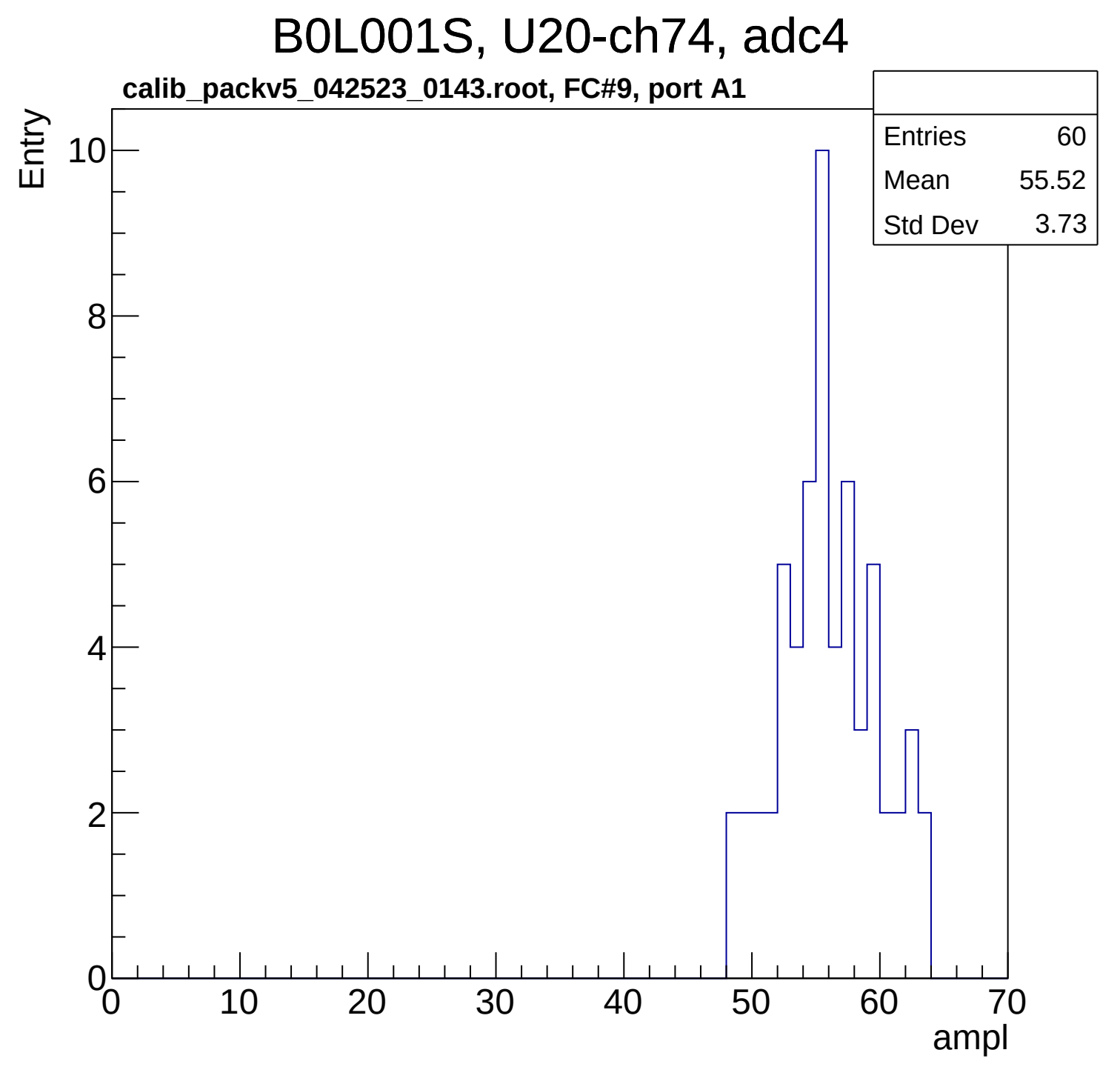
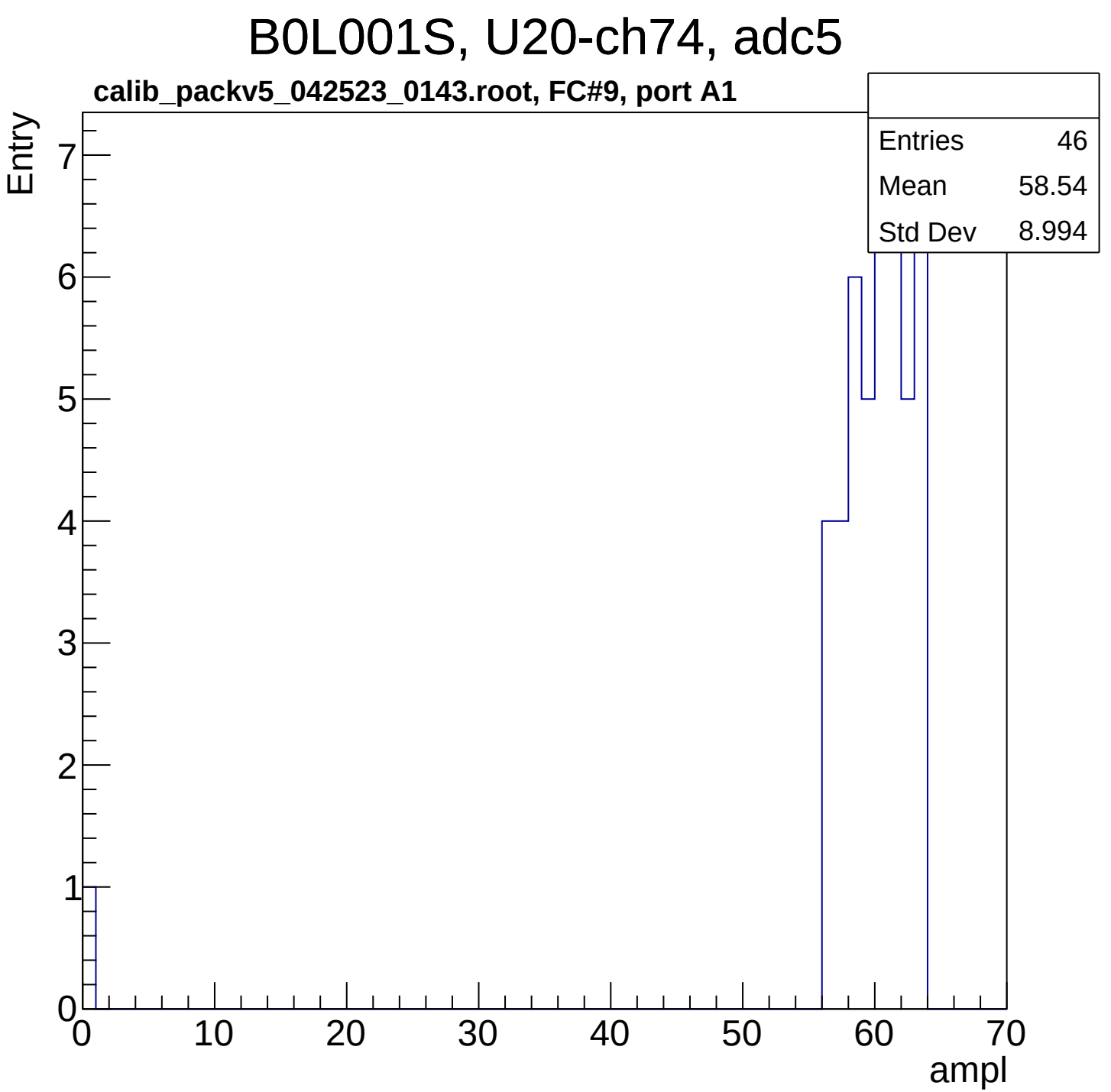
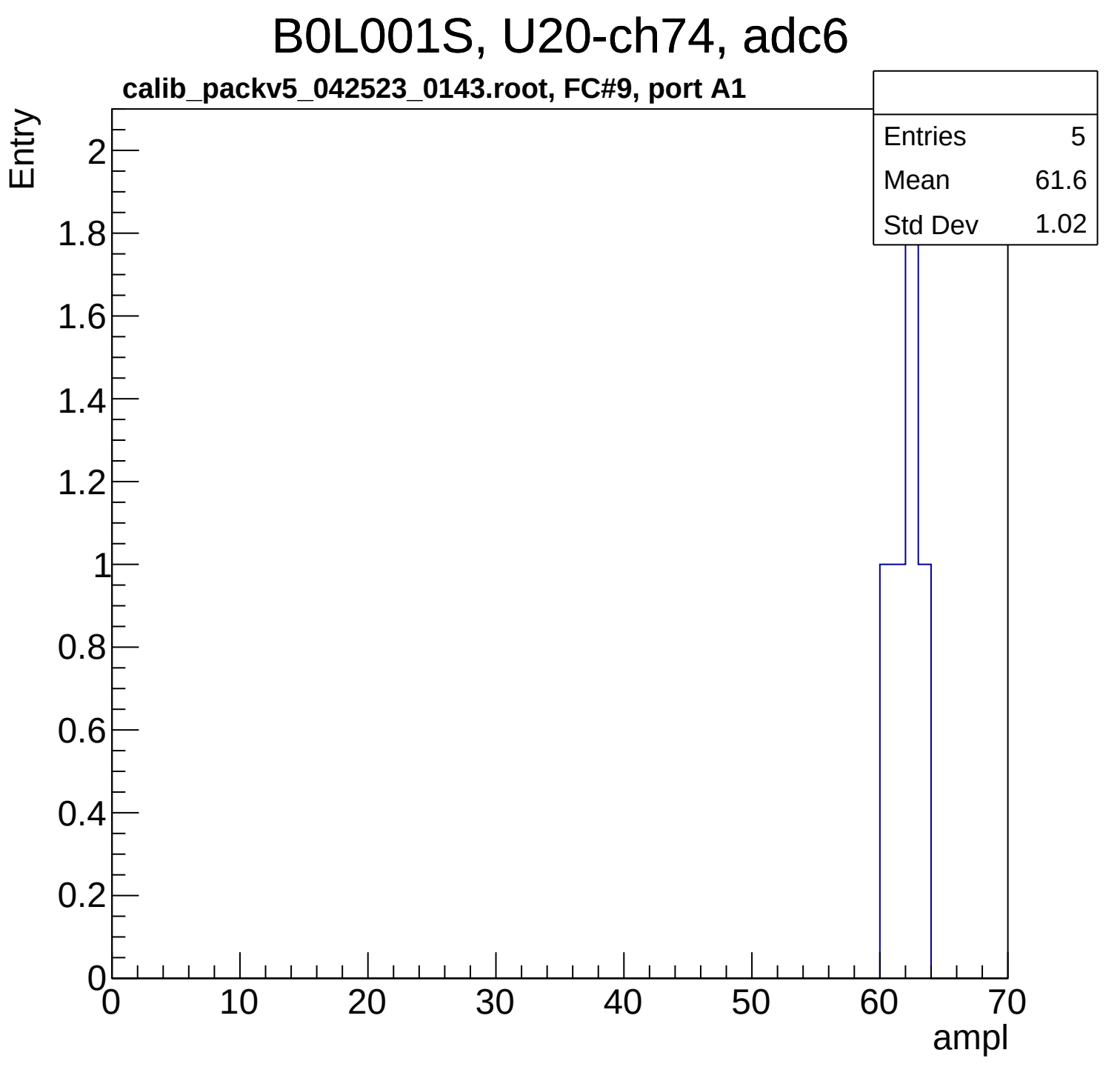
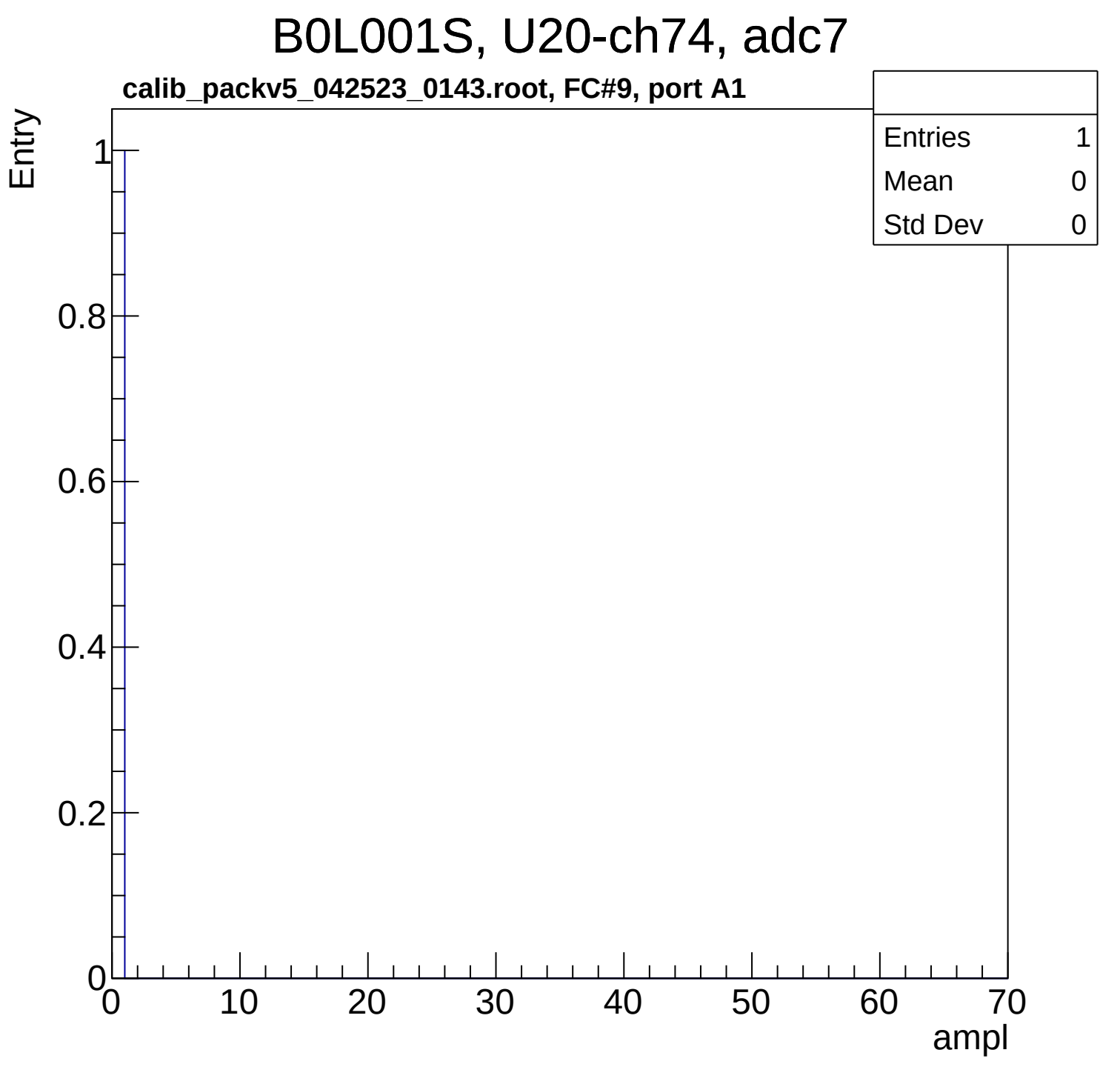
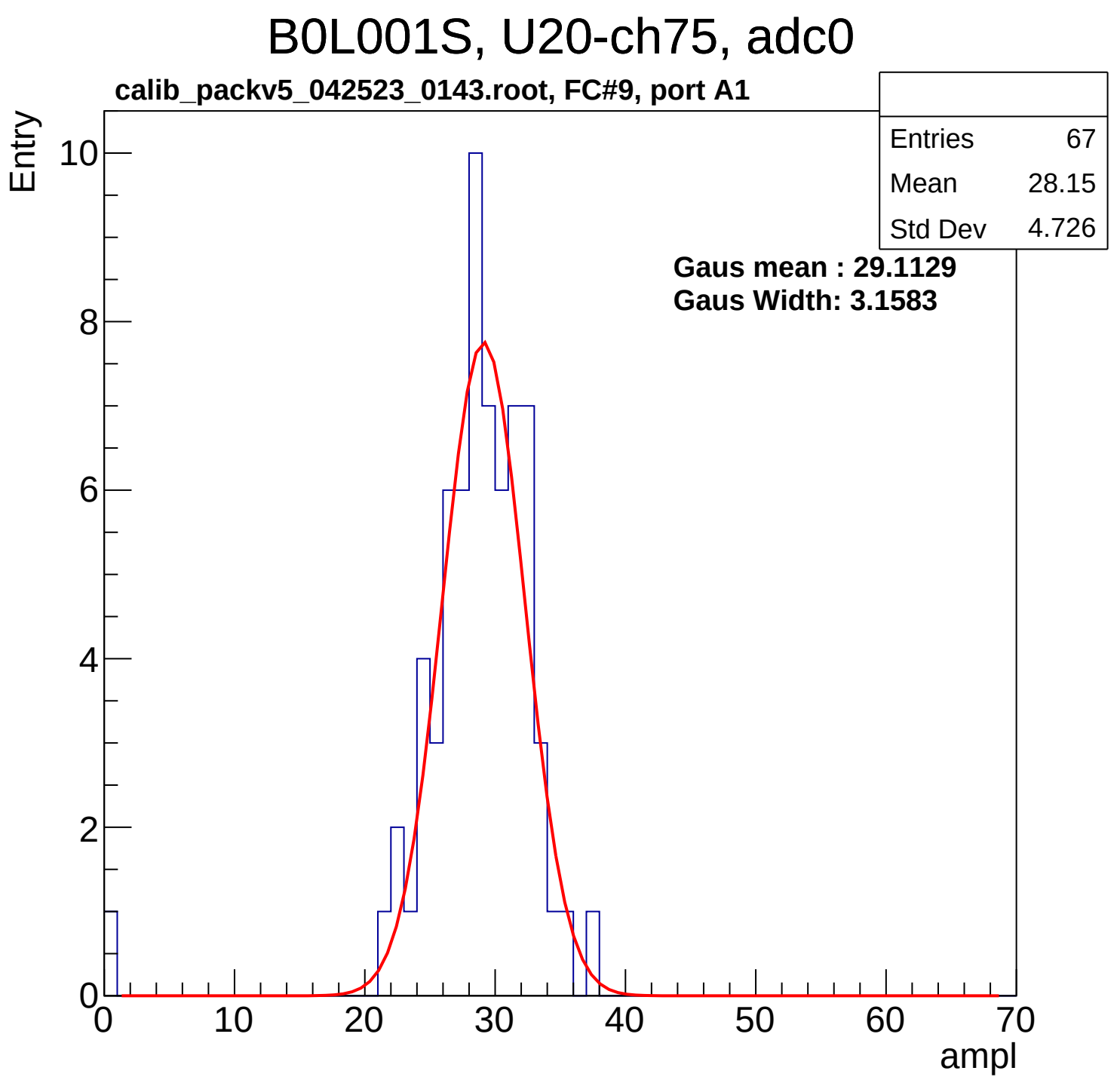

B0L001S, U20-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 50.54
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

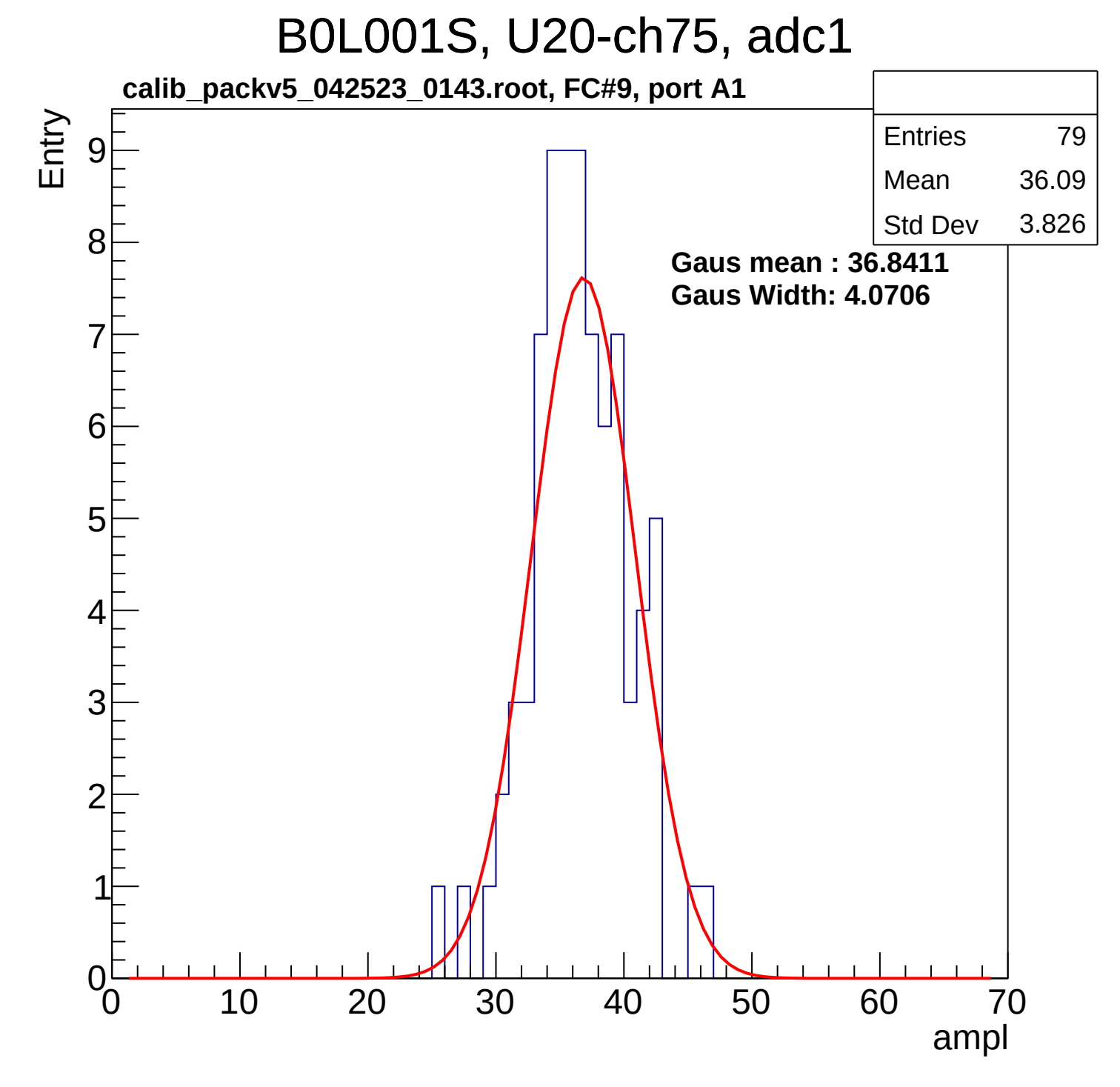

B0L001S, U20-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.52
Std Dev 3.73
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

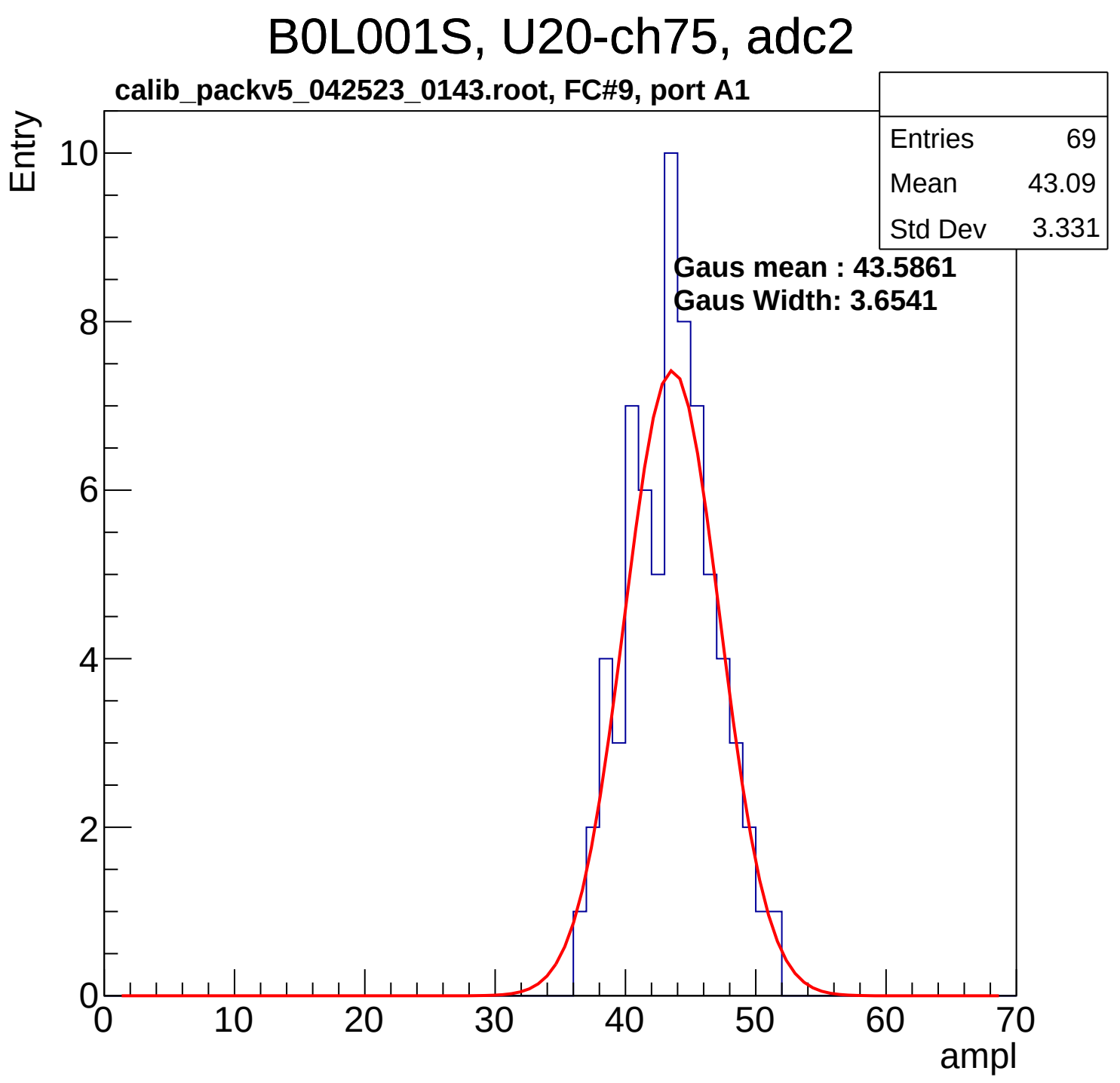

B0L001S, U20-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.54
Std Dev 8.994
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

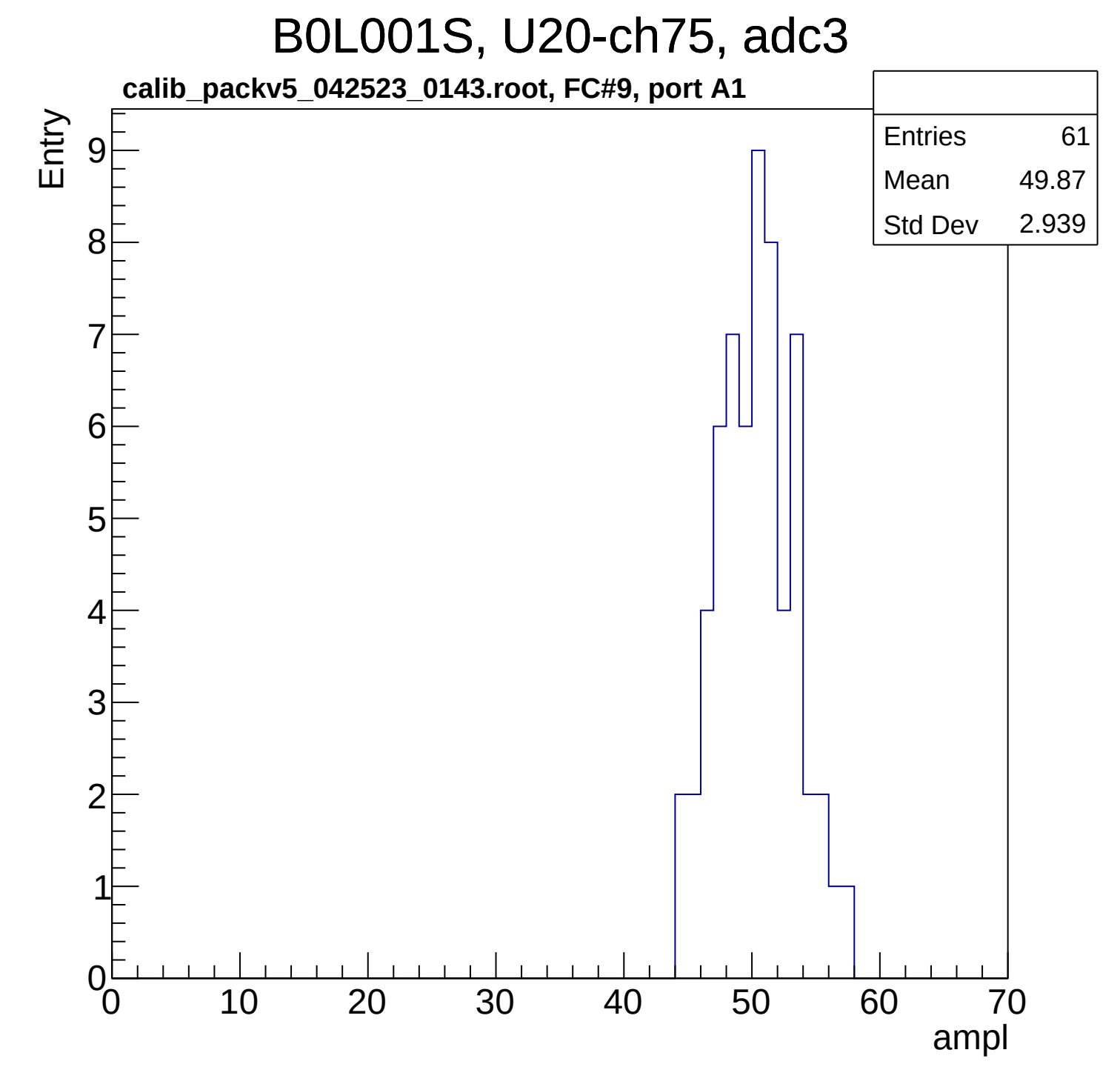

B0L001S, U20-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

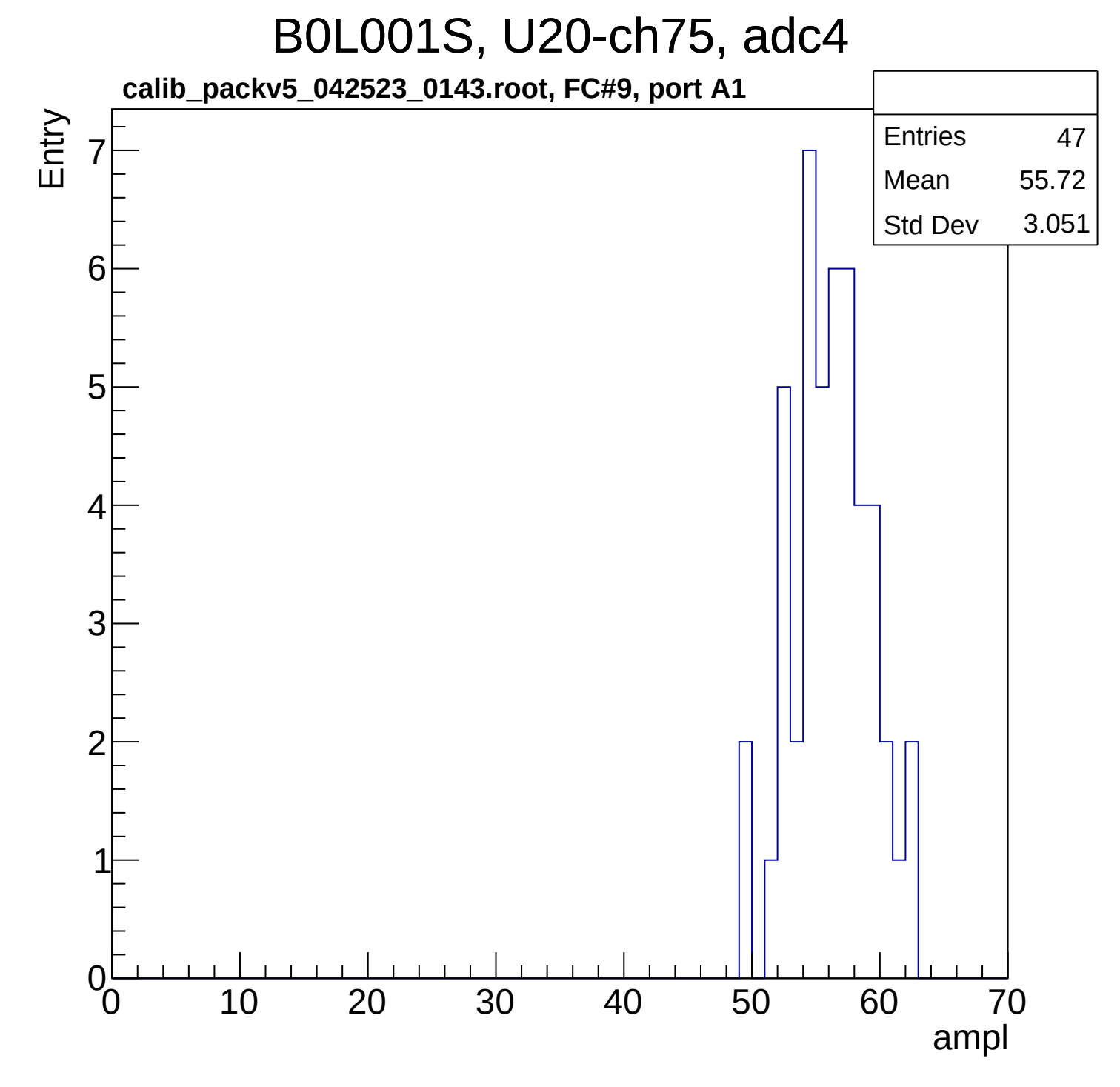

B0L001S, U20-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

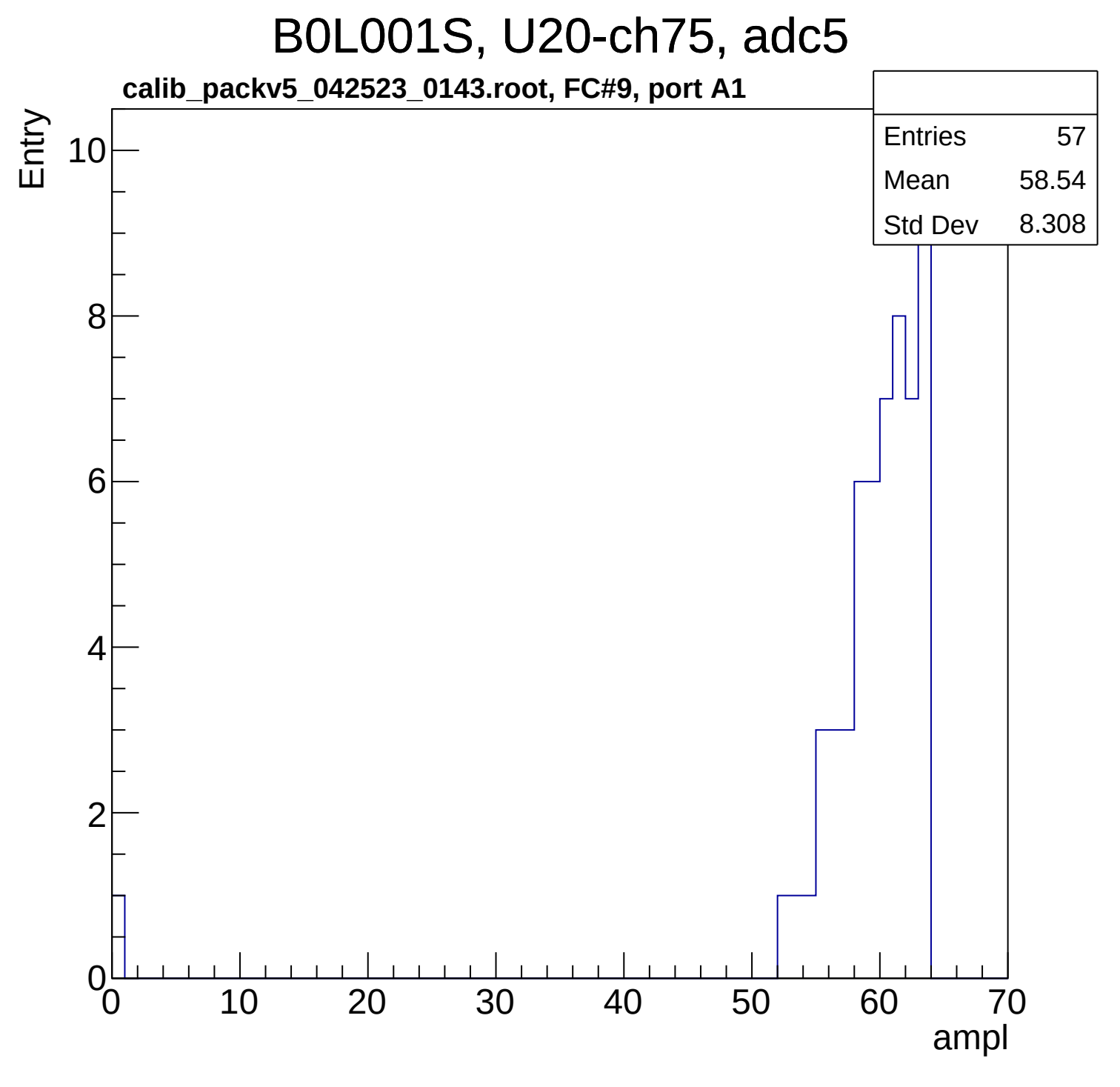

B0L001S, U20-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 28.15
Std Dev 4.726
Gaus mean : 29.1129
Gaus Width: 3.1583
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

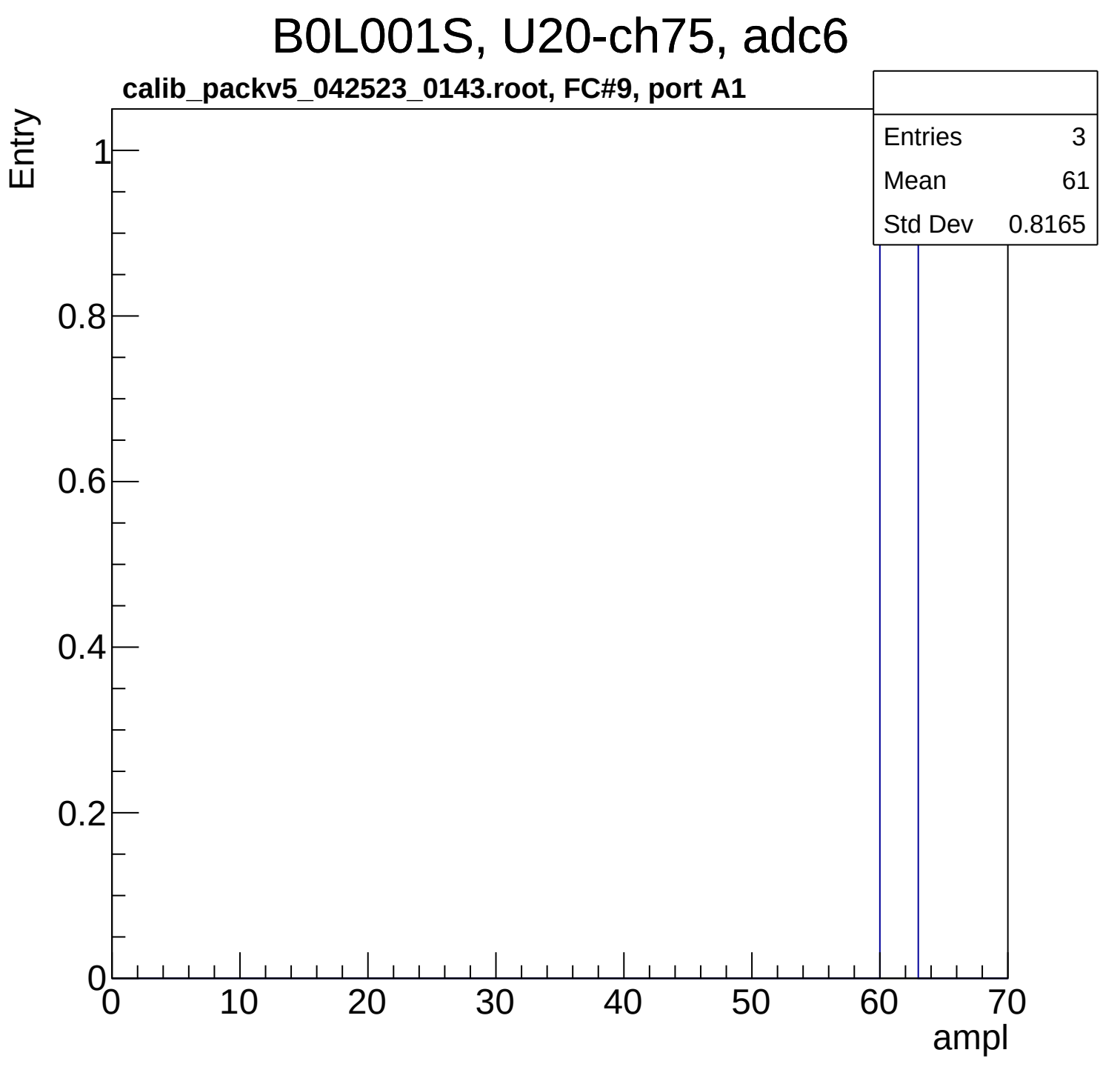

B0L001S, U20-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 36.09
Std Dev 3.826
Gaus mean : 36.8411
Gaus Width: 4.0706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

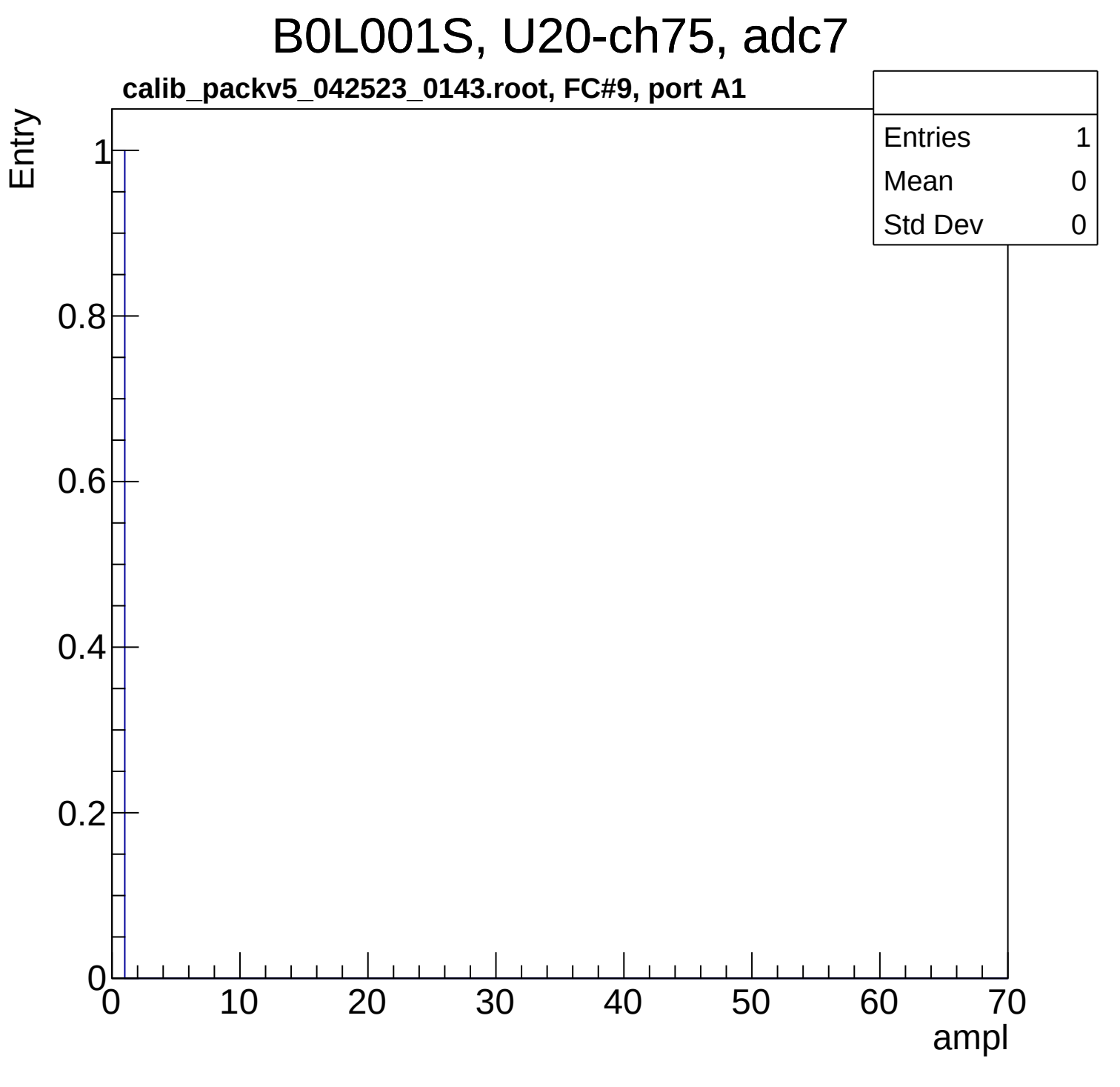

B0L001S, U20-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.09
Std Dev 3.331
Gaus mean : 43.5861
Gaus Width: 3.6541
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

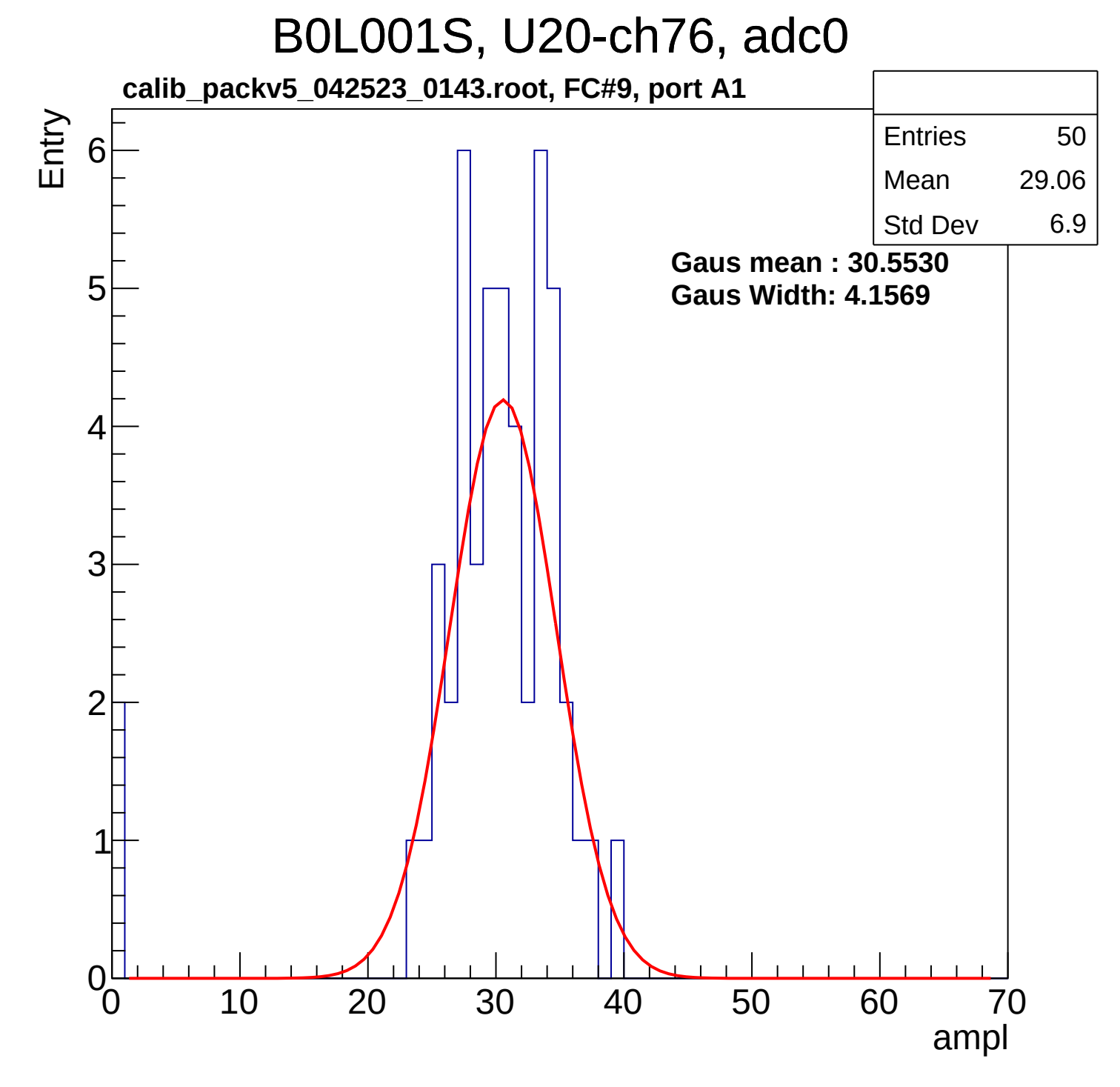

B0L001S, U20-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.87
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

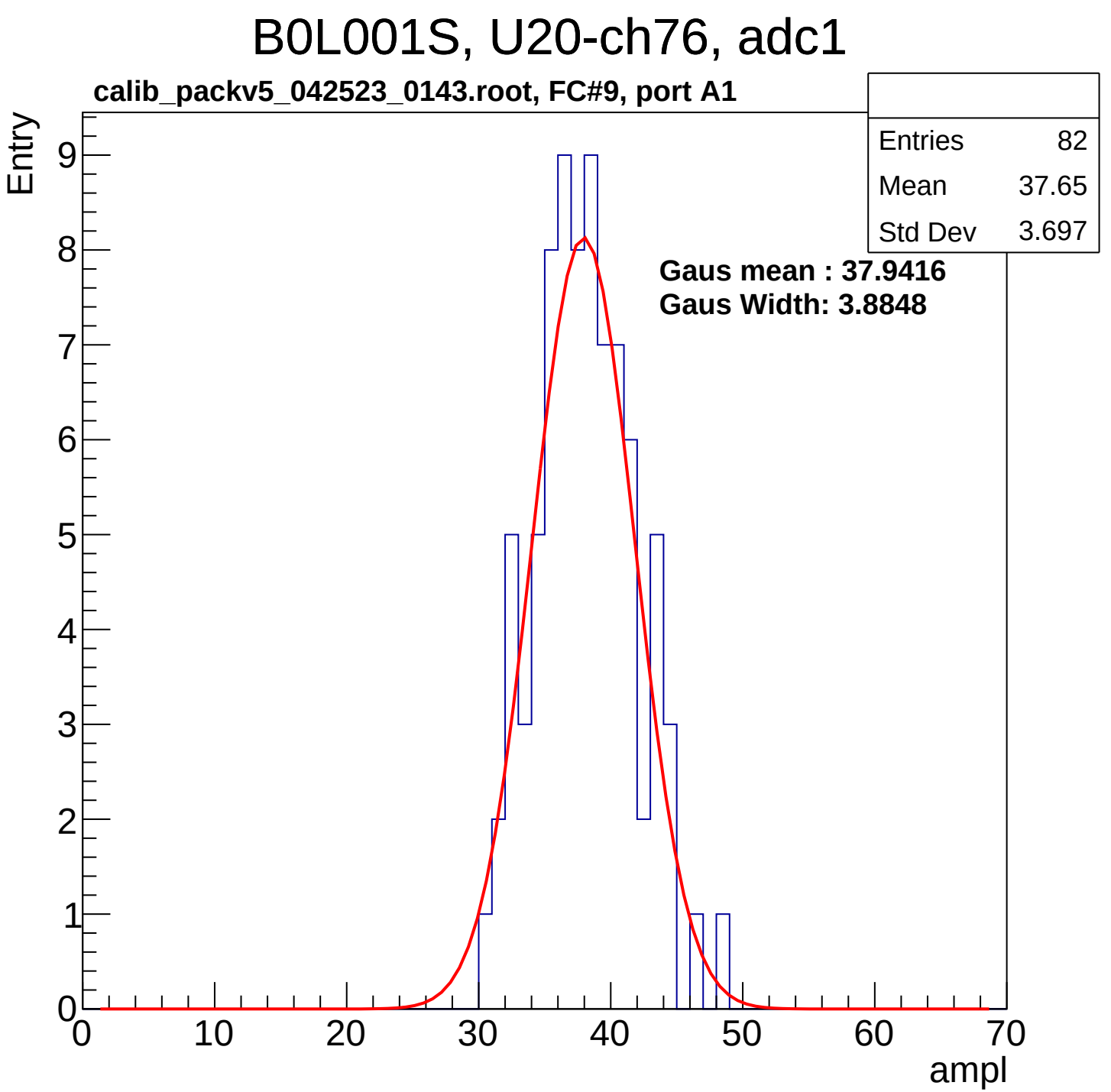

B0L001S, U20-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 55.72
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

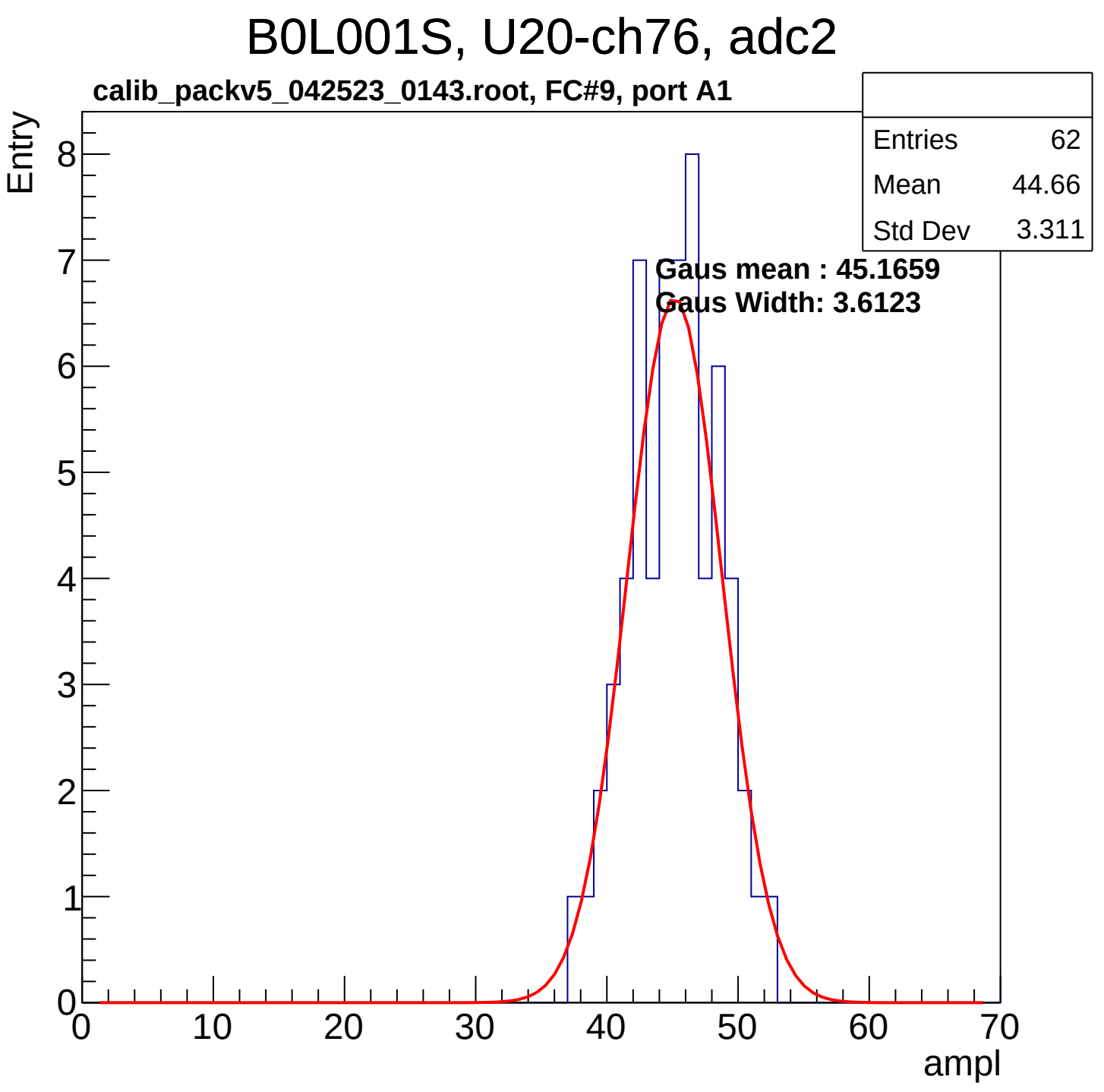

B0L001S, U20-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.54
Std Dev 8.308
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

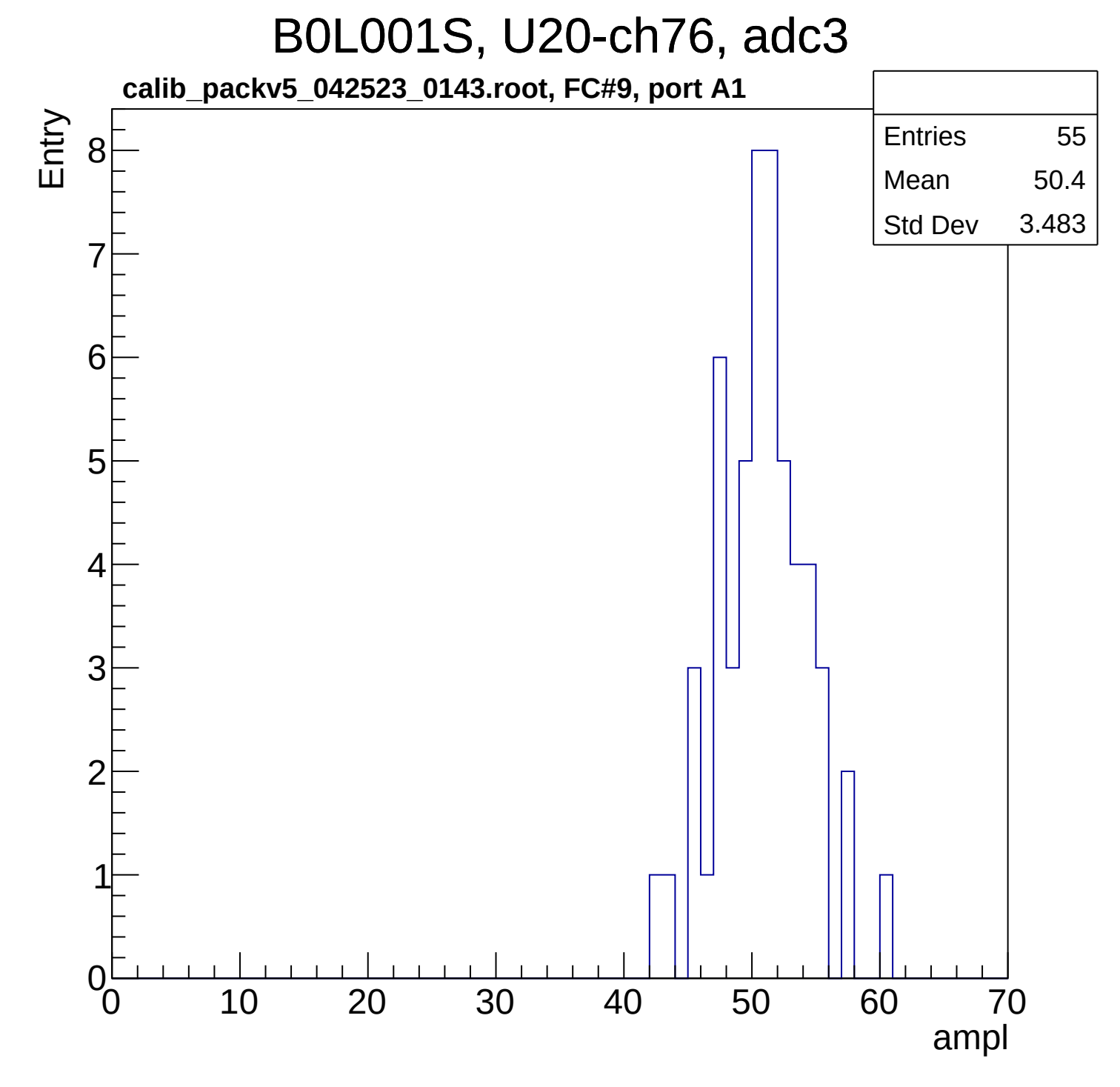

B0L001S, U20-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 29.06
Std Dev 6.9
Gaus mean : 30.5530
Gaus Width: 4.1569
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 37.65
Std Dev 3.697
Gaus mean : 37.9416
Gaus Width: 3.8848
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U20-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.66
Std Dev 3.311
Gaus mean : 45.1659
Gaus Width: 3.6123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.4
Std Dev 3.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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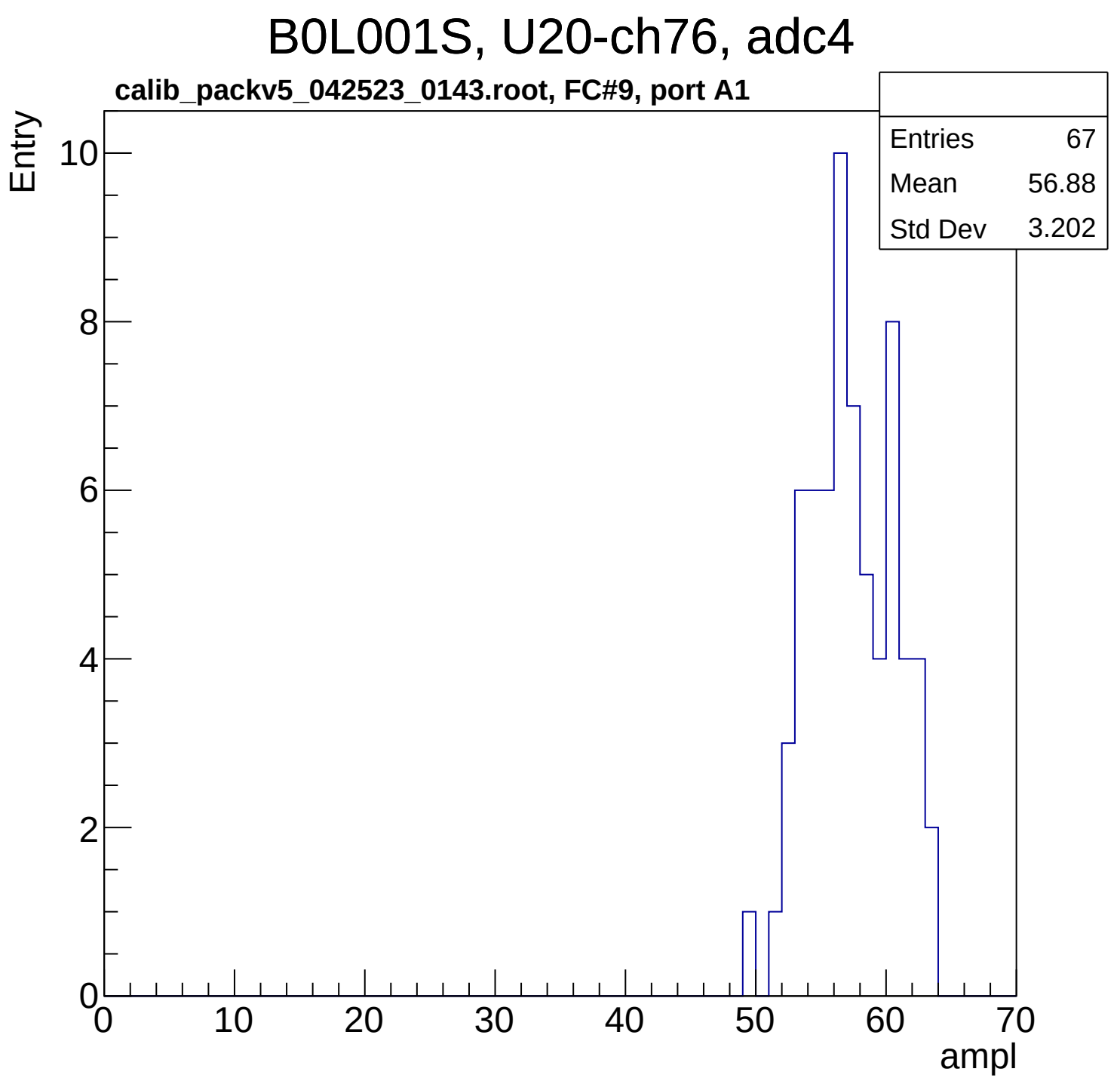
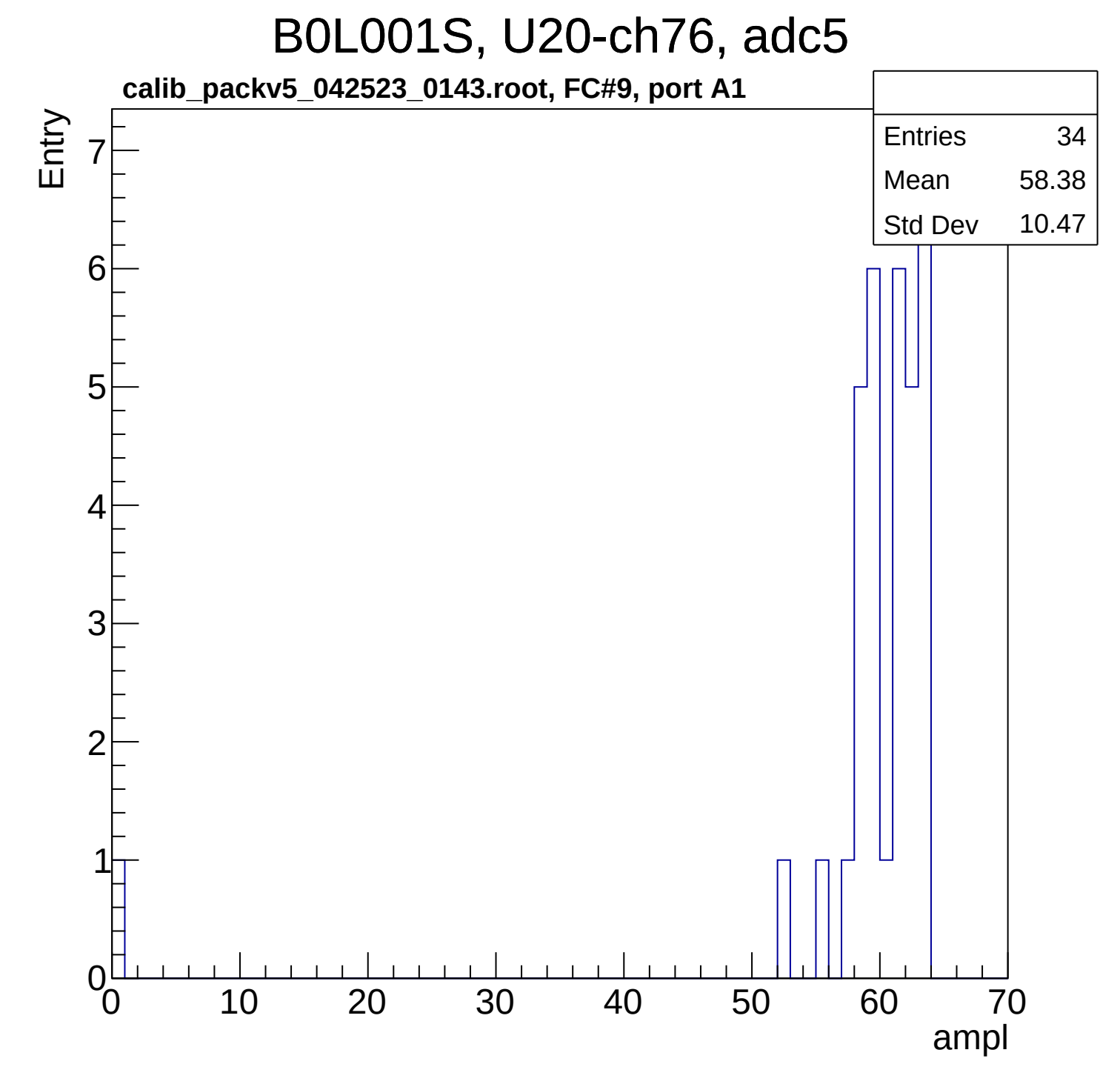
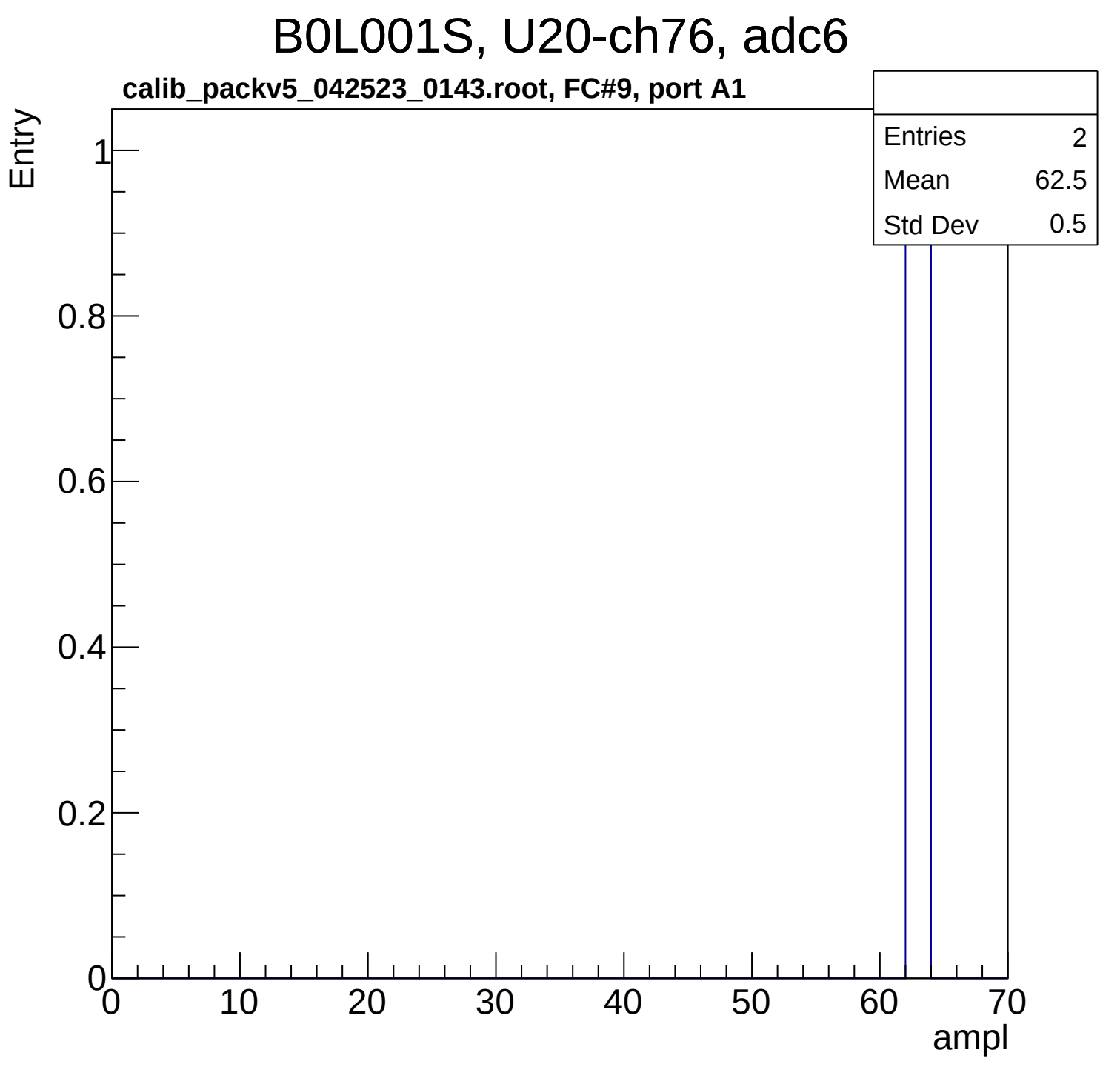
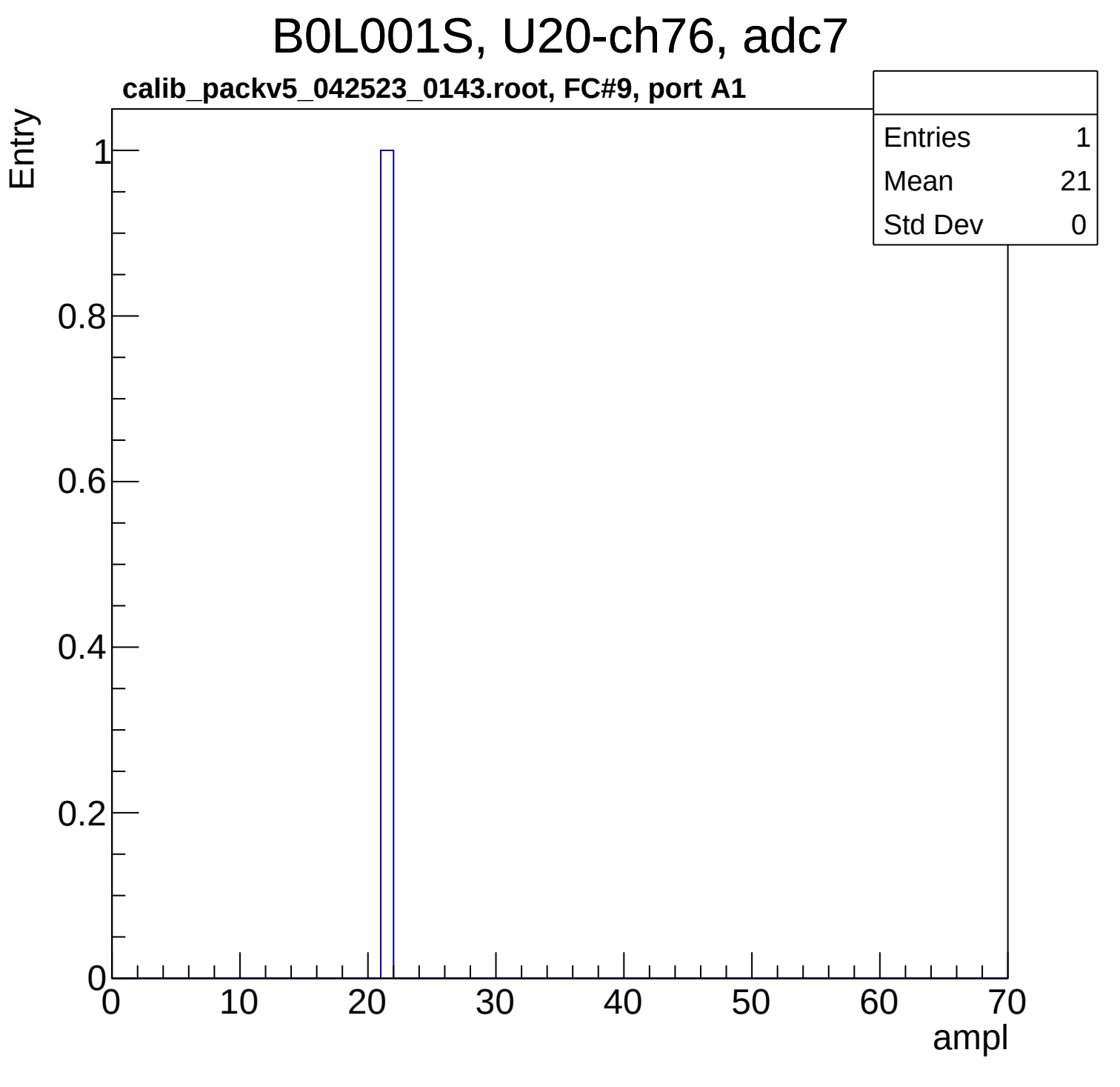
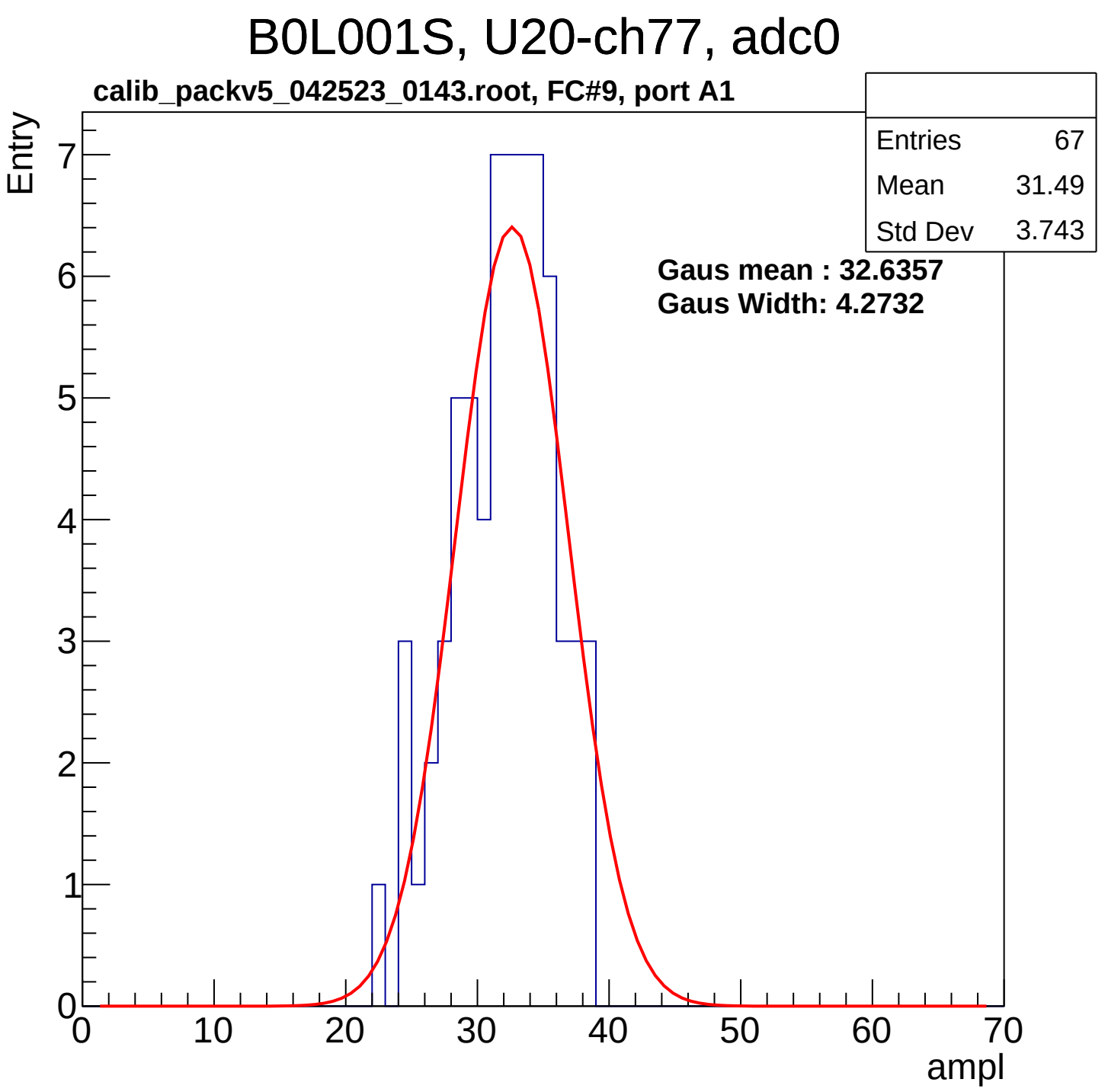
20
30
40
50
60
70

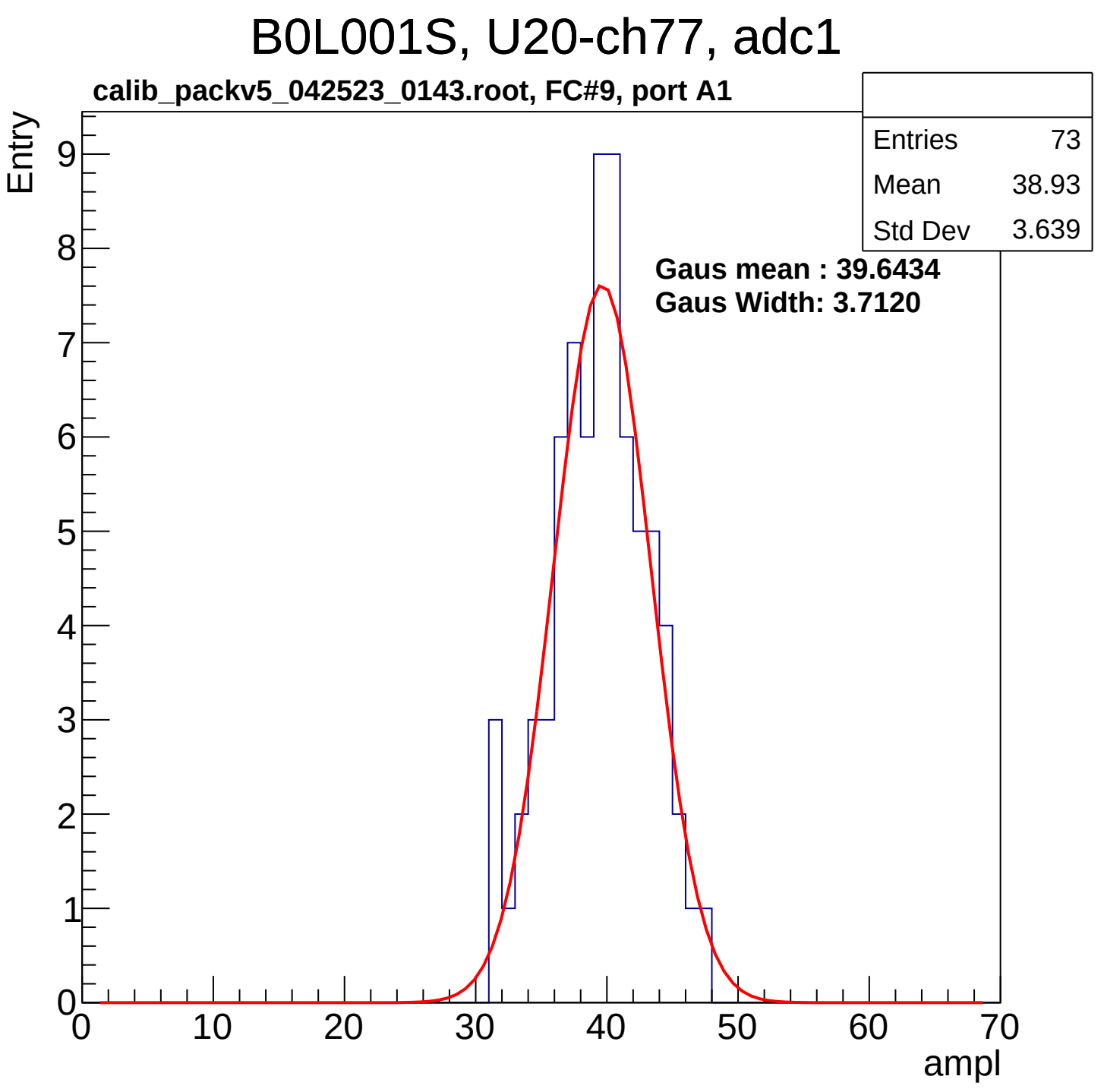

B0L001S, U20-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.88
Std Dev 3.202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

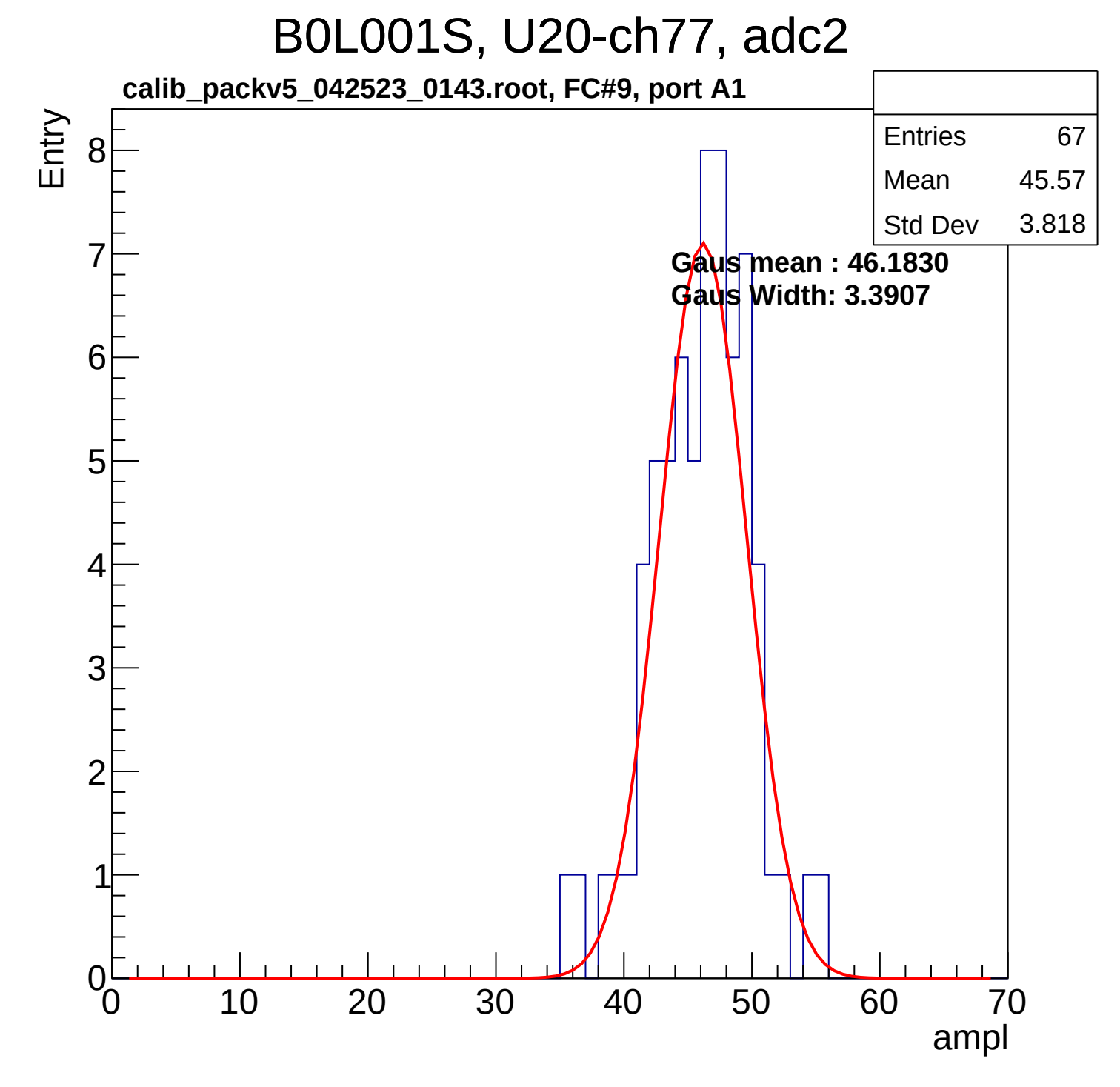

B0L001S, U20-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.38
Std Dev 10.47
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.49
Std Dev 3.743
Gaus mean : 32.6357
Gaus Width: 4.2732
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.93
Std Dev 3.639
Gaus mean : 39.6434
Gaus Width: 3.7120
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.57
Std Dev 3.818
Gaus mean : 46.1830
Gaus Width: 3.3907
Entry
ampl
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1
2
3
4
5
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7
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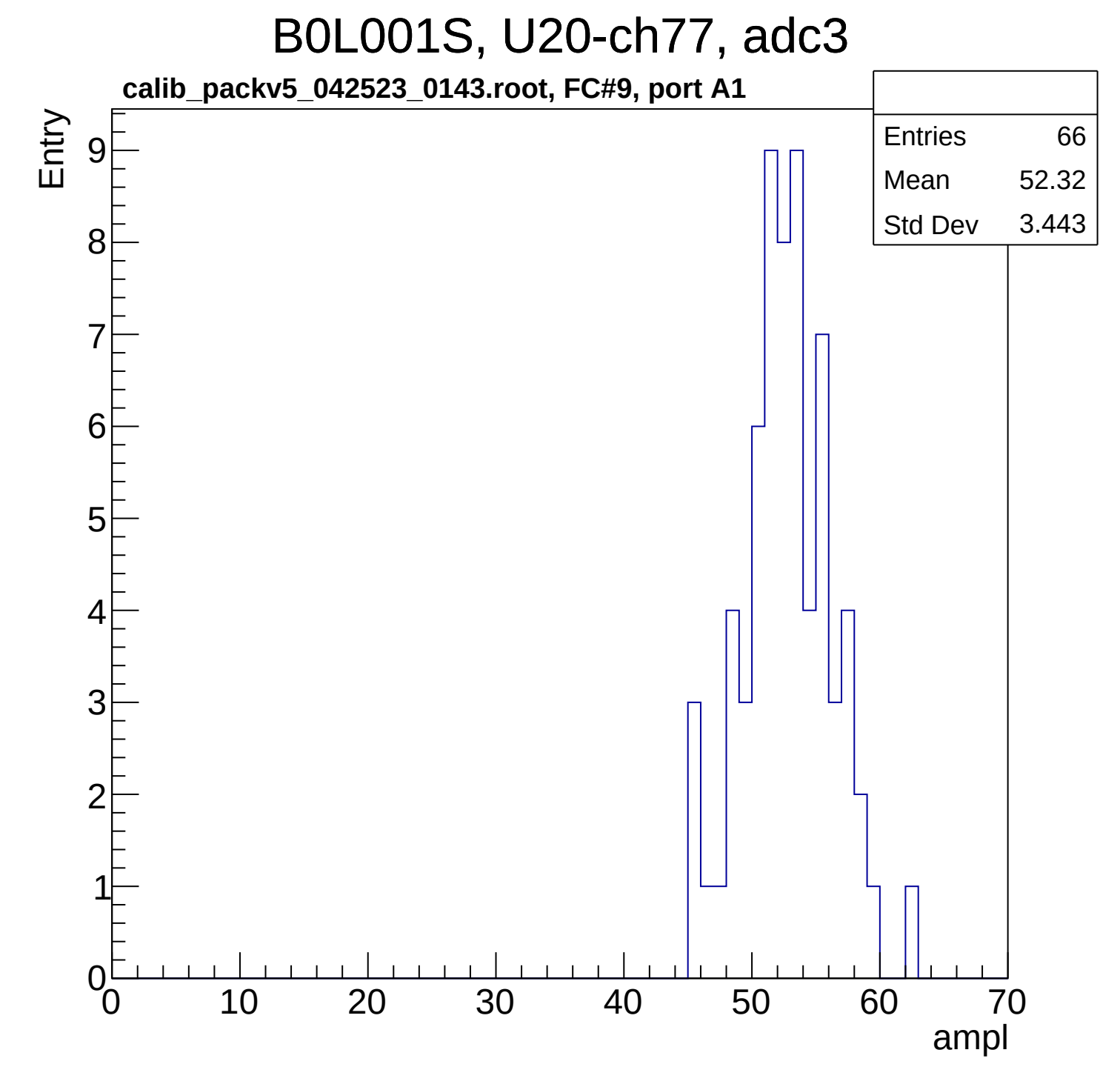
B0L001S, U20-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.32
Std Dev 3.443
Entry
ampl
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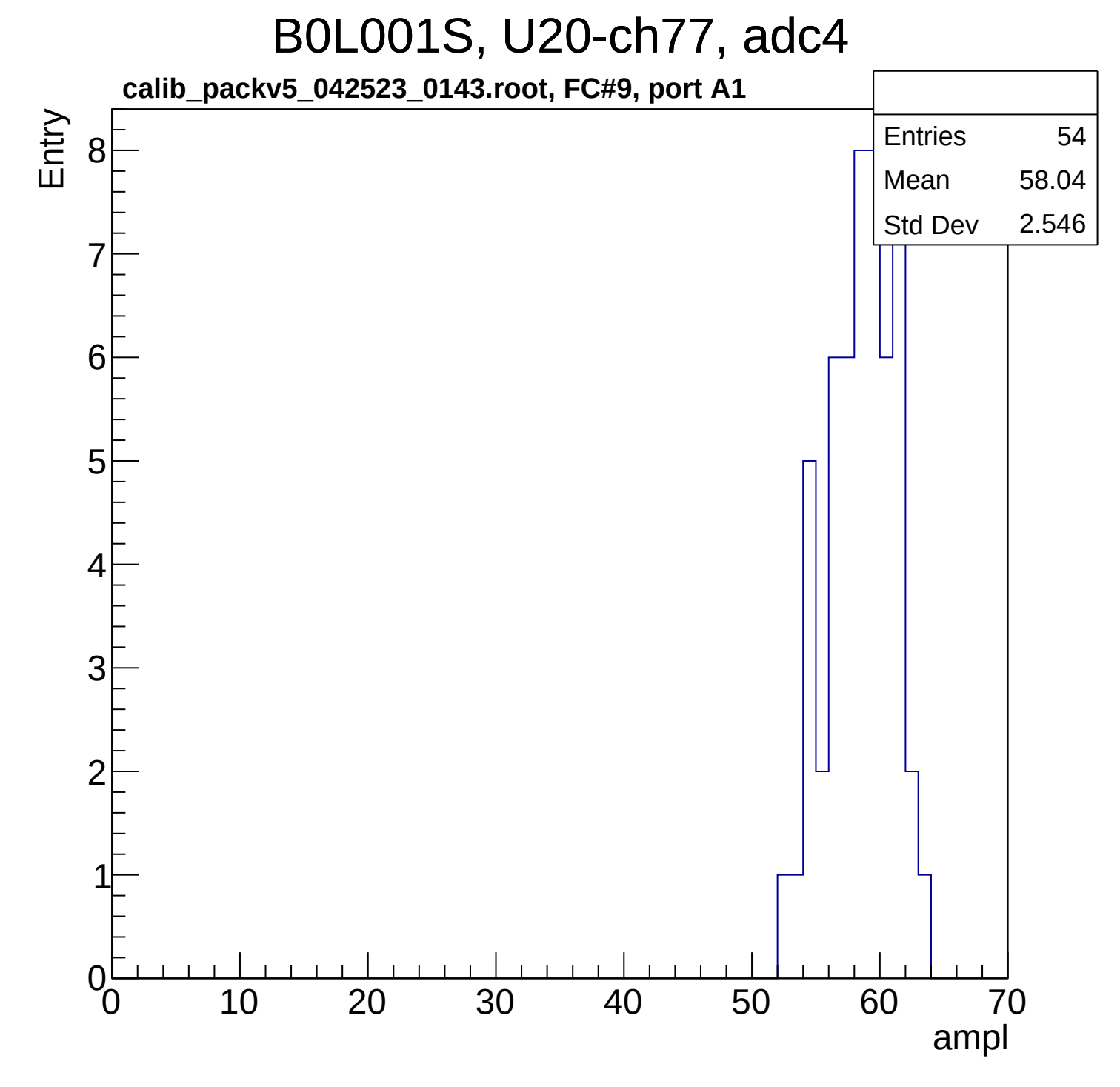
B0L001S, U20-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 58.04
Std Dev 2.546
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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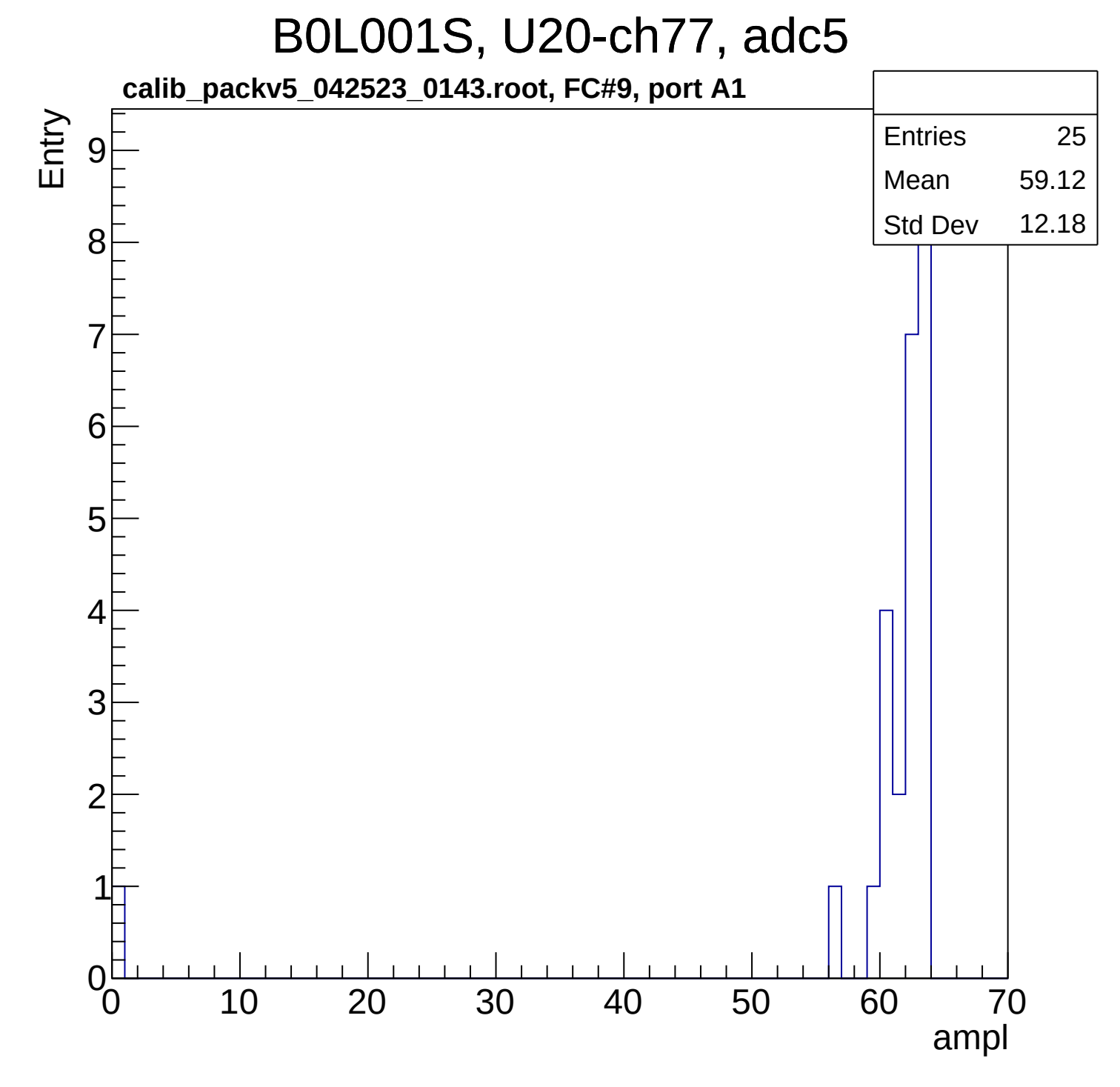
B0L001S, U20-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 59.12
Std Dev 12.18
Entry
ampl
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3
4
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6
7
8
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0
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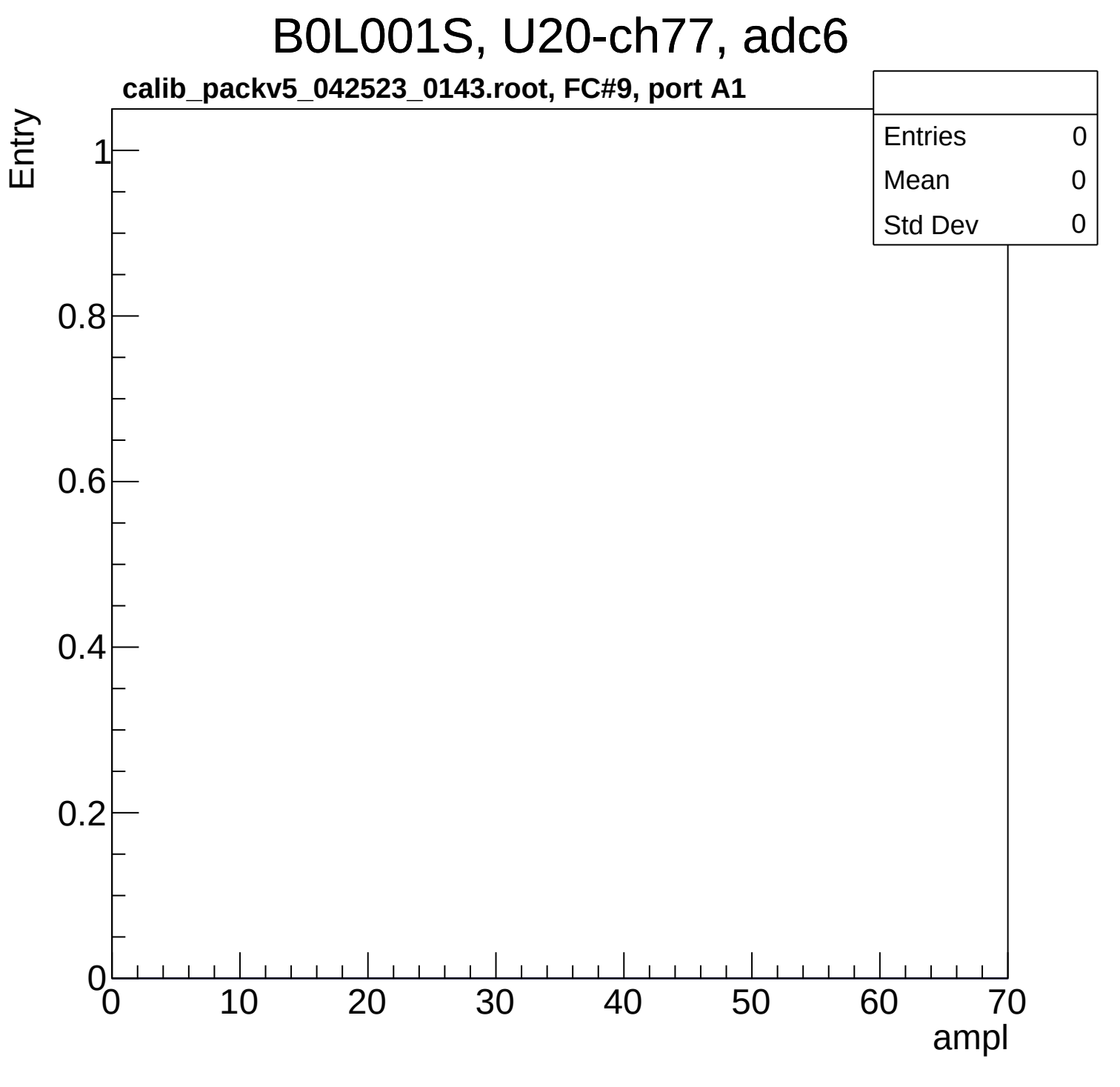
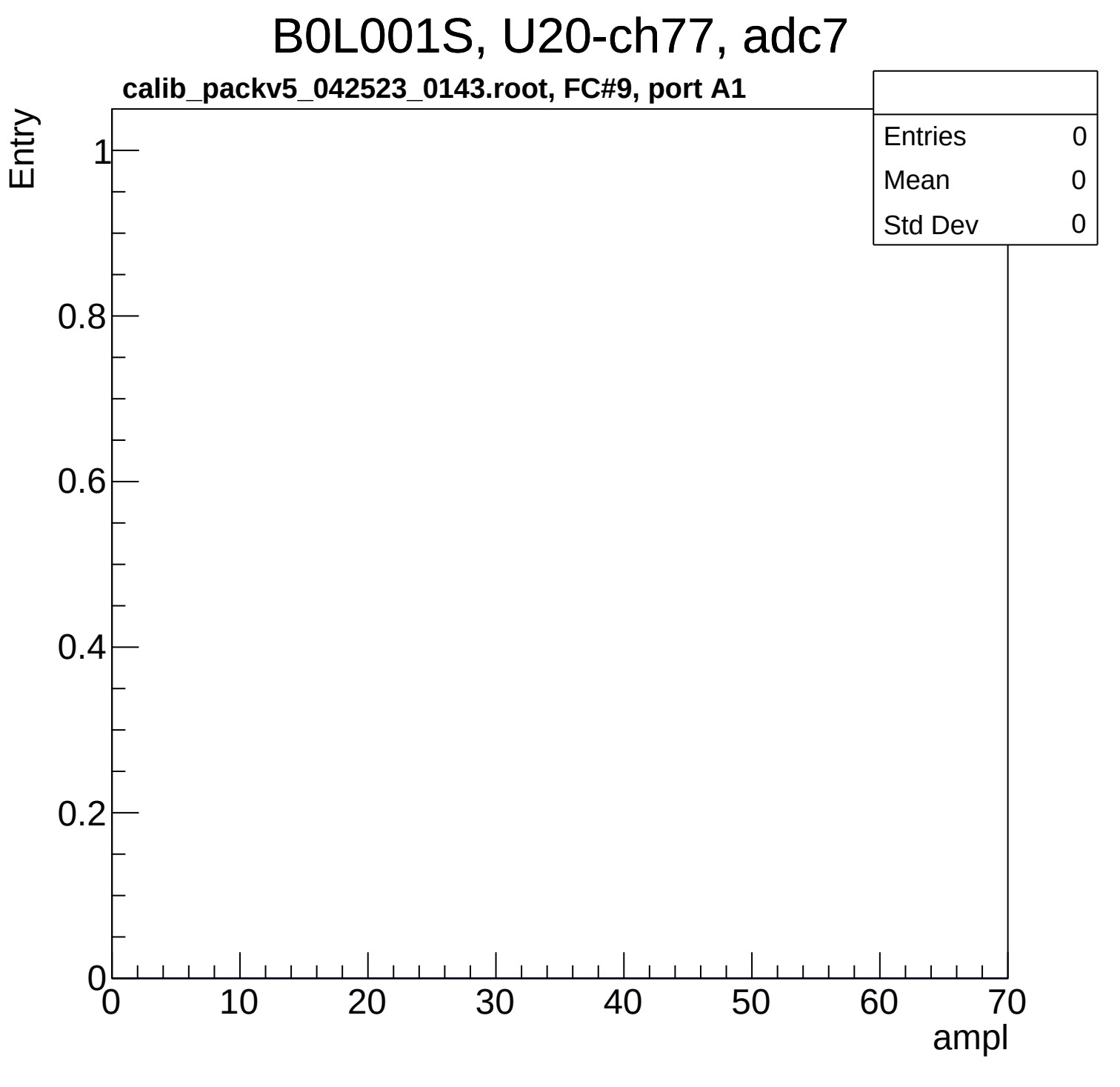

B0L001S, U20-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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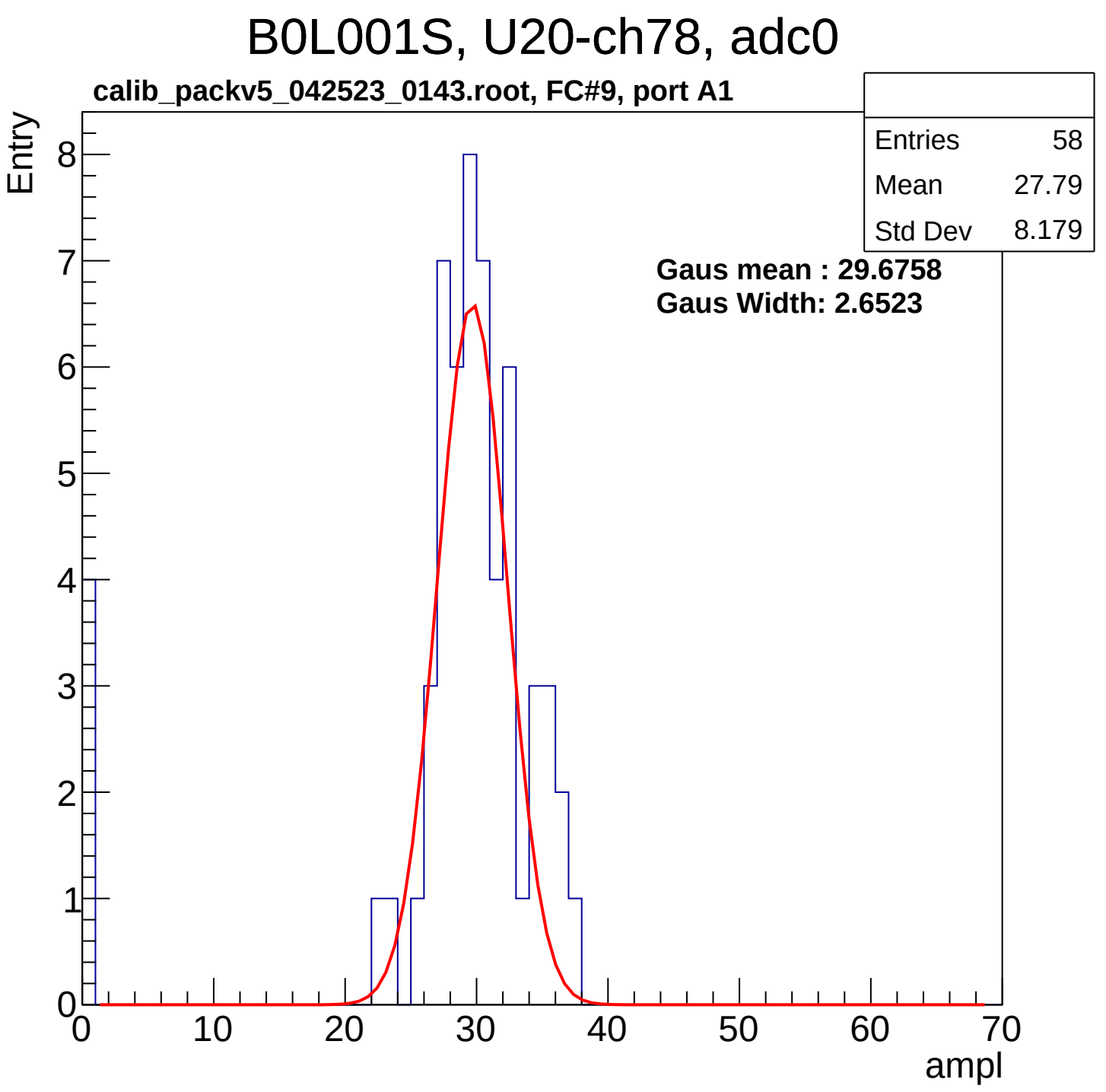
50
60
70
ampl


B0L001S, U20-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 27.79
Std Dev 8.179
Gaus mean : 29.6758
Gaus Width: 2.6523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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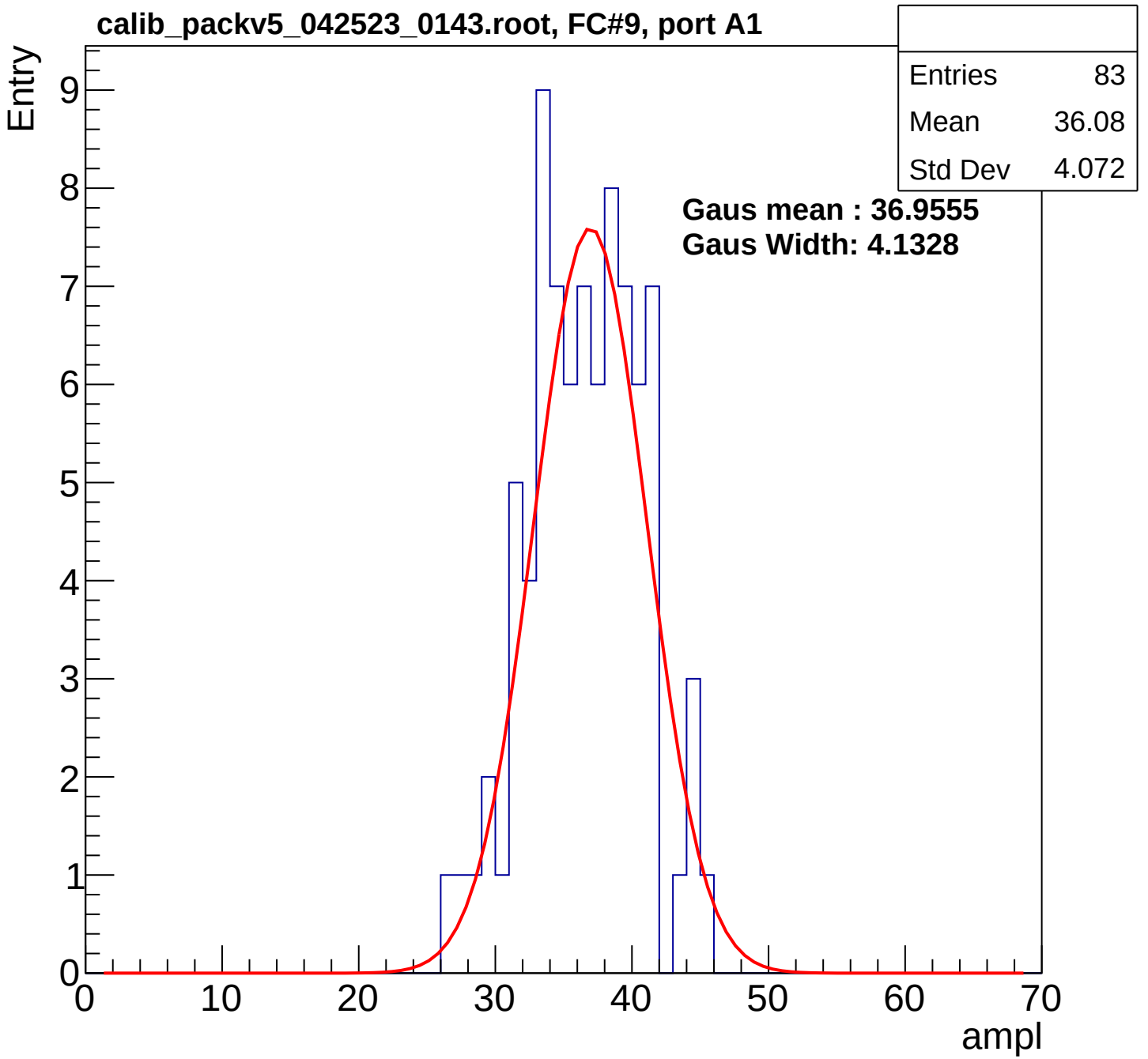
B0L001S, U20-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 36.08
Std Dev 4.072
Gaus mean : 36.9555
Gaus Width: 4.1328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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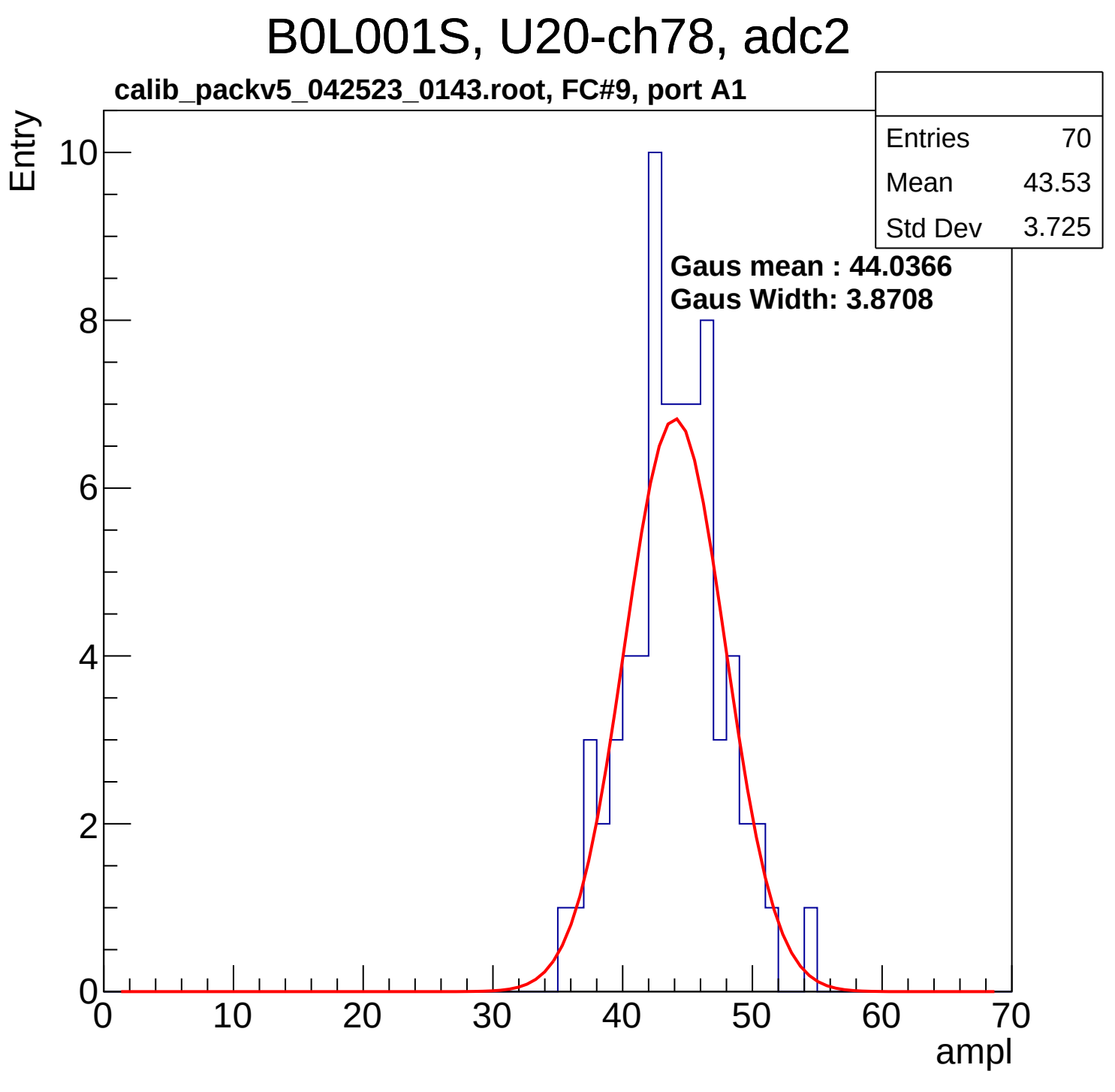
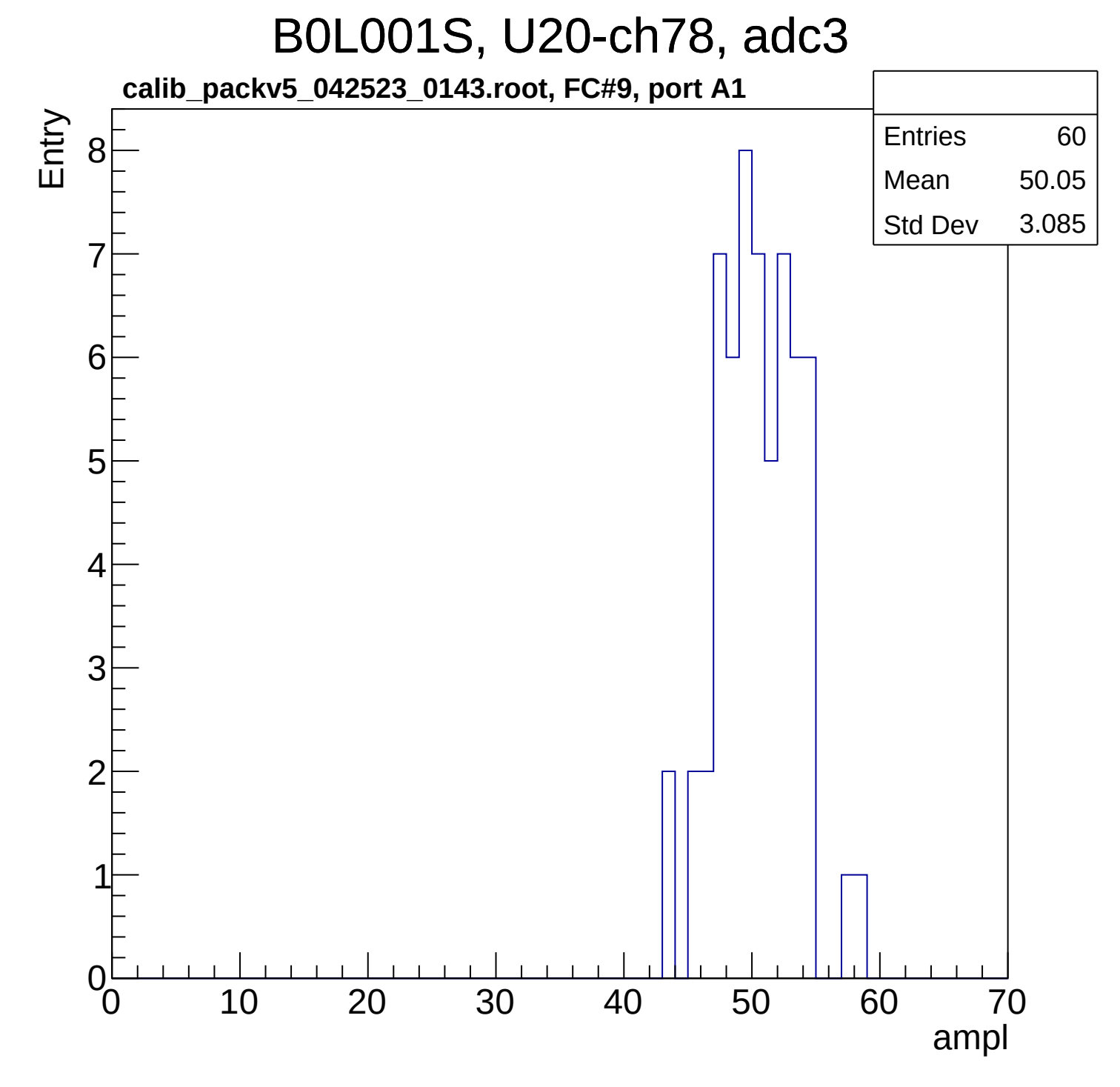
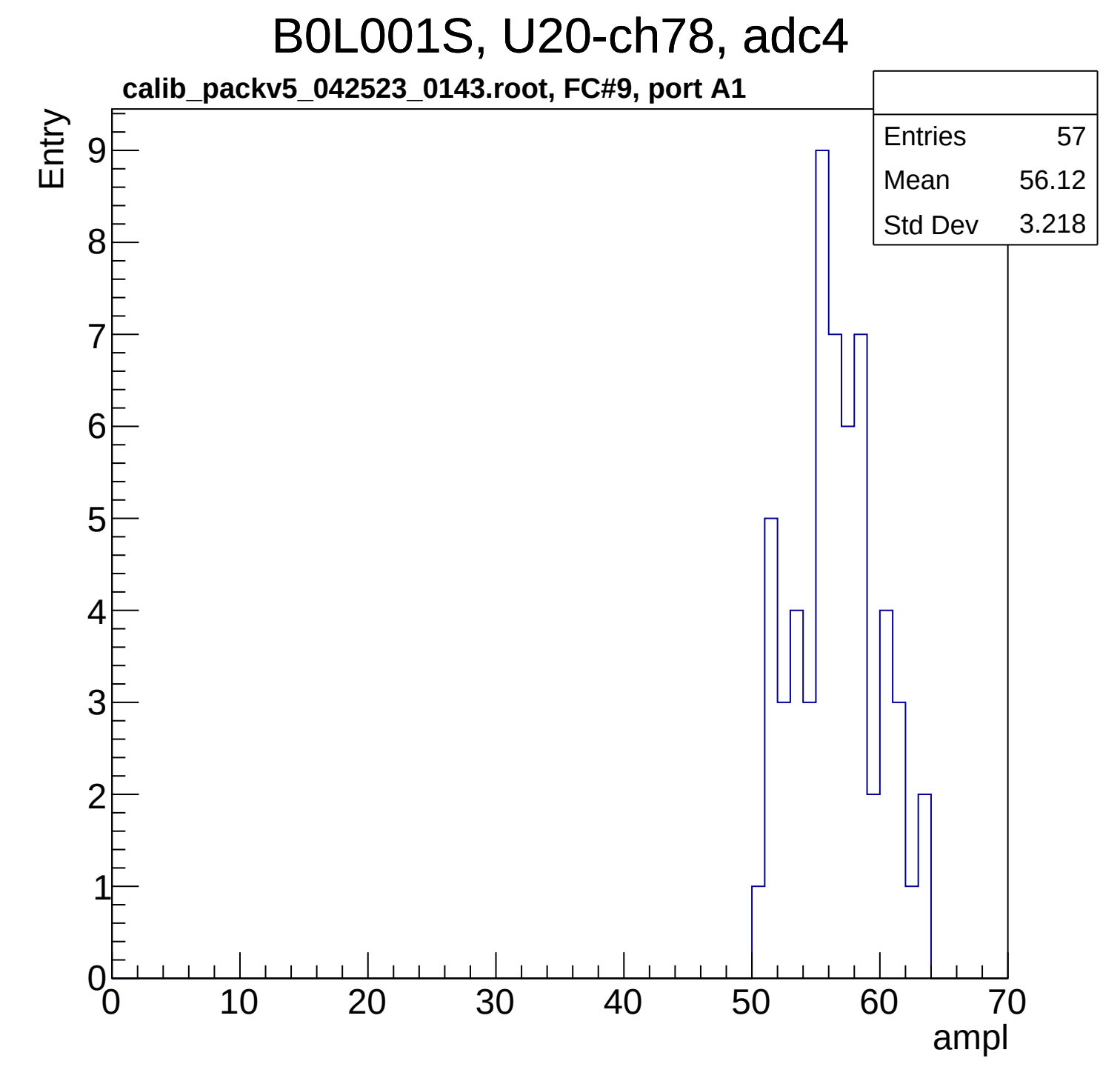
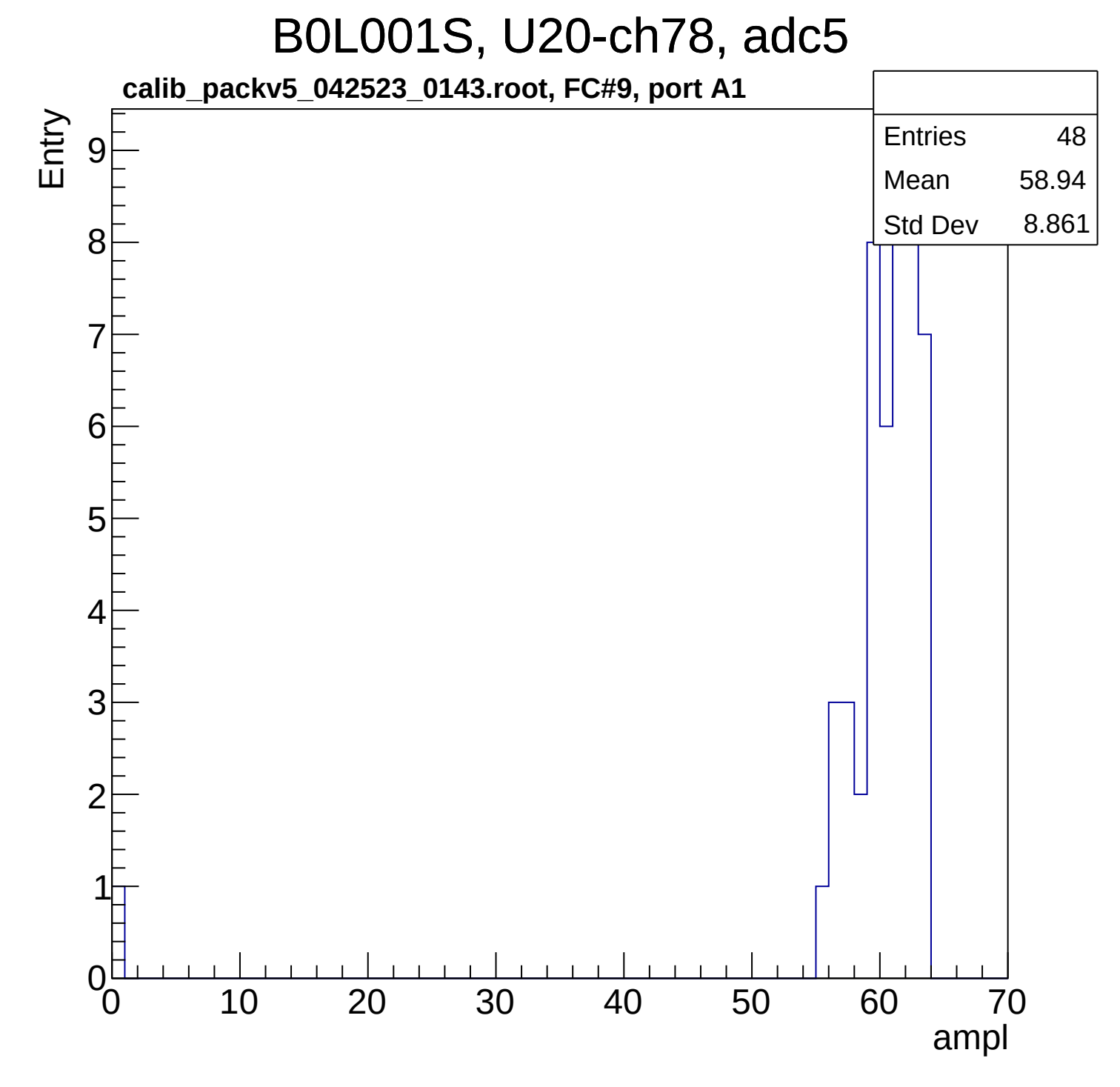

B0L001S, U20-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.53
Std Dev 3.725
Gaus mean : 44.0366
Gaus Width: 3.8708
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.05
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.12
Std Dev 3.218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.94
Std Dev 8.861
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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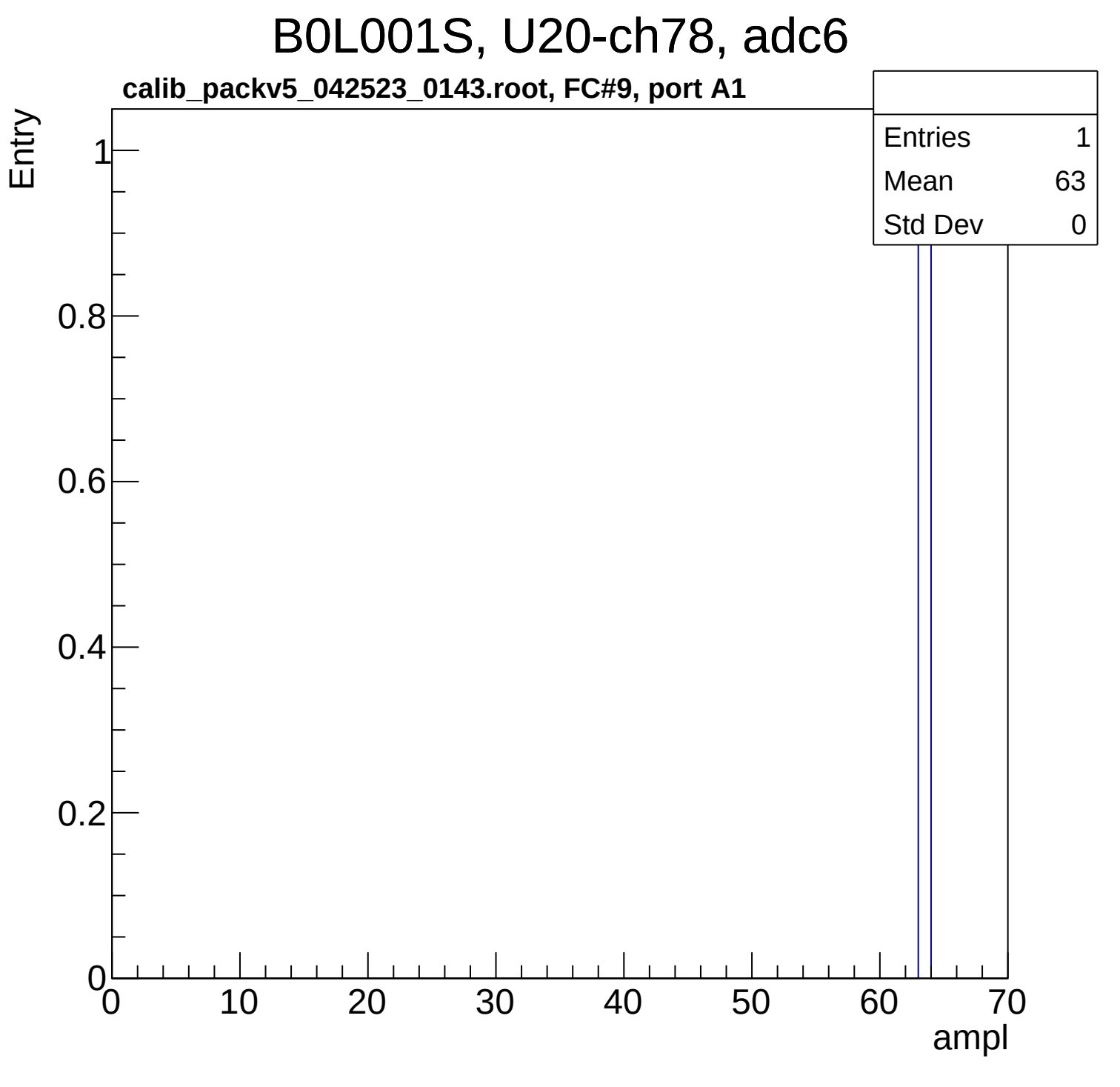
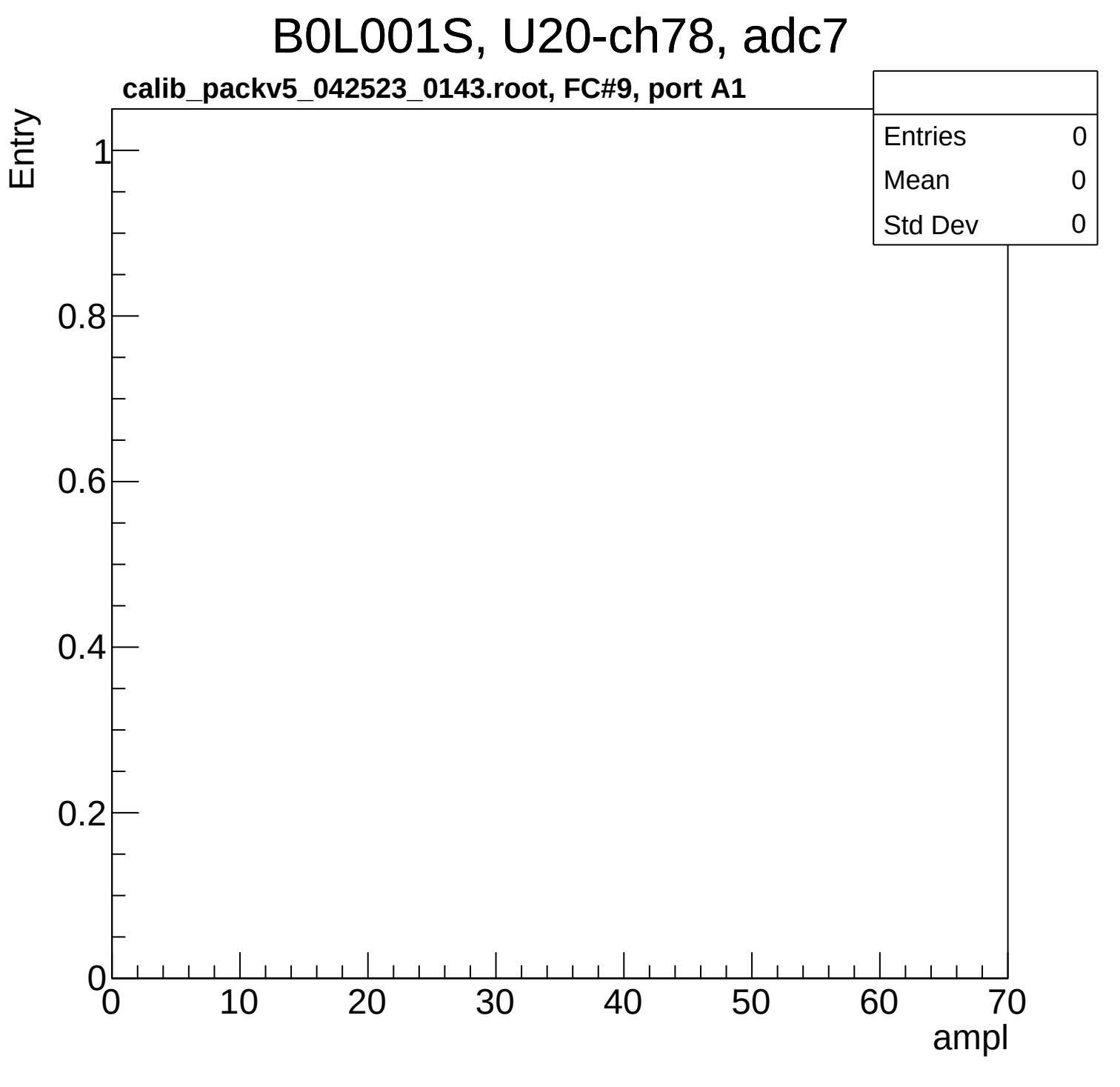
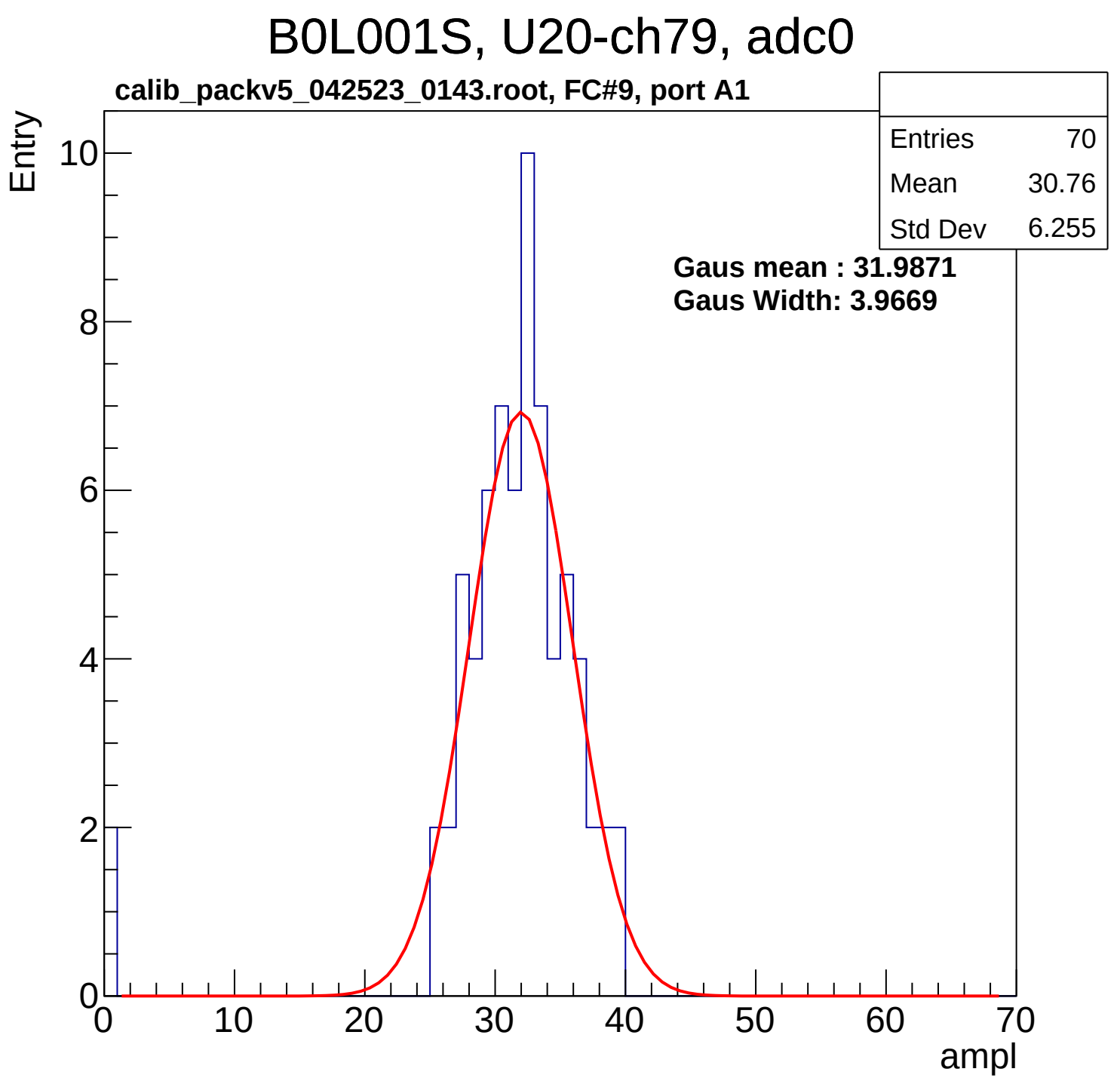
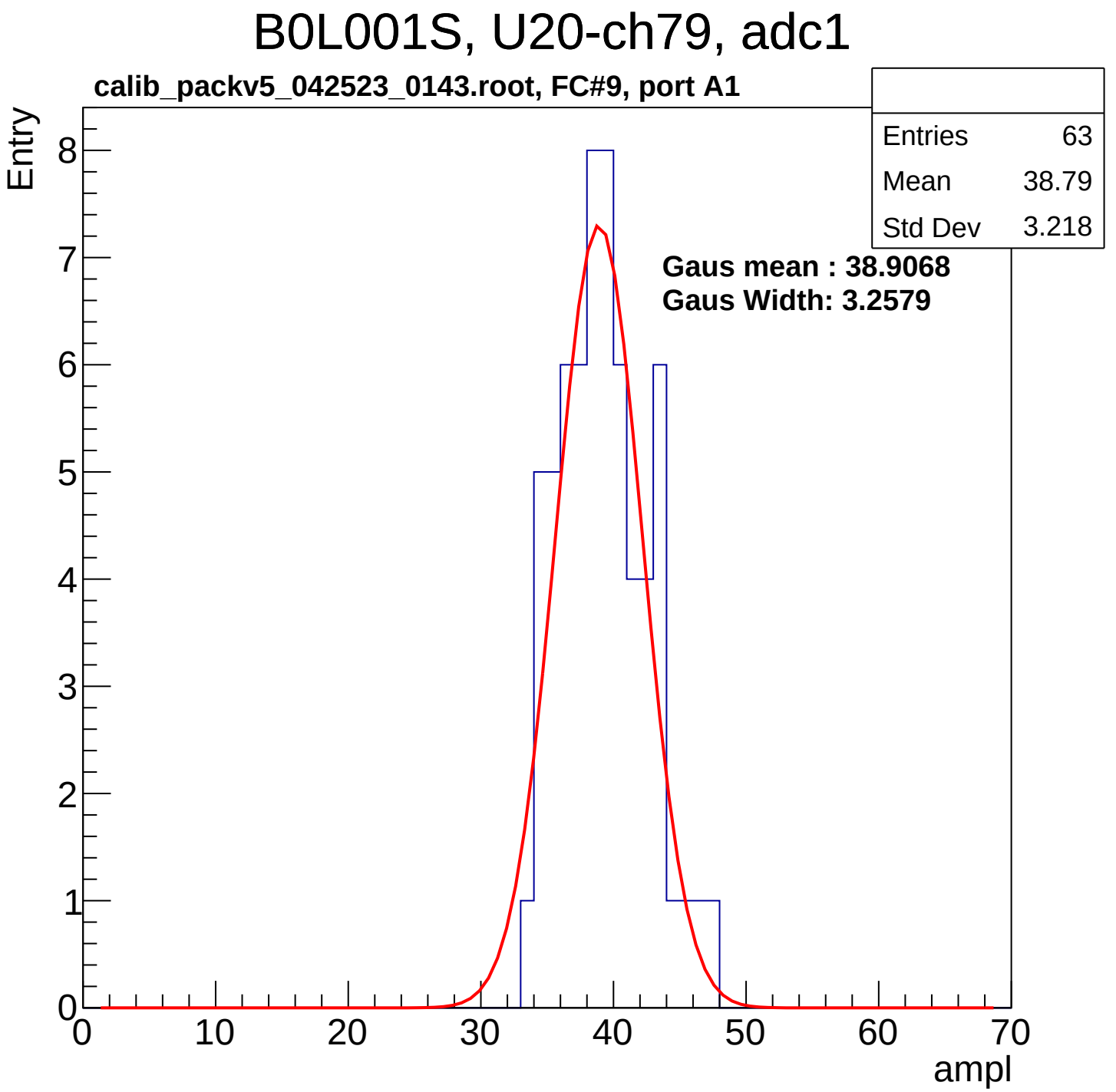
40
50
60
70


B0L001S, U20-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 30.76
Std Dev 6.255
Gaus mean : 31.9871
Gaus Width: 3.9669
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.79
Std Dev 3.218
Gaus mean : 38.9068
Gaus Width: 3.2579
Entry
ampl
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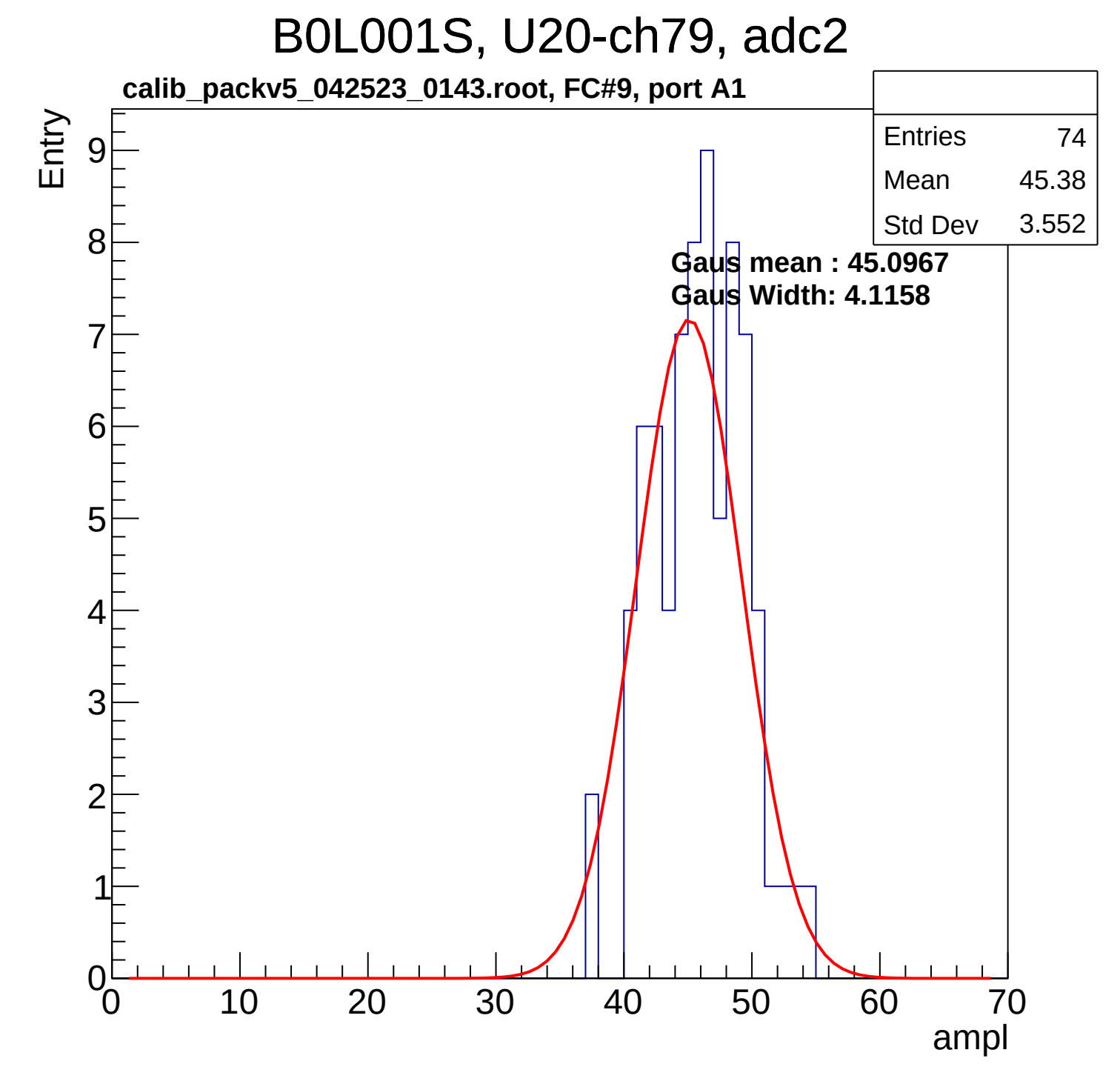
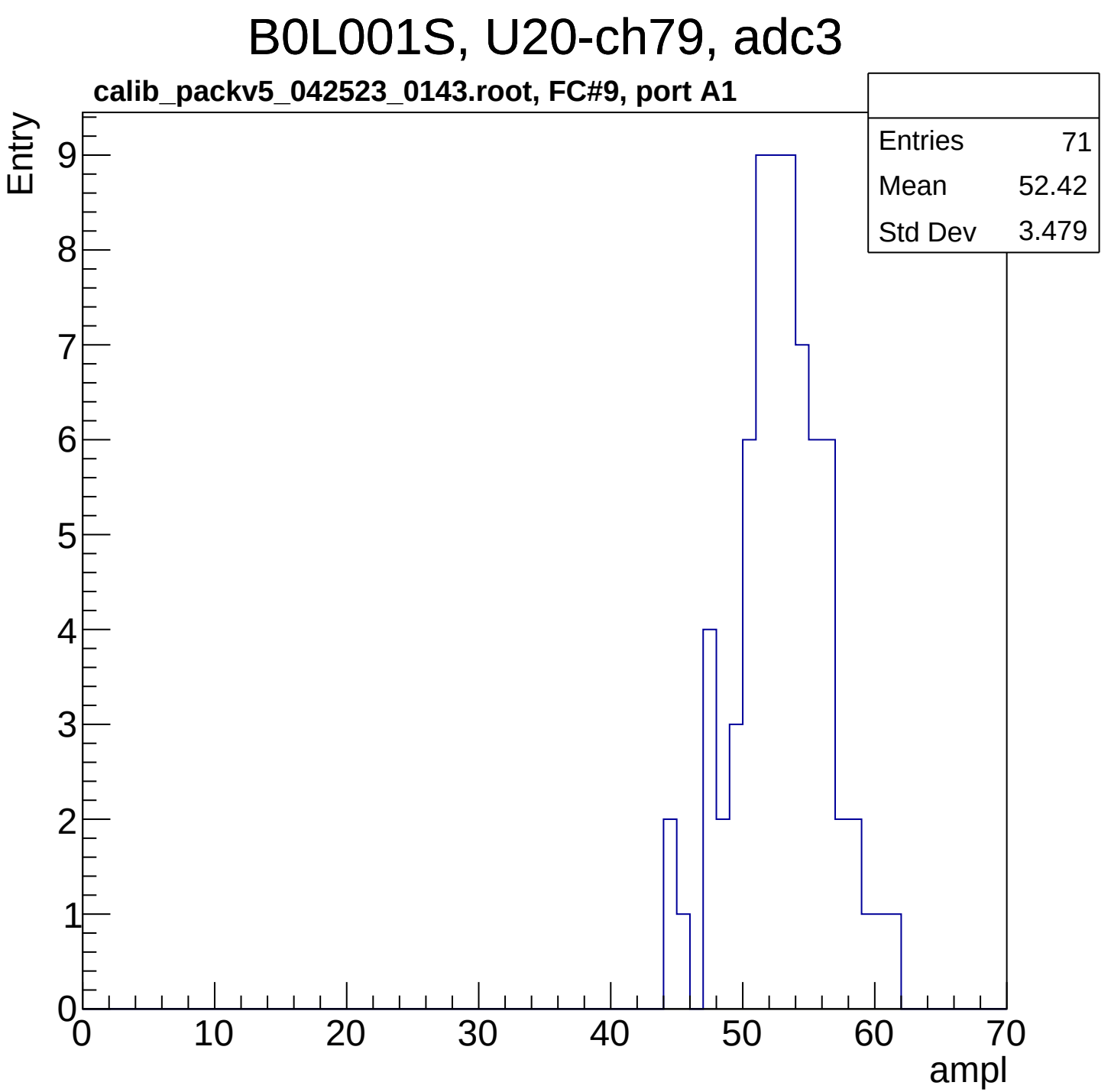
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 45.38
Std Dev 3.552
Gaus mean : 45.0967
Gaus Width: 4.1158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 52.42
Std Dev 3.479
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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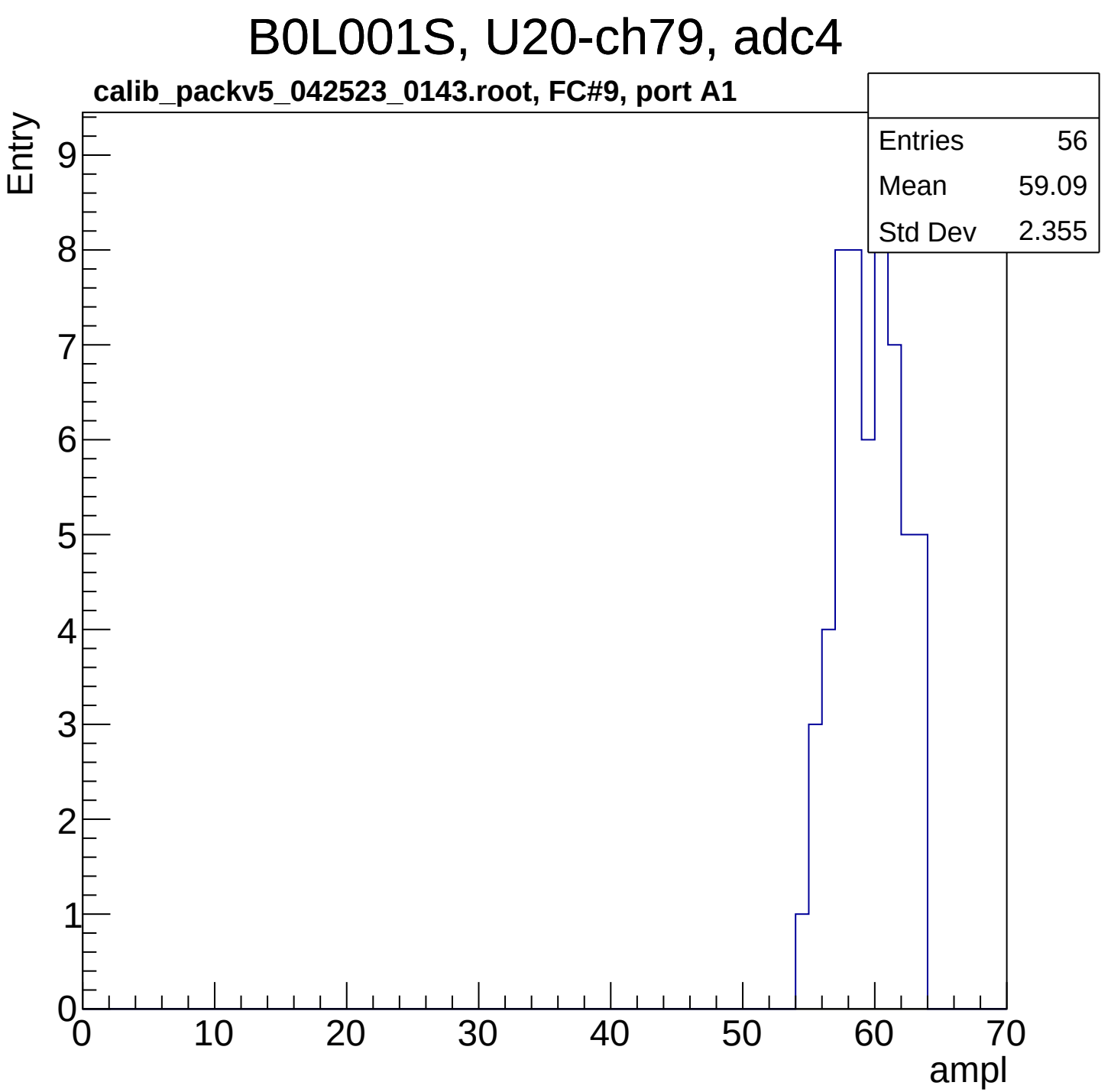
B0L001S, U20-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 59.09
Std Dev 2.355
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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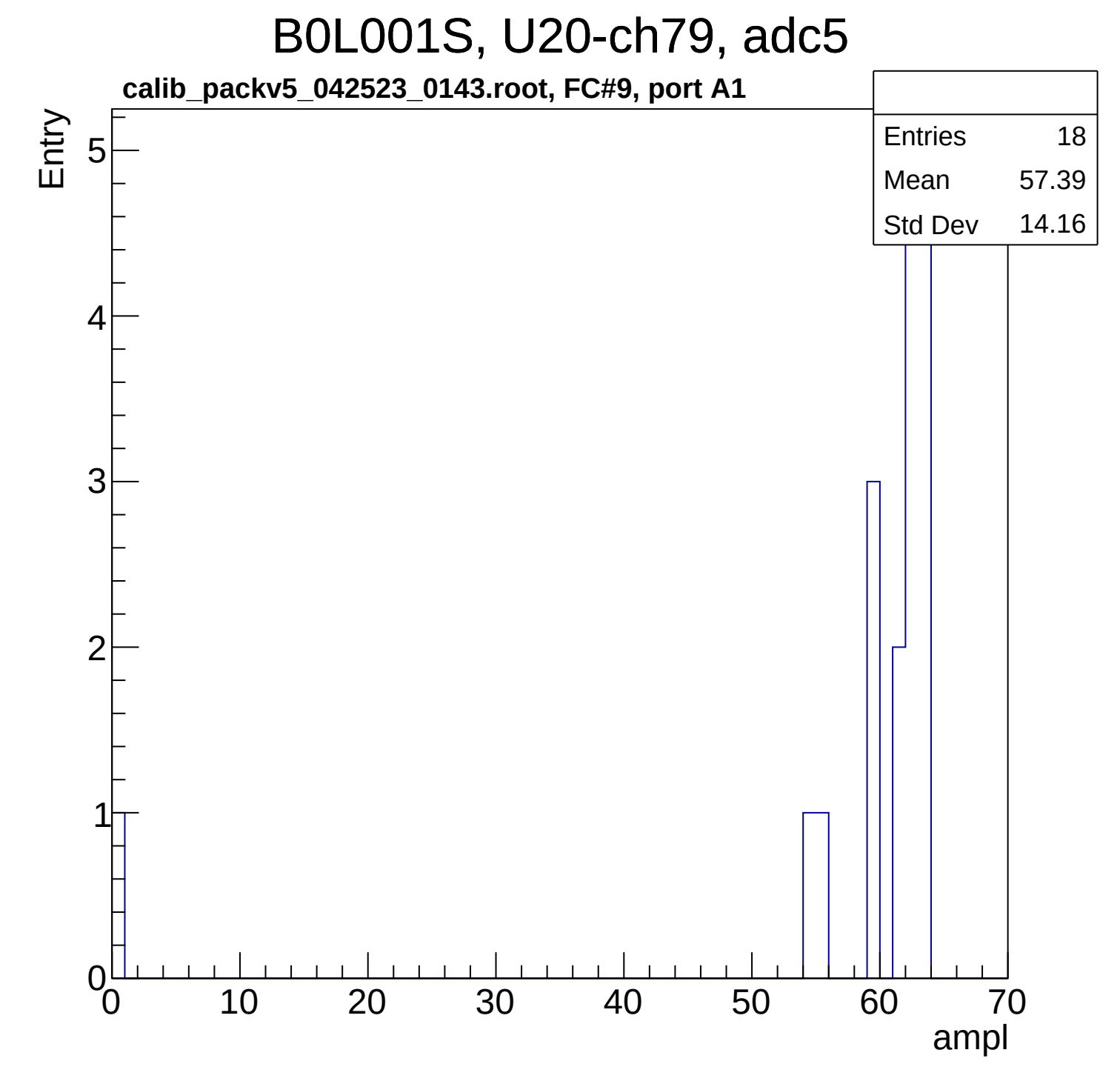
40
50
60
70


B0L001S, U20-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 57.39
Std Dev 14.16
Entry
0
1
2
3
4
5
0
10
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30
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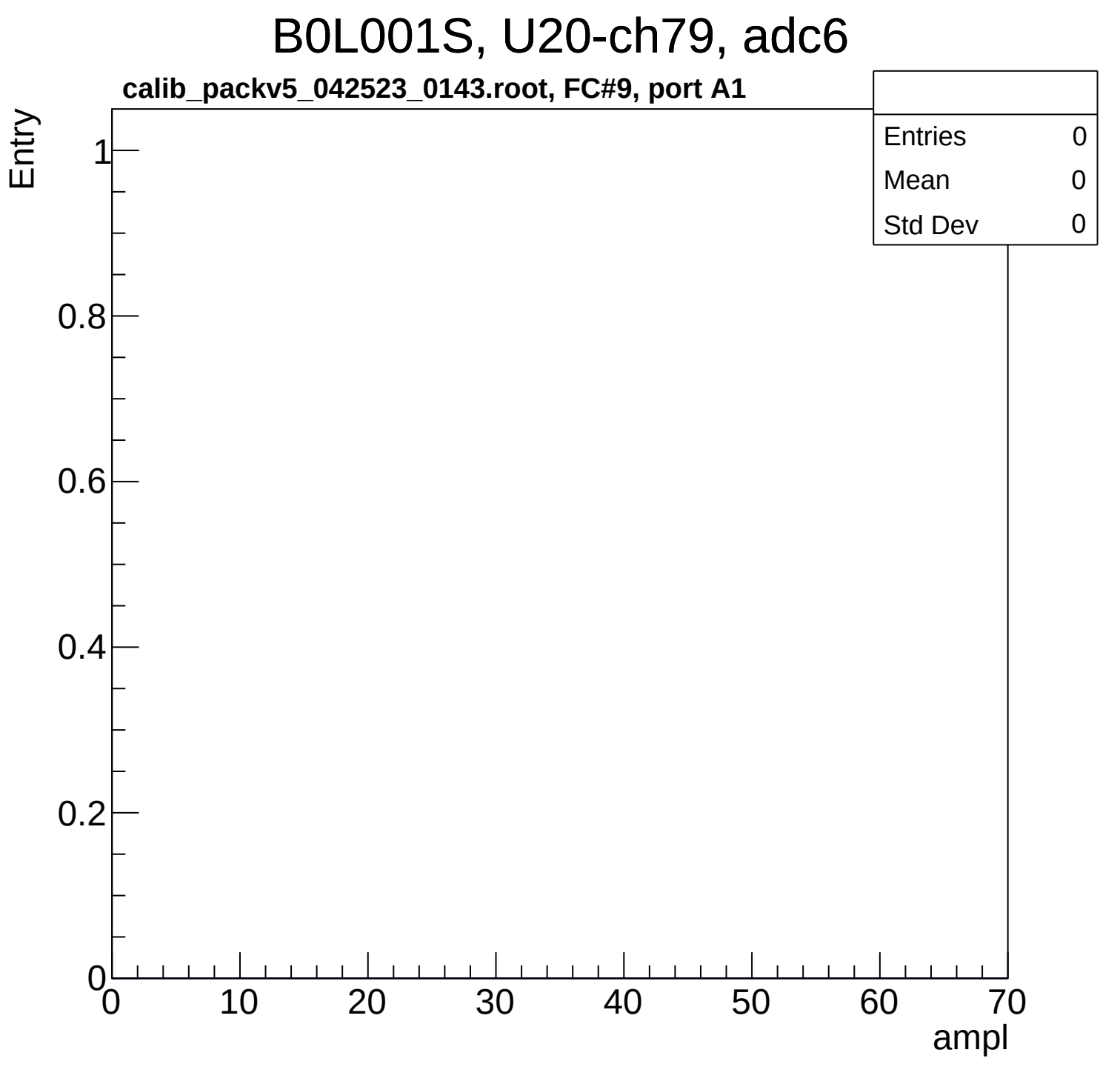
50
60
70
ampl


B0L001S, U20-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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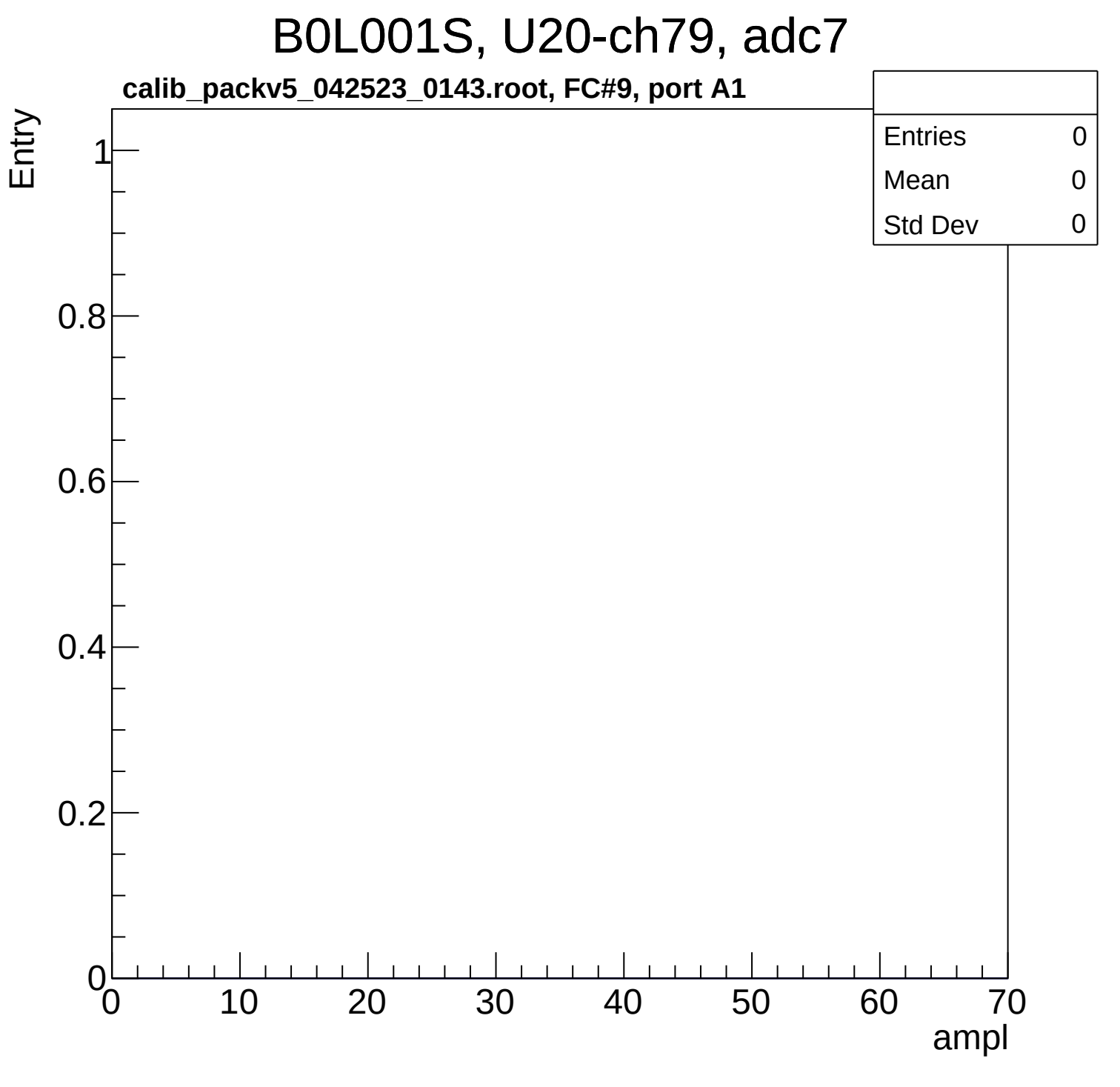
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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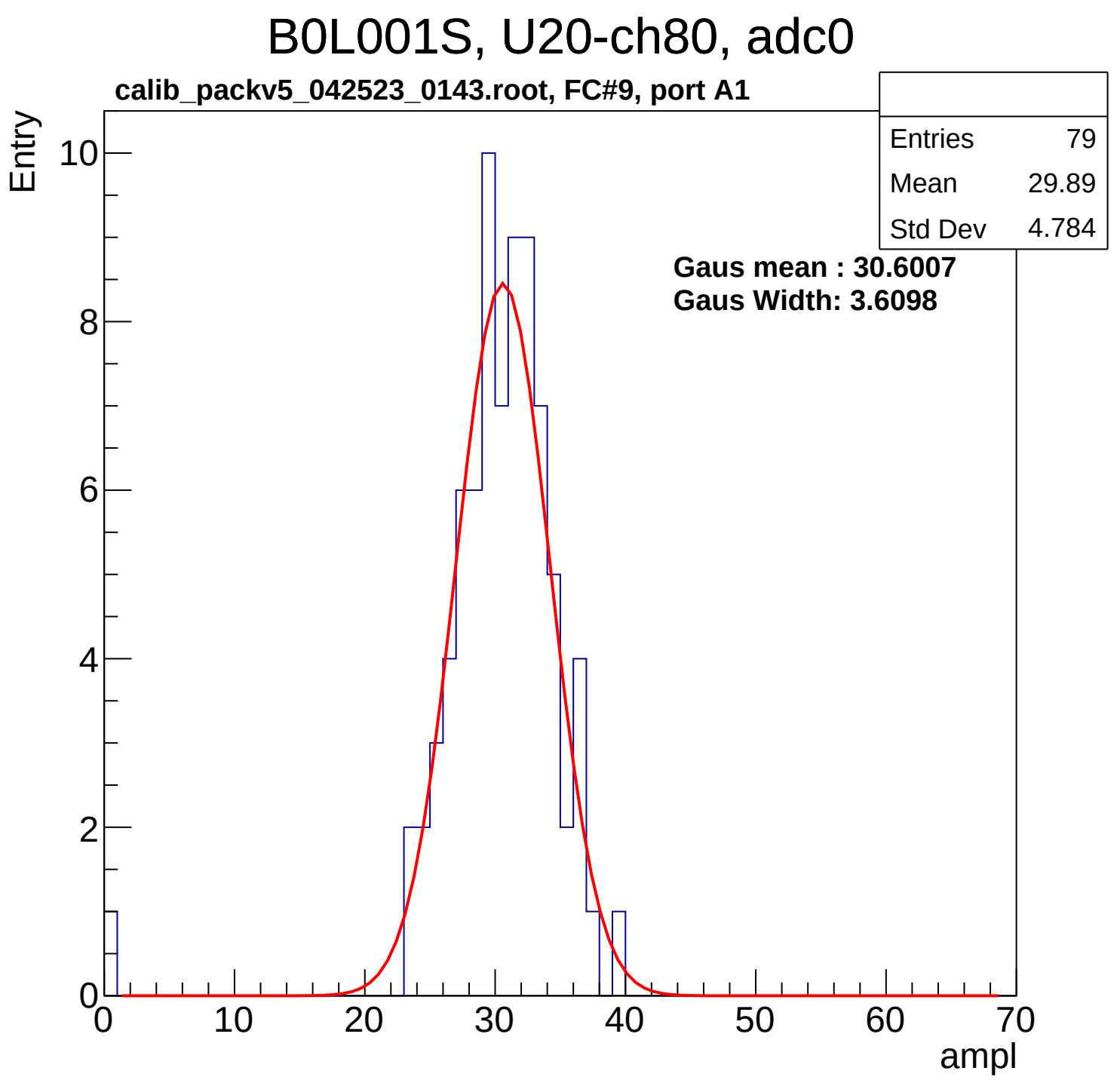
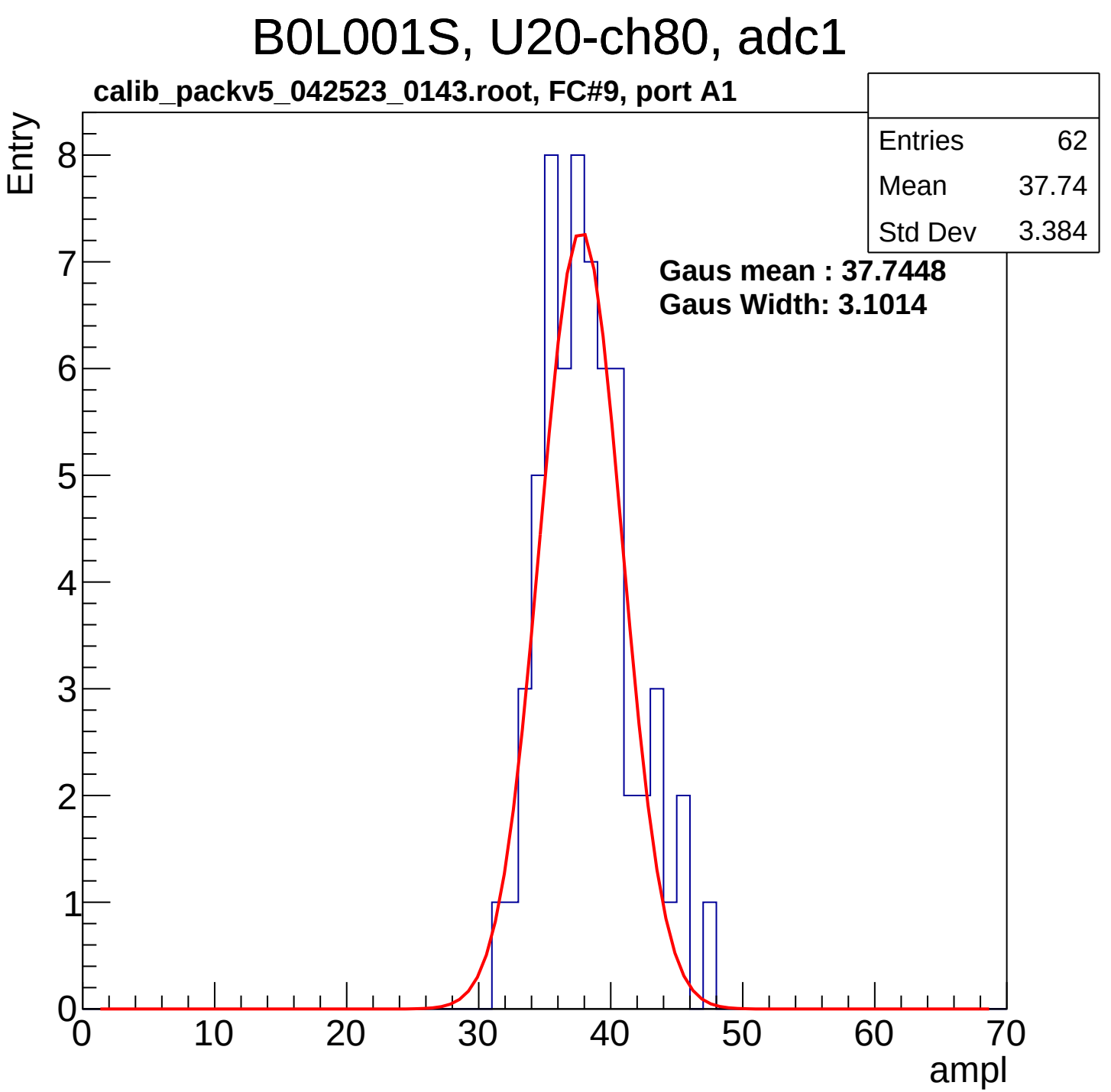
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 29.89
Std Dev 4.784
Gaus mean : 30.6007
Gaus Width: 3.6098
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.74
Std Dev 3.384
Gaus mean : 37.7448
Gaus Width: 3.1014
Entry
ampl
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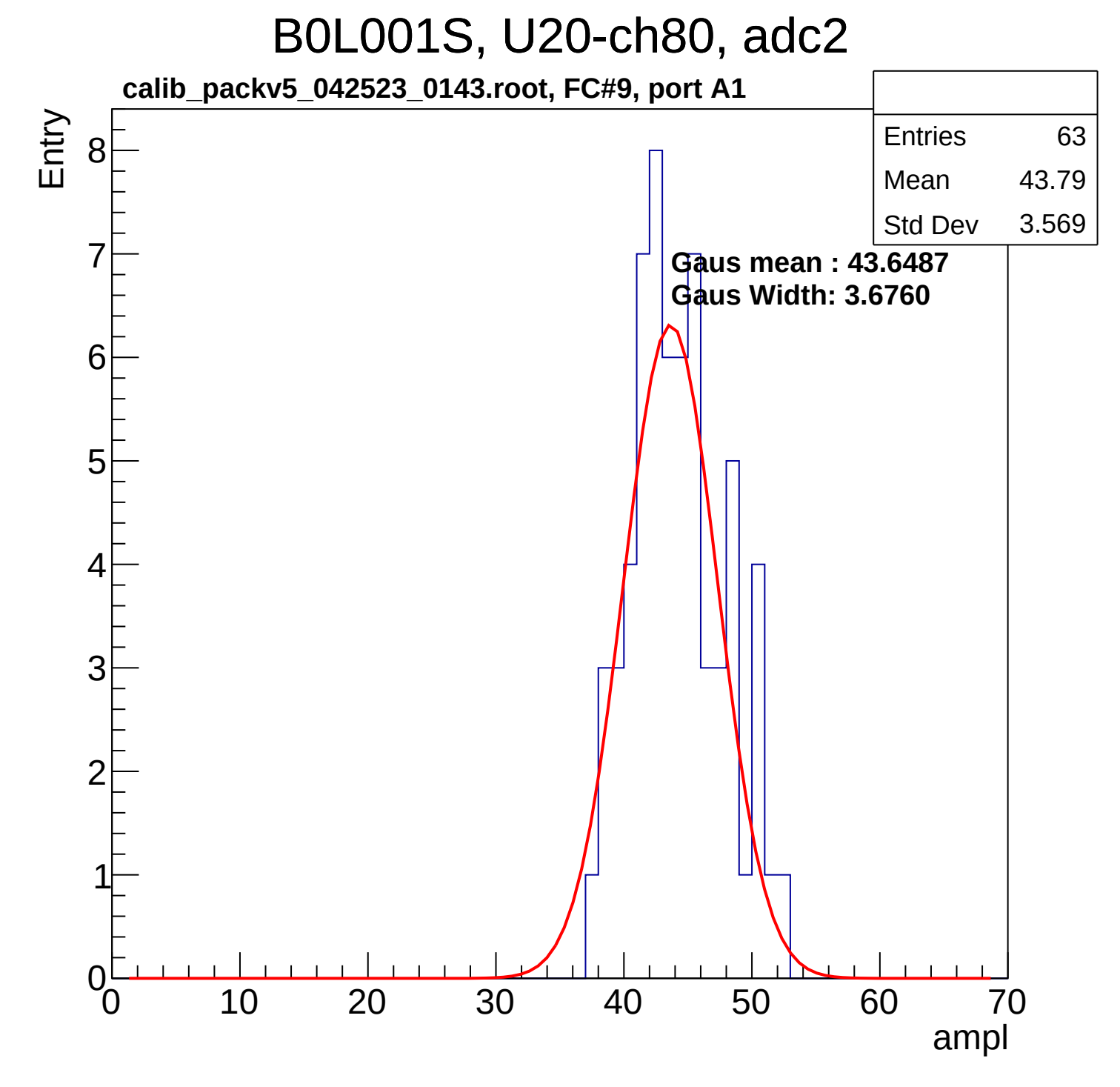
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.79
Std Dev 3.569
Gaus mean : 43.6487
Gaus Width: 3.6760
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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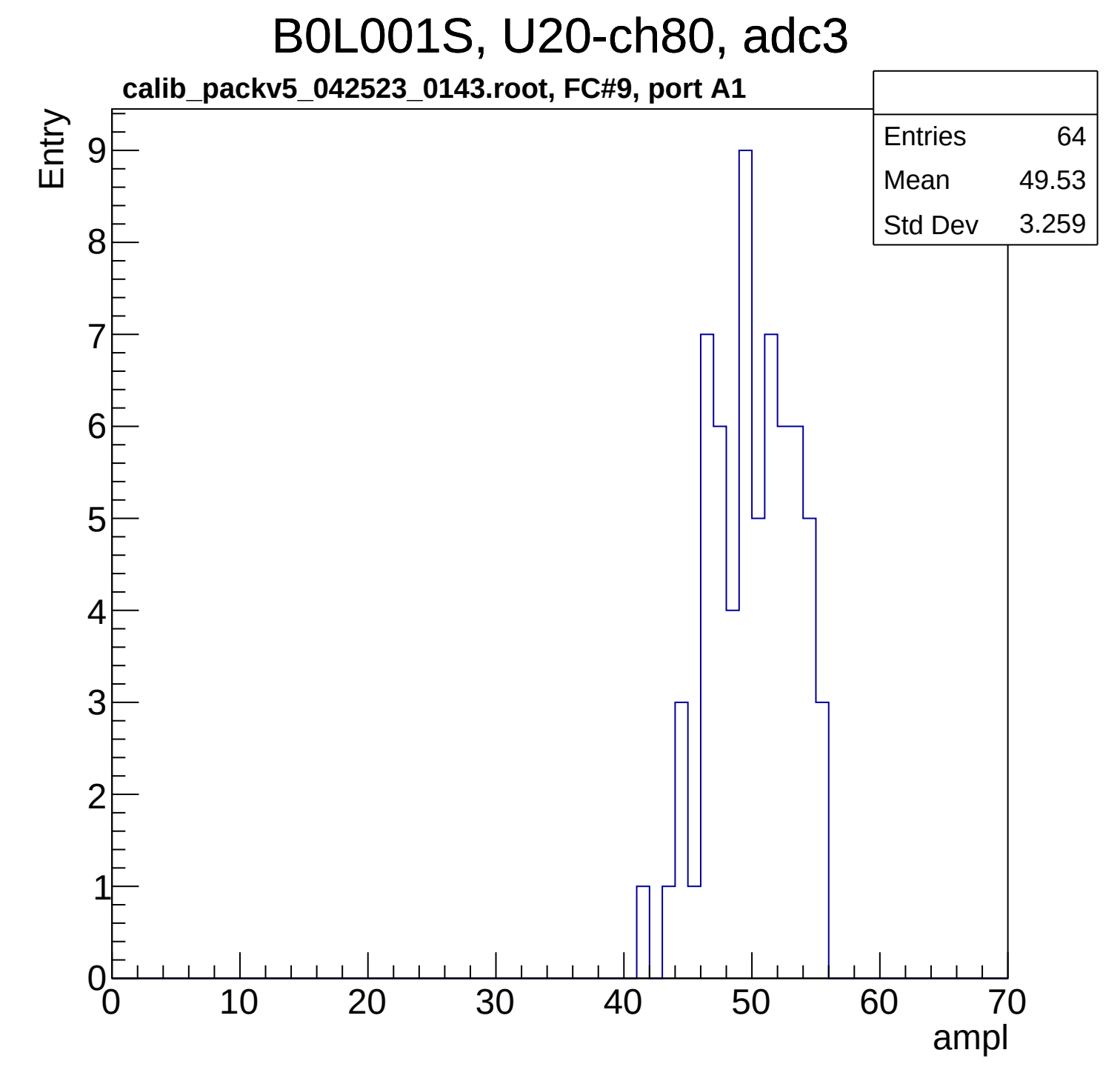
B0L001S, U20-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.53
Std Dev 3.259
Entry
ampl
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3
4
5
6
7
8
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0
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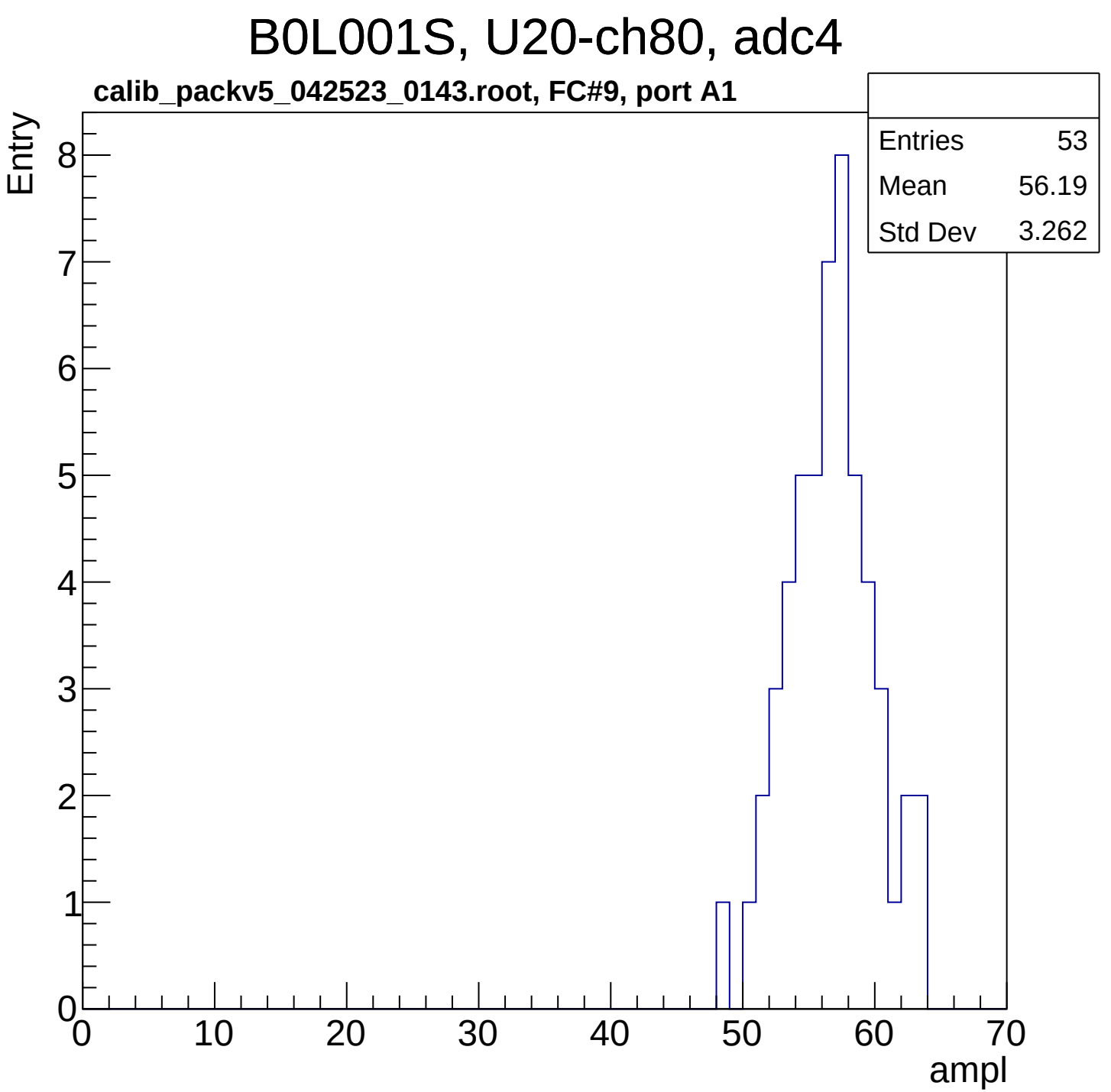
B0L001S, U20-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.19
Std Dev 3.262
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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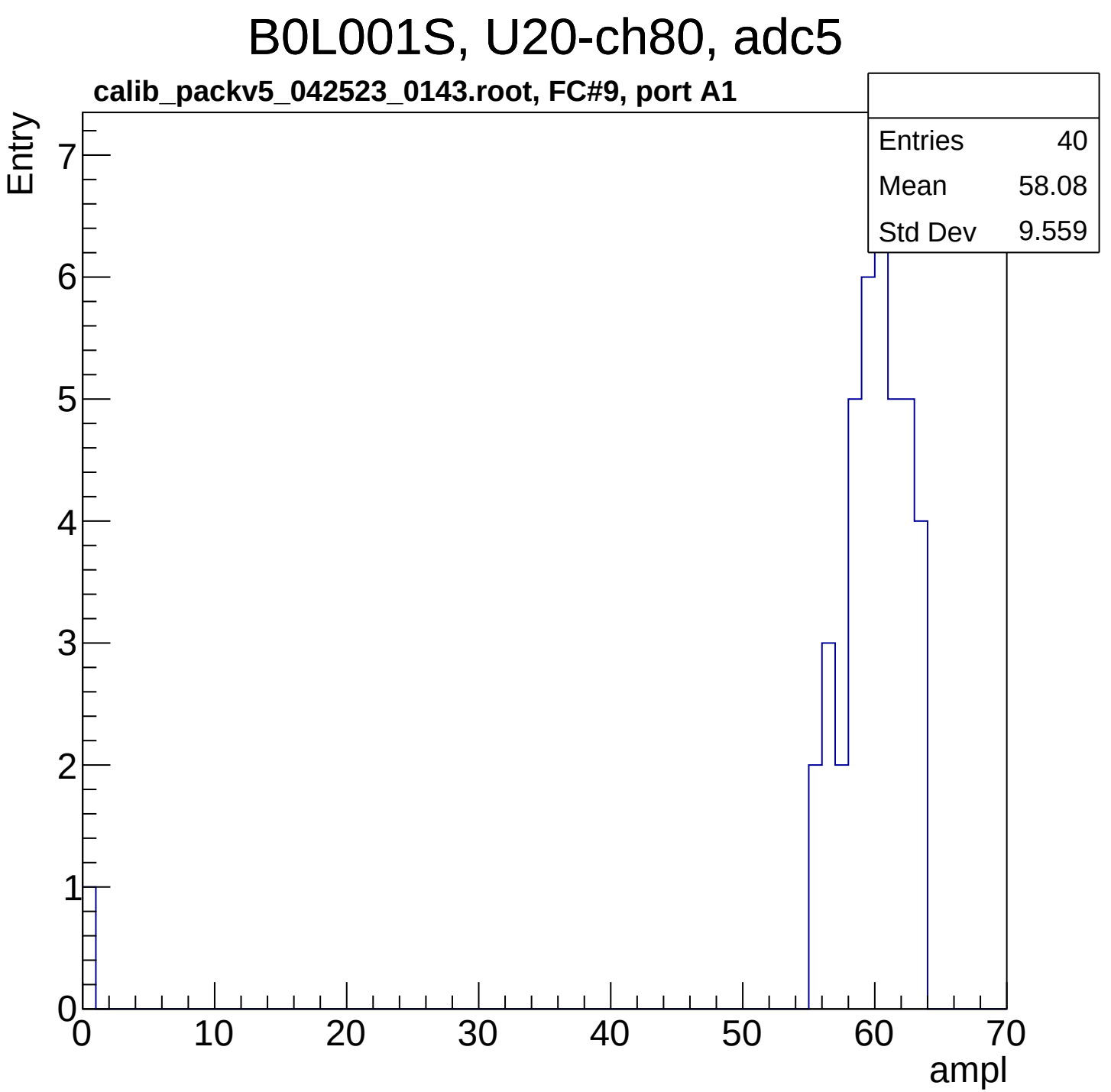
B0L001S, U20-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.08
Std Dev 9.559
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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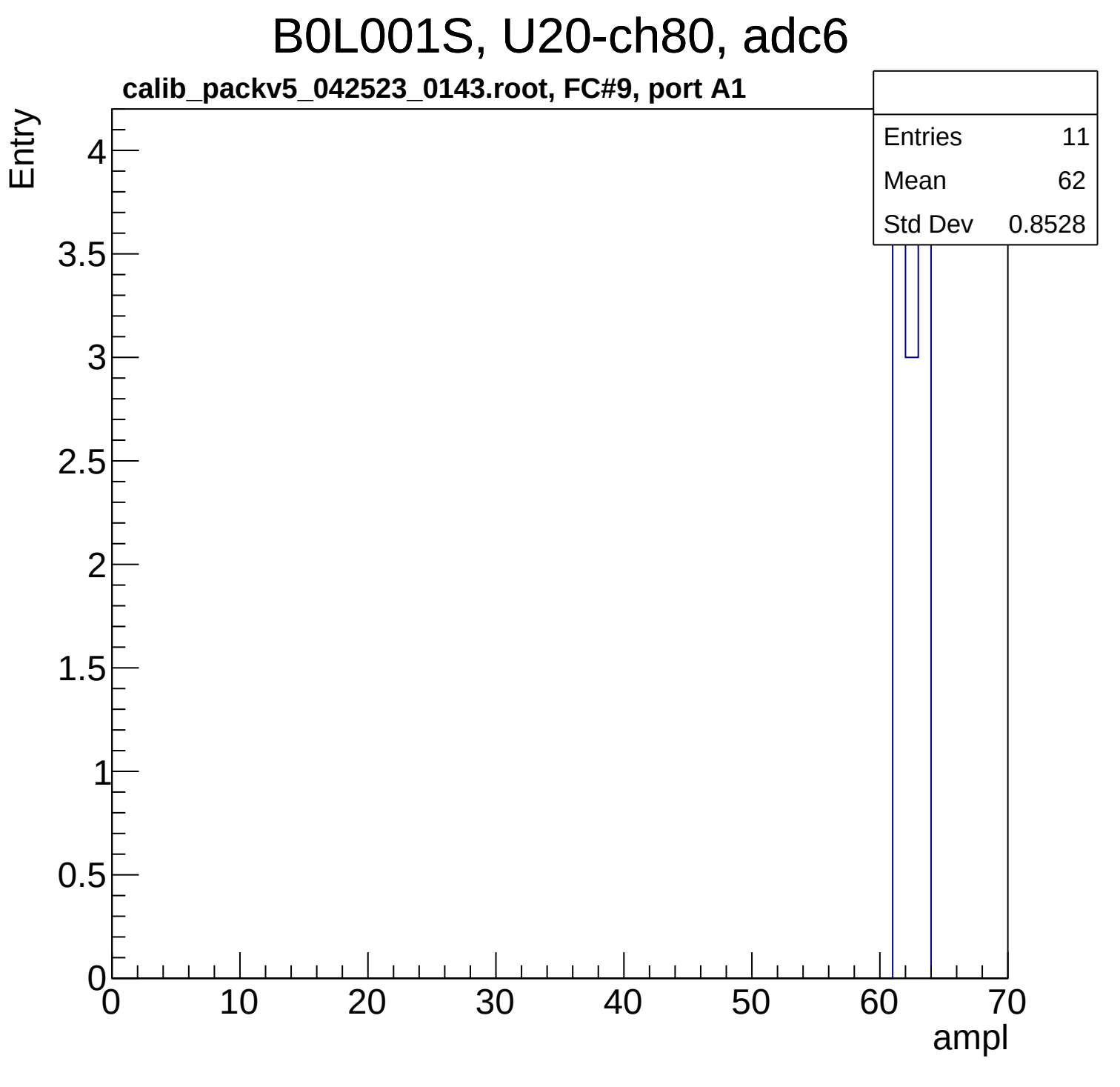
B0L001S, U20-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 62
Std Dev 0.8528
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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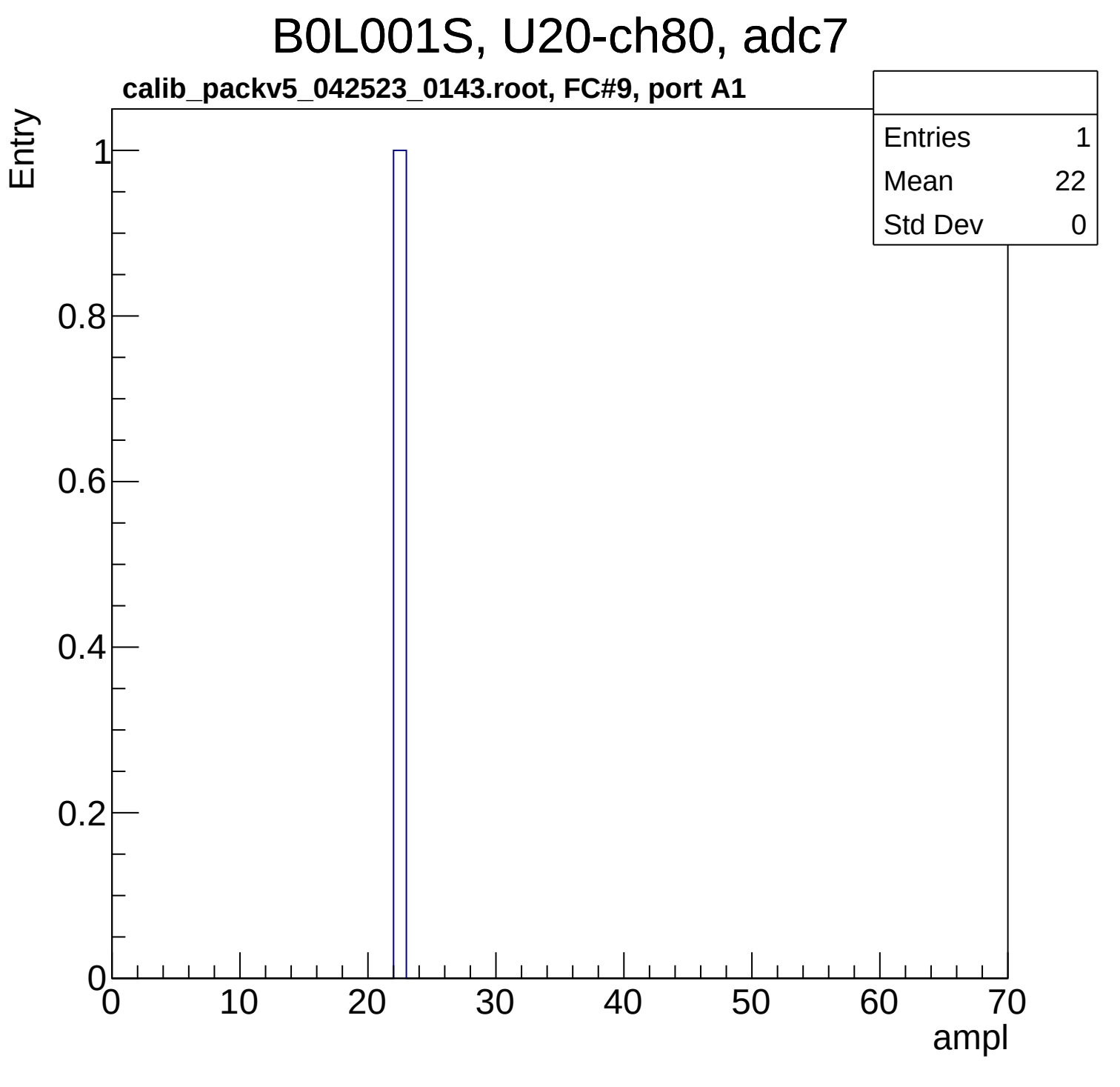
B0L001S, U20-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
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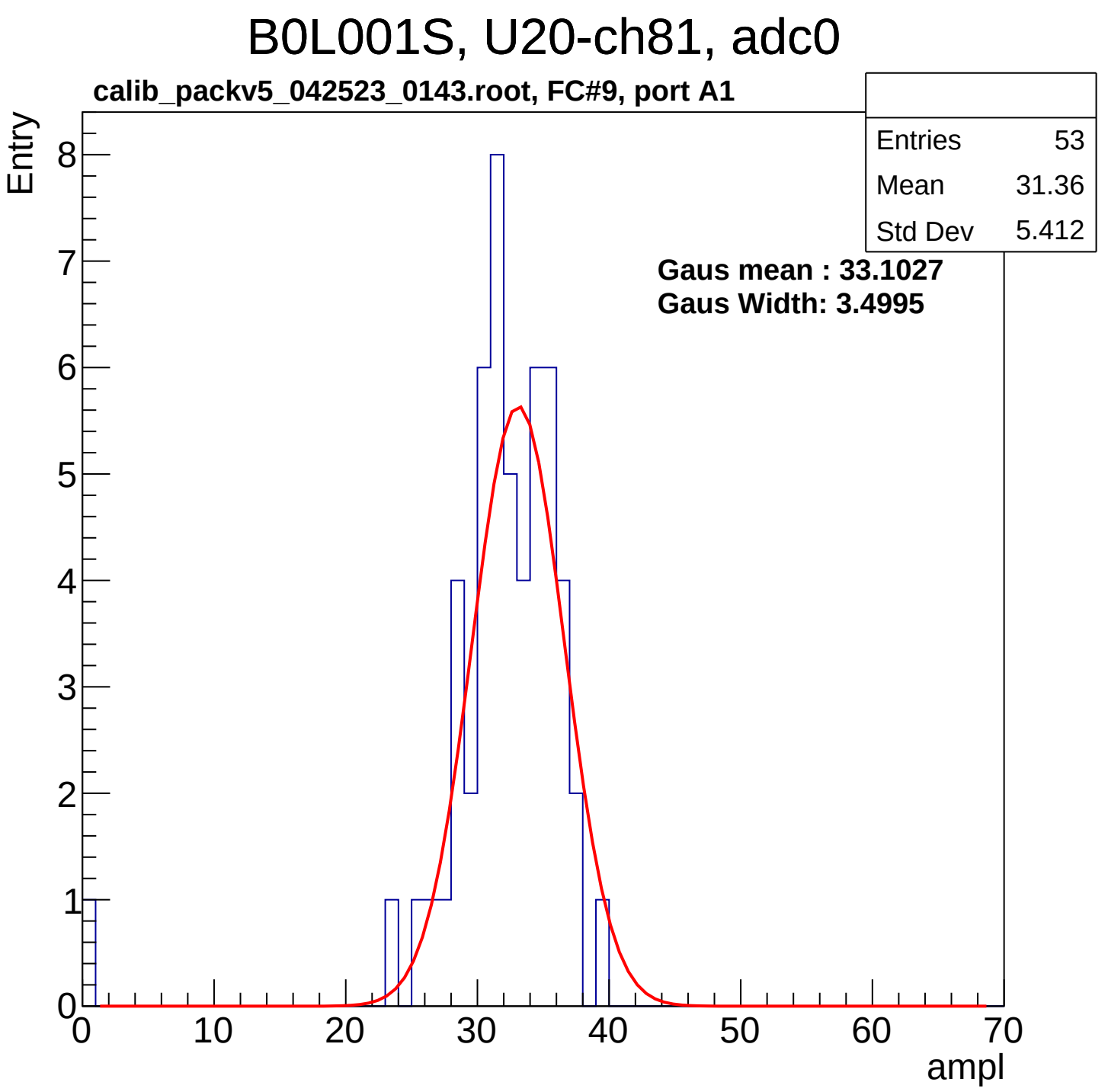
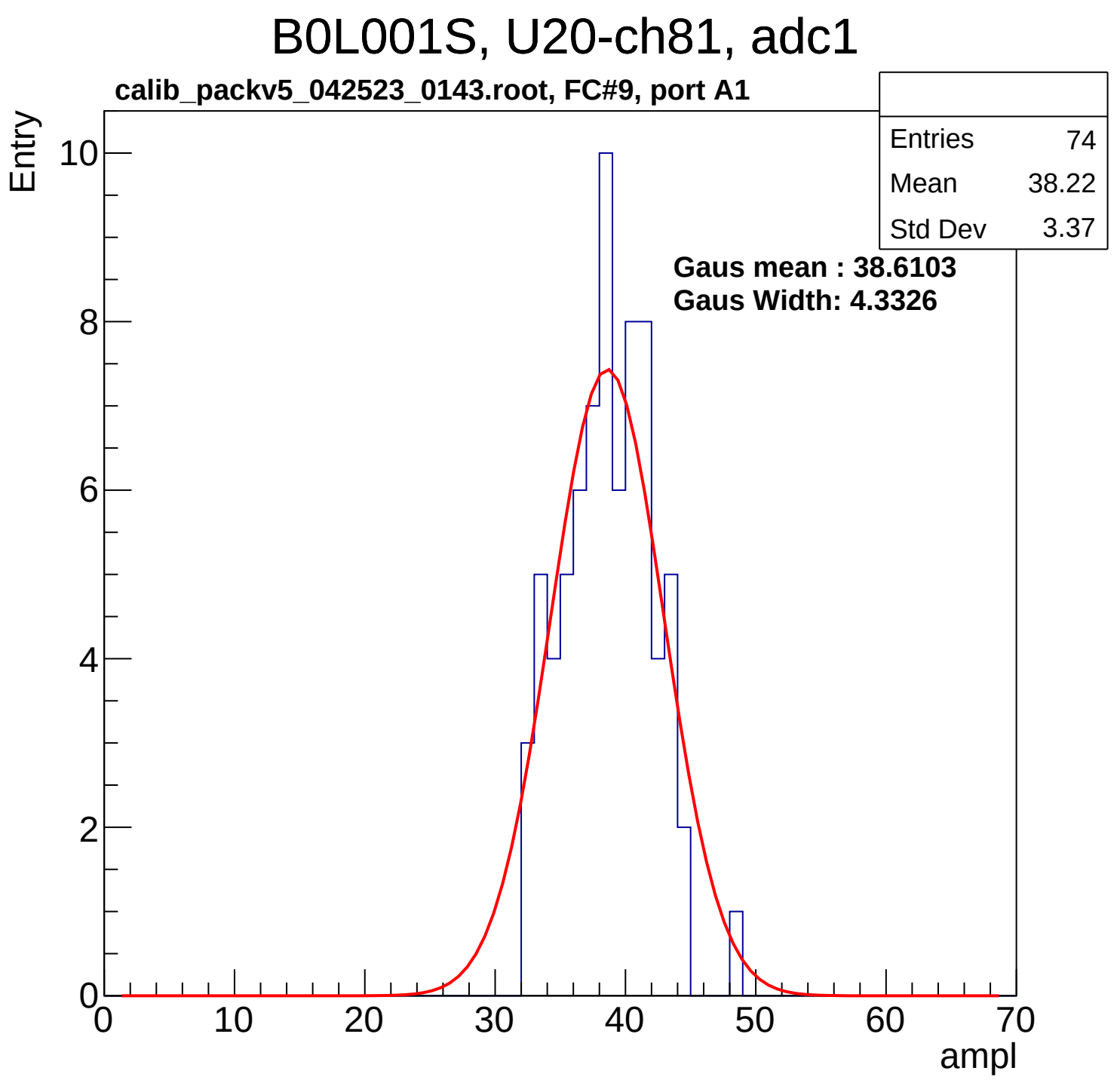
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 31.36
Std Dev 5.412
Gaus mean : 33.1027
Gaus Width: 3.4995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 38.22
Std Dev 3.37
Gaus mean : 38.6103
Gaus Width: 4.3326
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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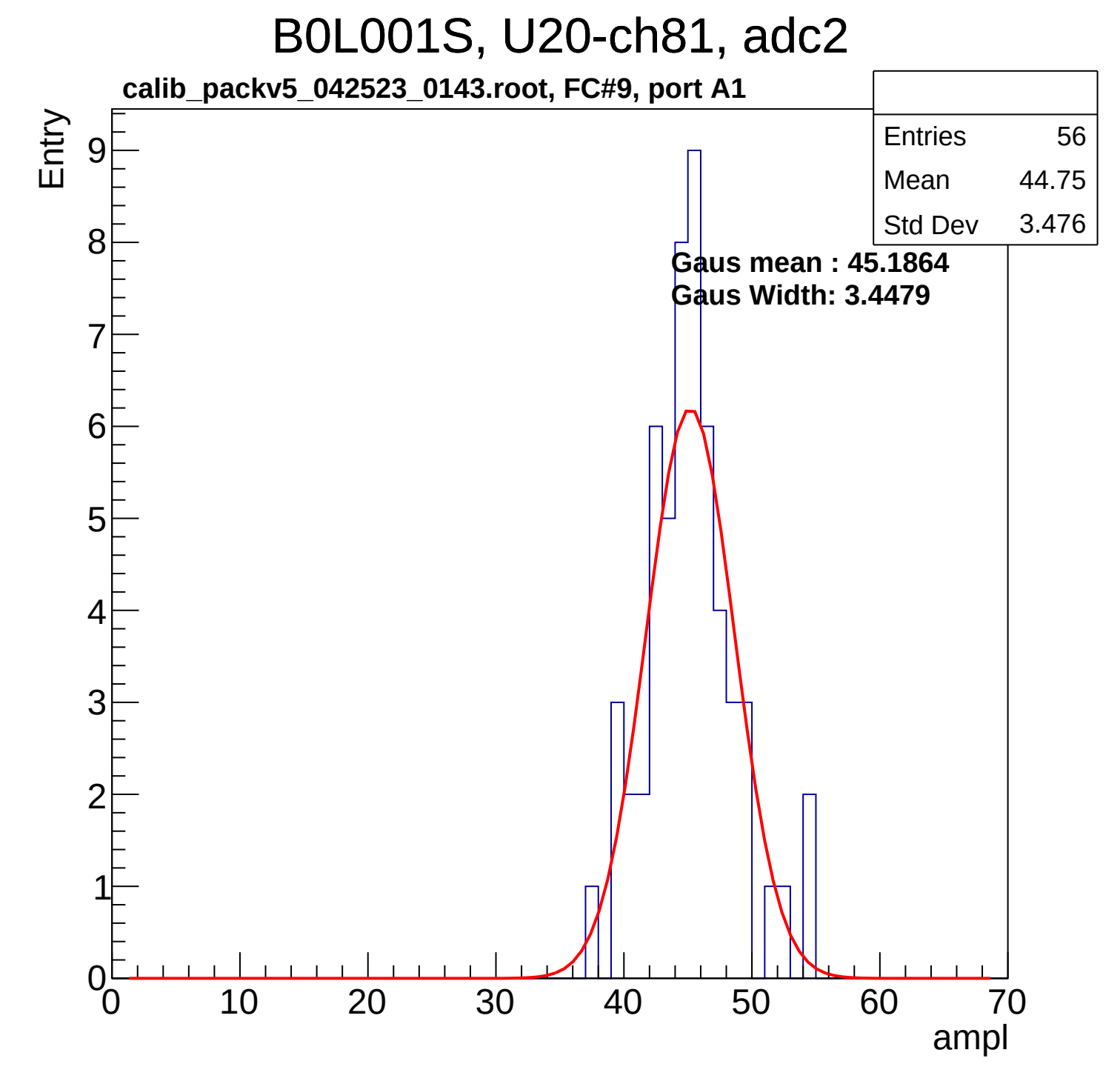
B0L001S, U20-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.75
Std Dev 3.476
Gaus mean : 45.1864
Gaus Width: 3.4479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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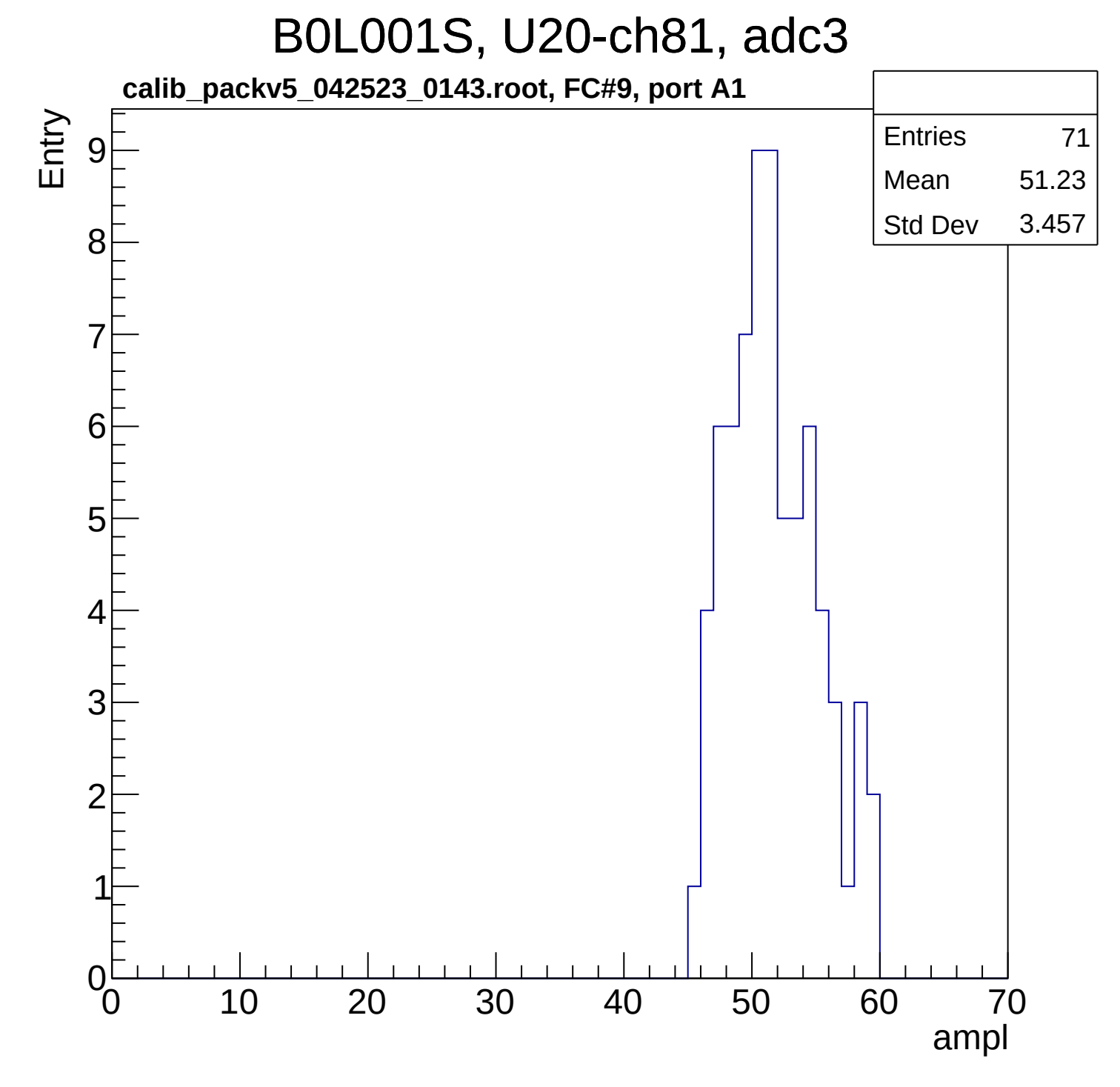
B0L001S, U20-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.23
Std Dev 3.457
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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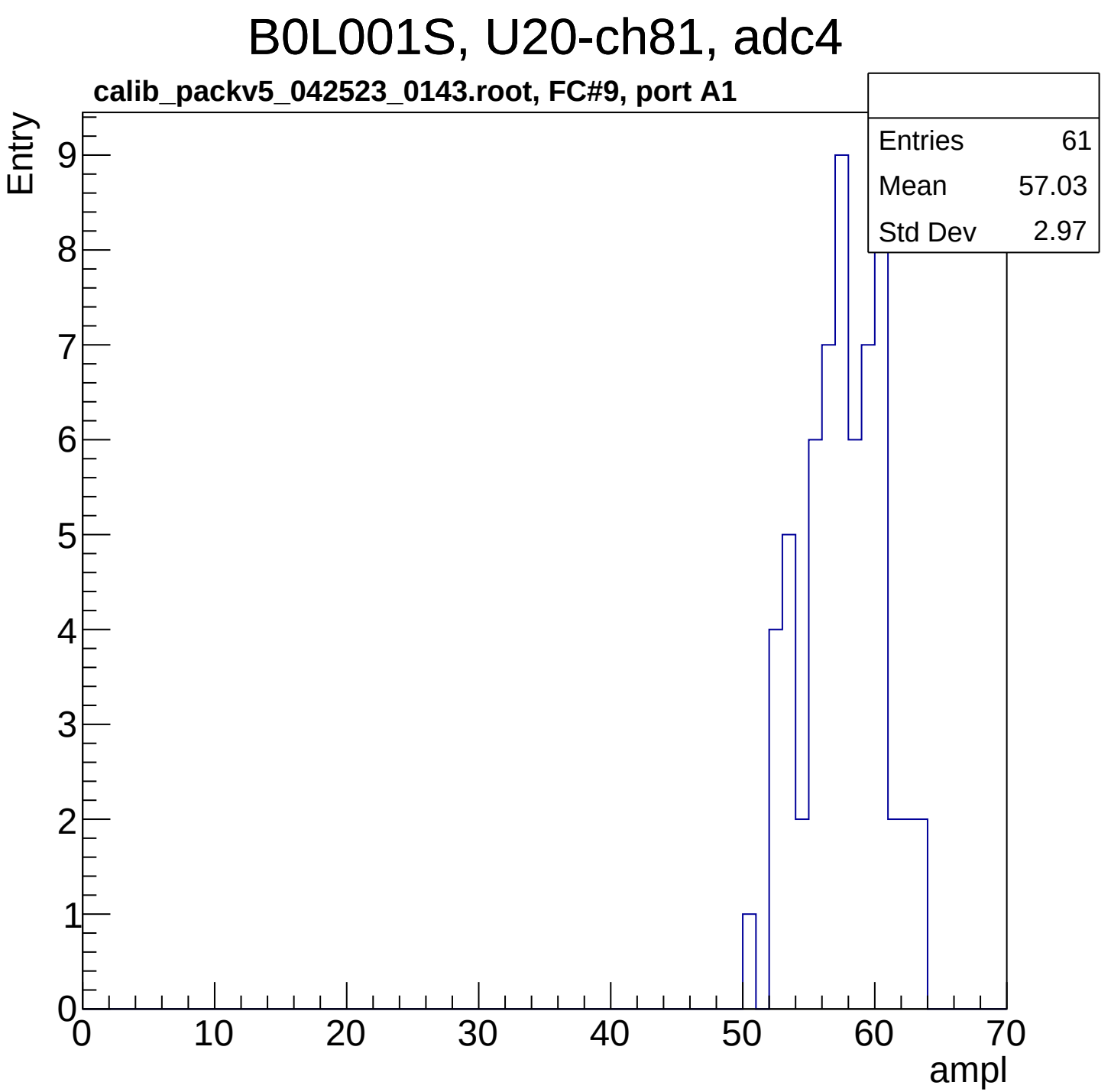
B0L001S, U20-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.03
Std Dev 2.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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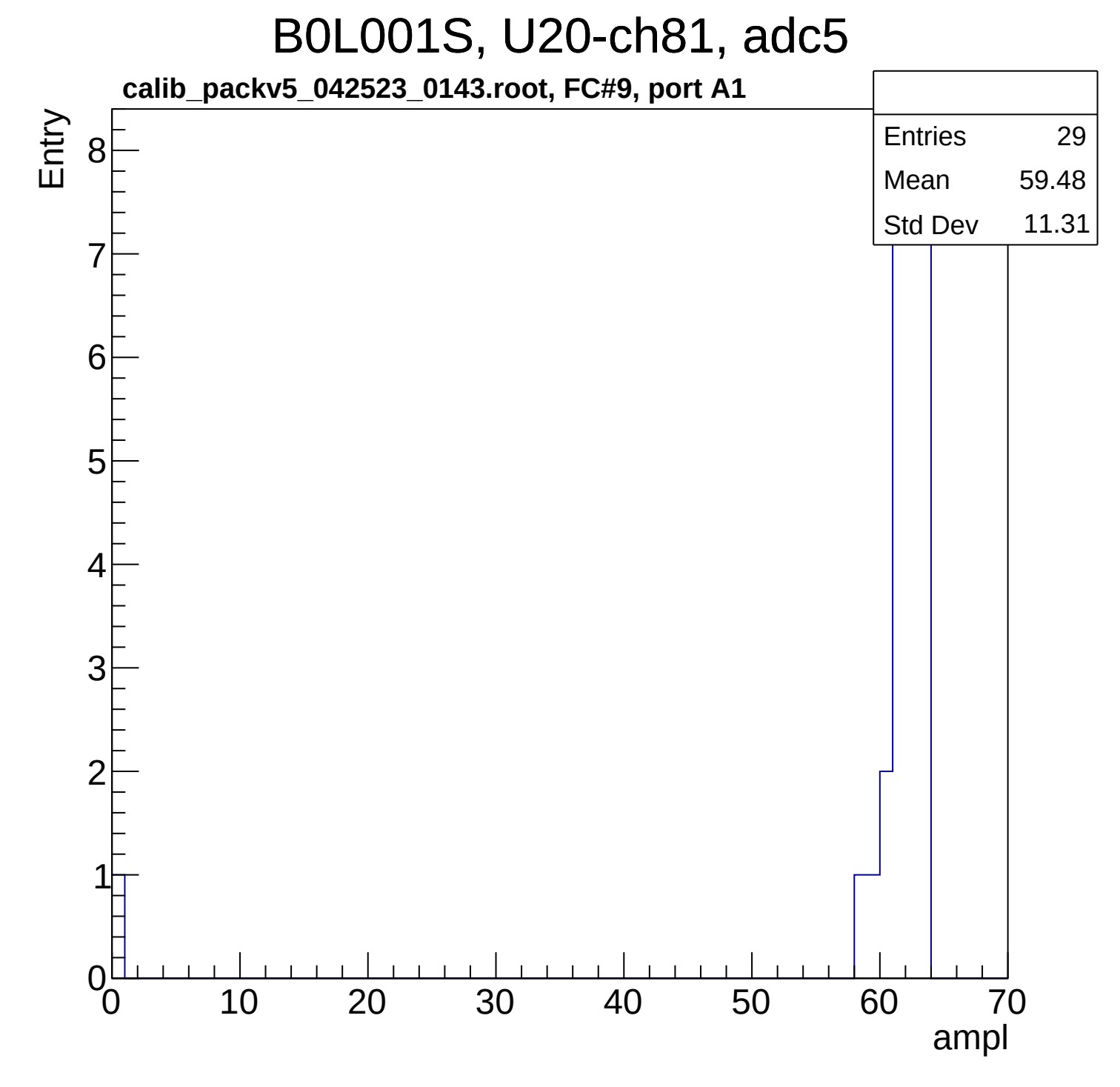
B0L001S, U20-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59.48
Std Dev 11.31
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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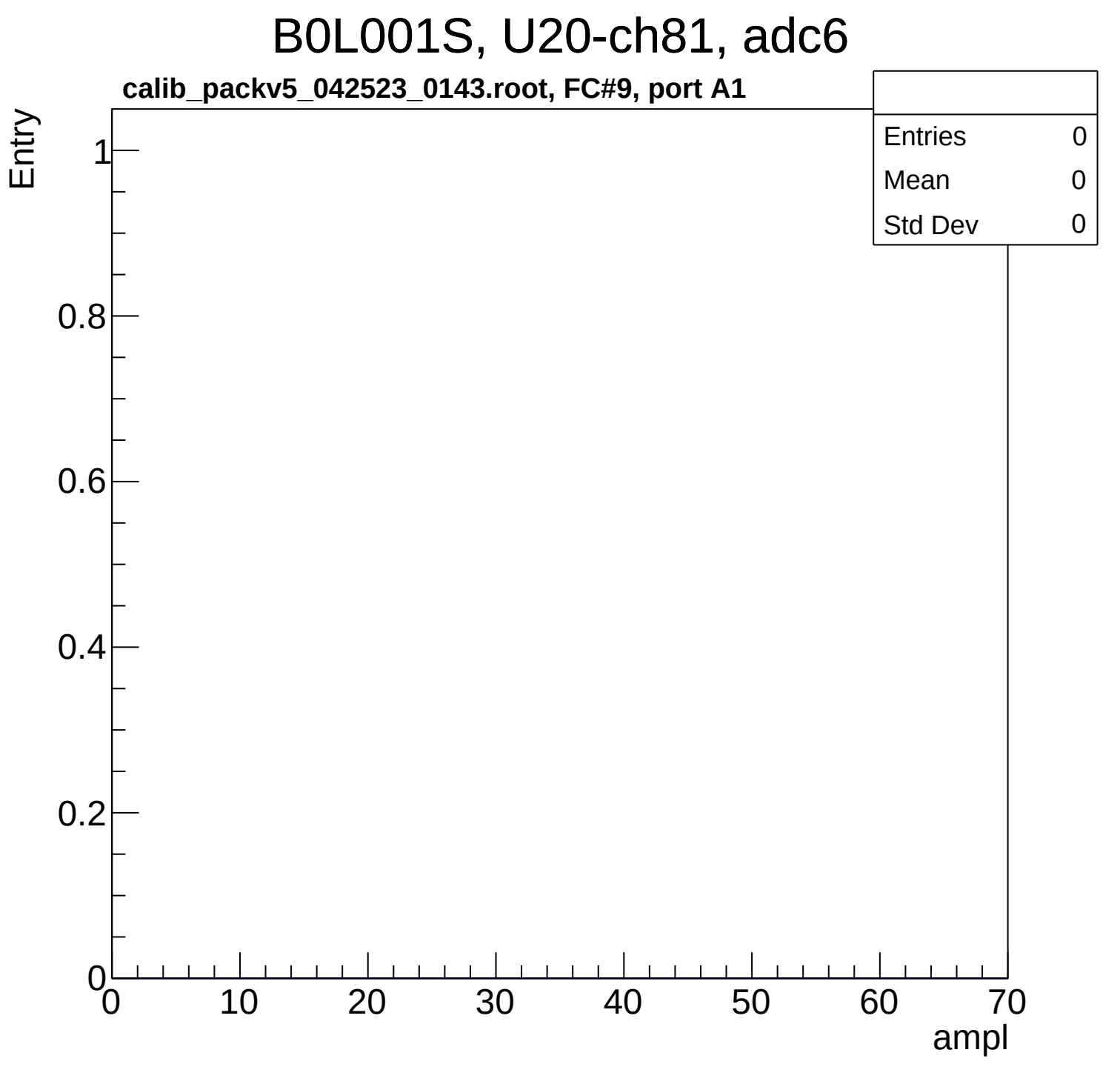
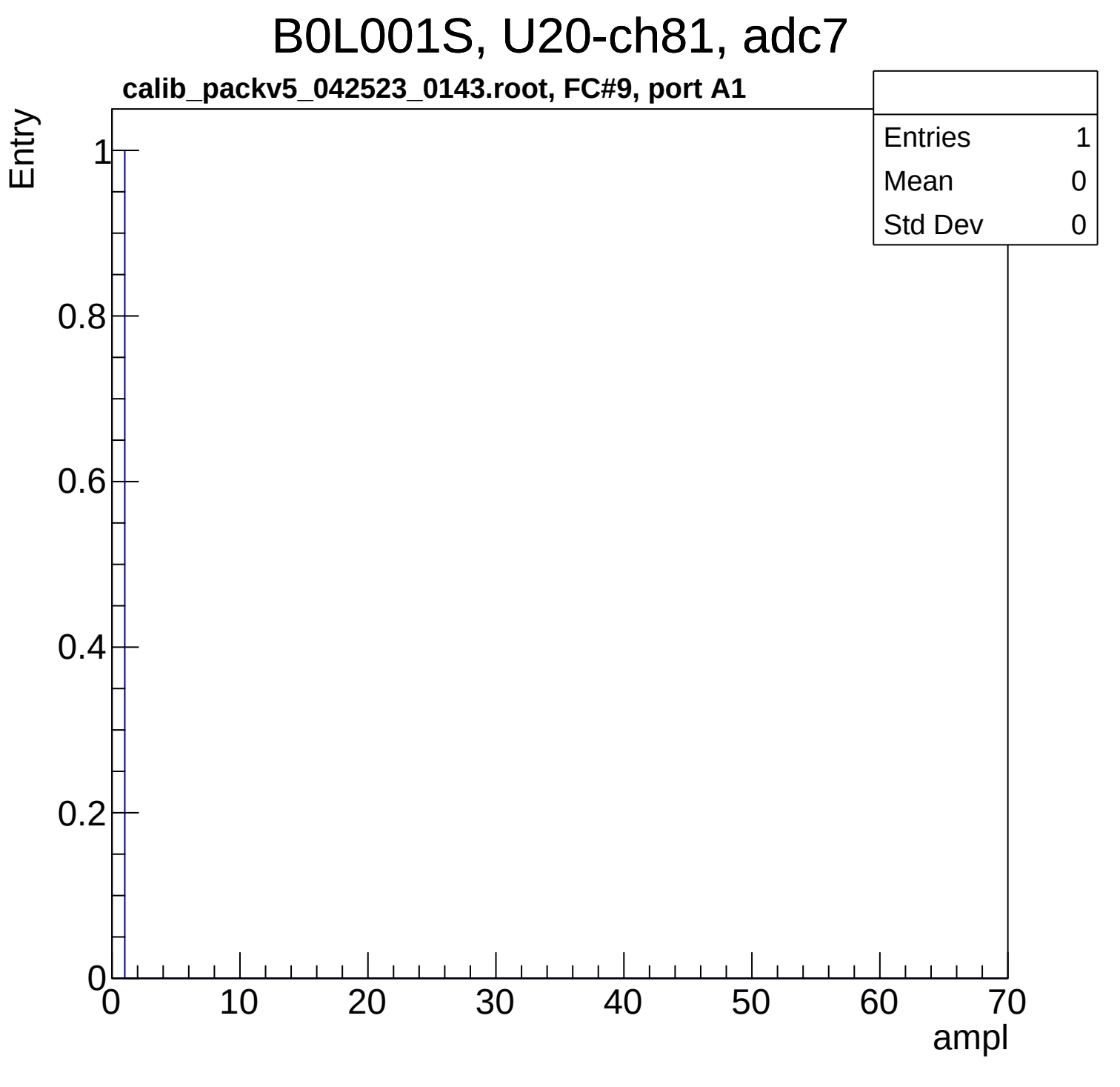

B0L001S, U20-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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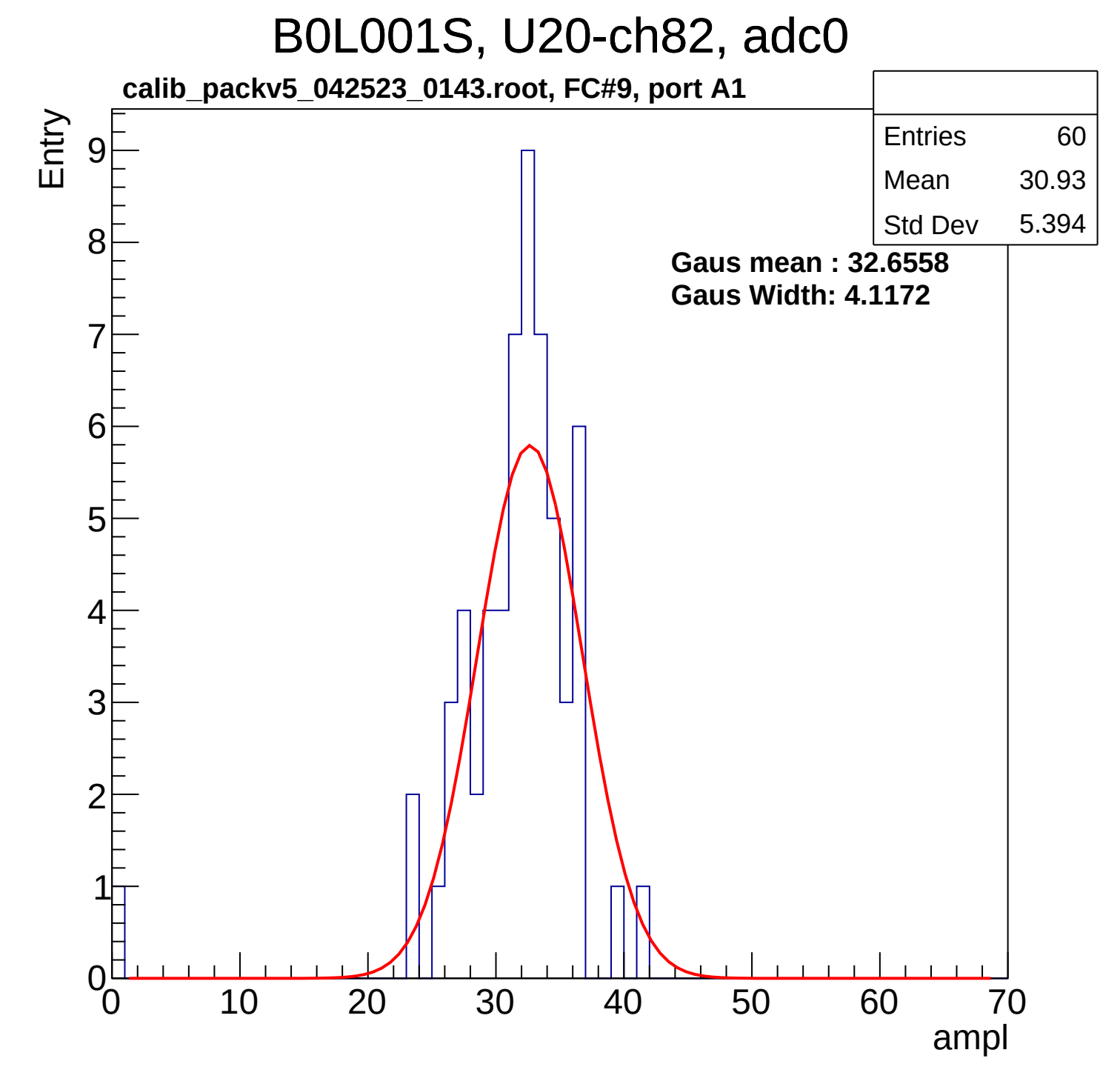
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.93
Std Dev 5.394
Gaus mean : 32.6558
Gaus Width: 4.1172
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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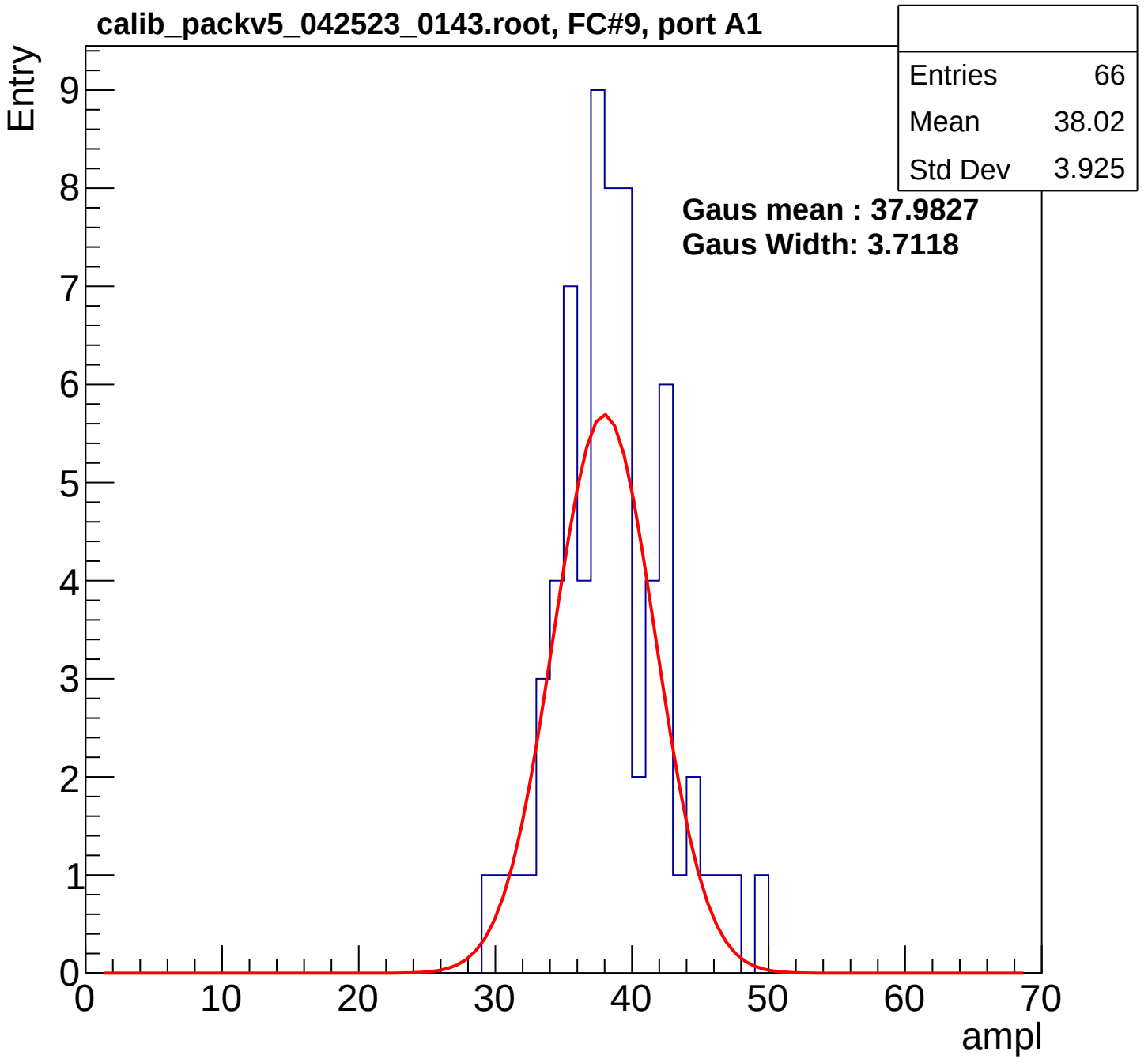
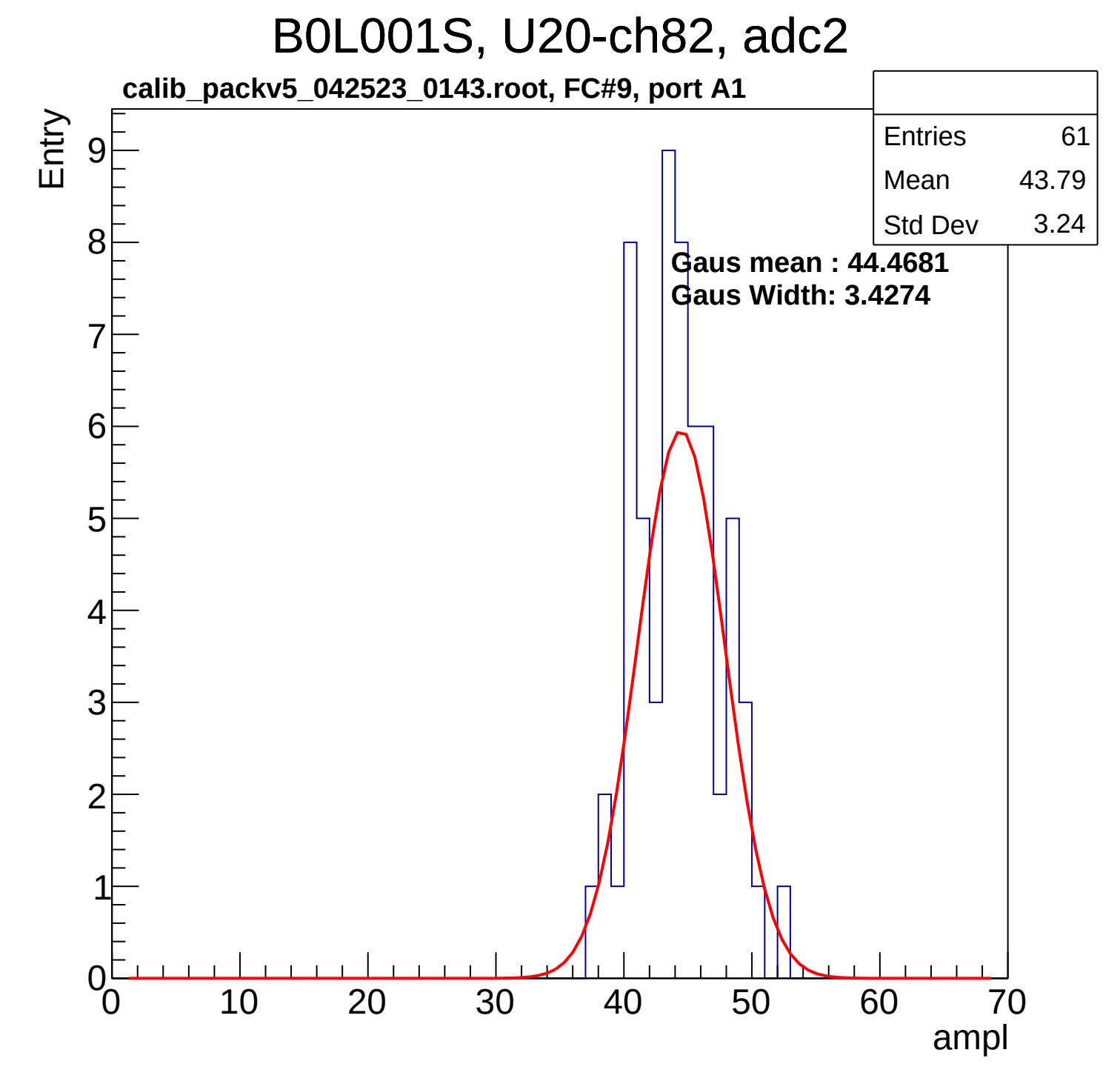

B0L001S, U20-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.02
Std Dev 3.925
Gaus mean : 37.9827
Gaus Width: 3.7118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.79
Std Dev 3.24
Gaus mean : 44.4681
Gaus Width: 3.4274
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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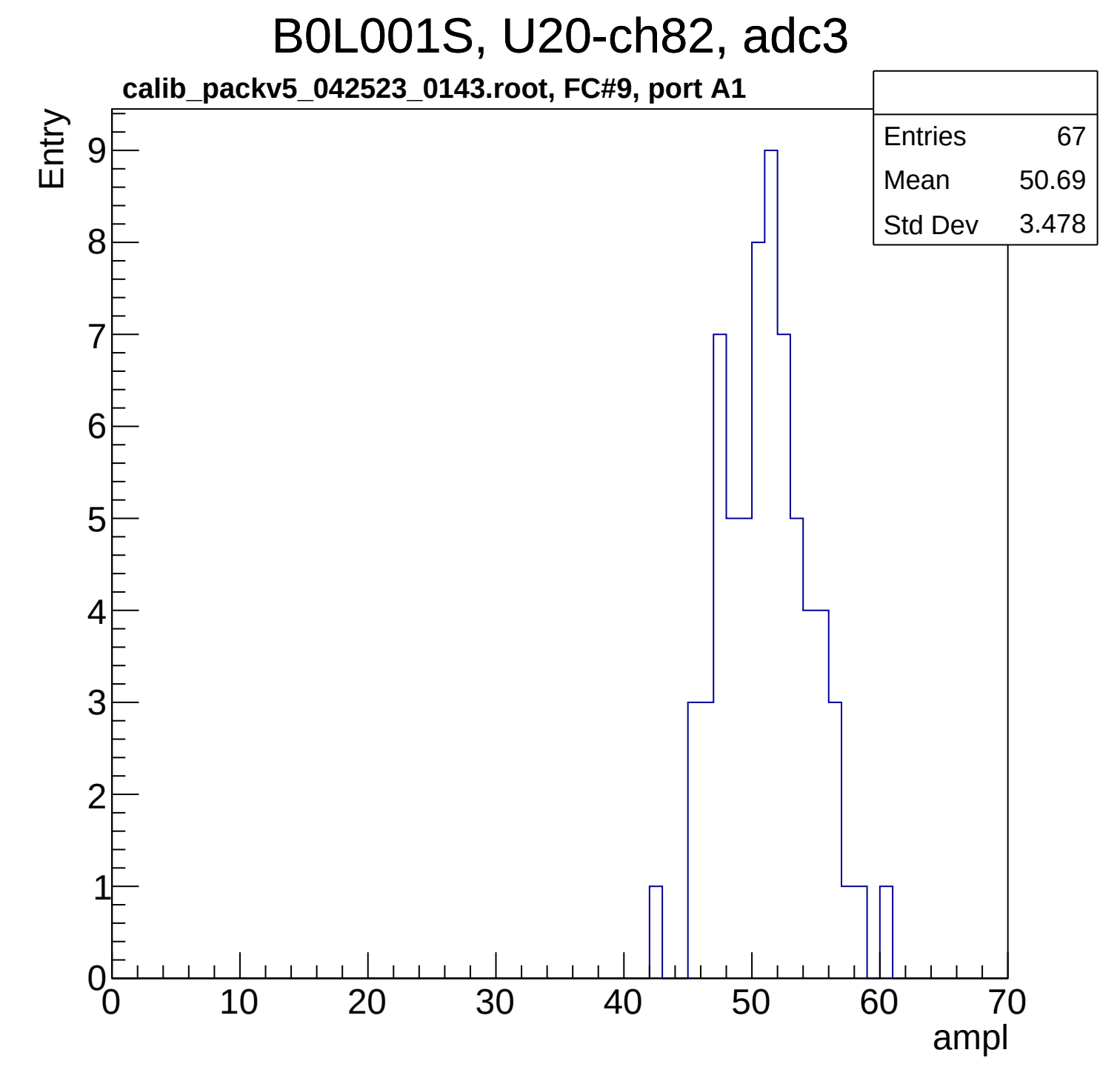
B0L001S, U20-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.69
Std Dev 3.478
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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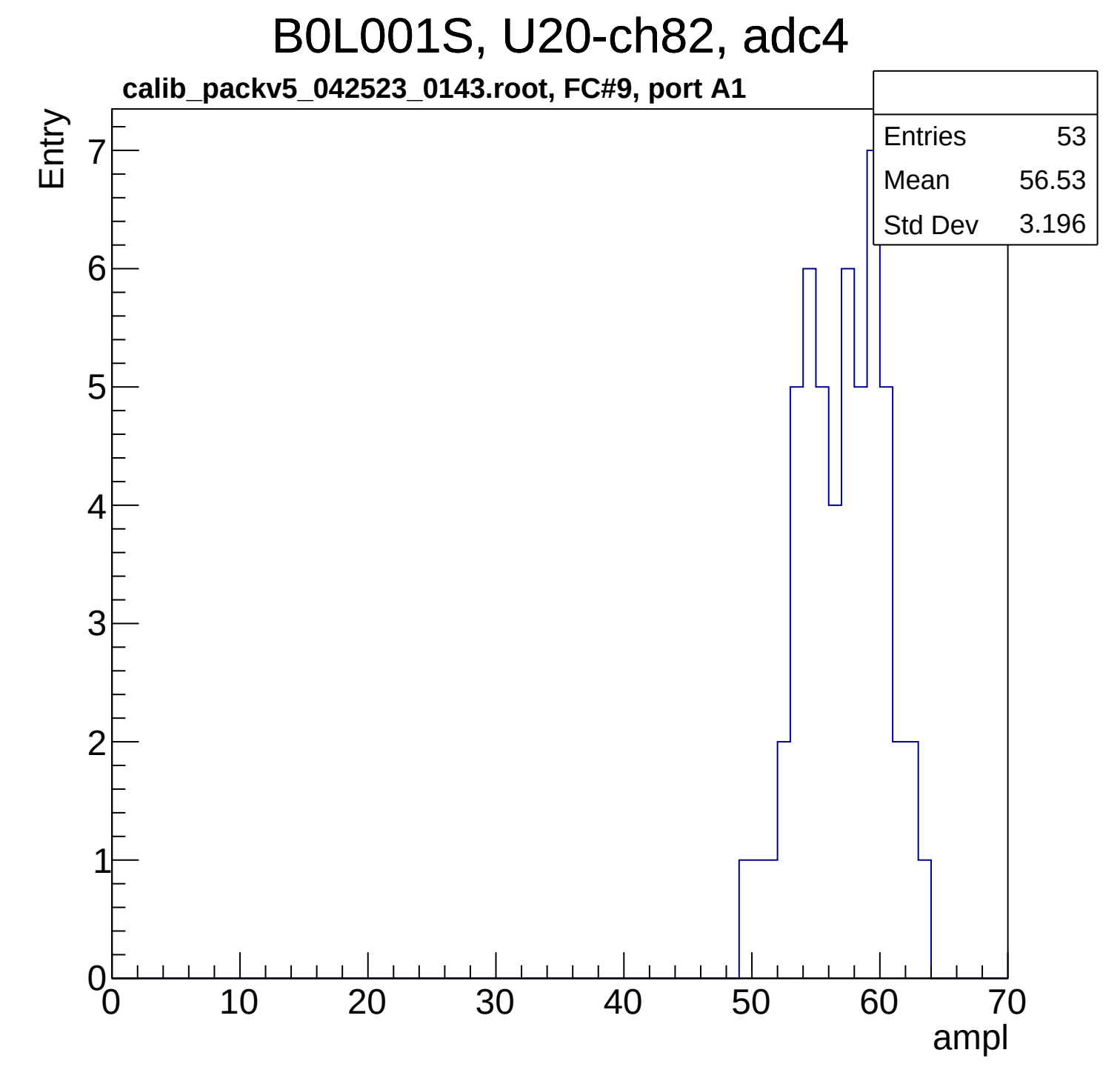
B0L001S, U20-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.53
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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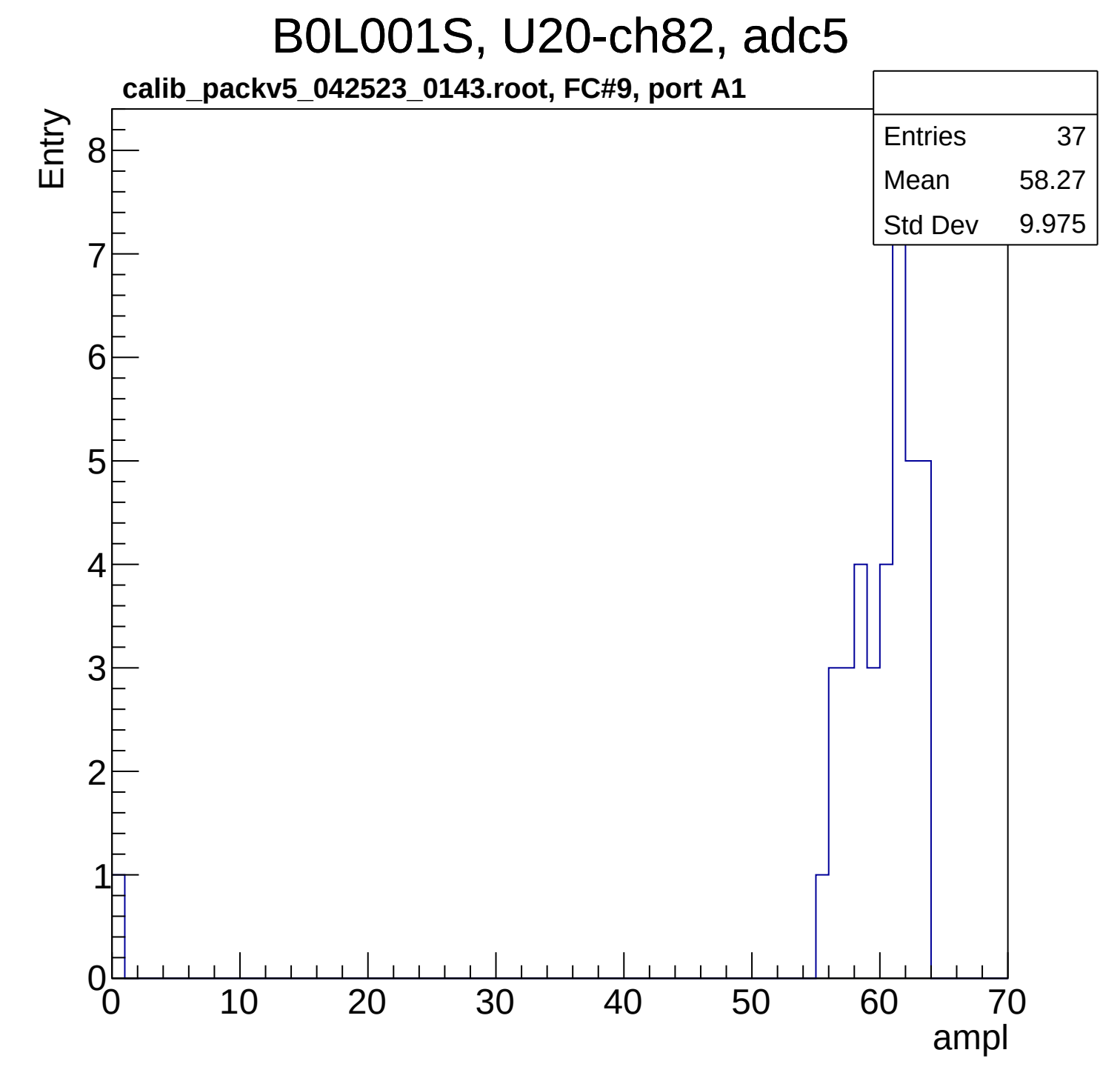
B0L001S, U20-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.27
Std Dev 9.975
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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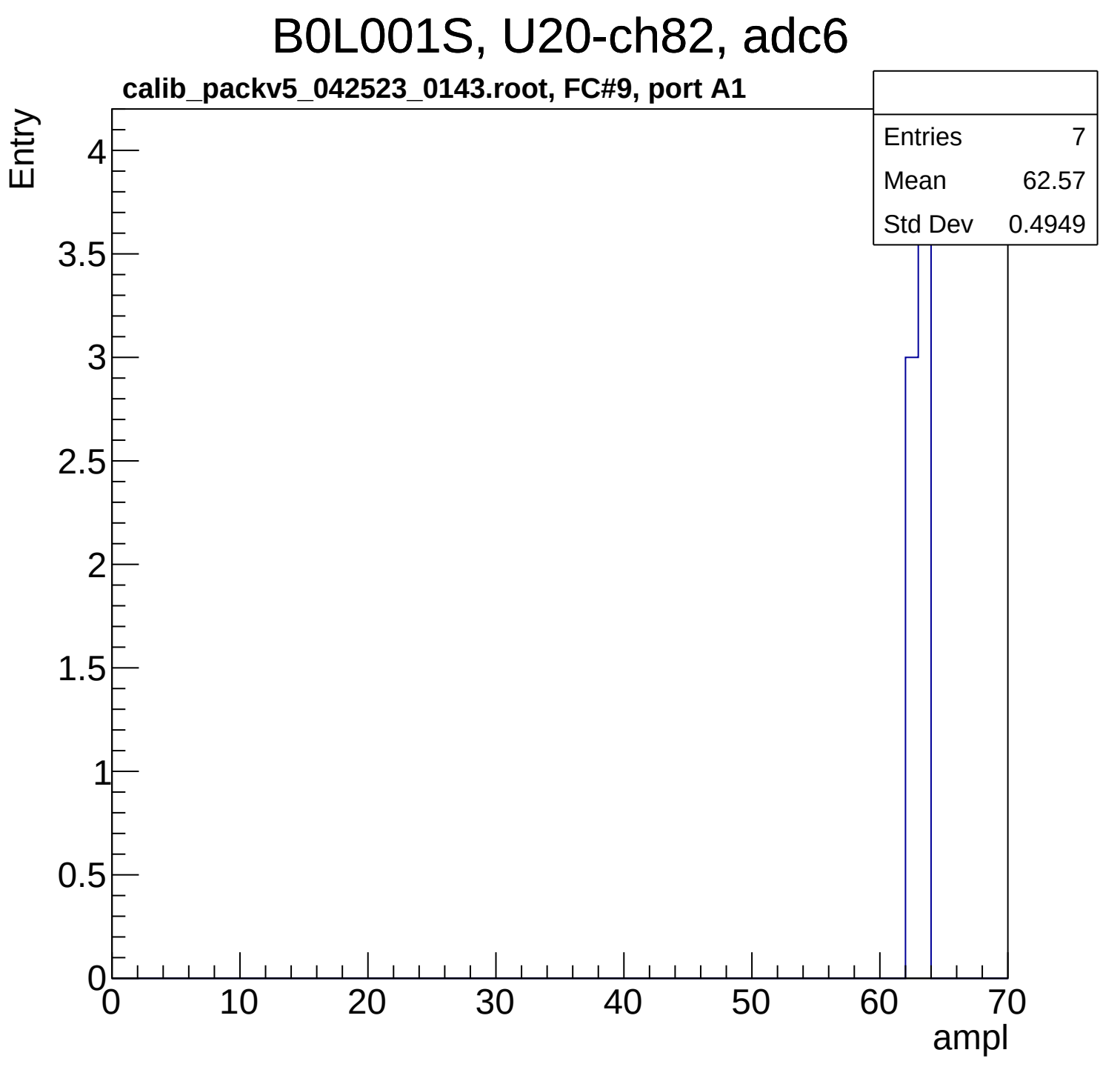
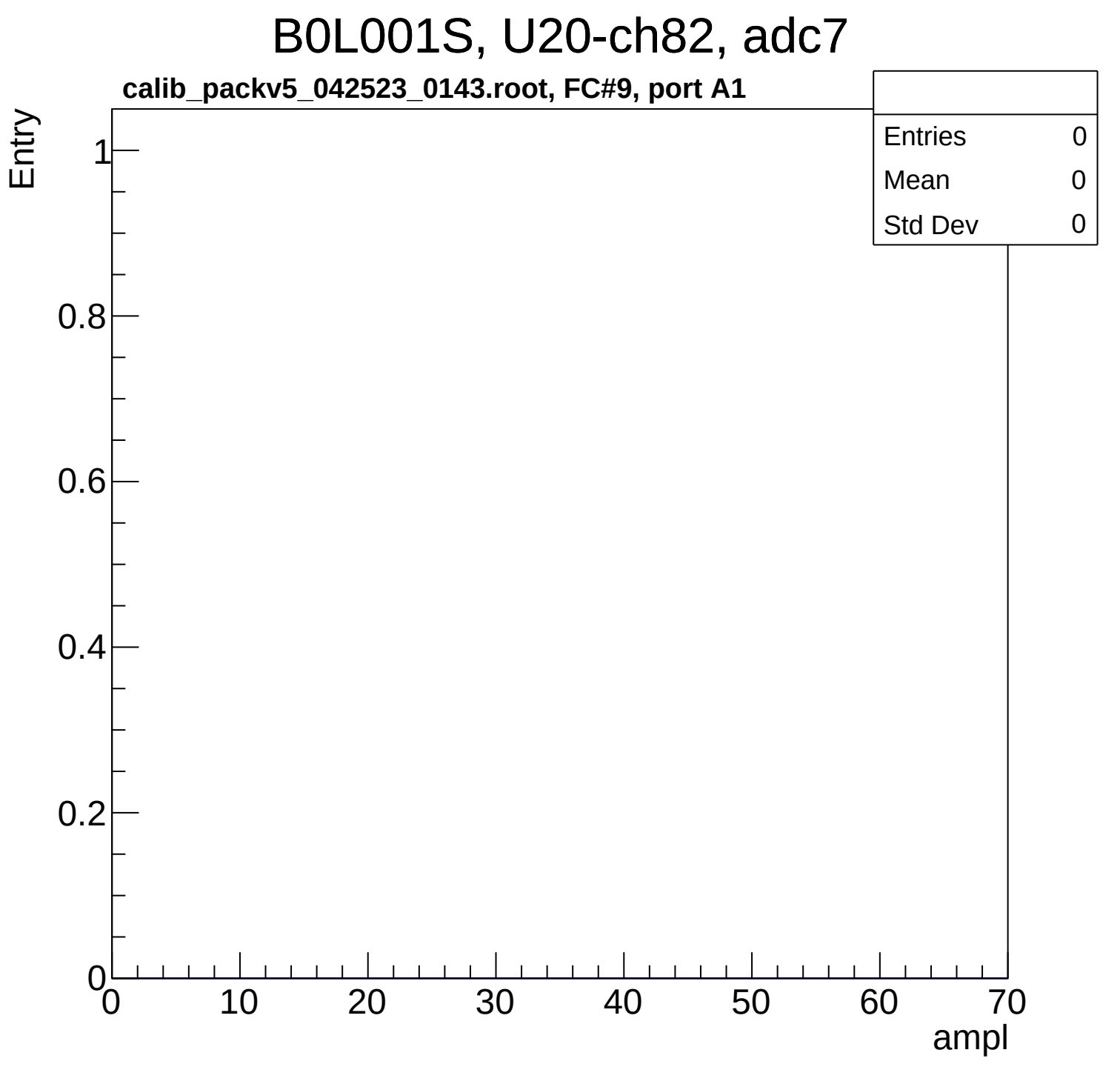

B0L001S, U20-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U20-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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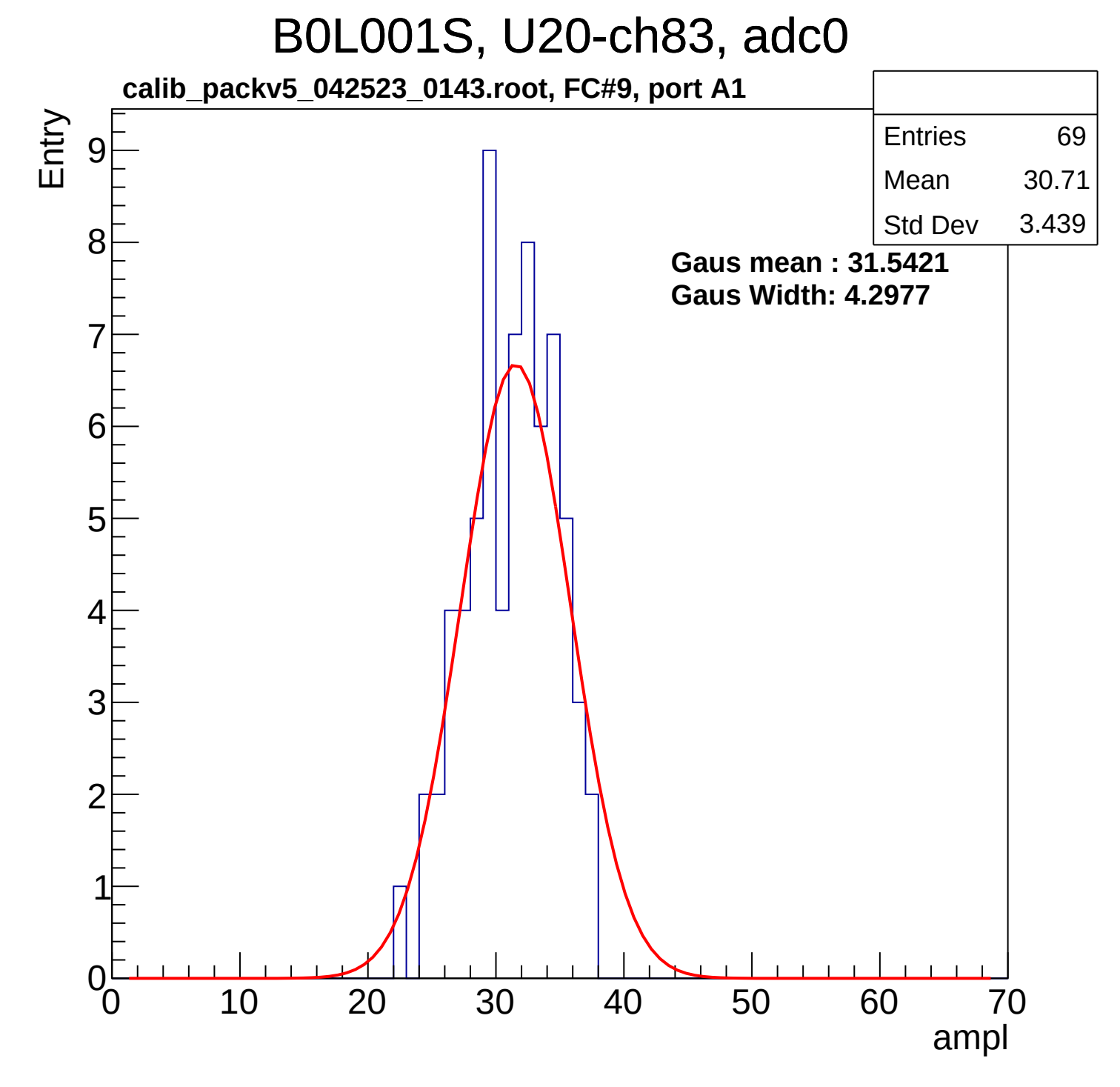
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.71
Std Dev 3.439
Gaus mean : 31.5421
Gaus Width: 4.2977
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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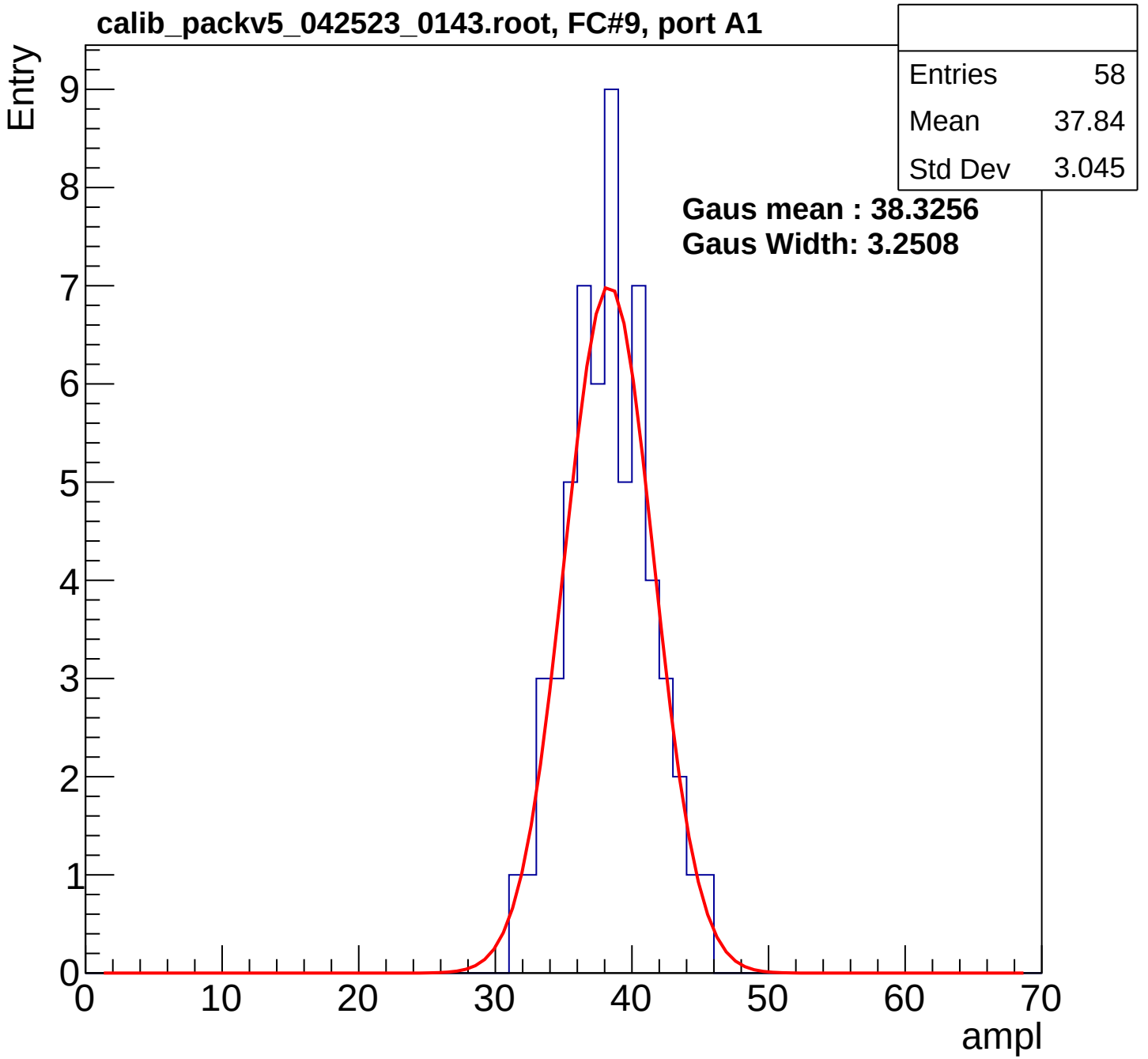
B0L001S, U20-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 37.84
Std Dev 3.045
Gaus mean : 38.3256
Gaus Width: 3.2508
Entry
ampl
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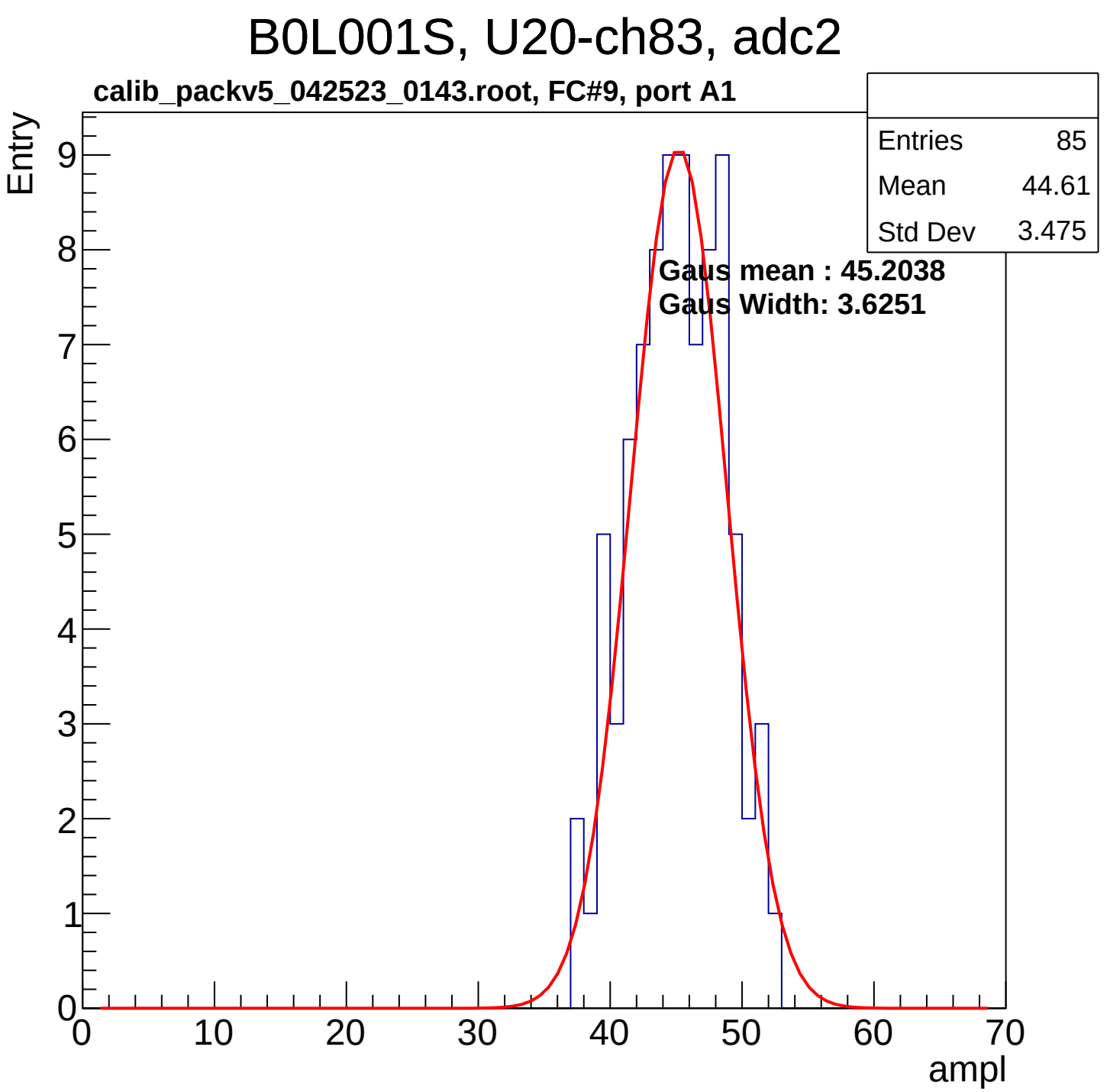
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 44.61
Std Dev 3.475
Gaus mean : 45.2038
Gaus Width: 3.6251
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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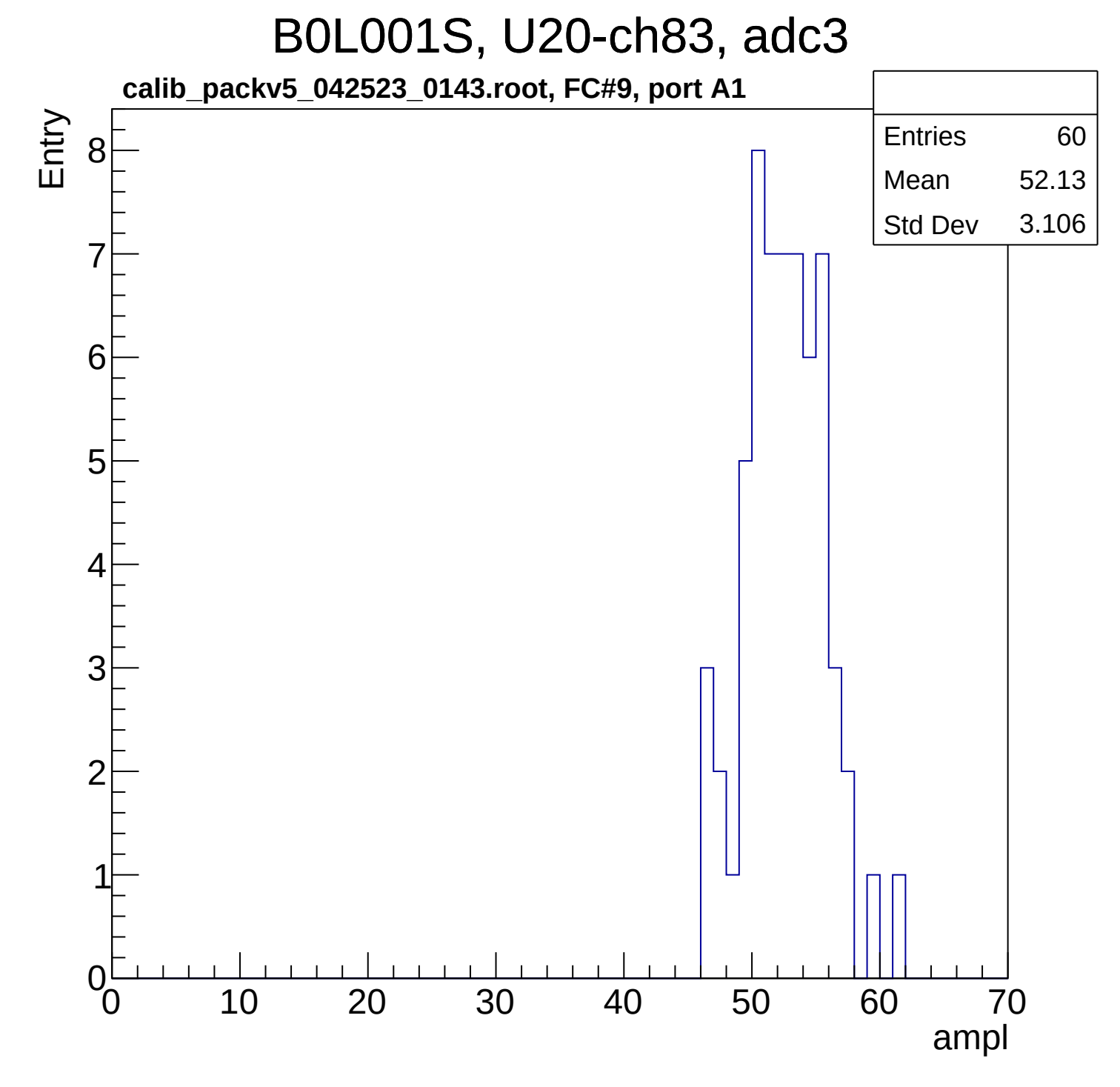
B0L001S, U20-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 52.13
Std Dev 3.106
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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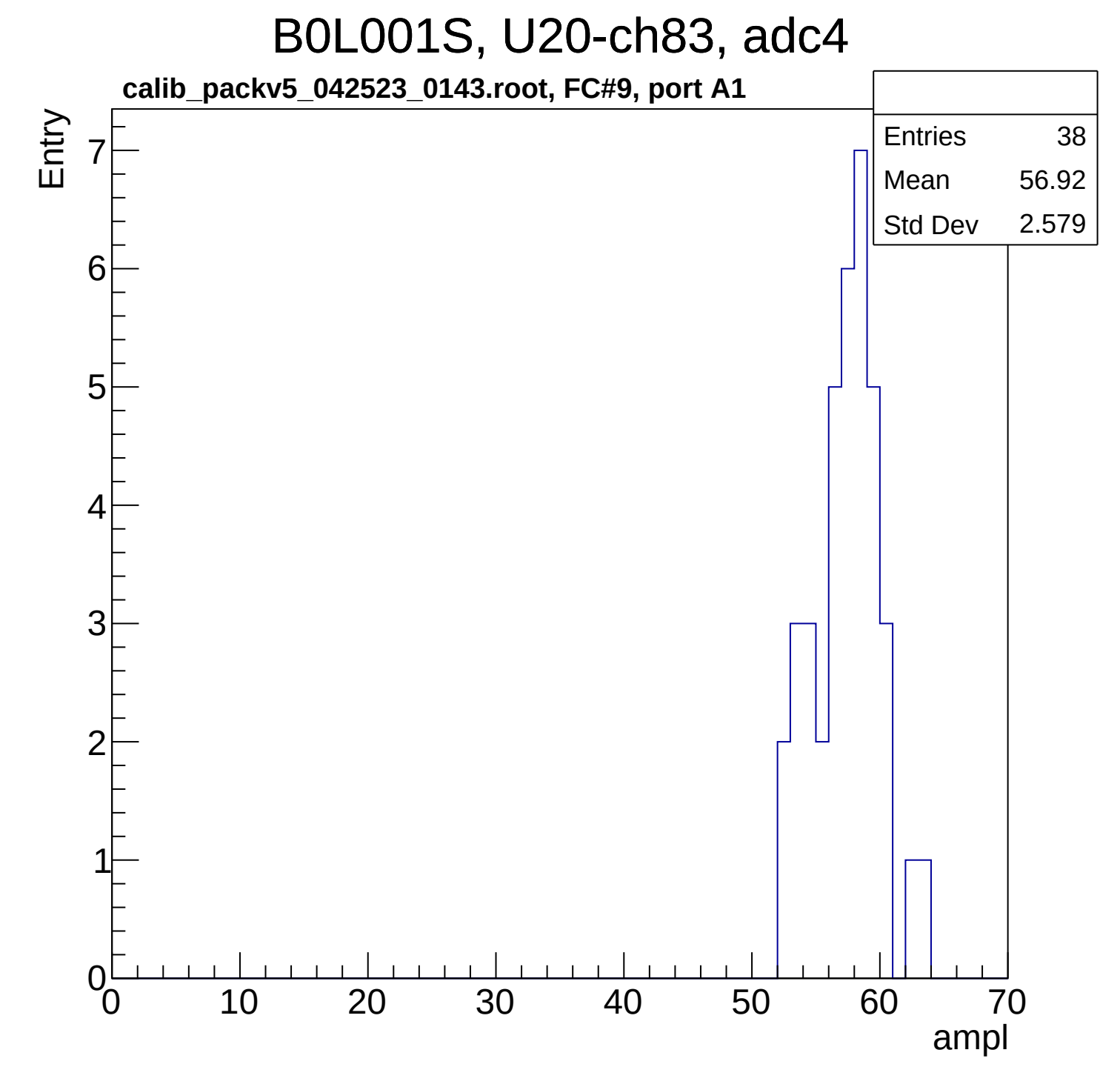
40
50
60
70


B0L001S, U20-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 56.92
Std Dev 2.579
Entry
ampl
0
1
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3
4
5
6
7
0
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20
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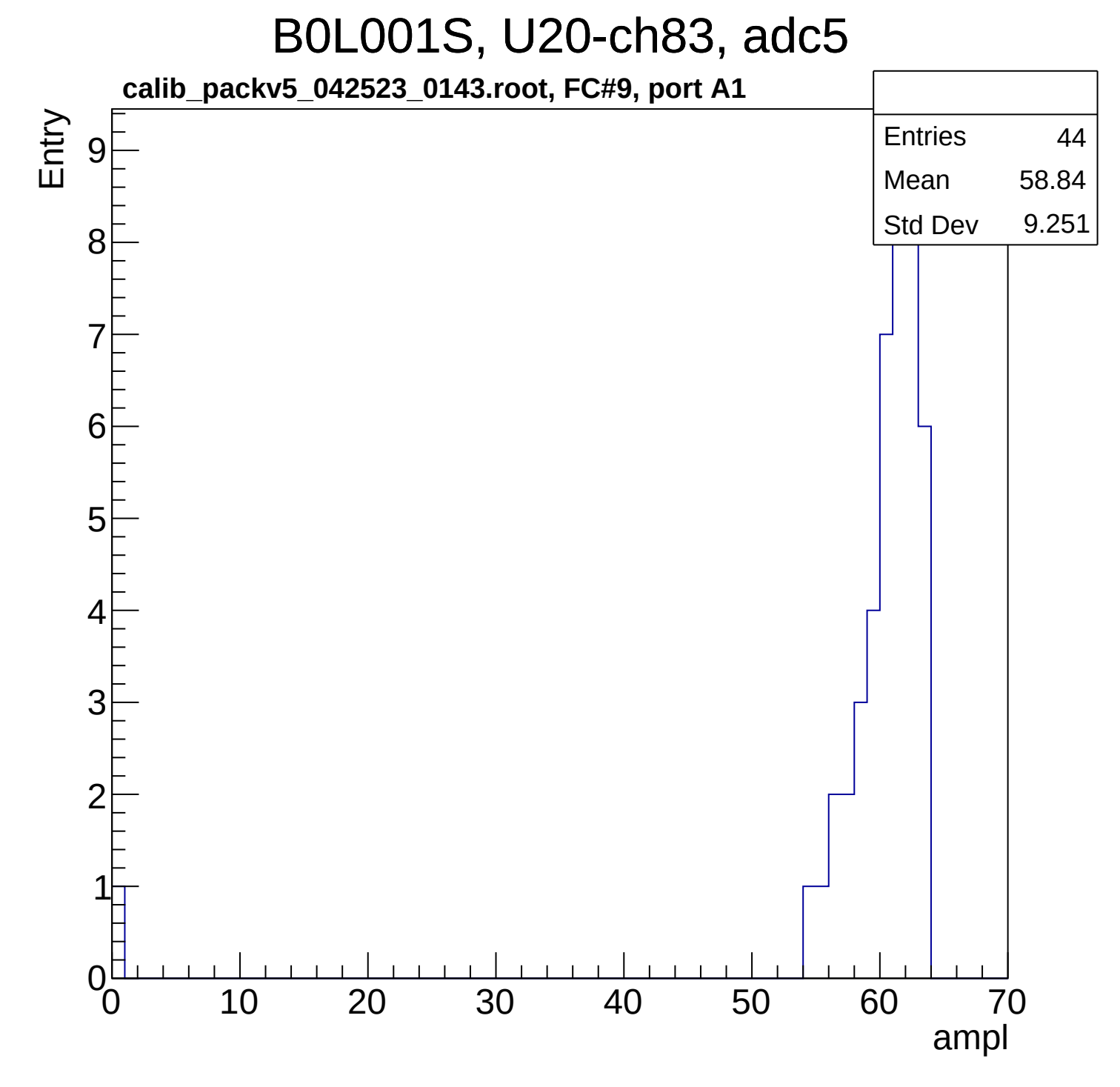
40
50
60
70


B0L001S, U20-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.84
Std Dev 9.251
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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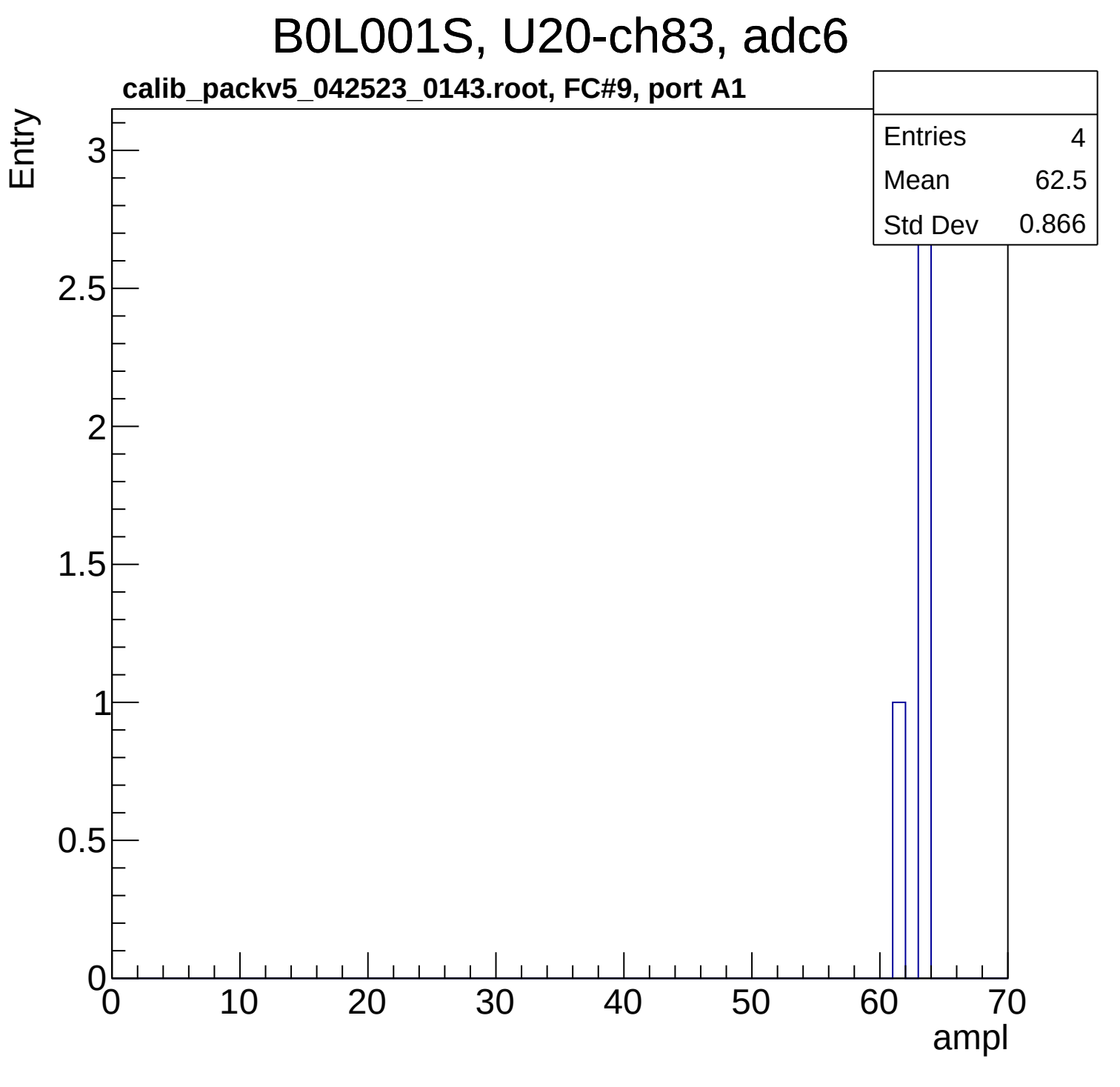
B0L001S, U20-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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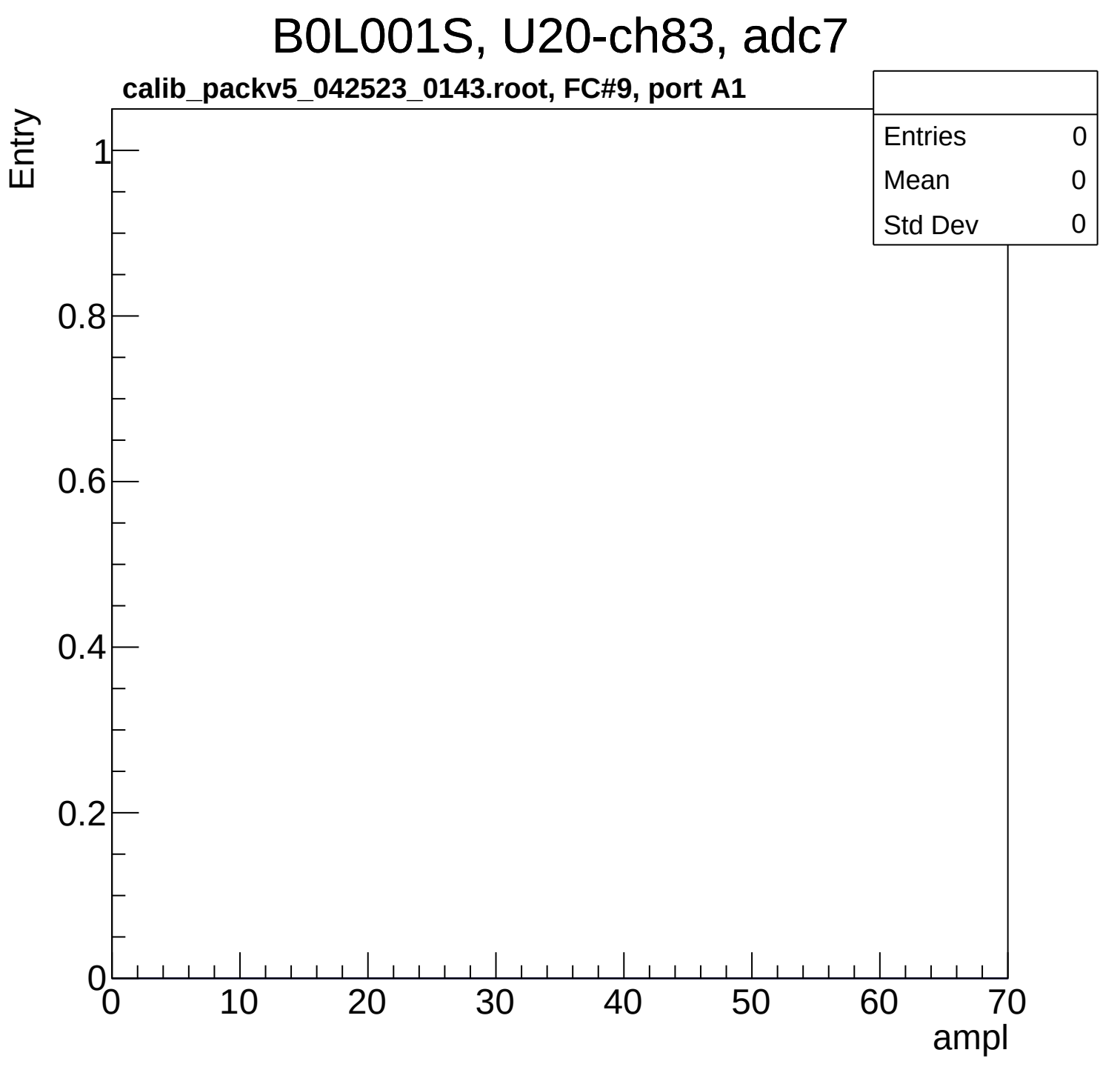
40
50
60
70


B0L001S, U20-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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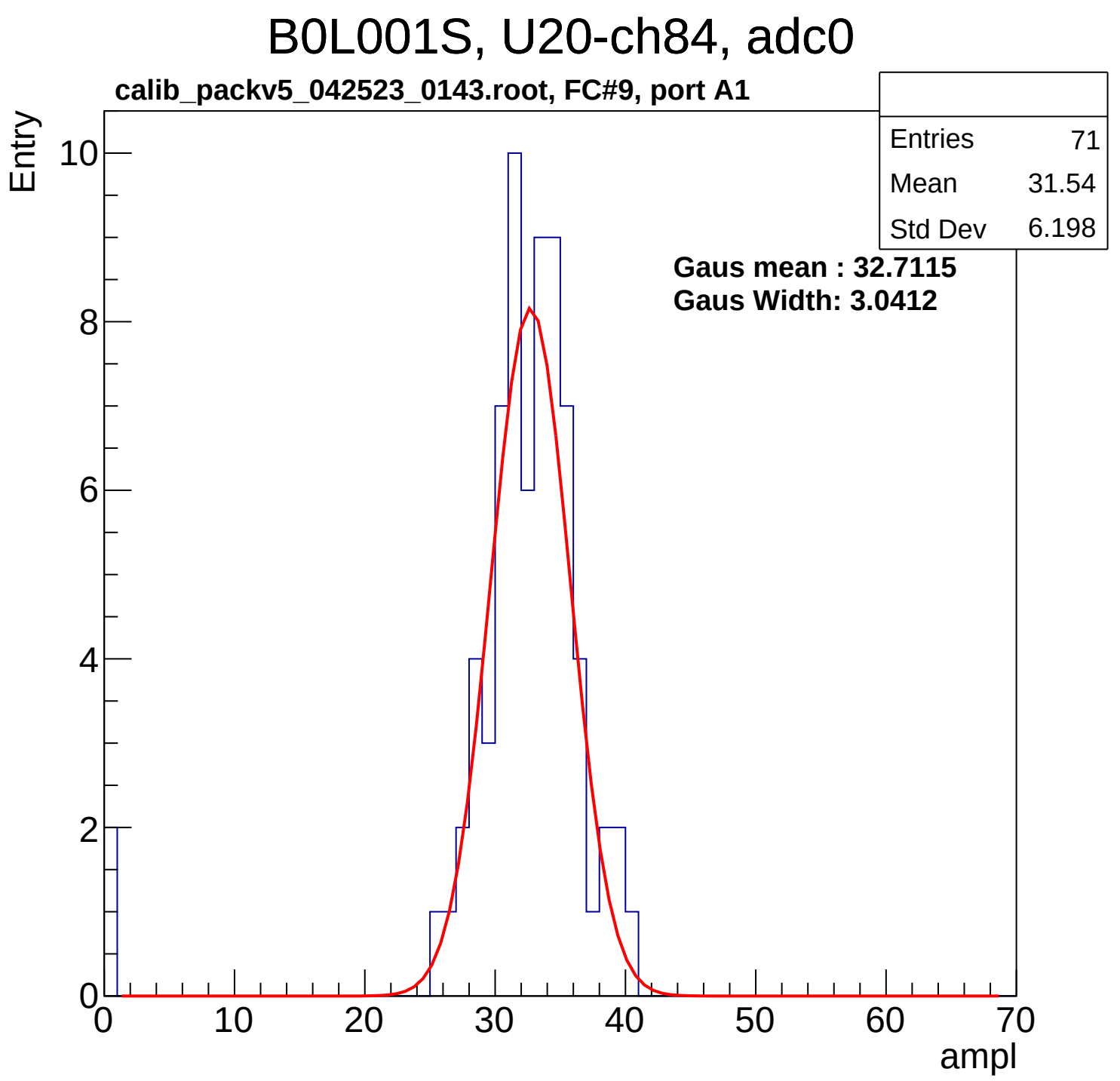
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.54
Std Dev 6.198
Gaus mean : 32.7115
Gaus Width: 3.0412
Entry
ampl
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2
4
6
8
10
0
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20
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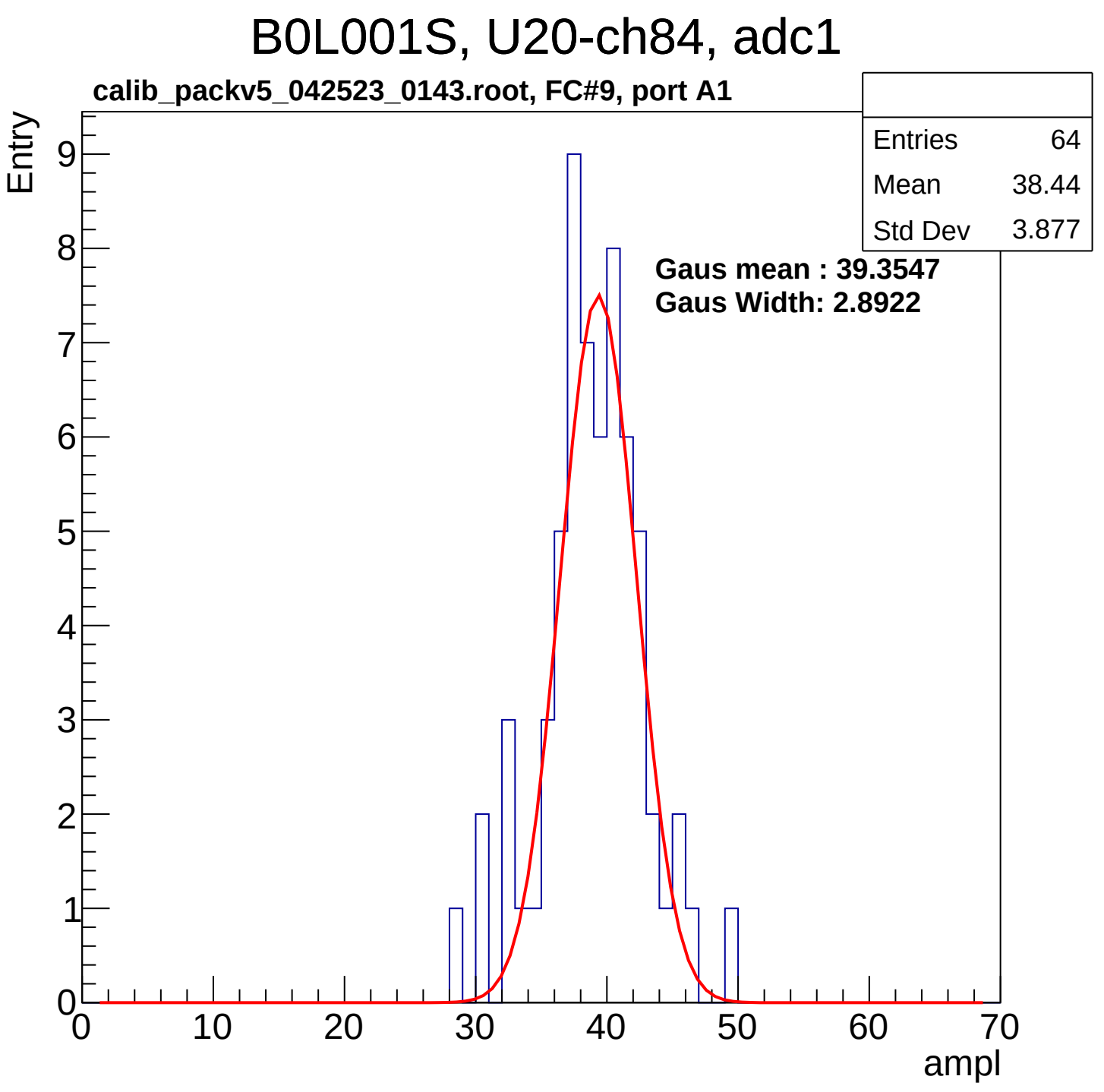
40
50
60
70


B0L001S, U20-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.44
Std Dev 3.877
Gaus mean : 39.3547
Gaus Width: 2.8922
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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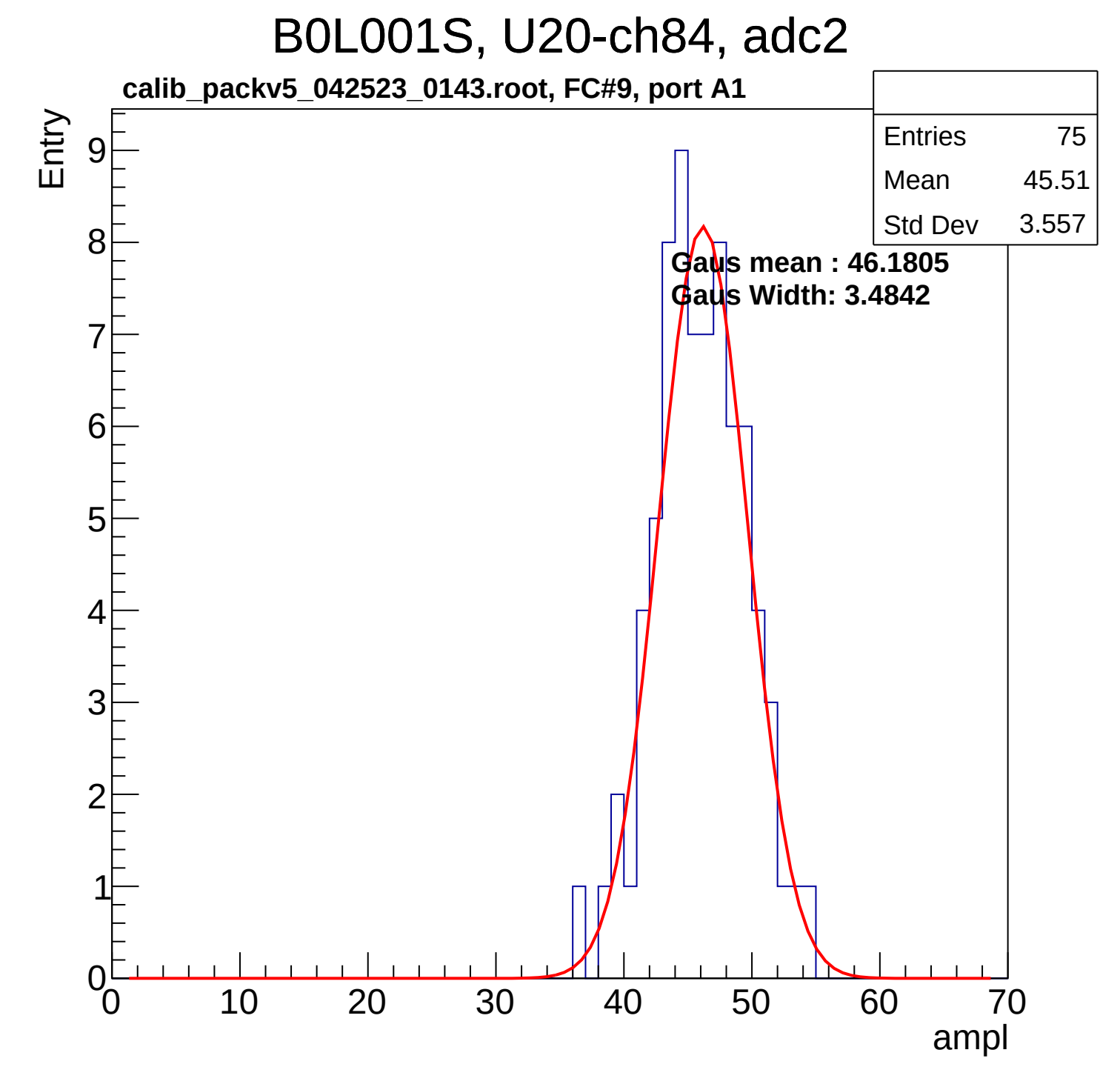
B0L001S, U20-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 45.51
Std Dev 3.557
Gaus mean : 46.1805
Gaus Width: 3.4842
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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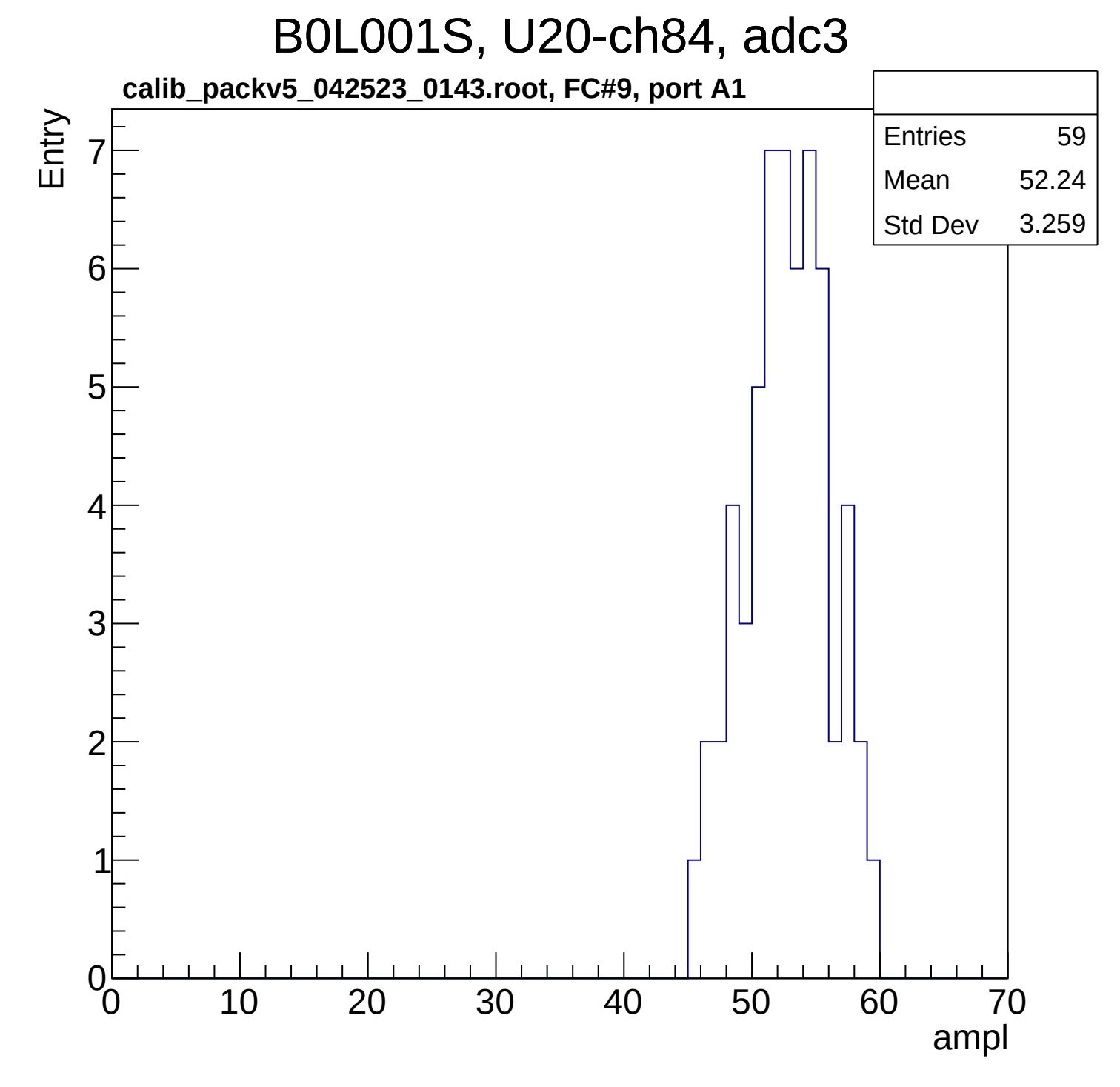
B0L001S, U20-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 52.24
Std Dev 3.259
Entry
ampl
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1
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3
4
5
6
7
0
10
20
30
40
50
60
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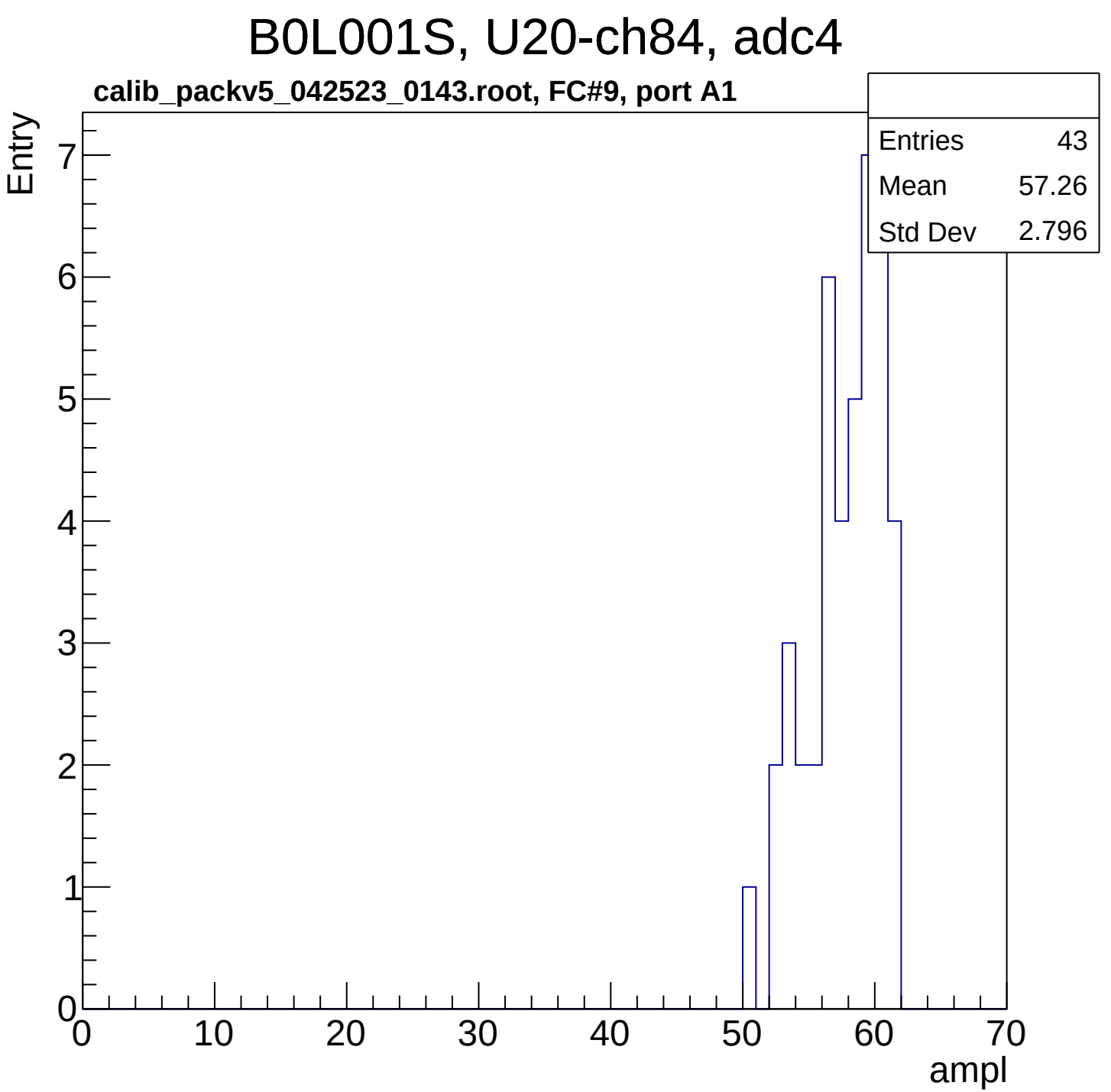
B0L001S, U20-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.26
Std Dev 2.796
Entry
ampl
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3
4
5
6
7
0
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20
30
40
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60
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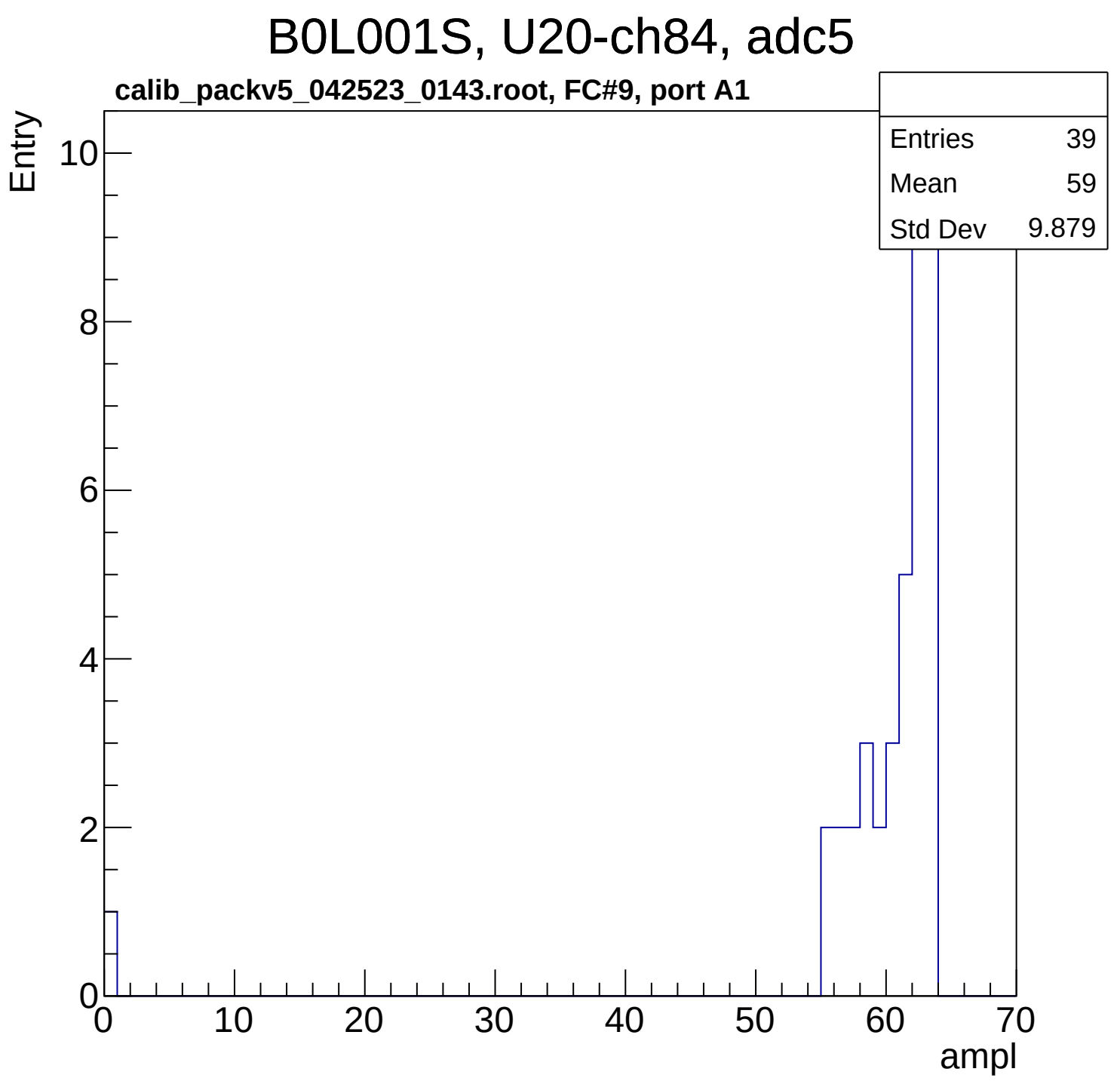
B0L001S, U20-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59
Std Dev 9.879
Entry
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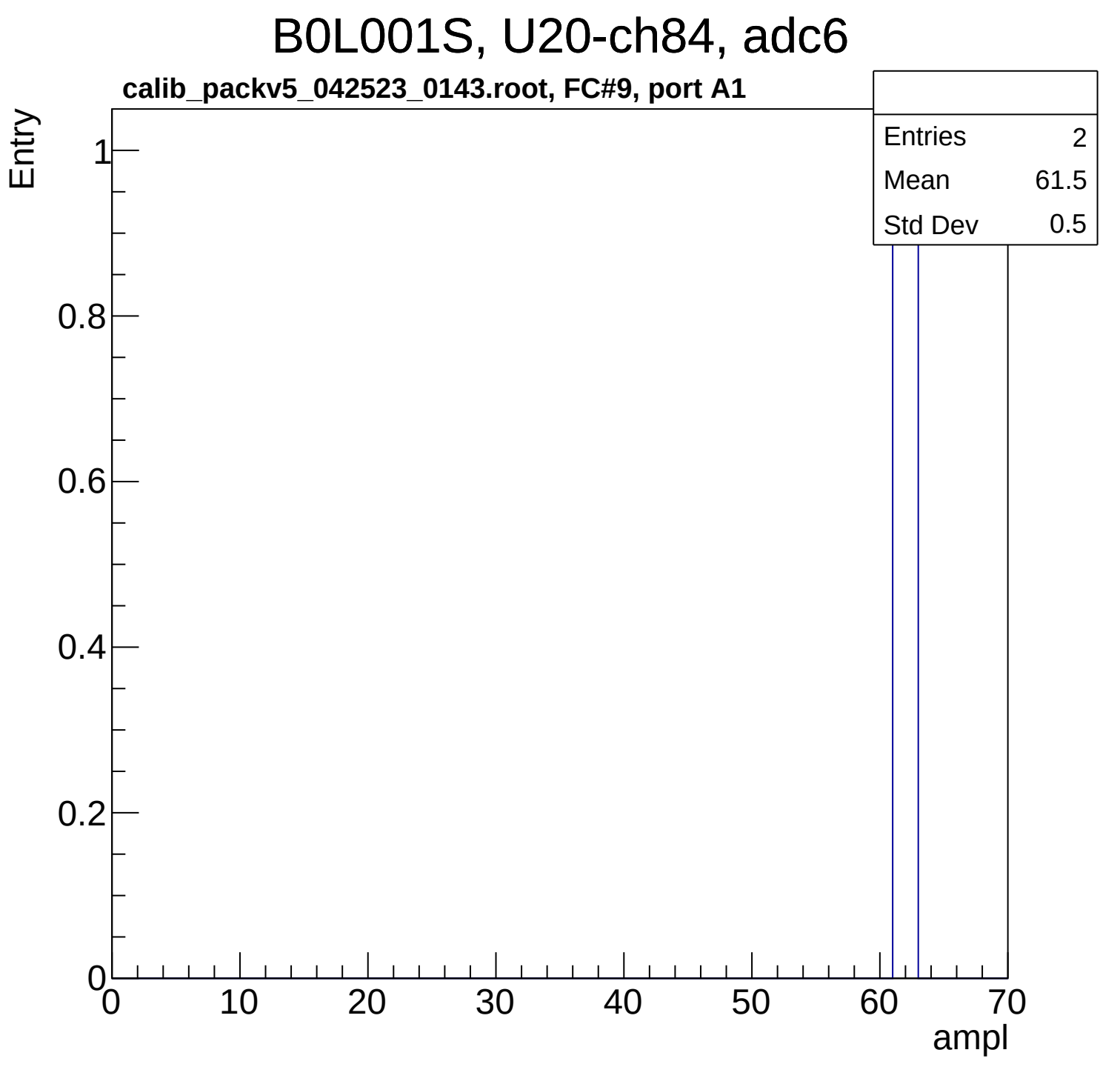
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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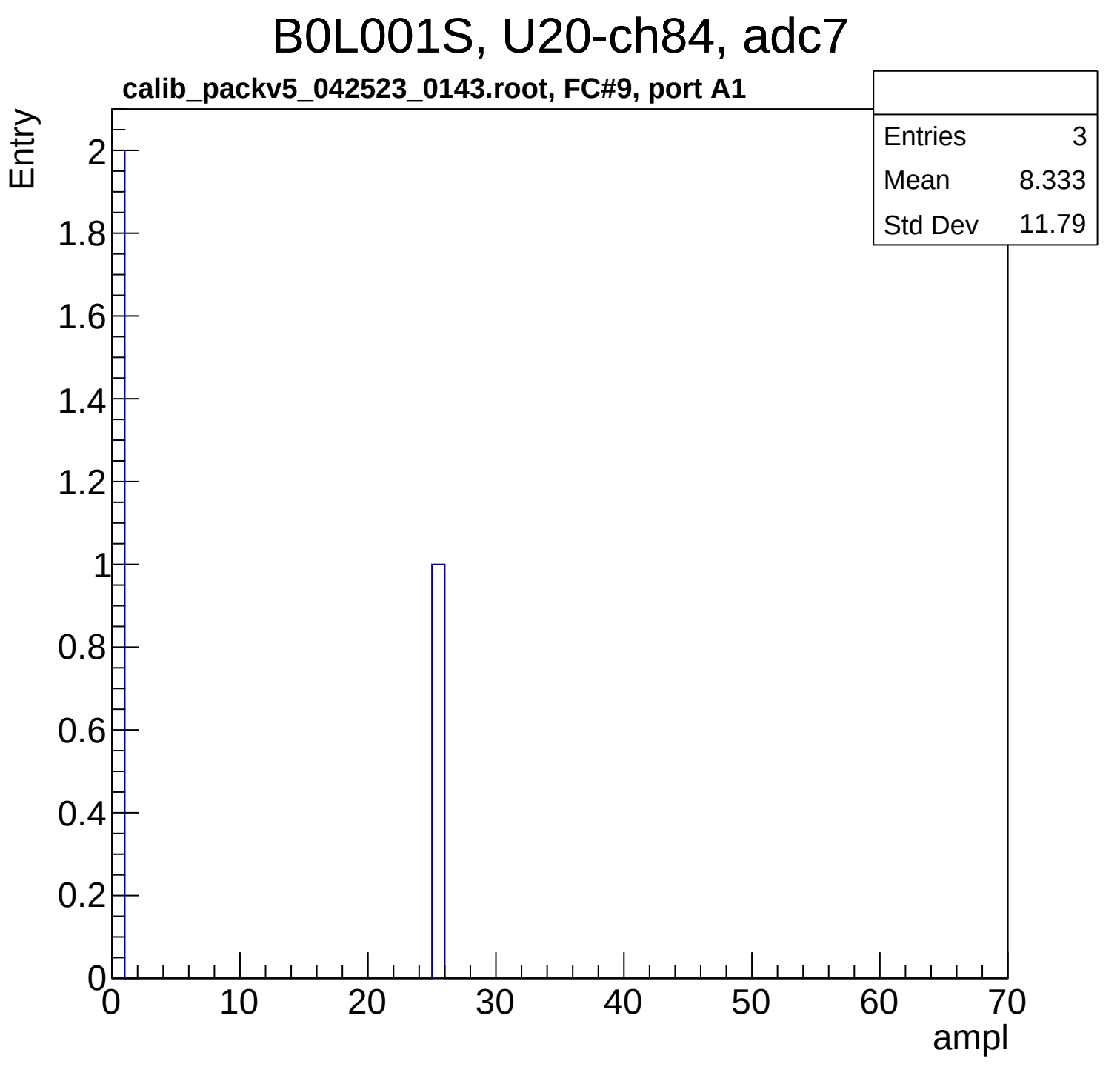
50
60
70
ampl


B0L001S, U20-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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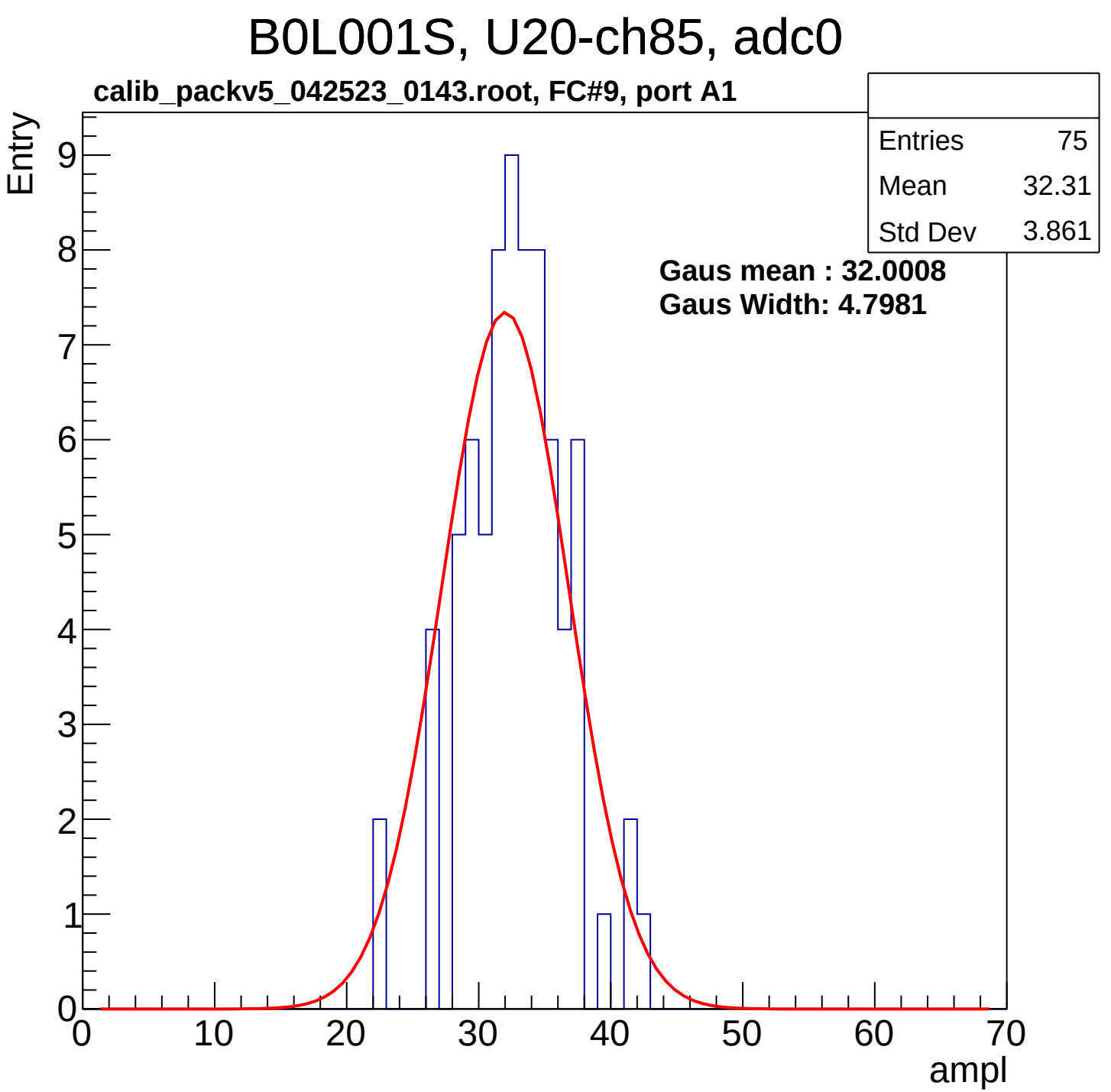
B0L001S, U20-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 32.31
Std Dev 3.861
Gaus mean : 32.0008
Gaus Width: 4.7981
Entry
ampl
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2
3
4
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0
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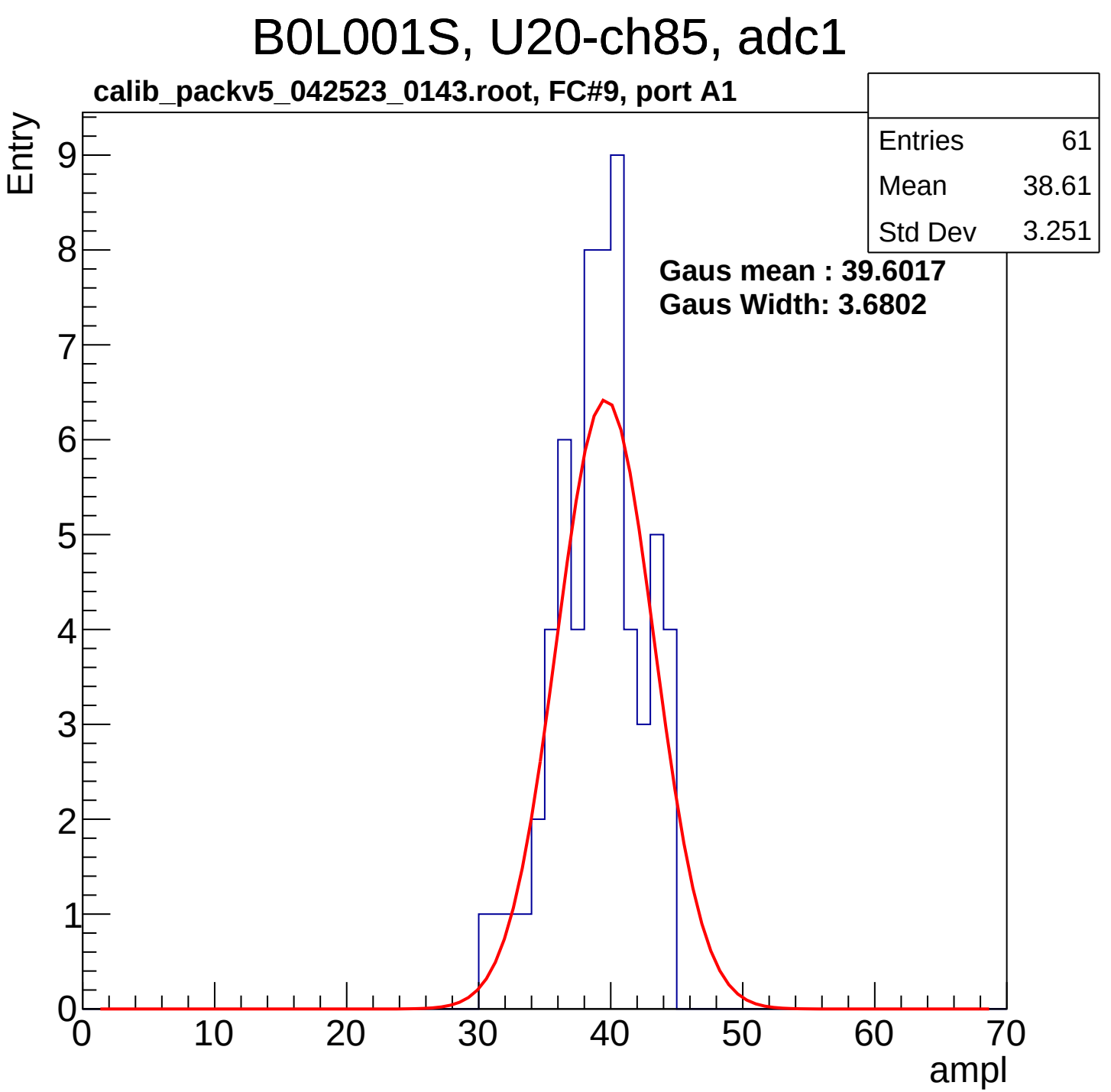
B0L001S, U20-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 38.61
Std Dev 3.251
Gaus mean : 39.6017
Gaus Width: 3.6802
Entry
ampl
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3
4
5
6
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0
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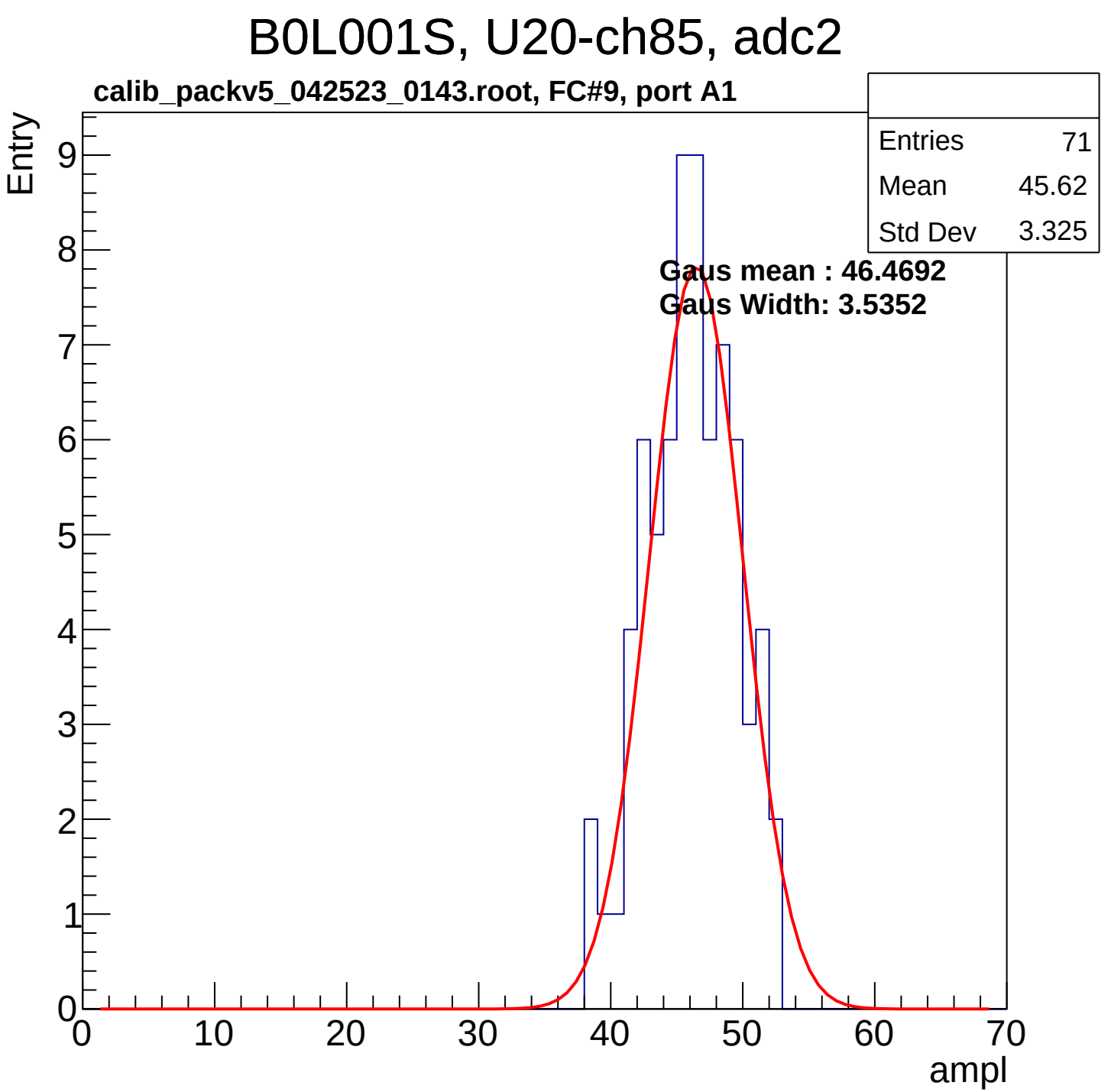
B0L001S, U20-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.62
Std Dev 3.325
Gaus mean : 46.4692
Gaus Width: 3.5352
Entry
ampl
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0
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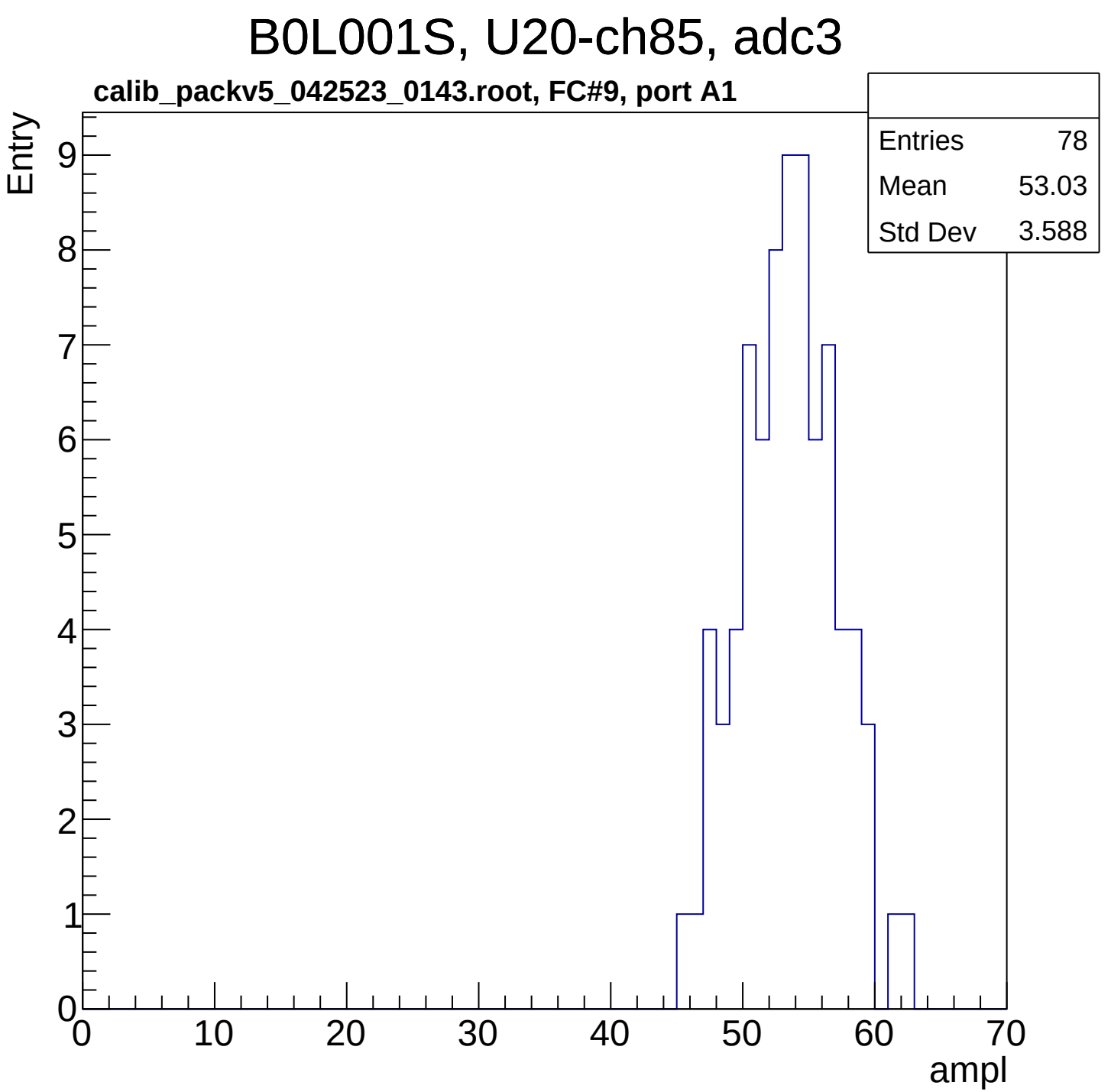
B0L001S, U20-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 53.03
Std Dev 3.588
Entry
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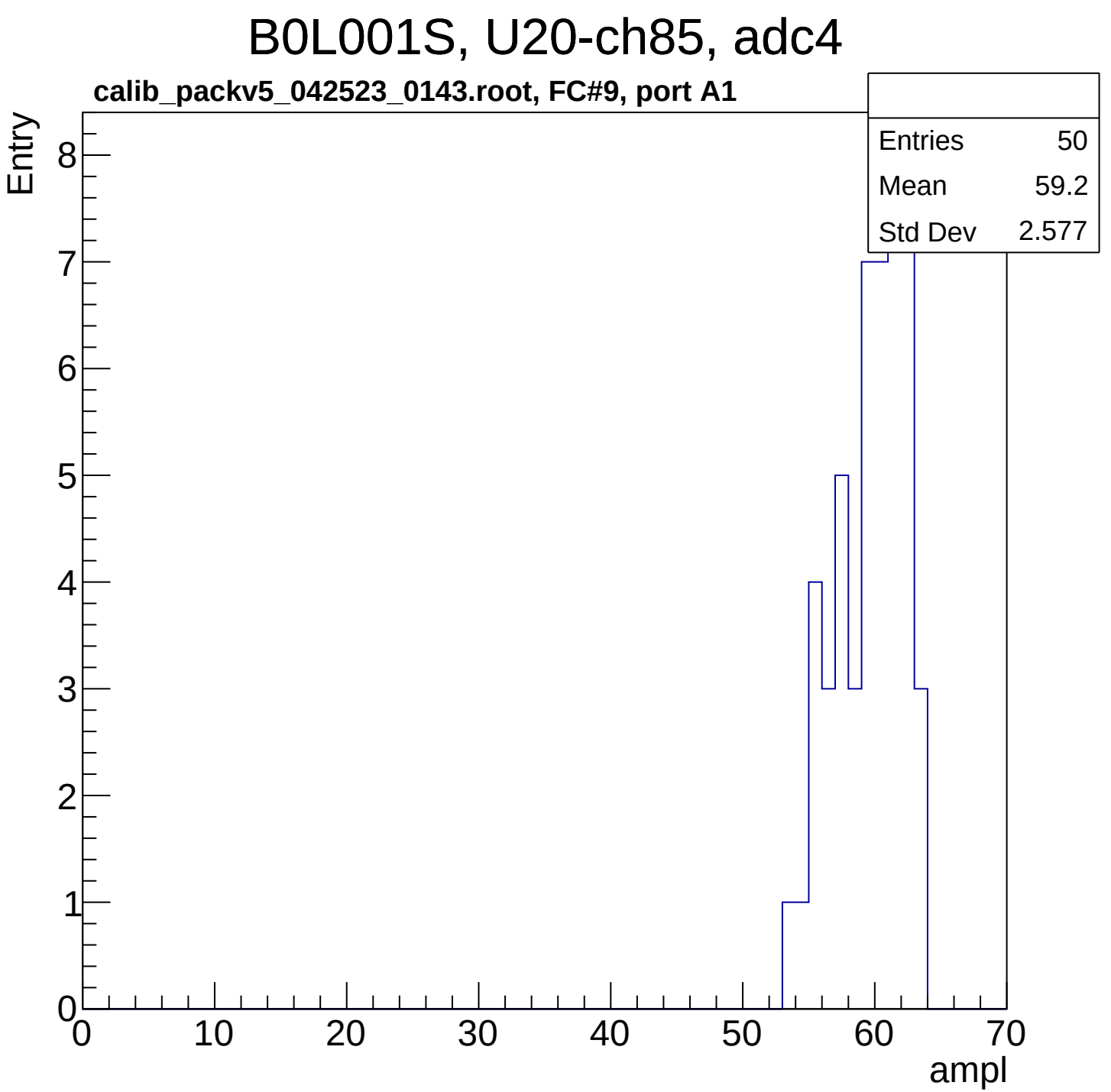
ampl


B0L001S, U20-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.2
Std Dev 2.577
Entry
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2
3
4
5
6
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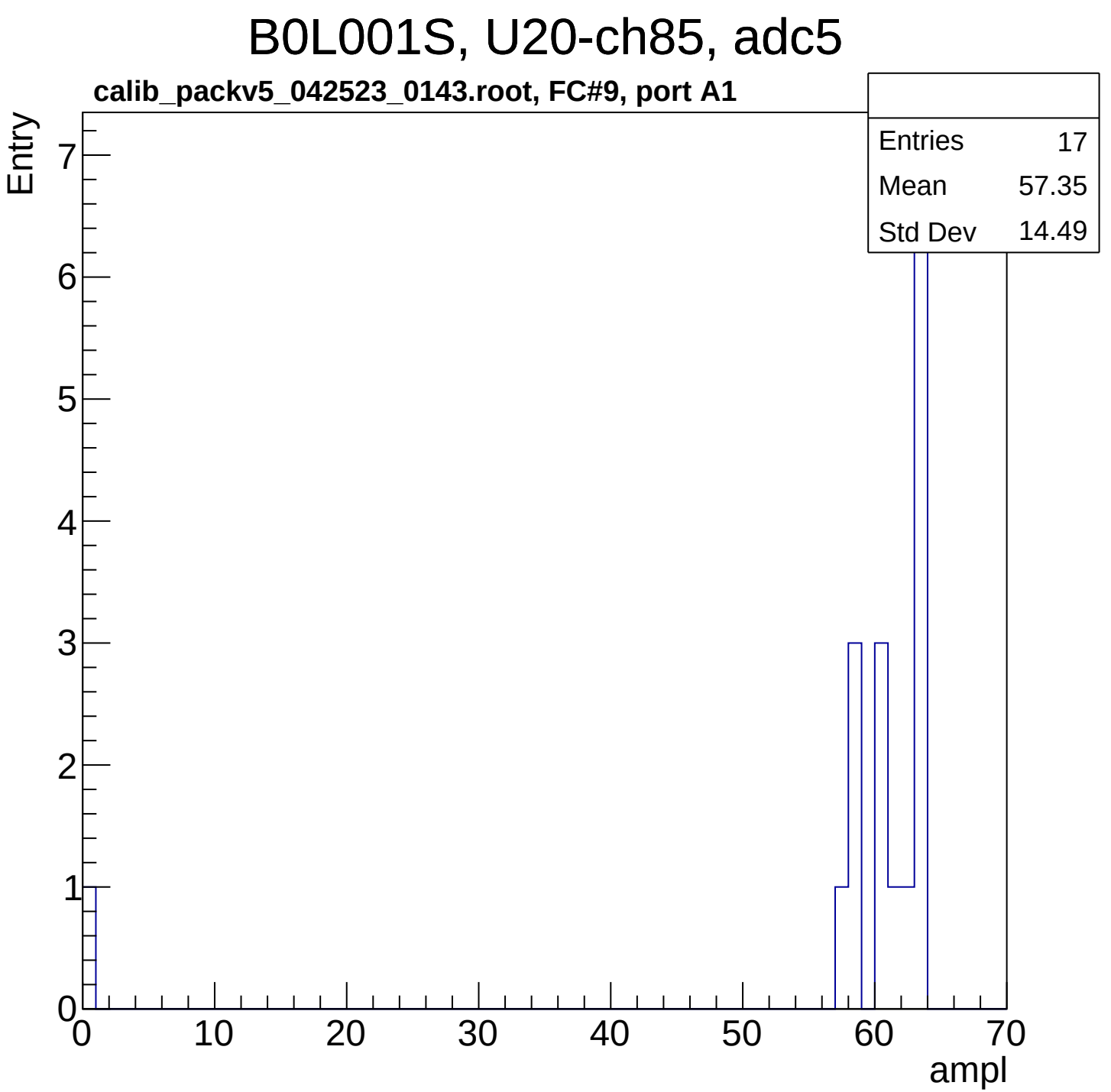
70
ampl


B0L001S, U20-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 57.35
Std Dev 14.49
Entry
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1
2
3
4
5
6
7
0
10
20
30
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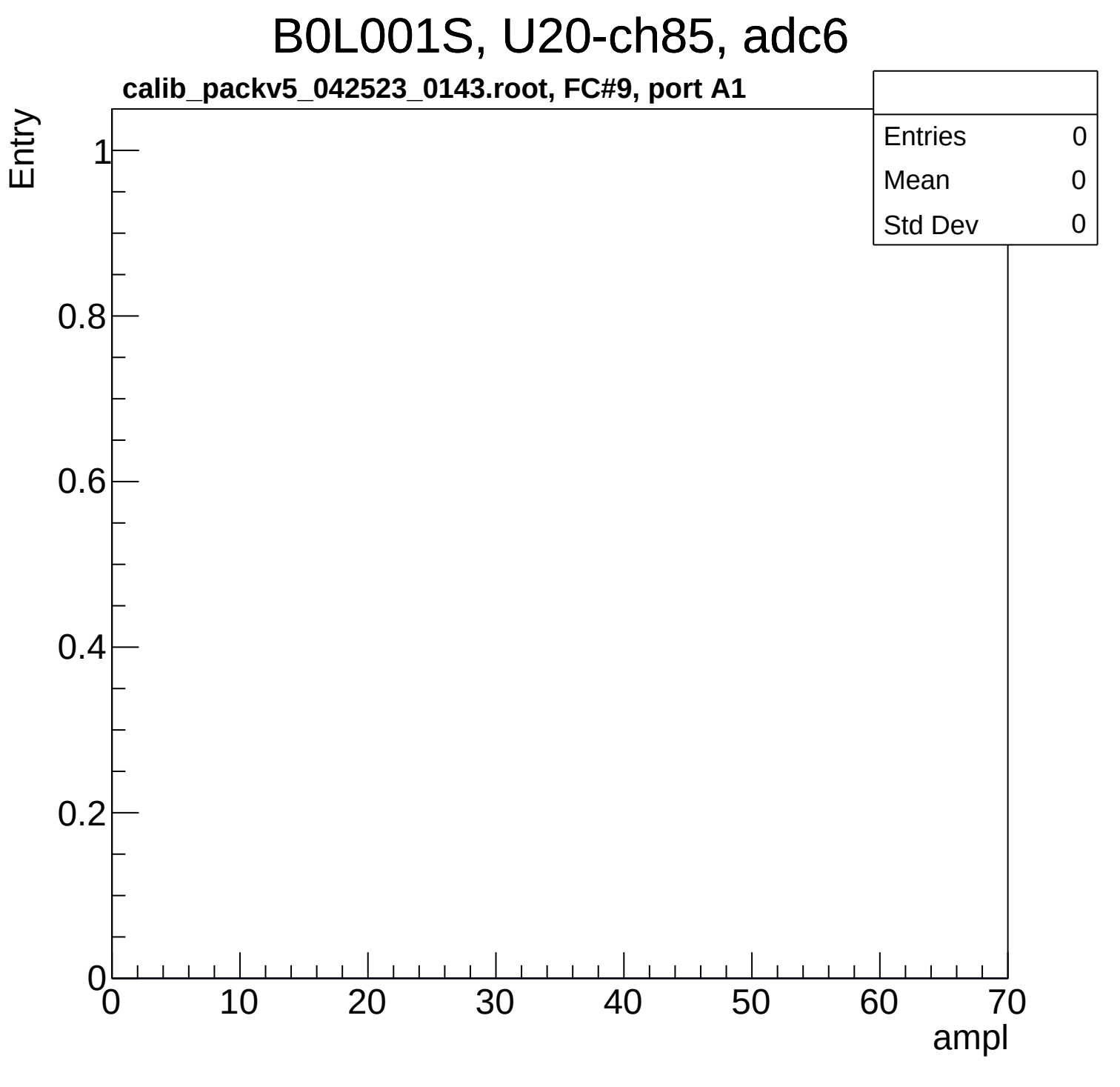
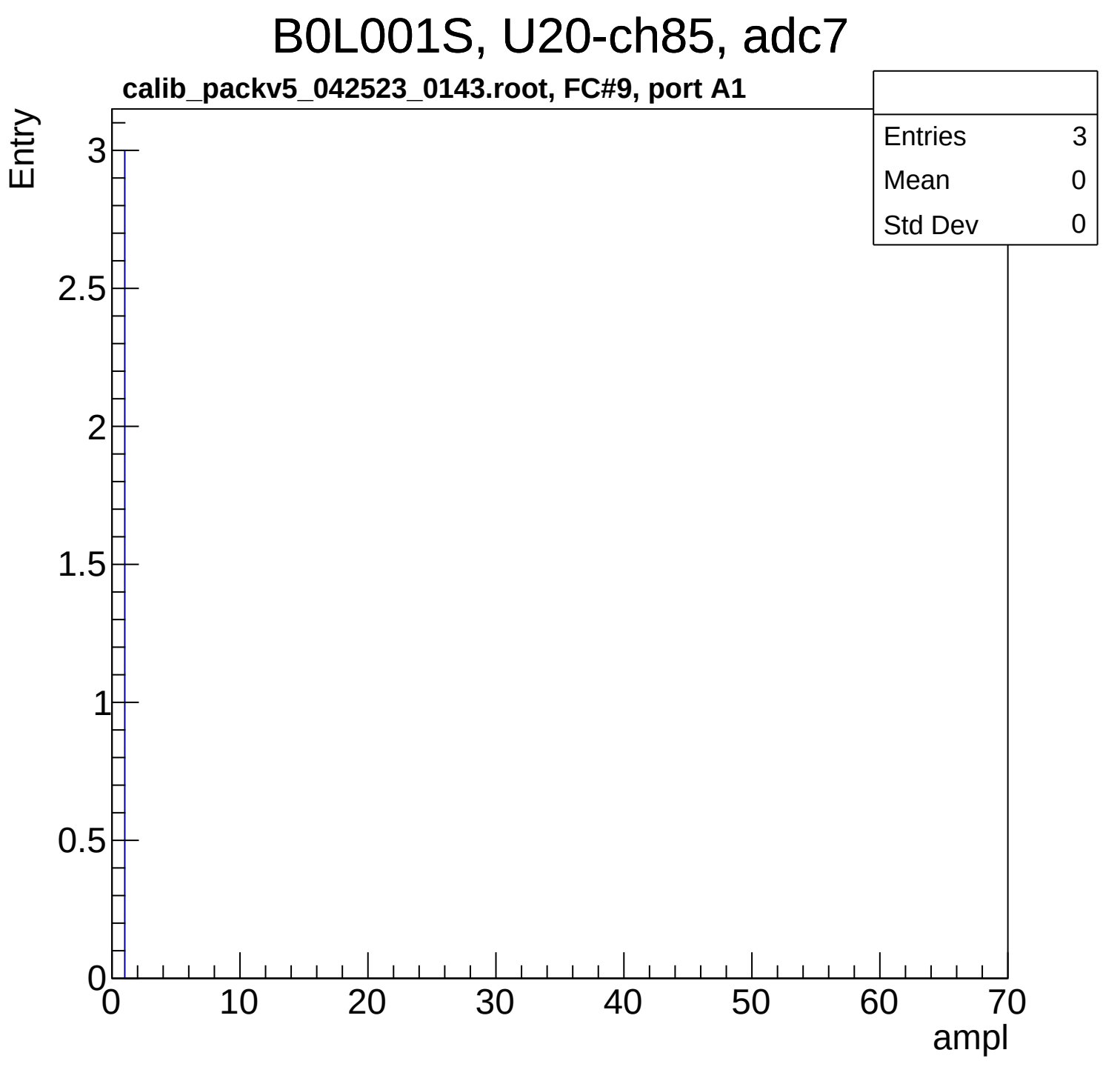
50
60
70
ampl


B0L001S, U20-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
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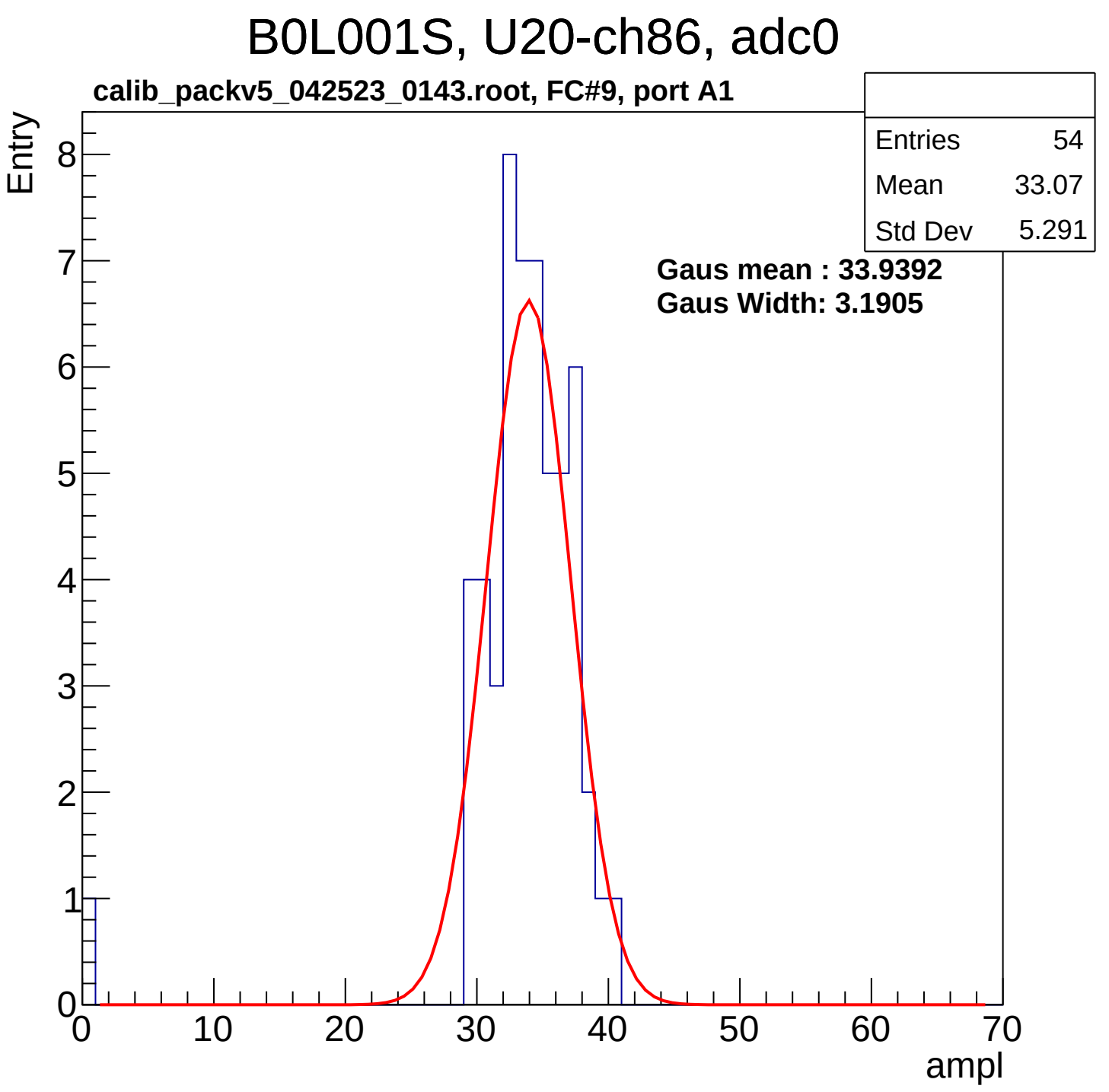
50
60
70
ampl


B0L001S, U20-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 33.07
Std Dev 5.291
Gaus mean : 33.9392
Gaus Width: 3.1905
Entry
ampl
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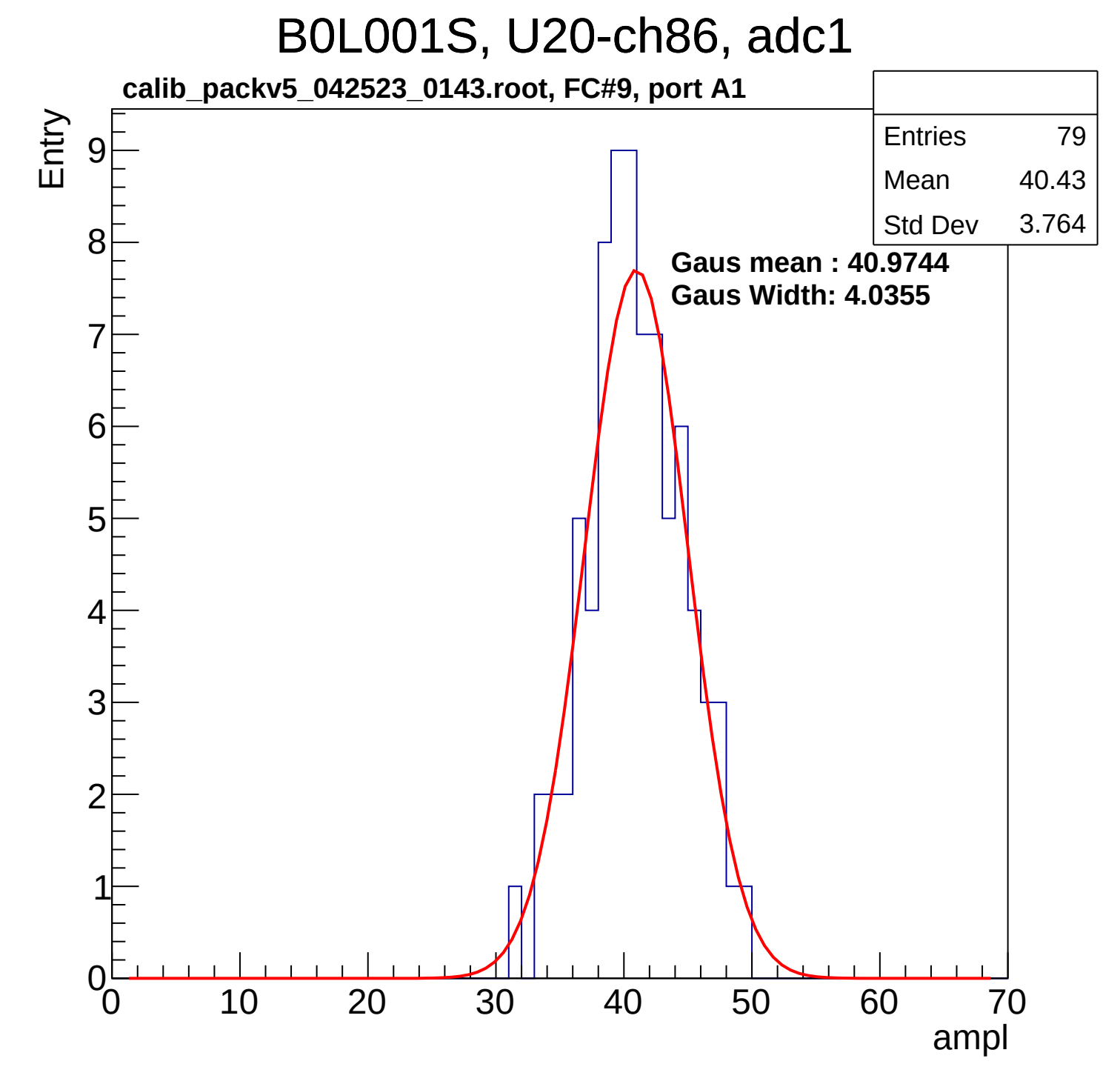
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 40.43
Std Dev 3.764
Gaus mean : 40.9744
Gaus Width: 4.0355
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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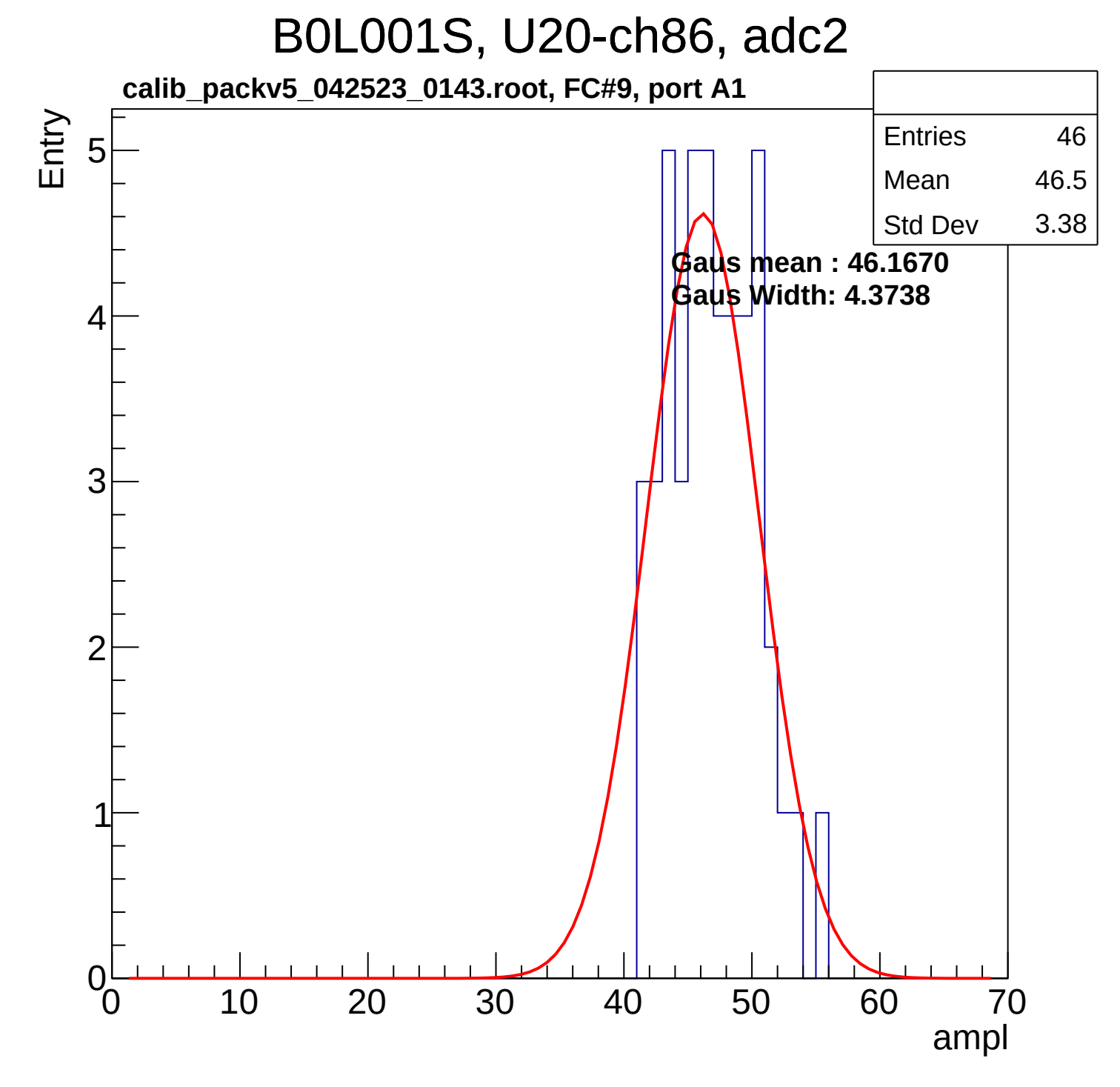
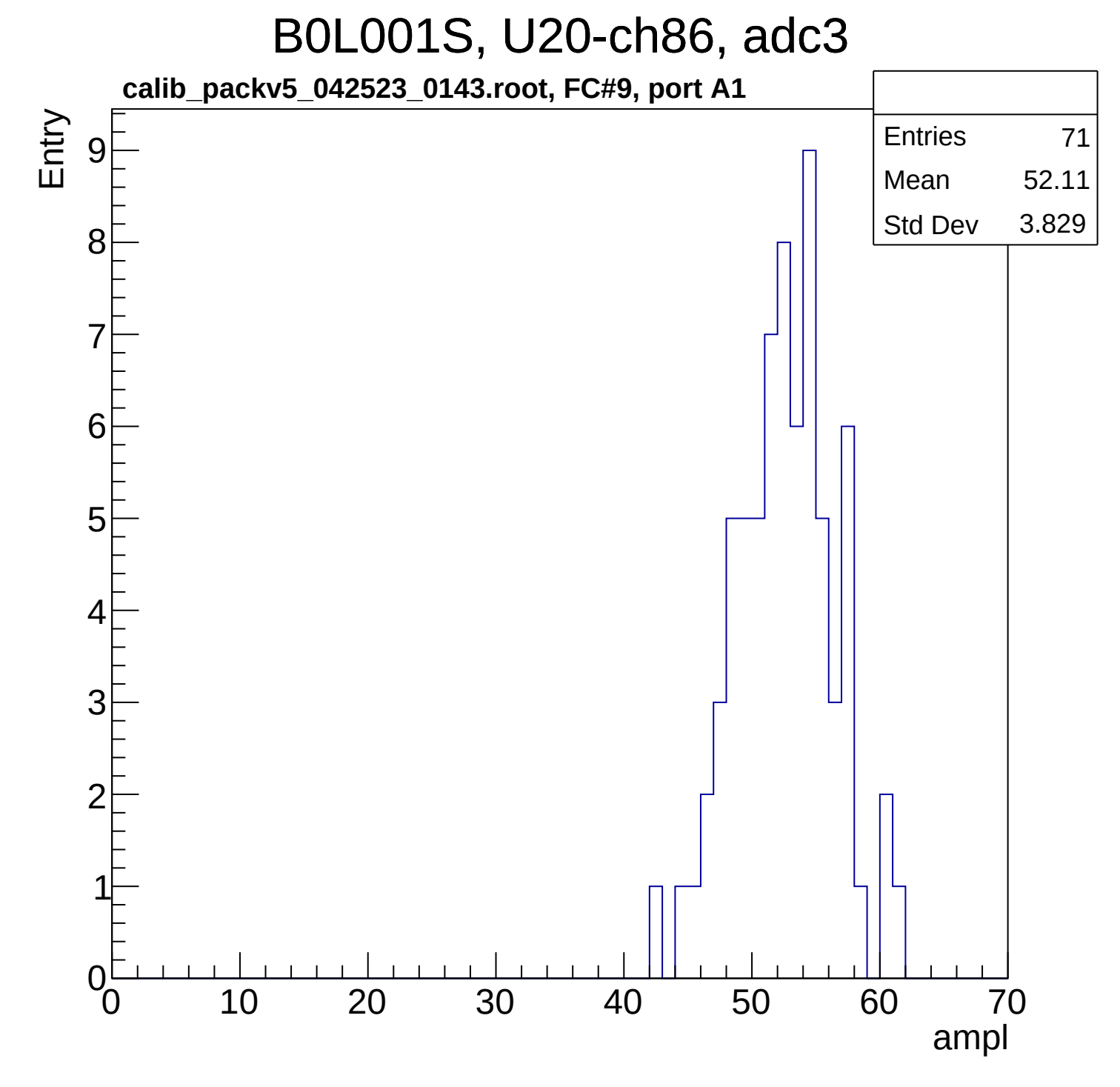

B0L001S, U20-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 46.5
Std Dev 3.38
Gaus mean : 46.1670
Gaus Width: 4.3738
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U20-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 52.11
Std Dev 3.829
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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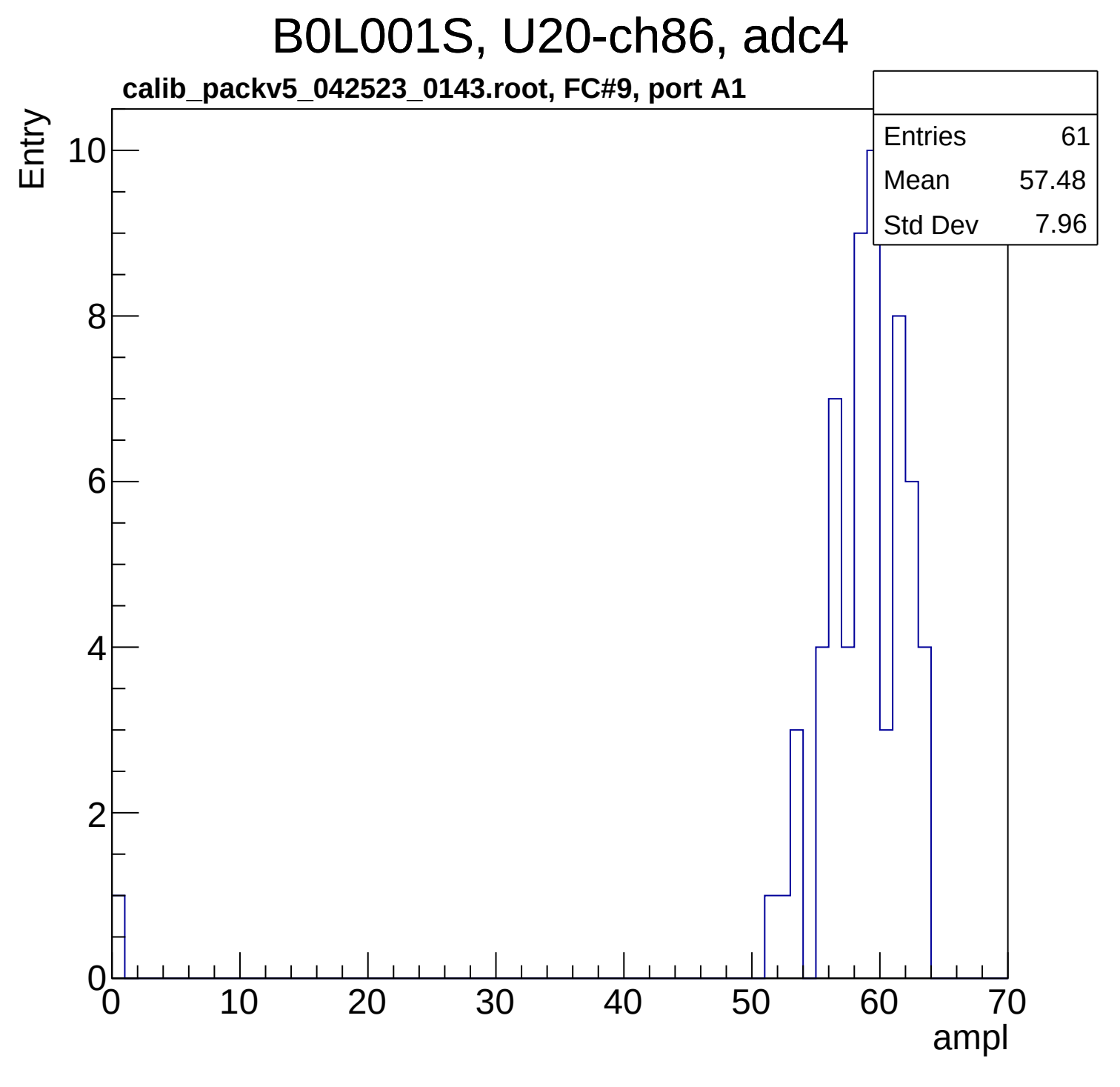
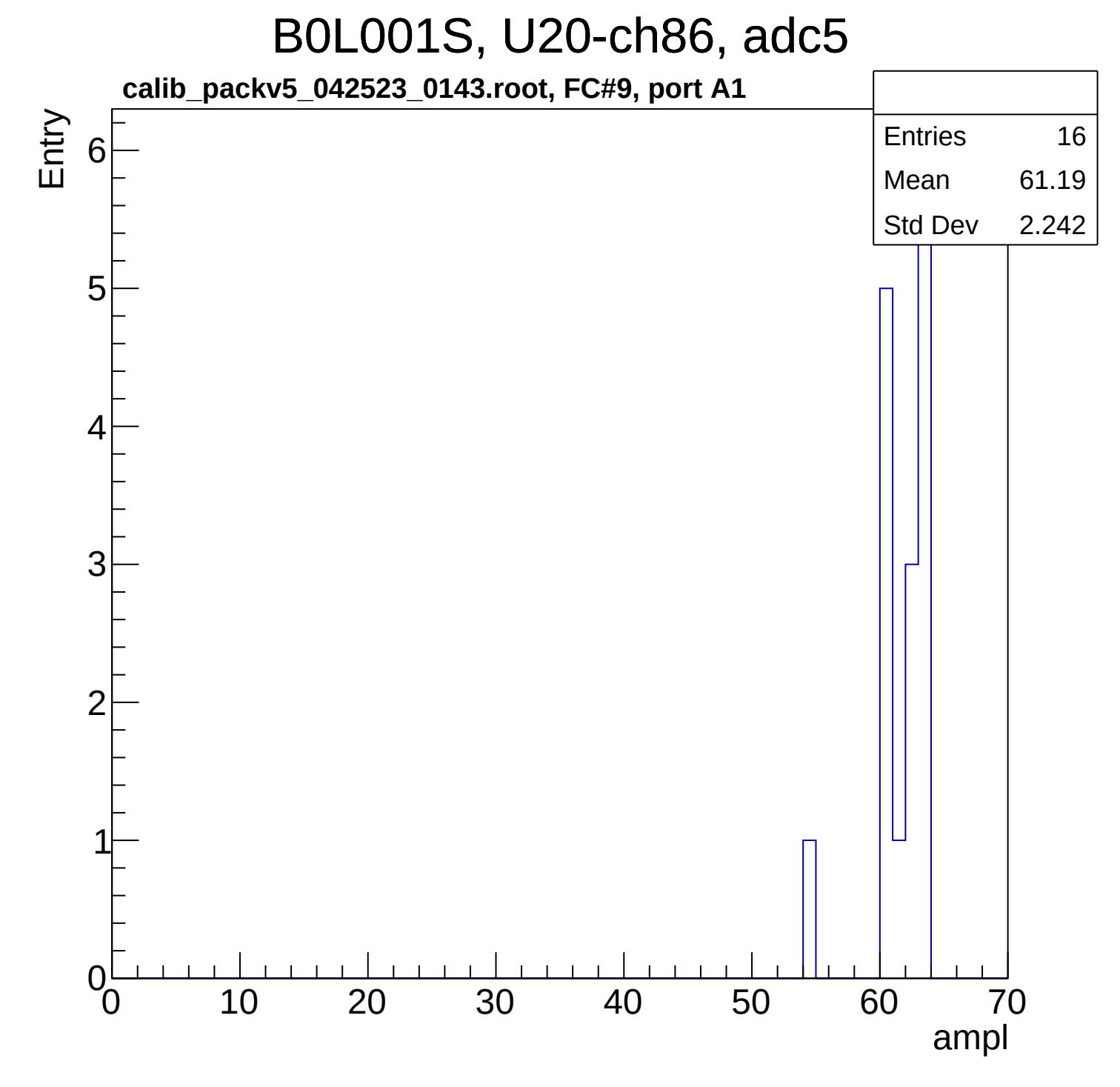

B0L001S, U20-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.48
Std Dev 7.96
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 61.19
Std Dev 2.242
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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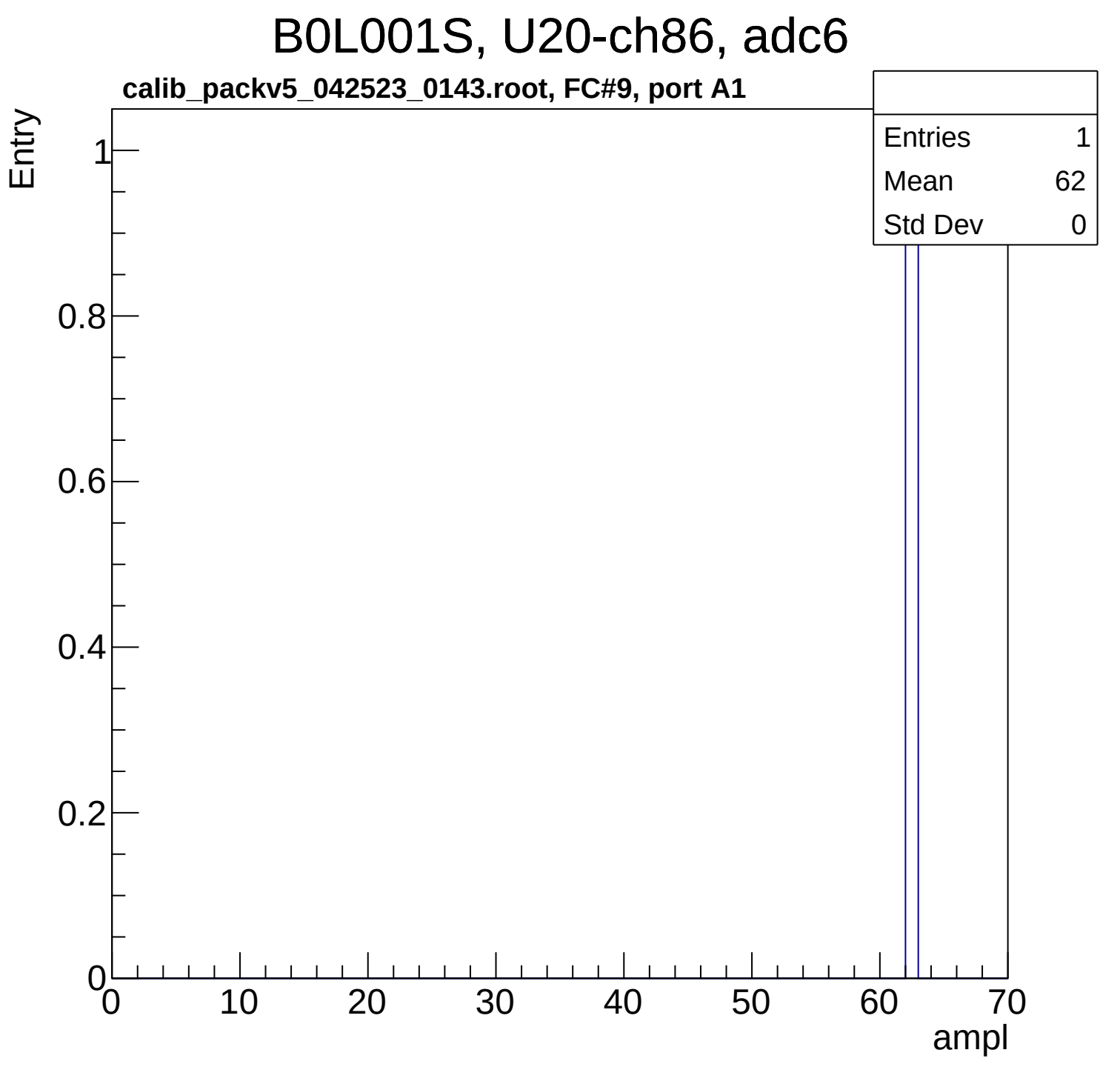
B0L001S, U20-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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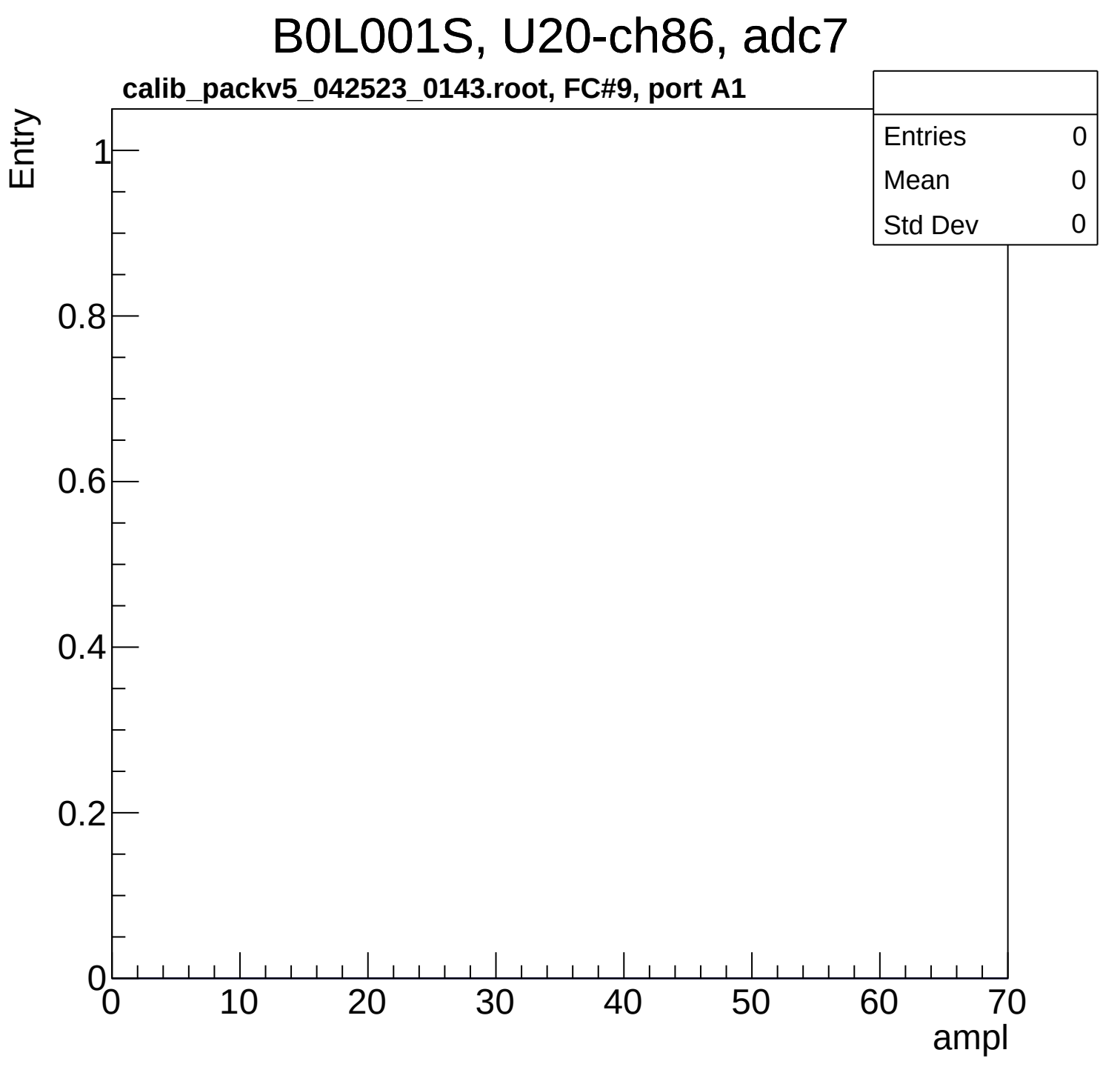
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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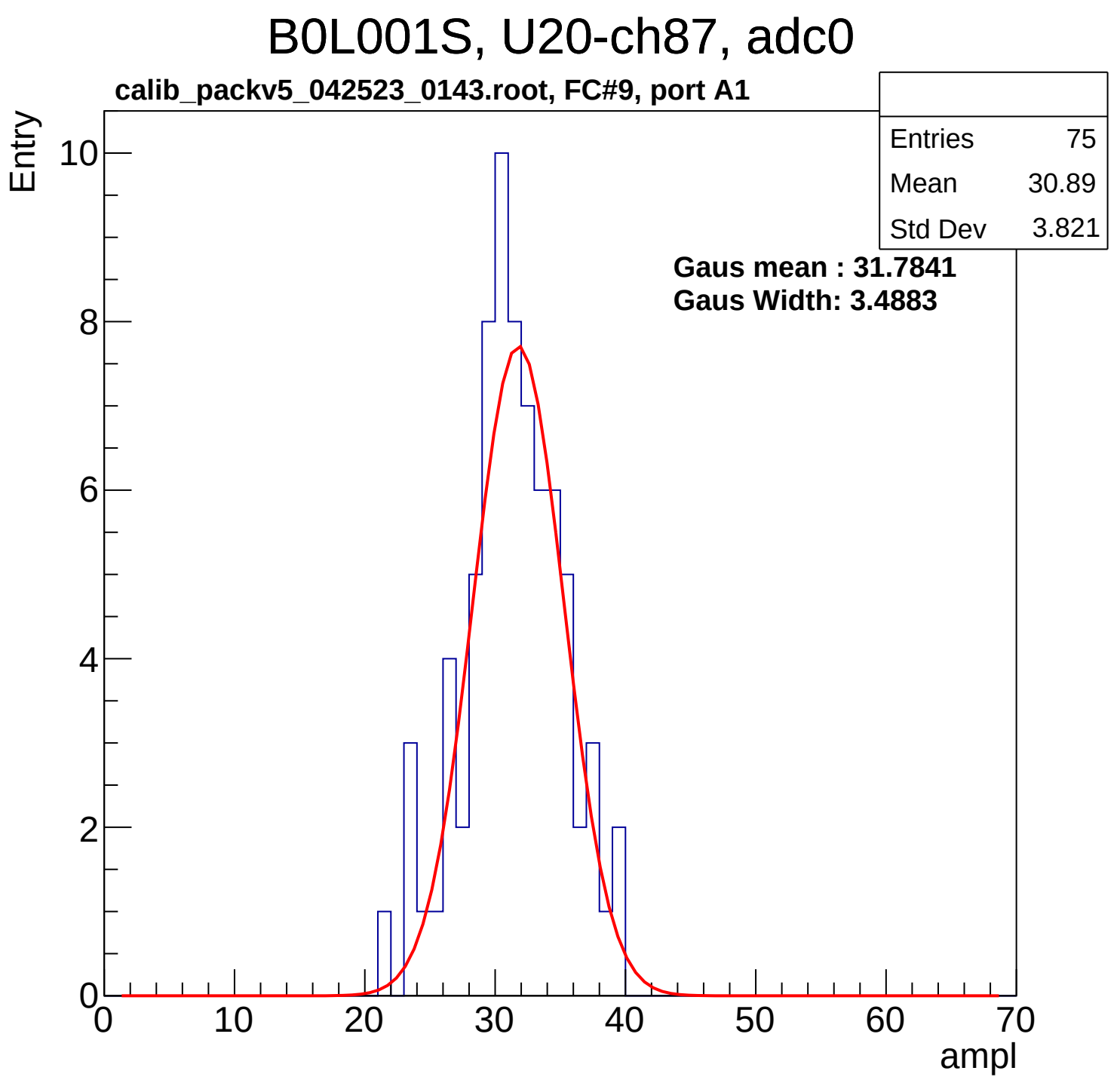
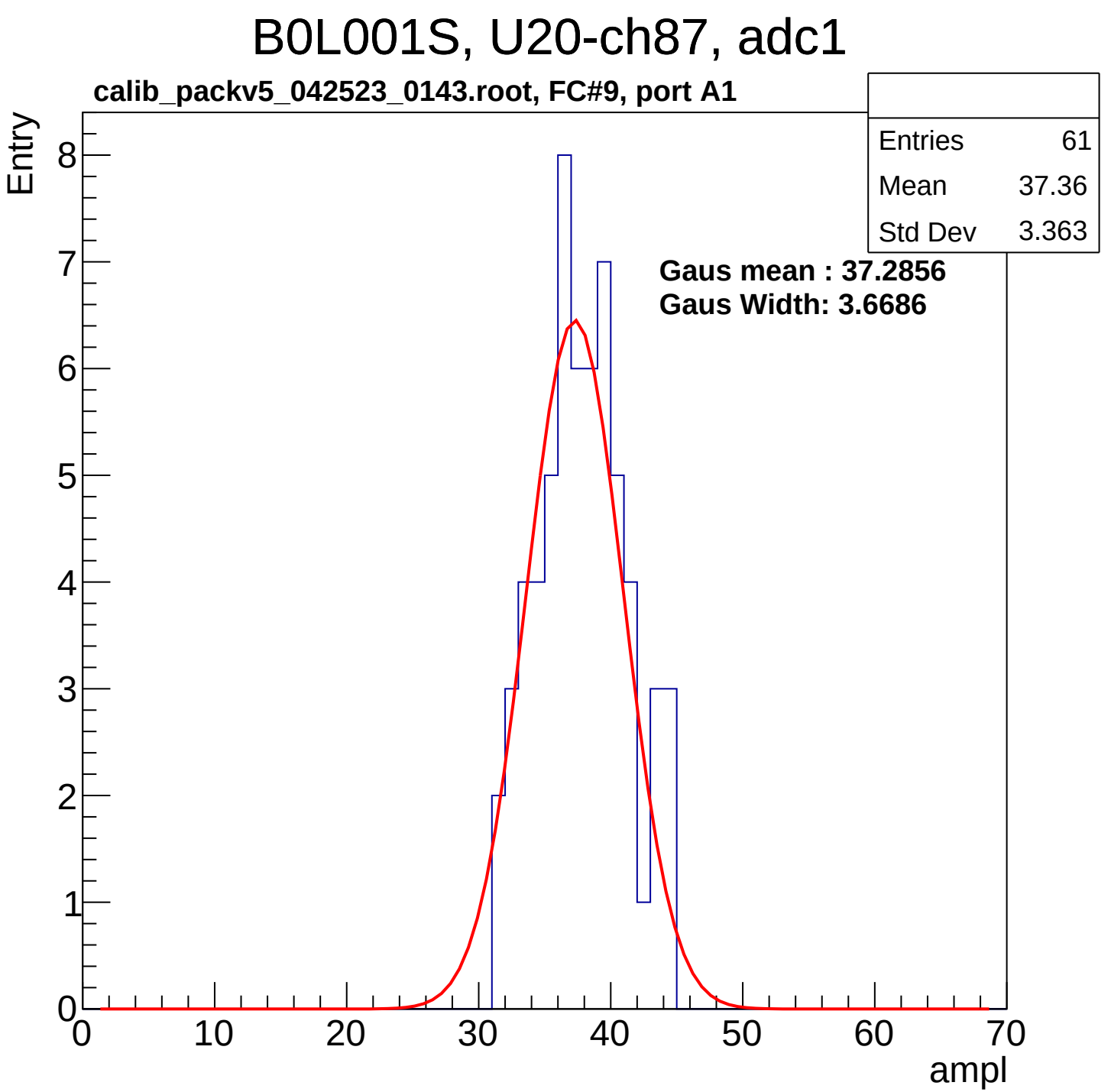
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.89
Std Dev 3.821
Gaus mean : 31.7841
Gaus Width: 3.4883
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.36
Std Dev 3.363
Gaus mean : 37.2856
Gaus Width: 3.6686
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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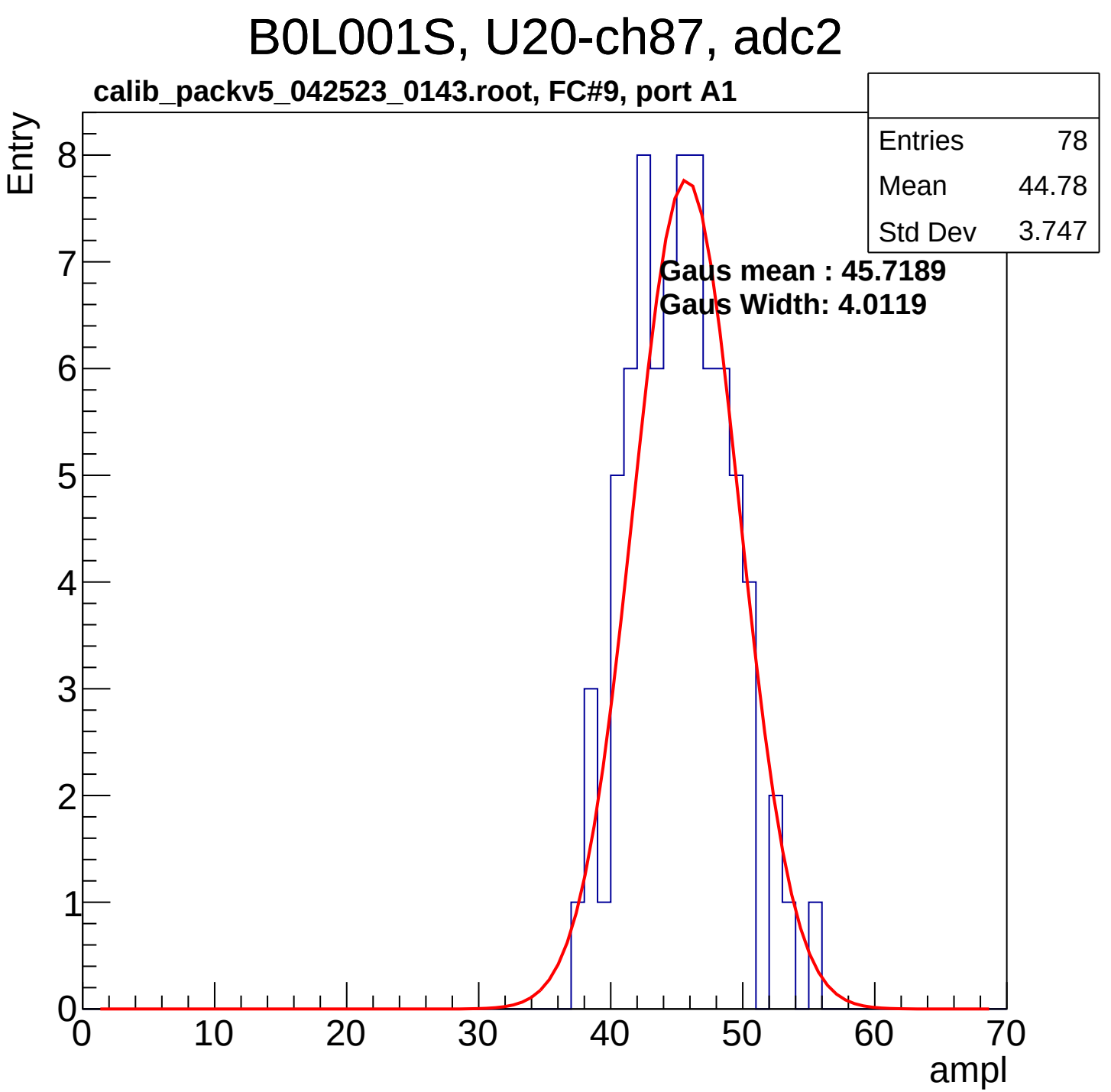
B0L001S, U20-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.78
Std Dev 3.747
Gaus mean : 45.7189
Gaus Width: 4.0119
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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60
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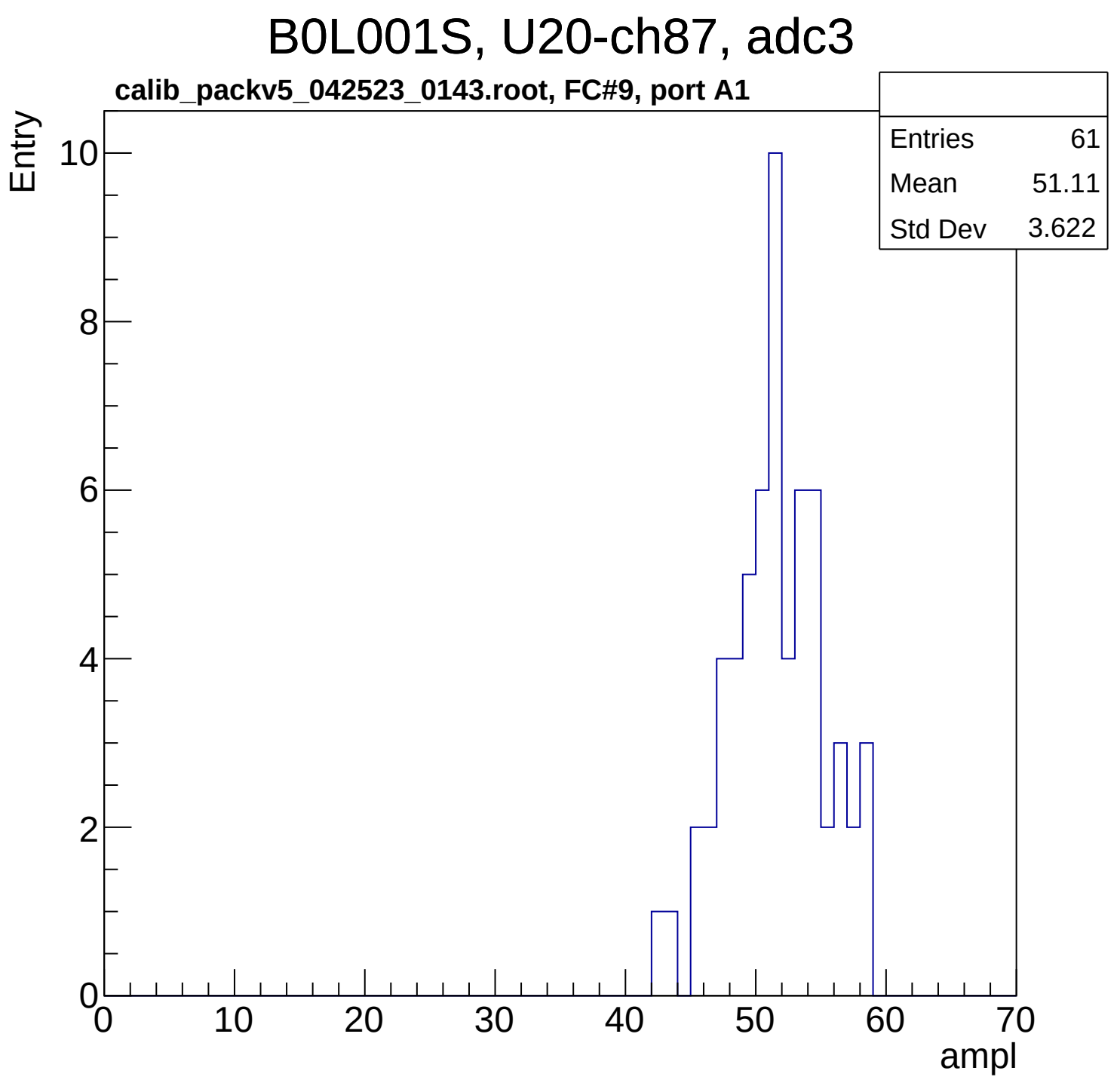
B0L001S, U20-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.11
Std Dev 3.622
Entry
0
2
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6
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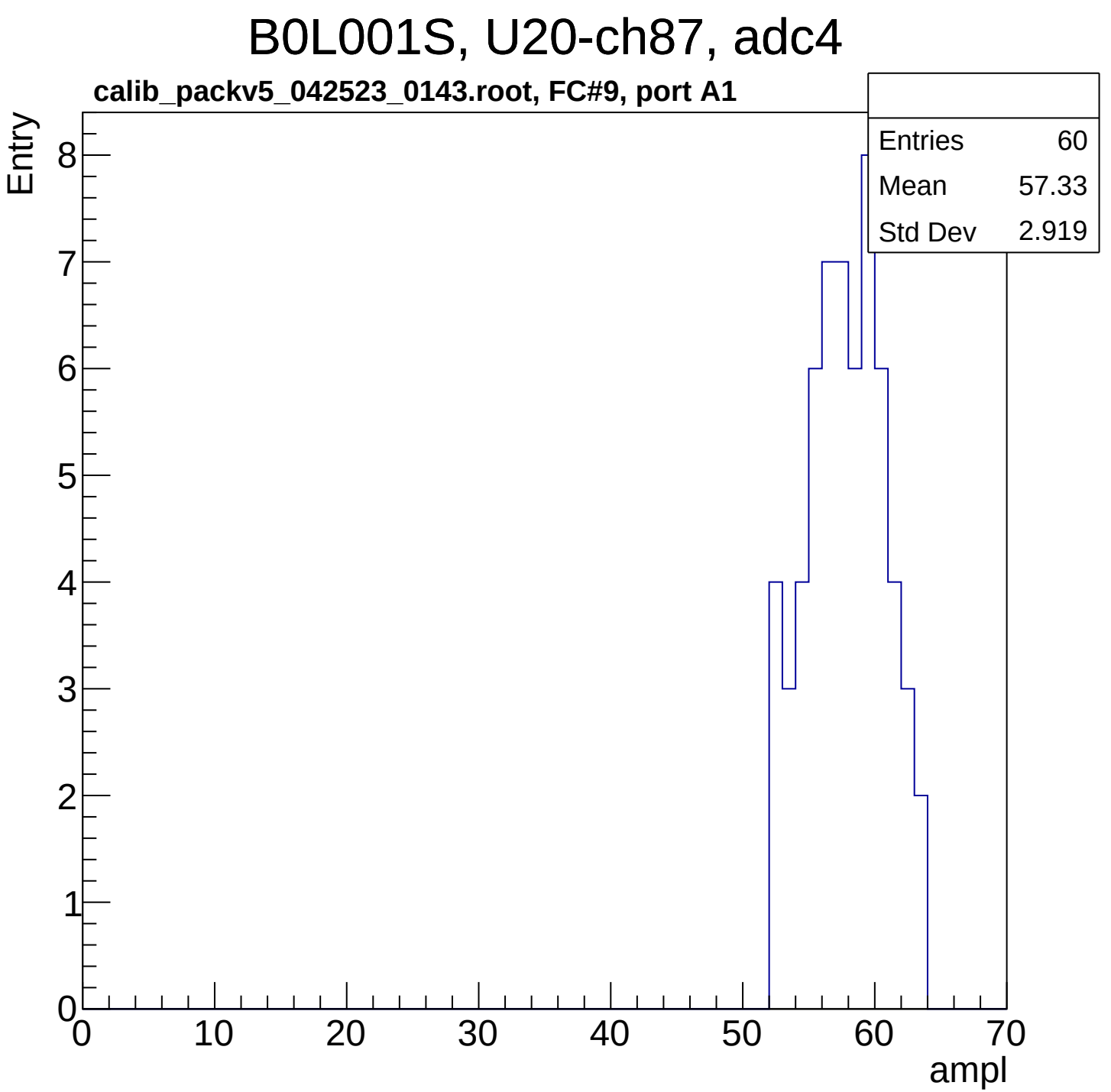
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.33
Std Dev 2.919
Entry
ampl
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1
2
3
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5
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0
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20
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60
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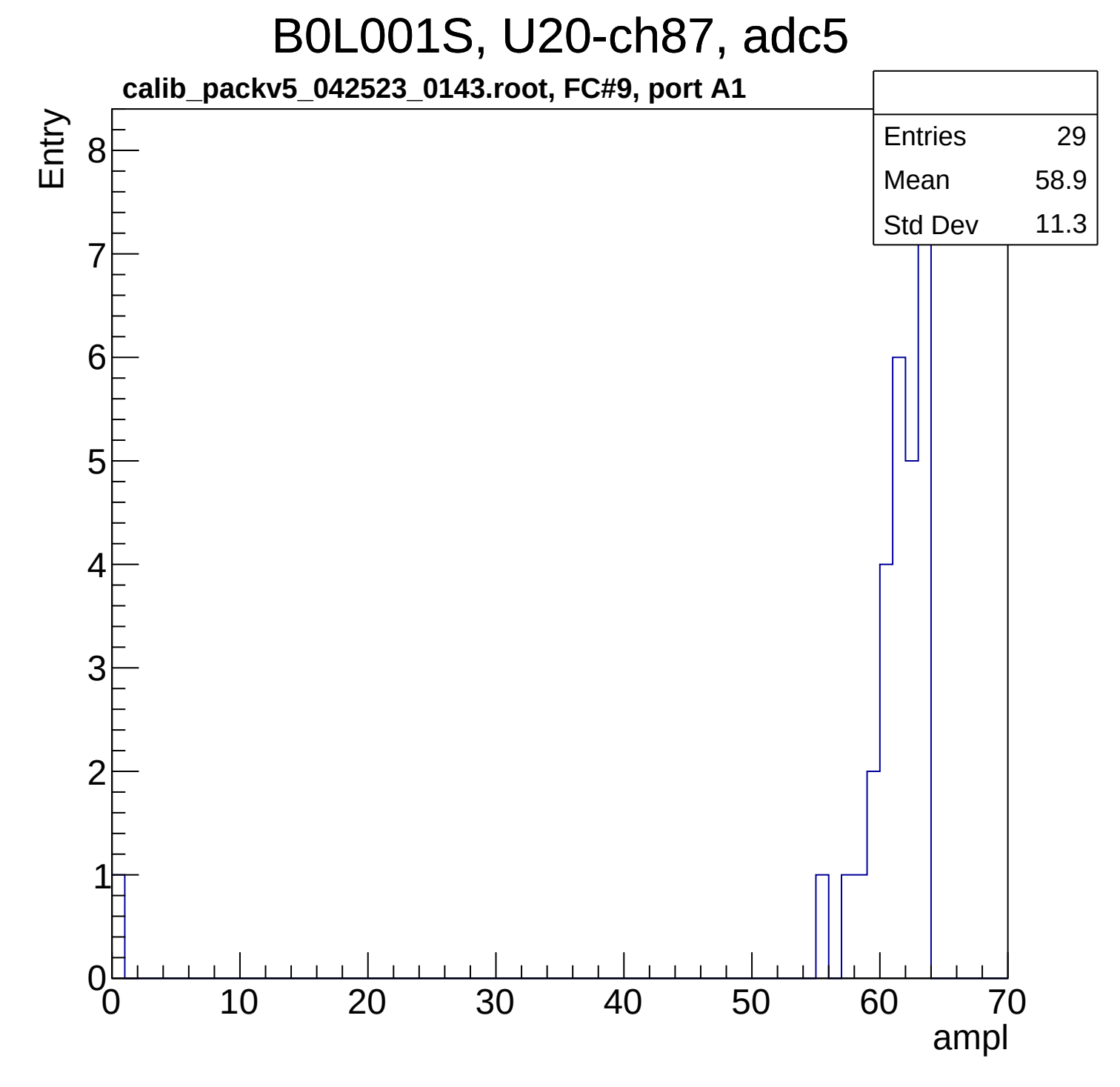
B0L001S, U20-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.9
Std Dev 11.3
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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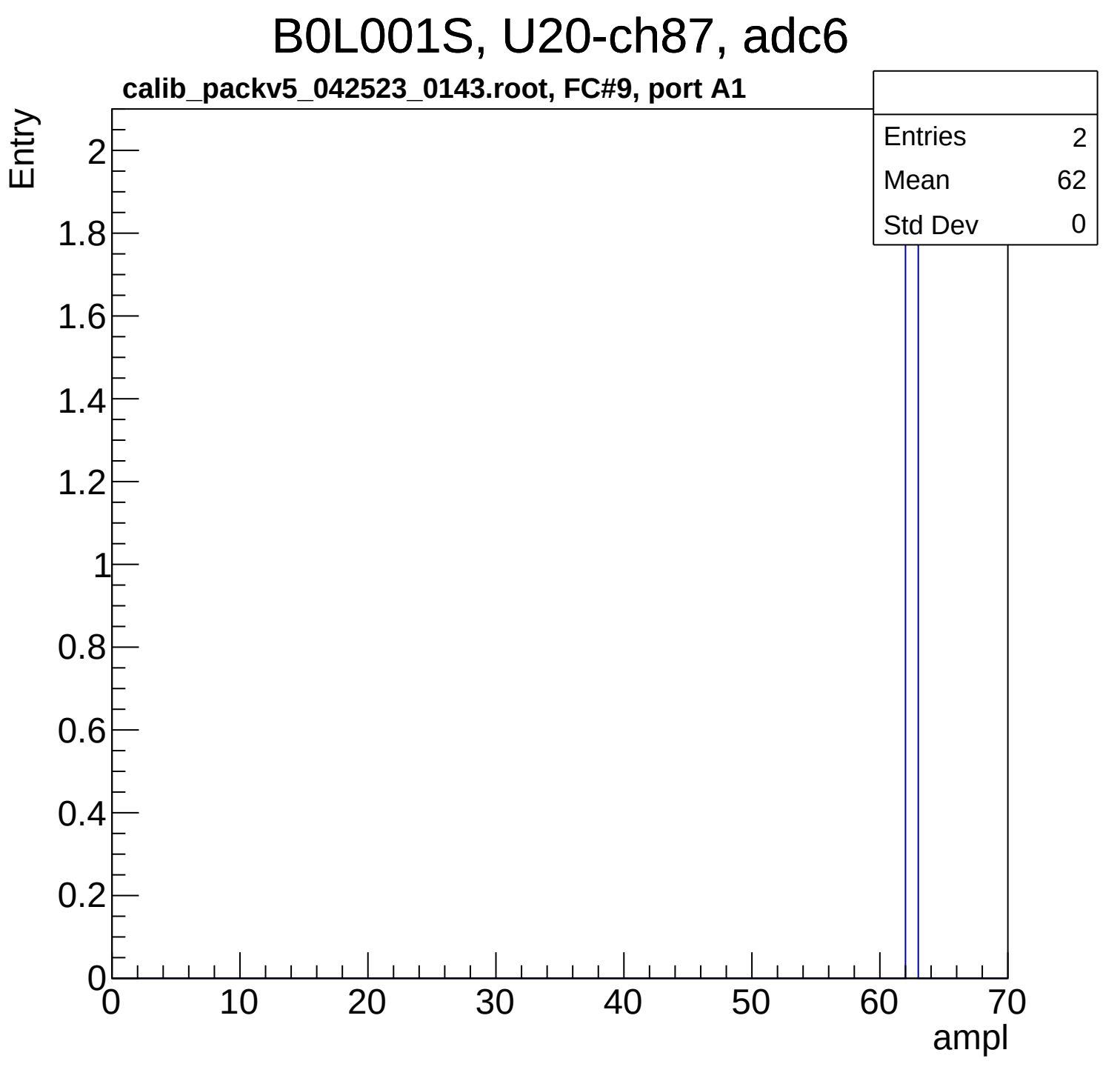
B0L001S, U20-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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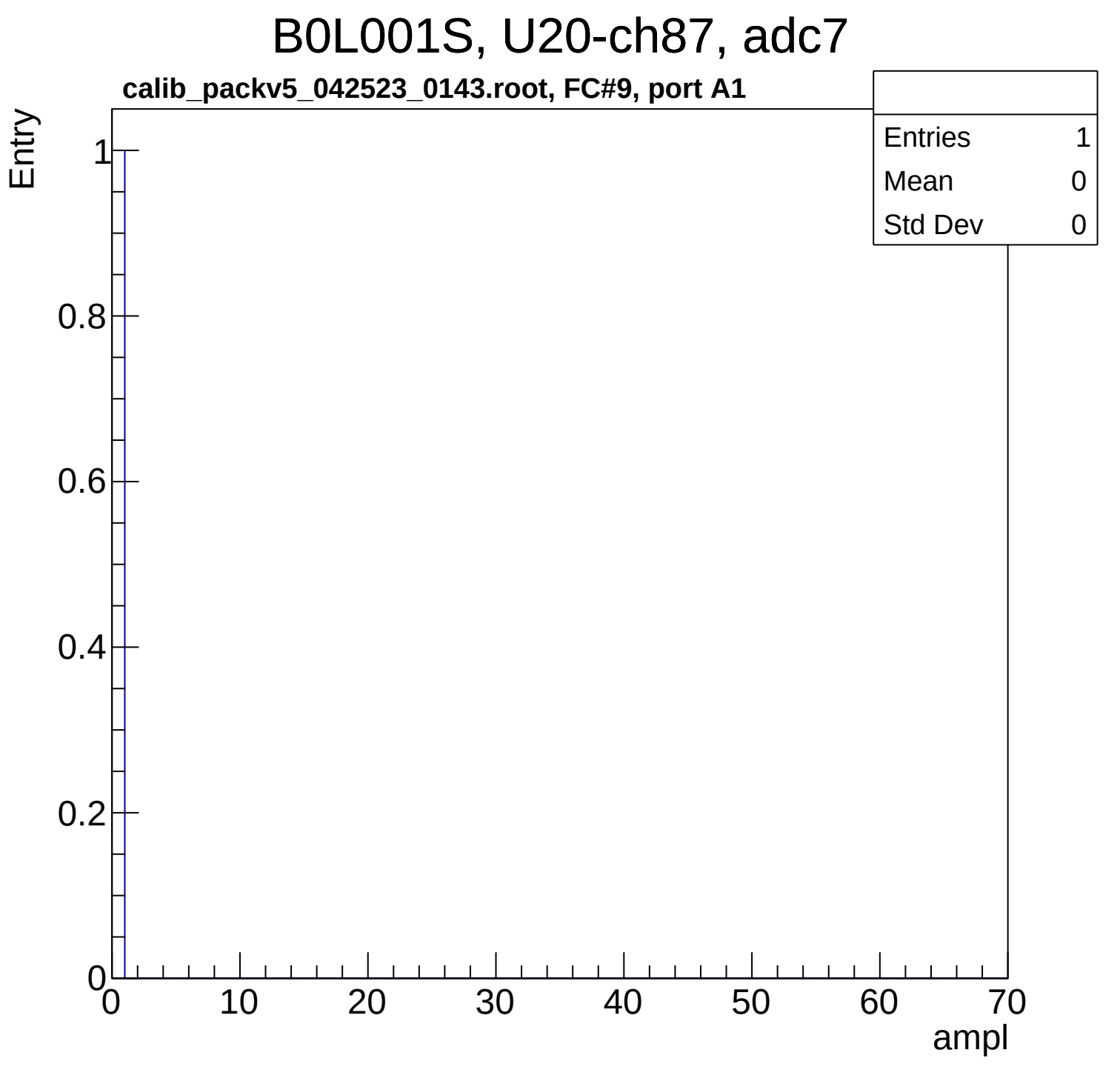
B0L001S, U20-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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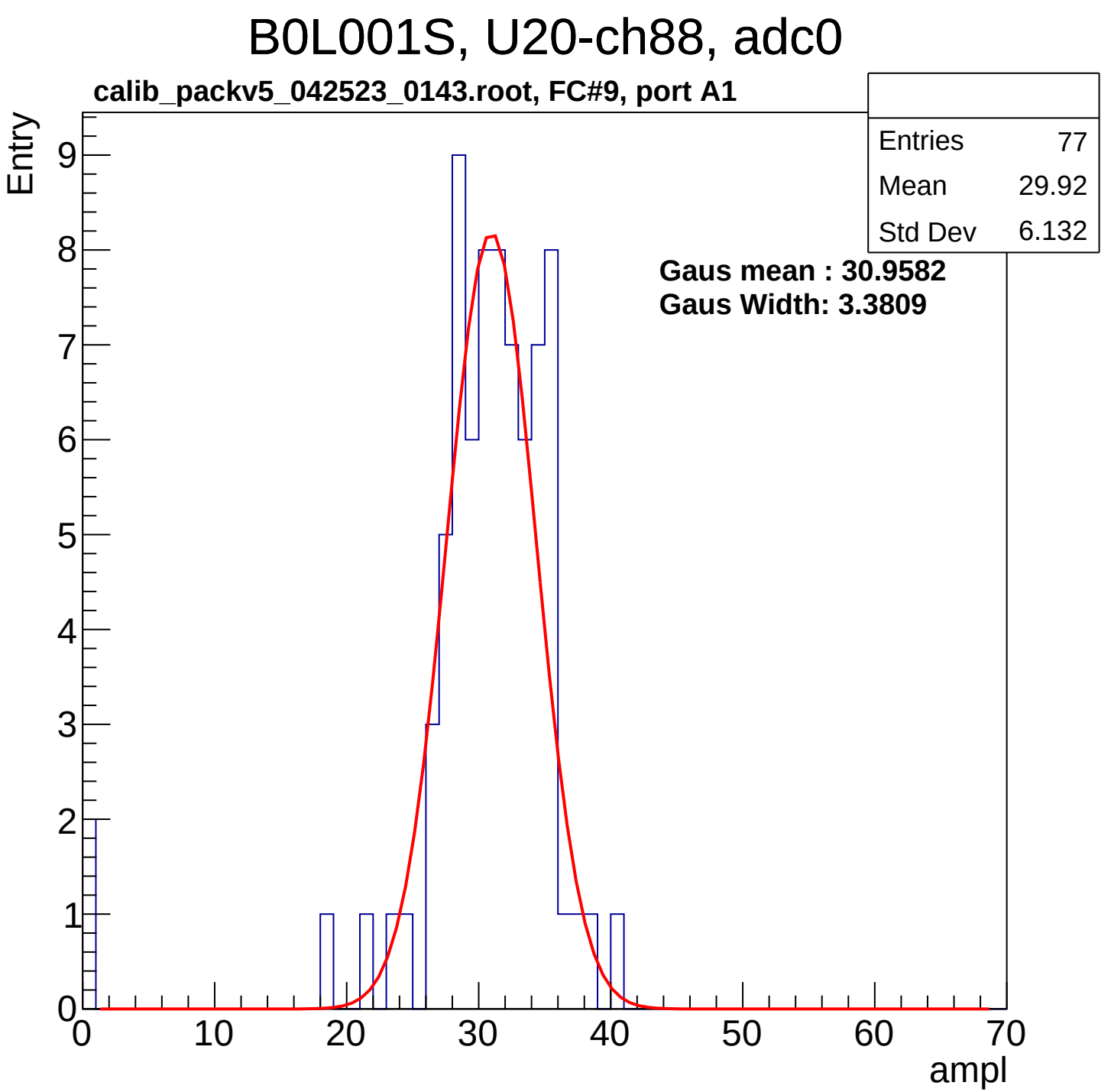
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.92
Std Dev 6.132
Gaus mean : 30.9582
Gaus Width: 3.3809
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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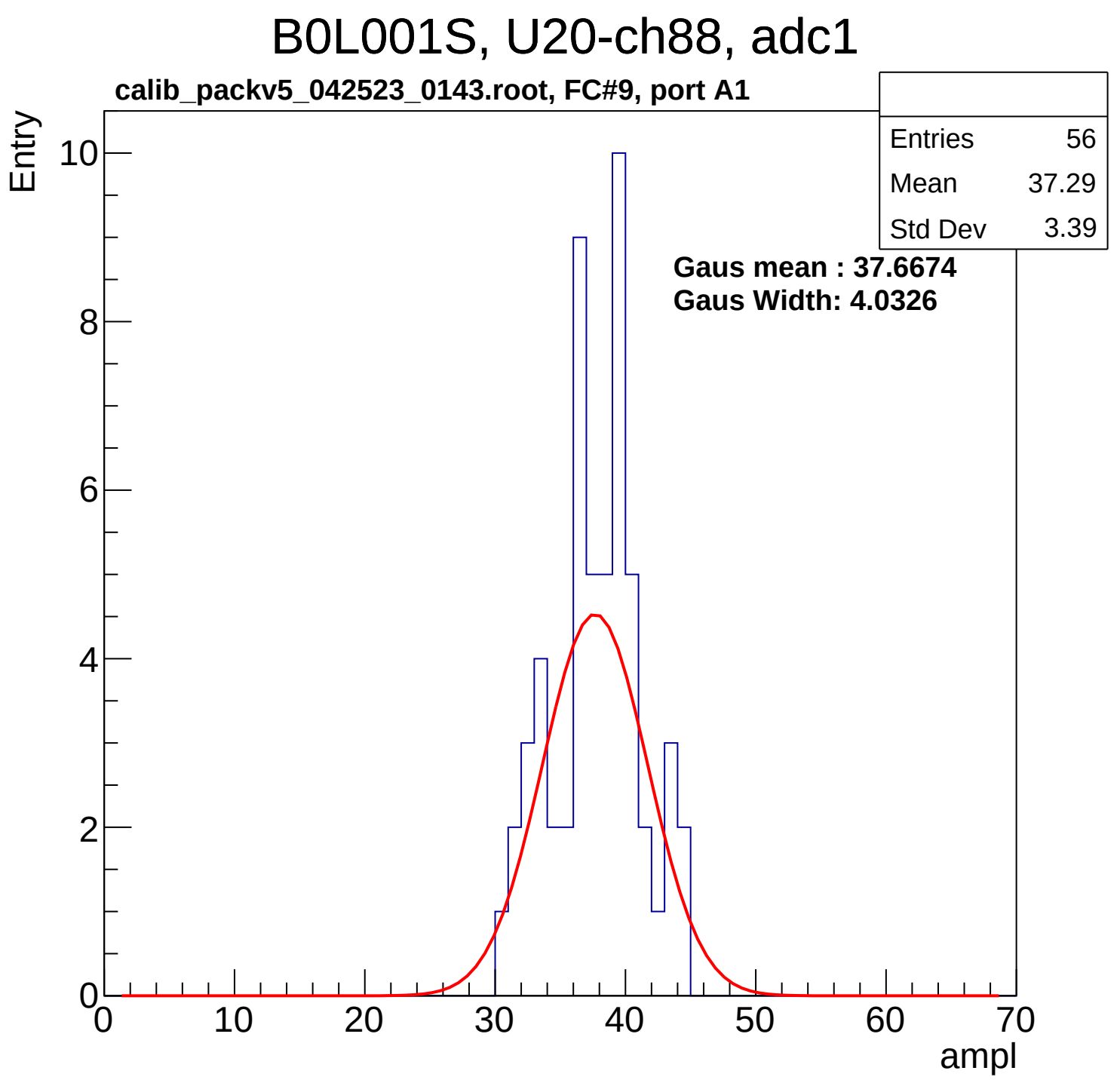
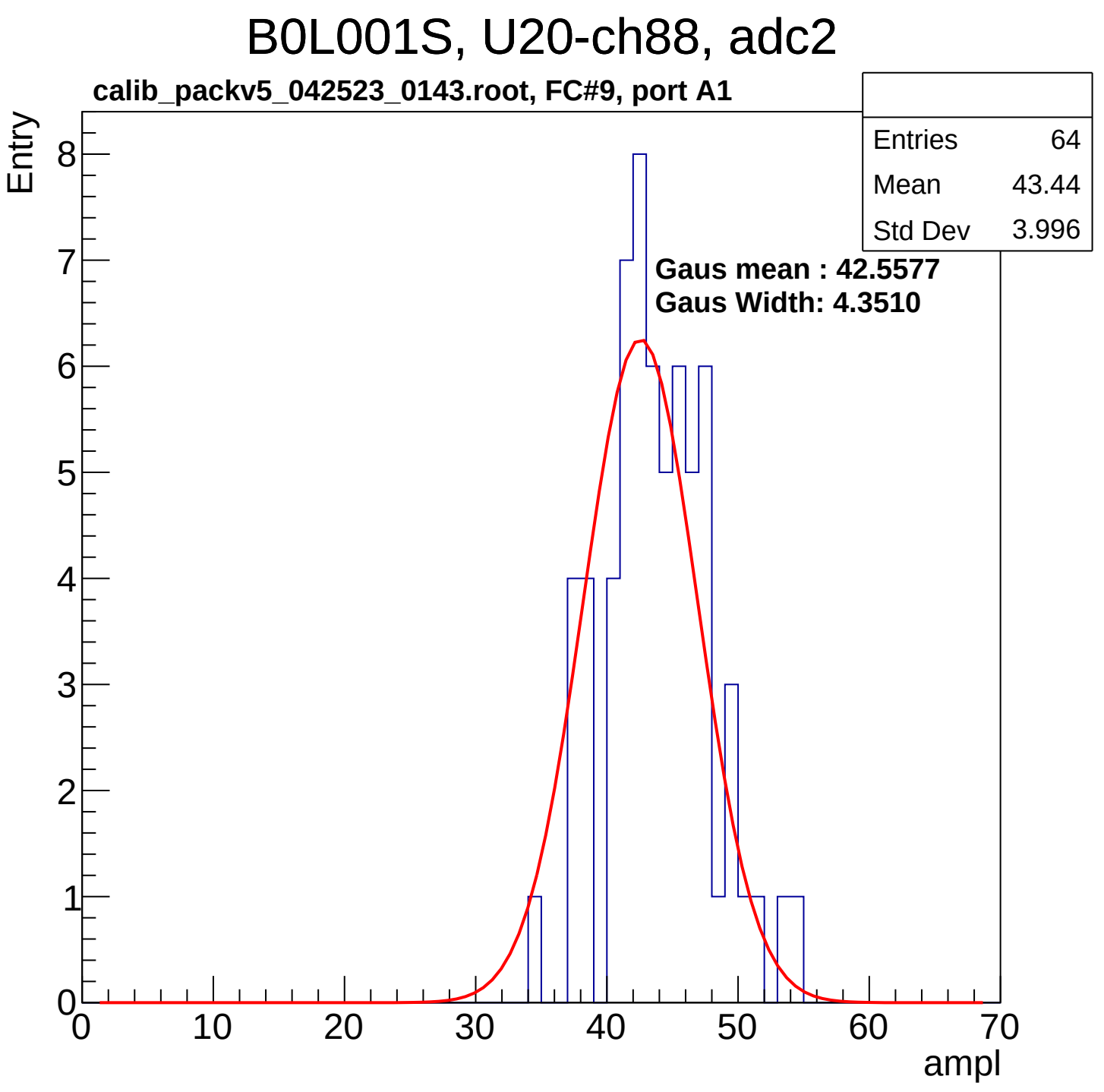

B0L001S, U20-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 37.29
Std Dev 3.39
Gaus mean : 37.6674
Gaus Width: 4.0326
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.44
Std Dev 3.996
Gaus mean : 42.5577
Gaus Width: 4.3510
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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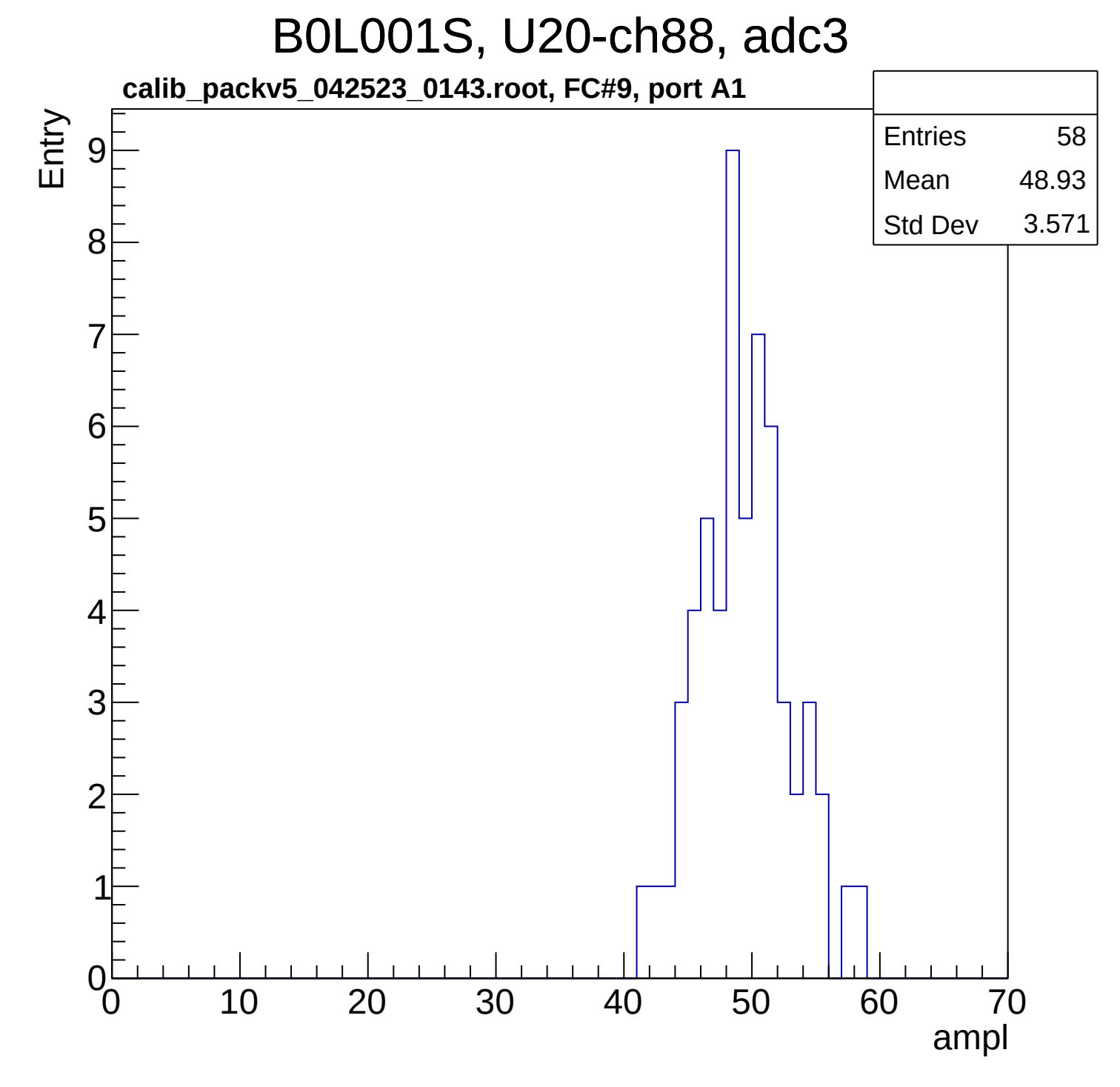
B0L001S, U20-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 48.93
Std Dev 3.571
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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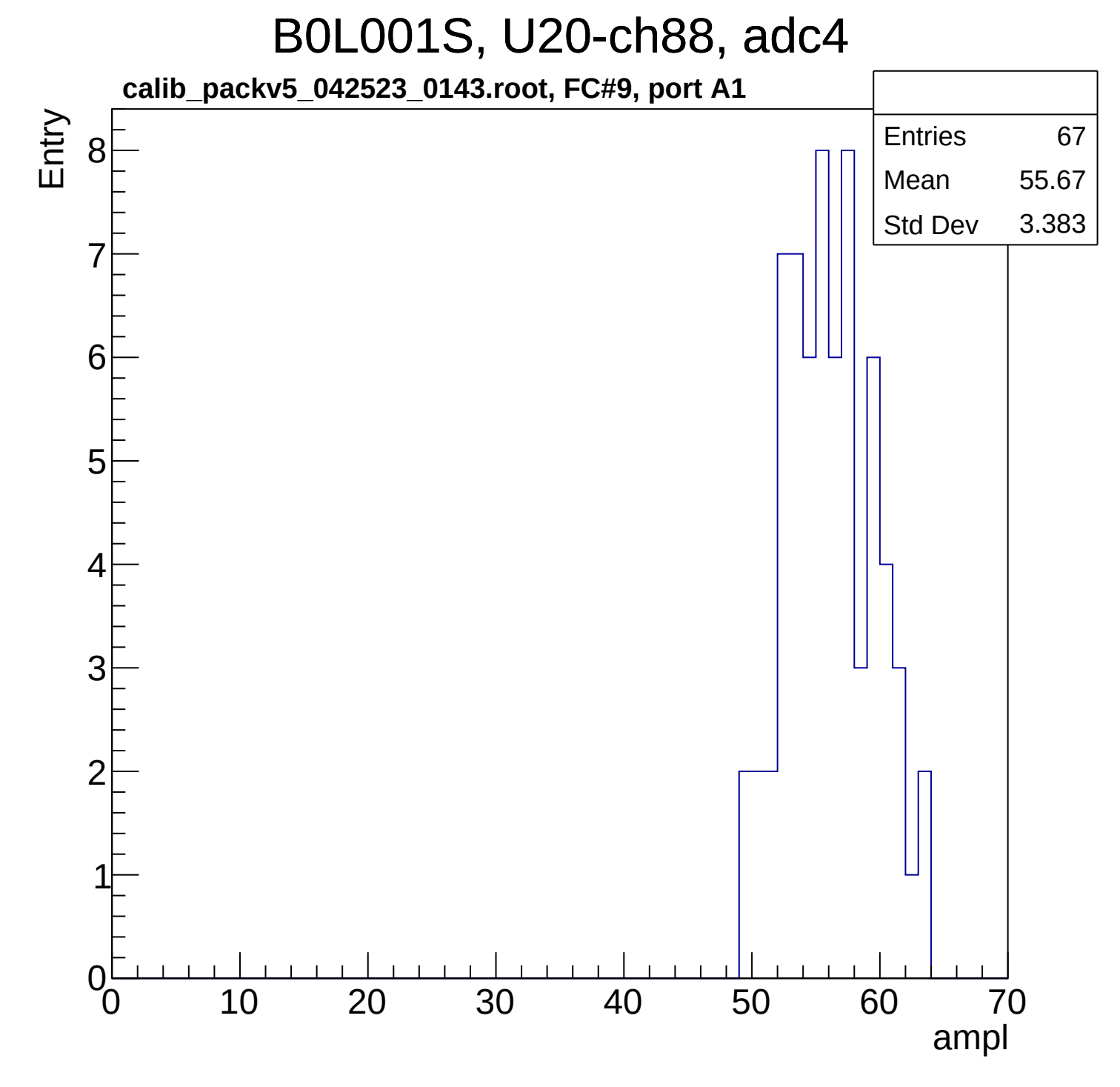
B0L001S, U20-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.67
Std Dev 3.383
Entry
ampl
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2
3
4
5
6
7
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40
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60
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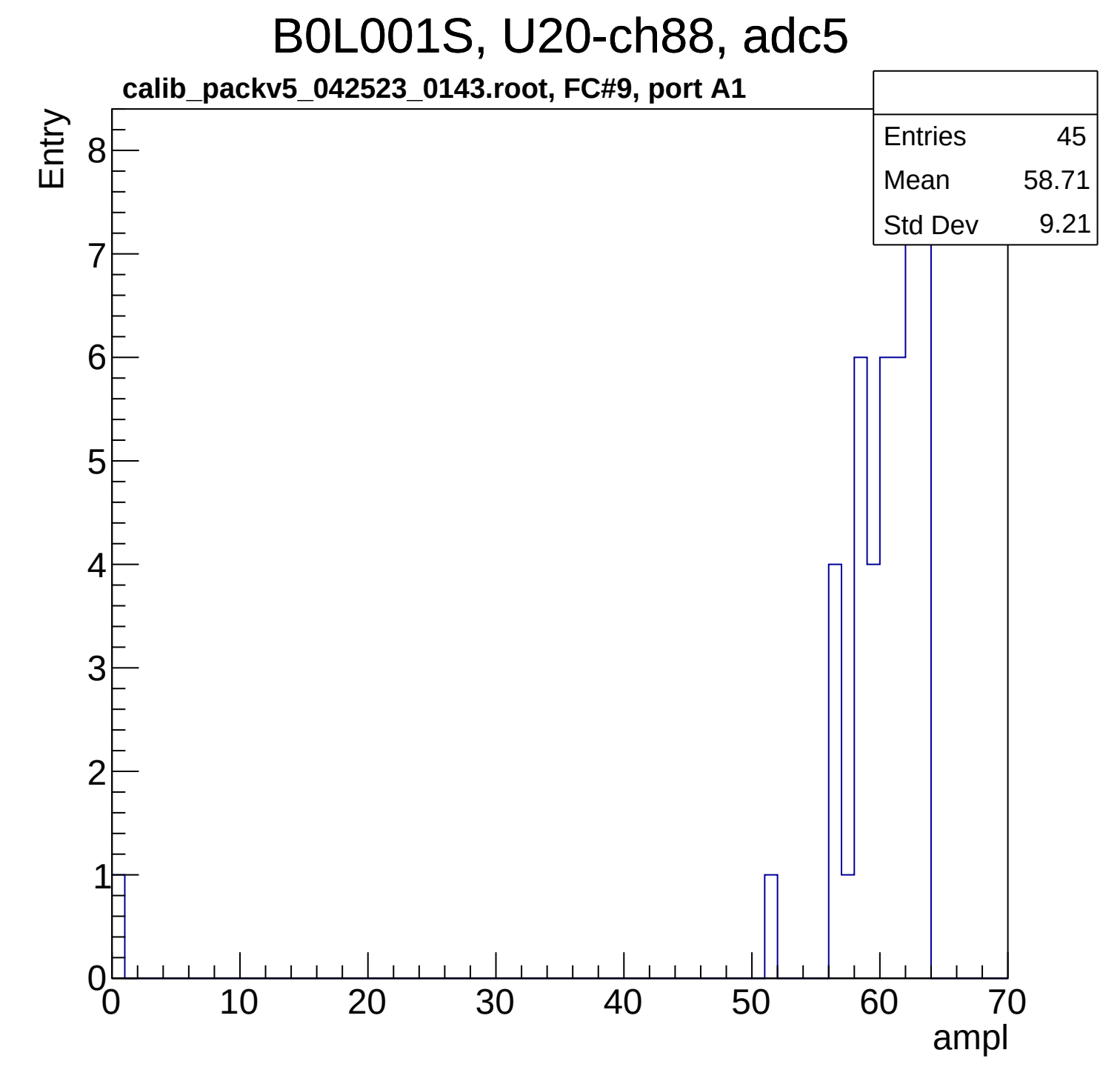
B0L001S, U20-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.71
Std Dev 9.21
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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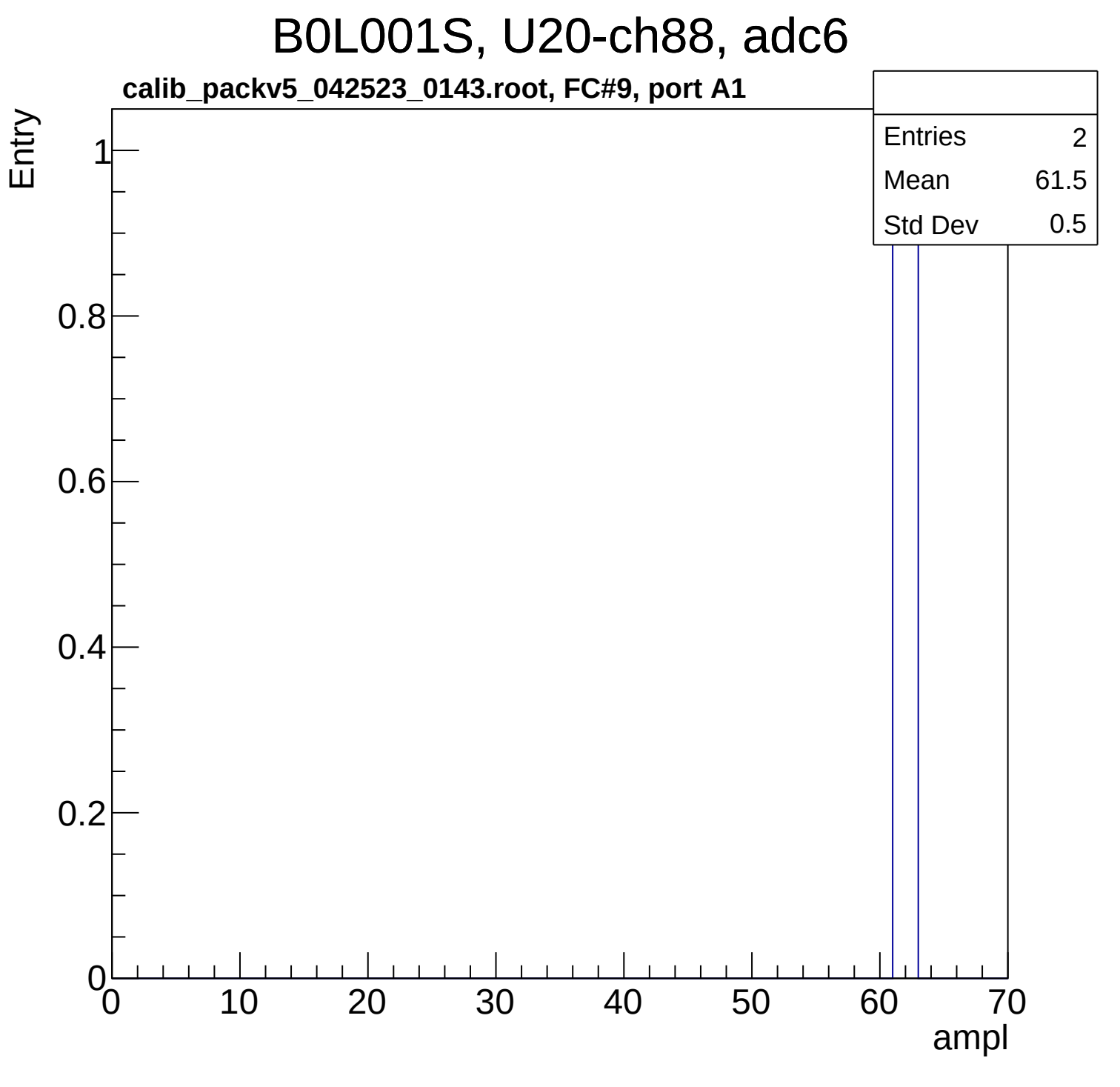
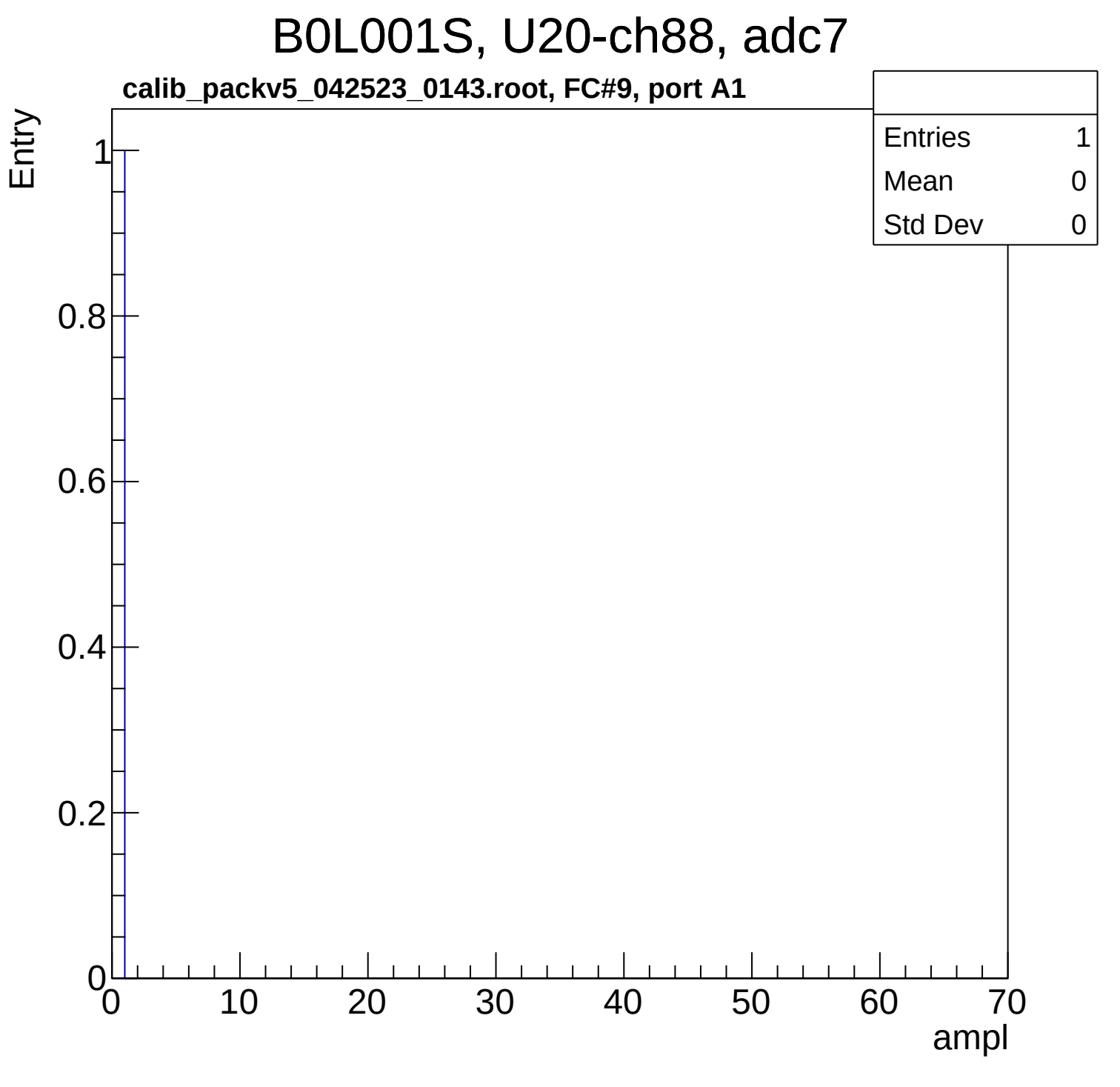

B0L001S, U20-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
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0
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50
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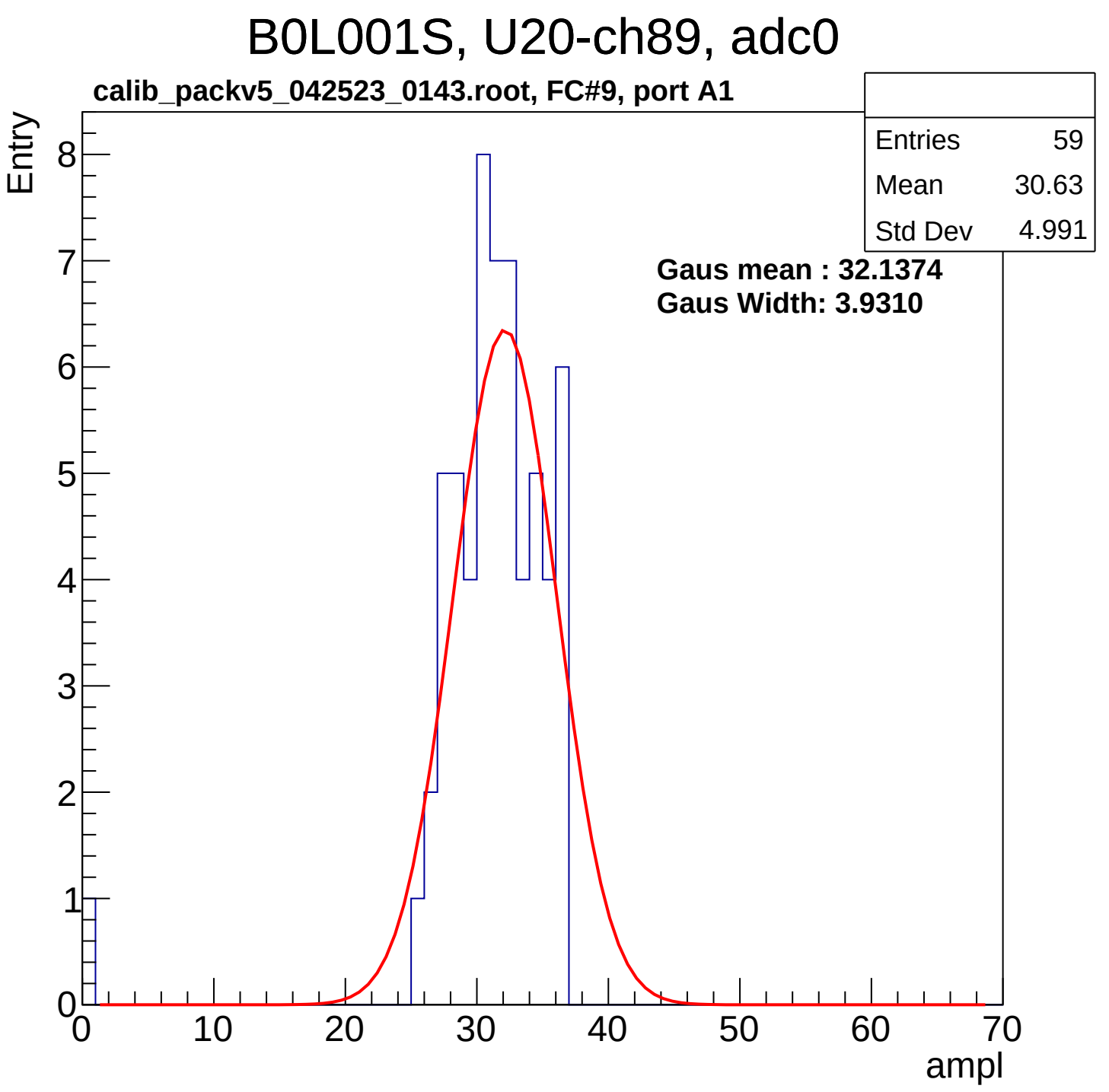
70
ampl


B0L001S, U20-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.63
Std Dev 4.991
Gaus mean : 32.1374
Gaus Width: 3.9310
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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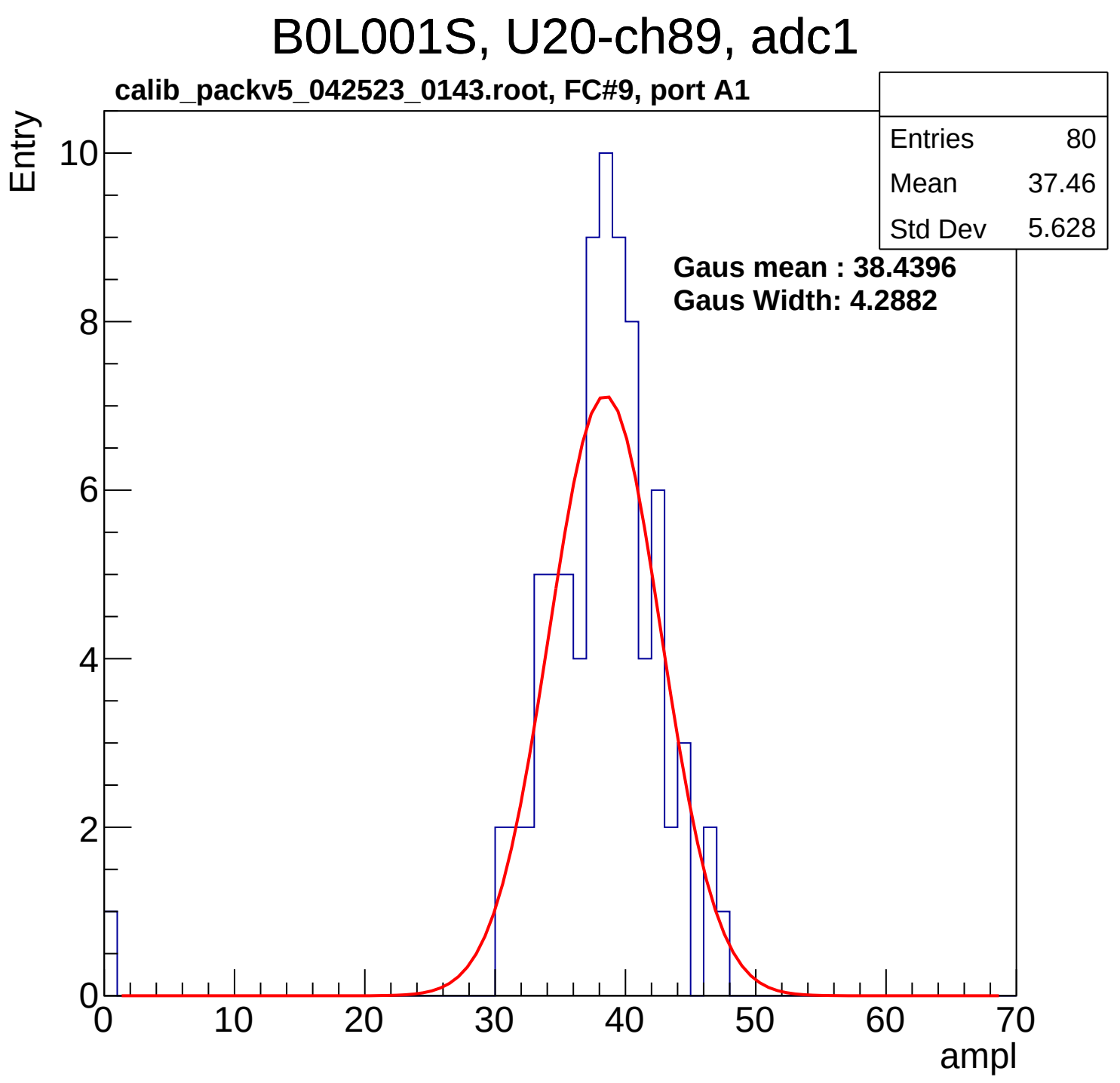
B0L001S, U20-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.46
Std Dev 5.628
Gaus mean : 38.4396
Gaus Width: 4.2882
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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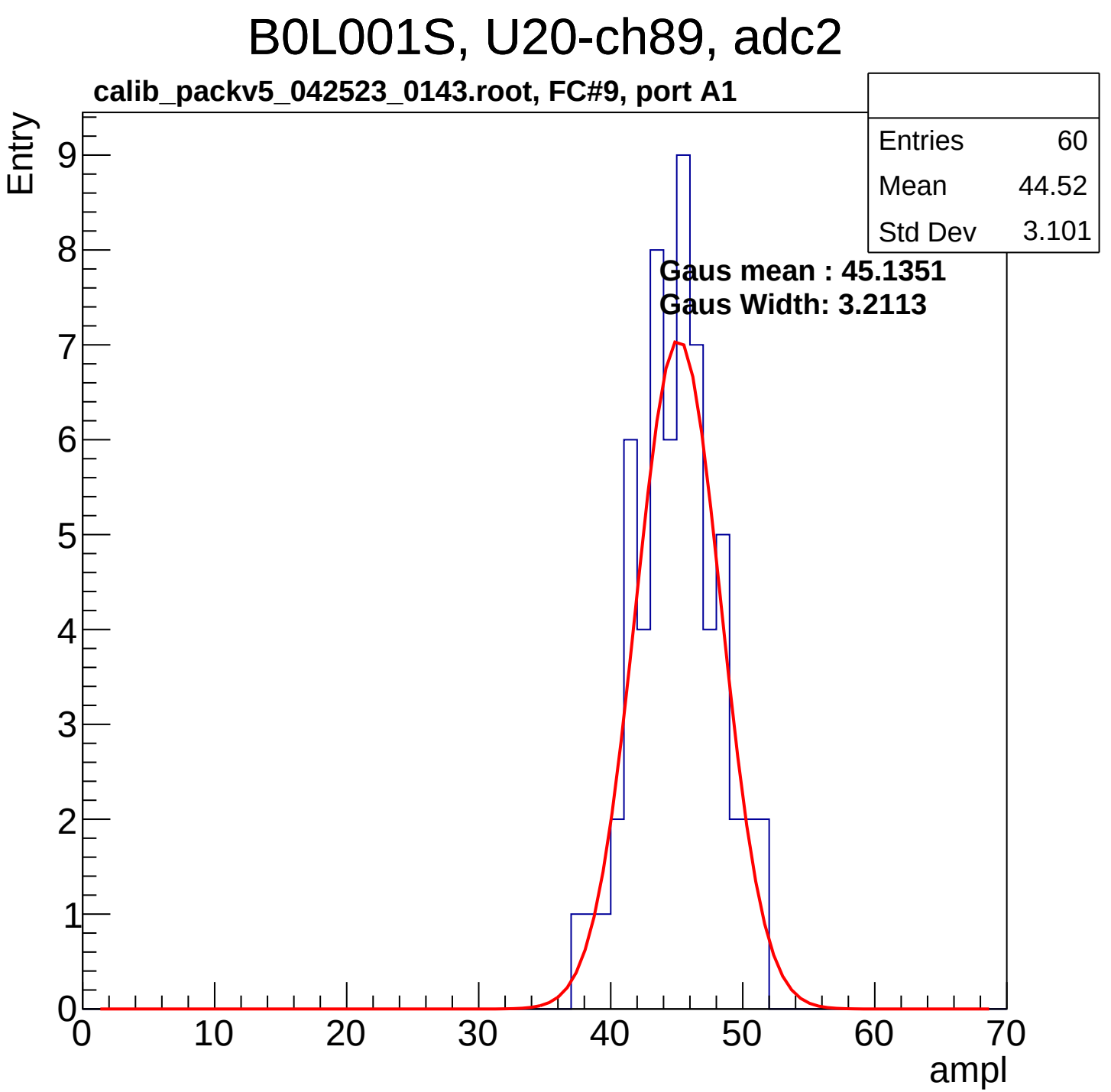
B0L001S, U20-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.52
Std Dev 3.101
Gaus mean : 45.1351
Gaus Width: 3.2113
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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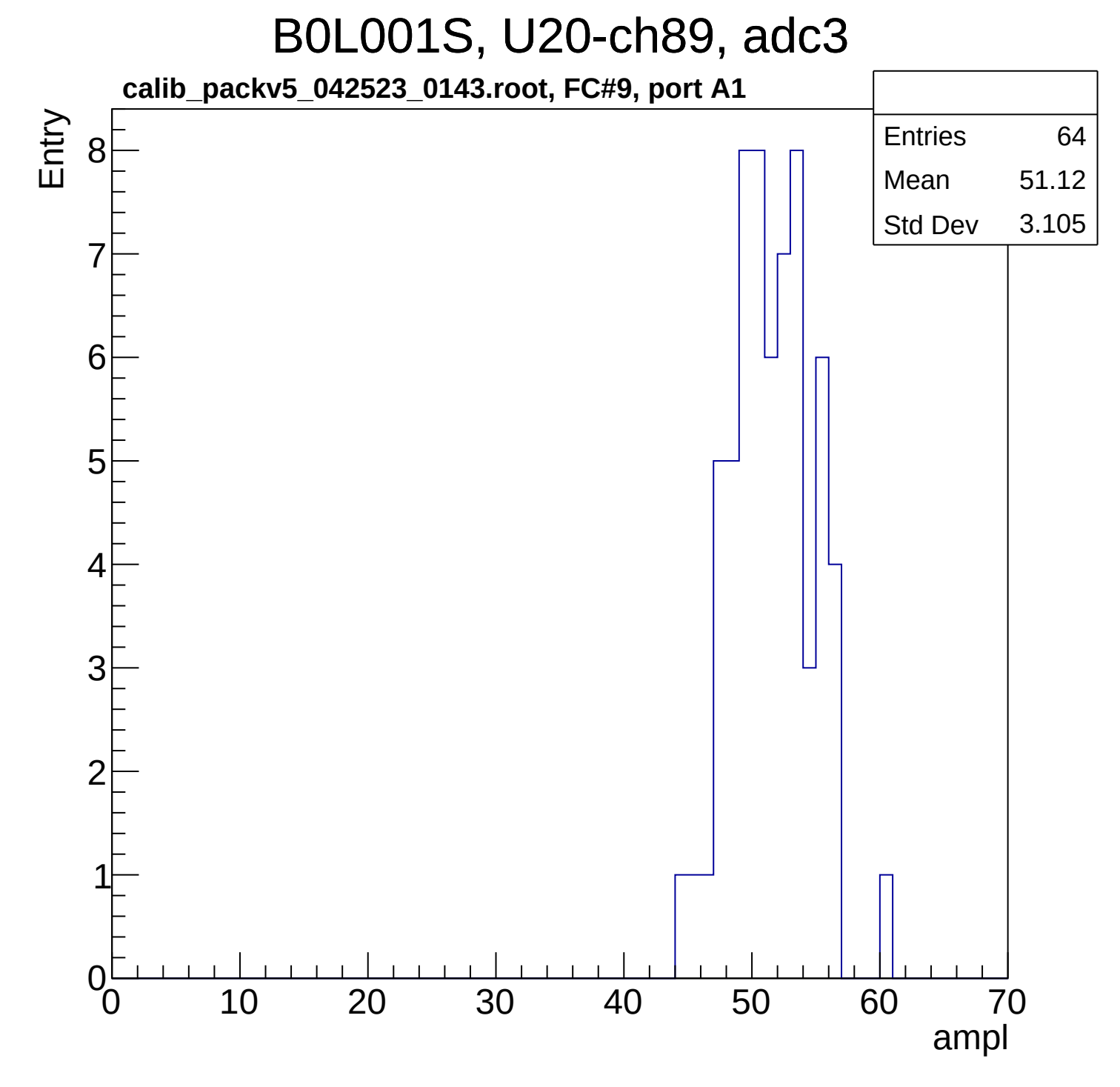
B0L001S, U20-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.12
Std Dev 3.105
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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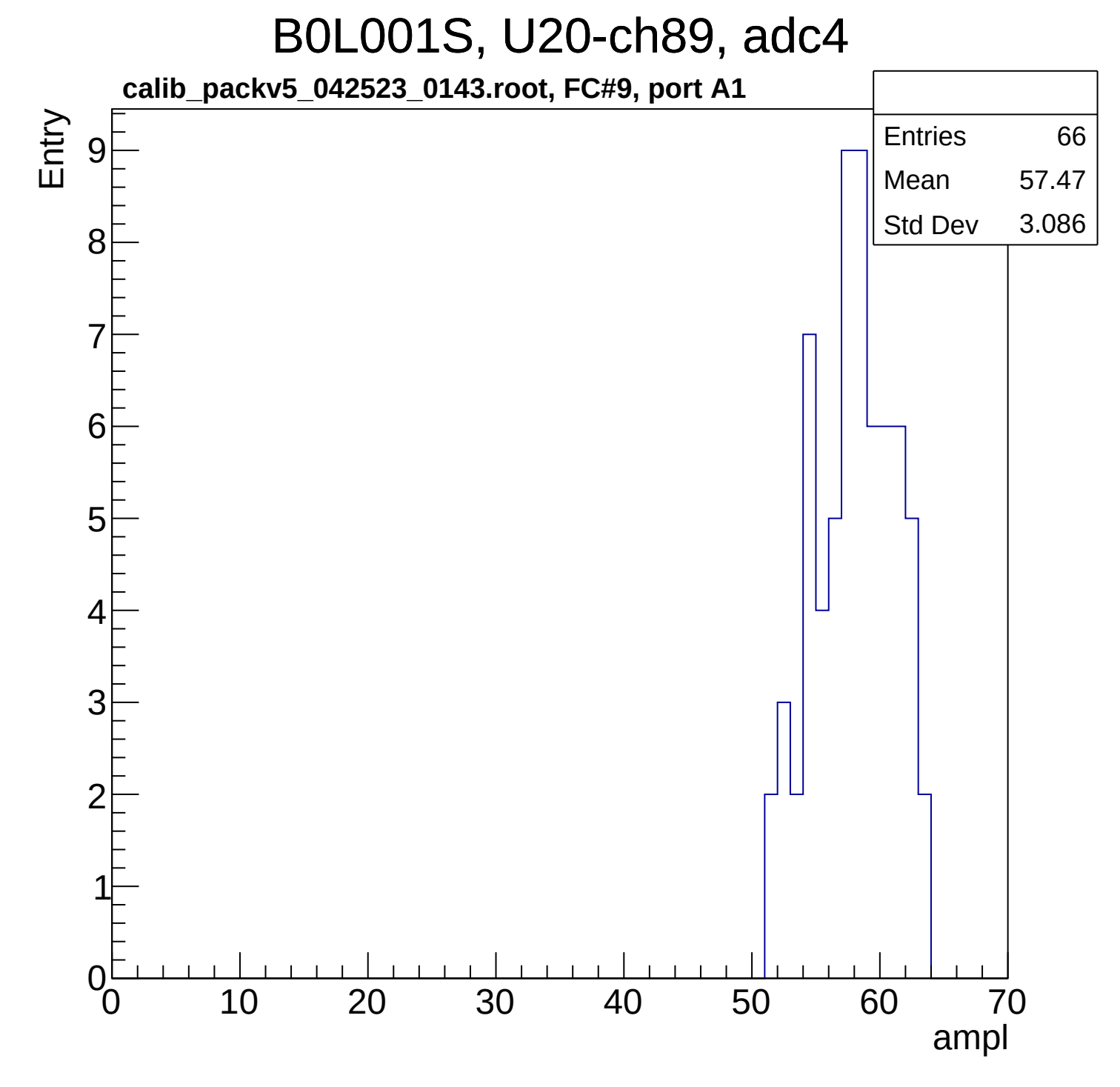
B0L001S, U20-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.47
Std Dev 3.086
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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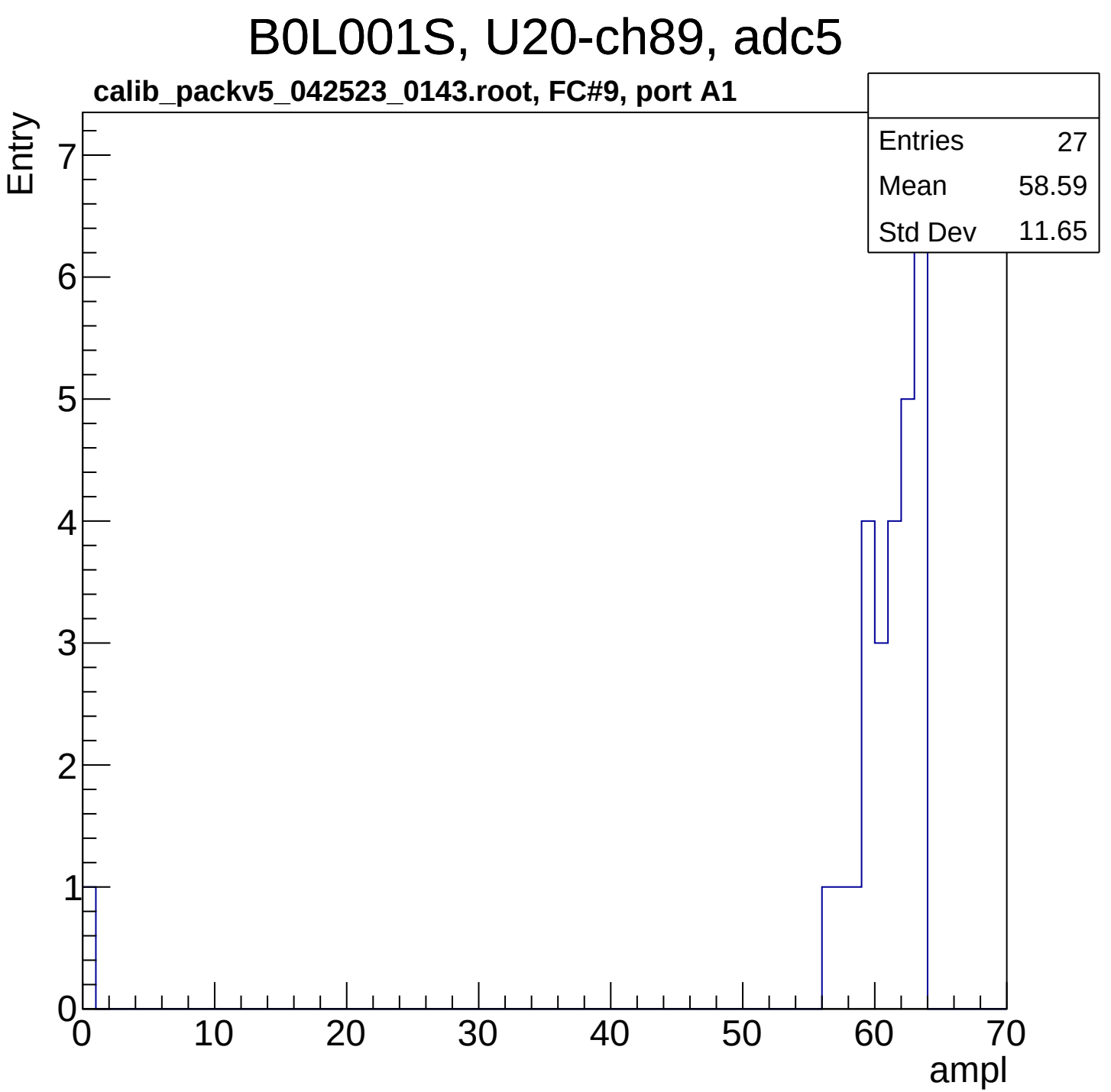
B0L001S, U20-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.59
Std Dev 11.65
Entry
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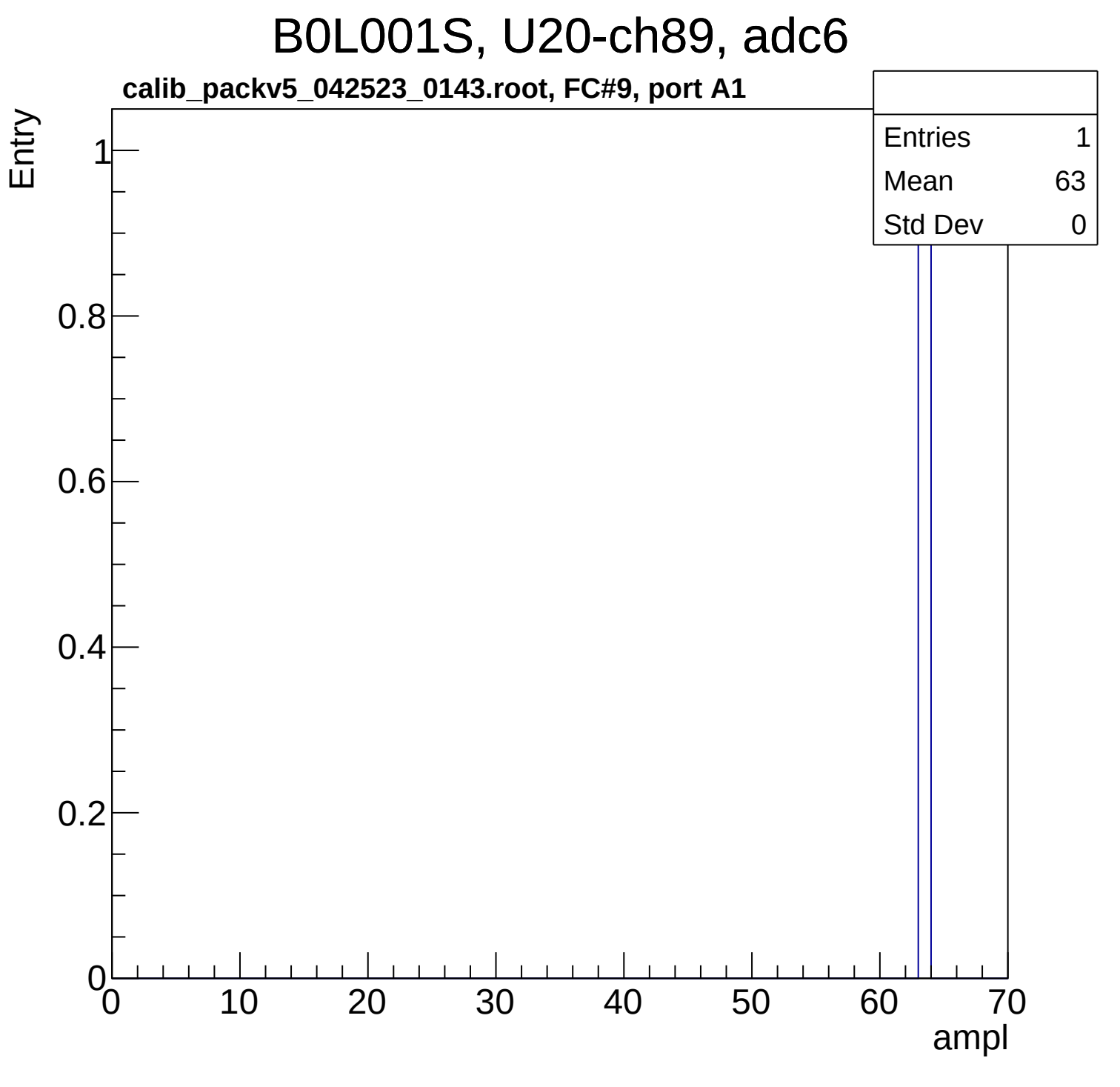
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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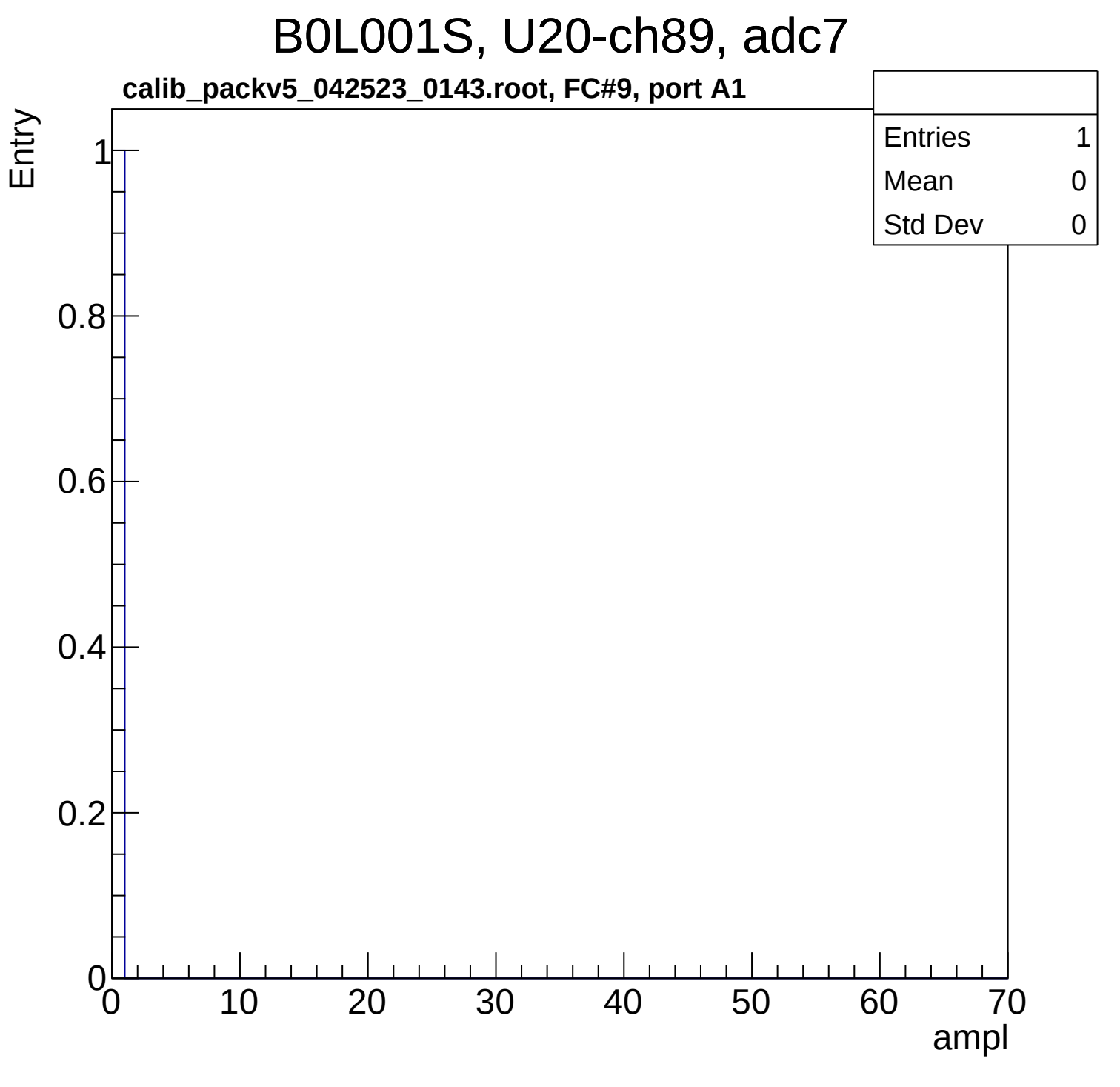
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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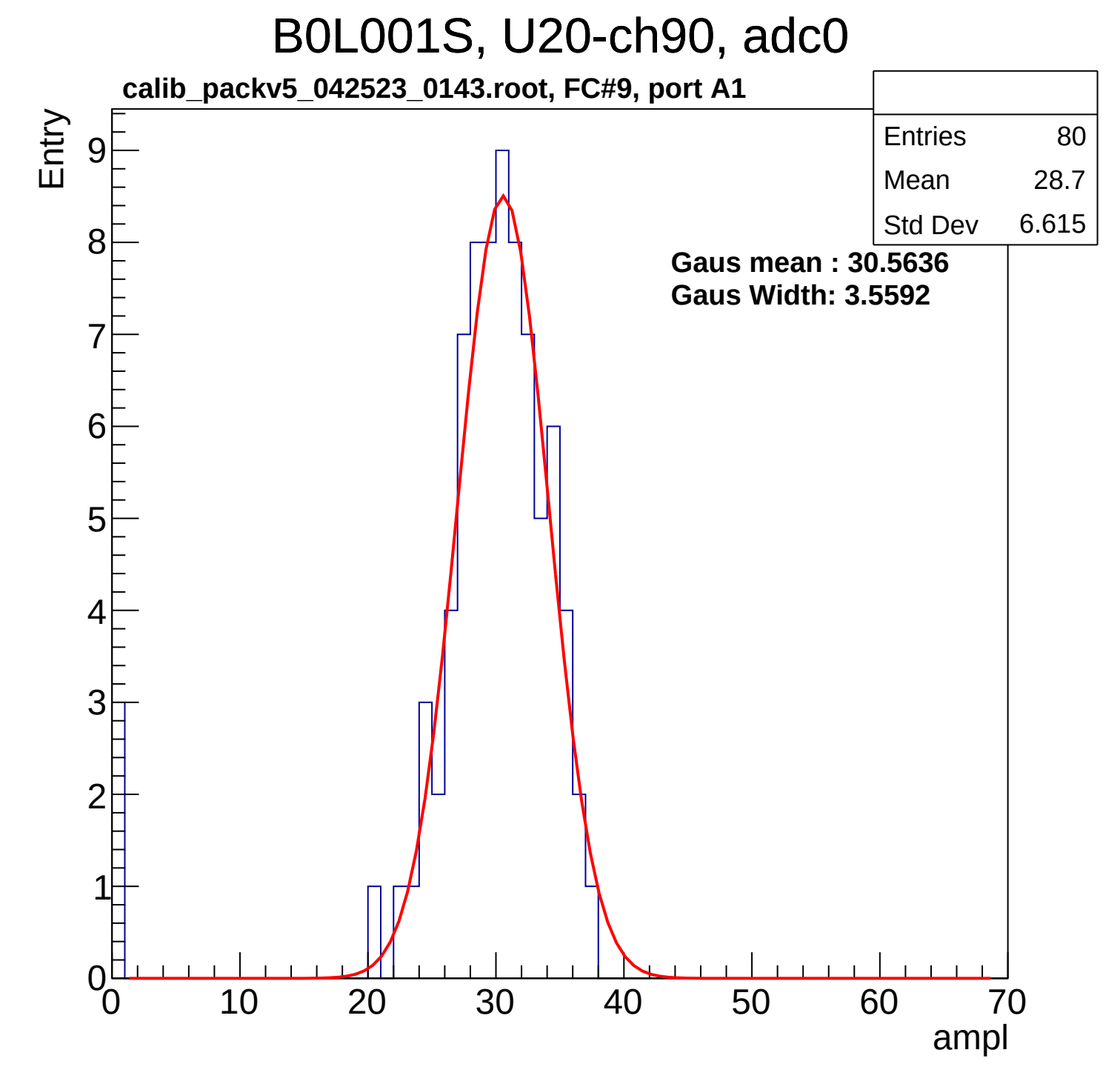
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 28.7
Std Dev 6.615
Gaus mean : 30.5636
Gaus Width: 3.5592
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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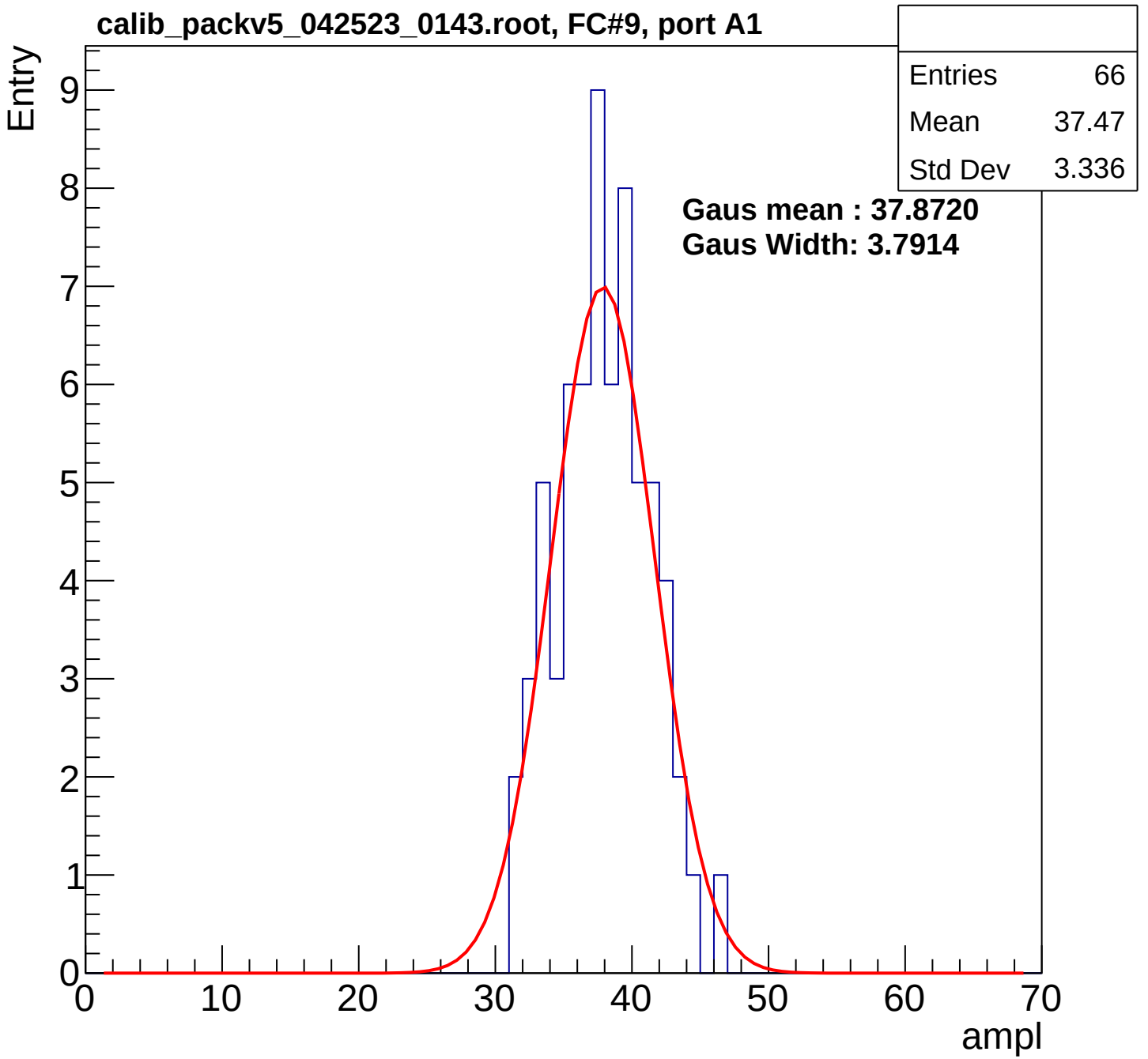
B0L001S, U20-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.47
Std Dev 3.336
Gaus mean : 37.8720
Gaus Width: 3.7914
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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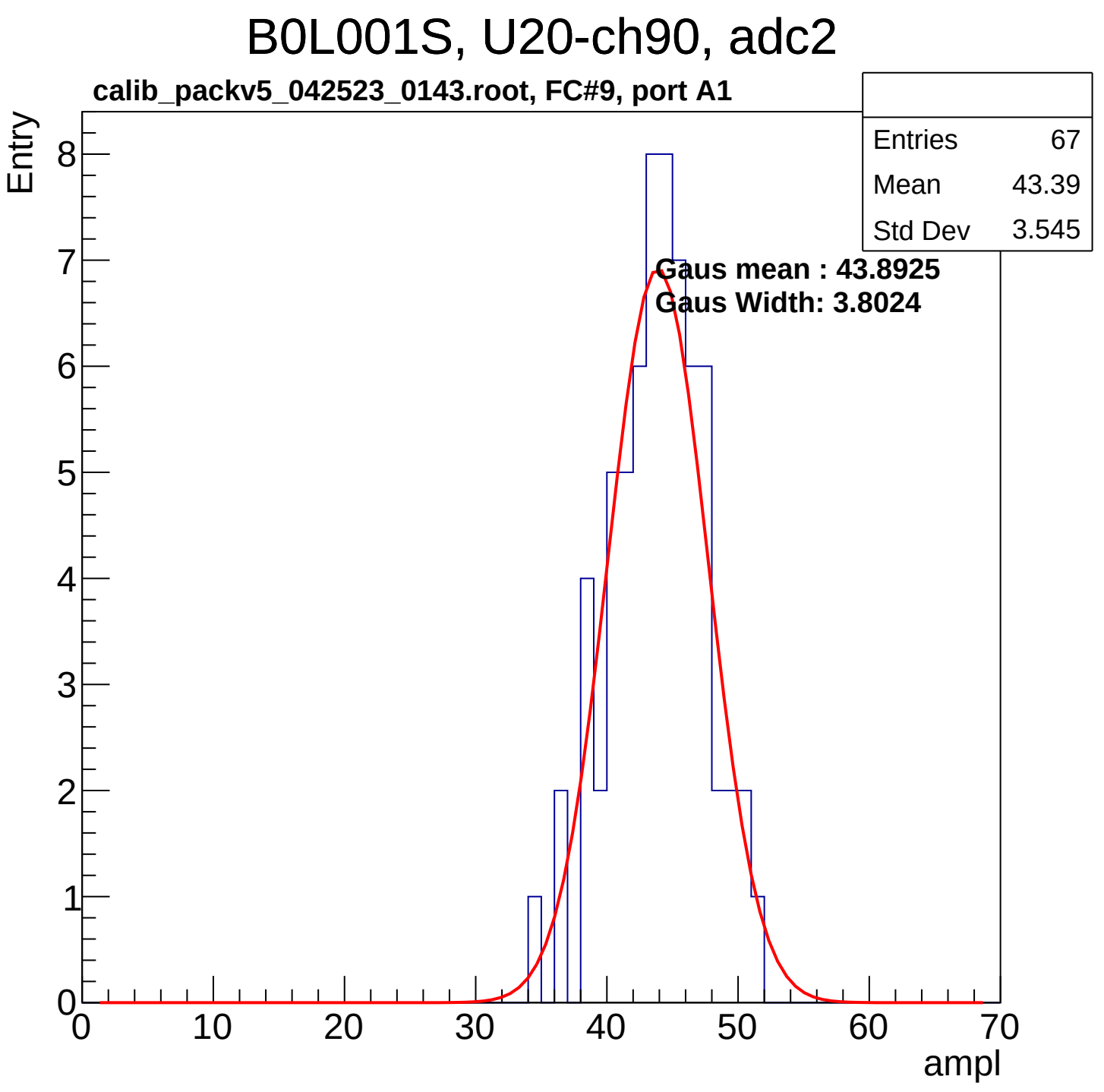
B0L001S, U20-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.39
Std Dev 3.545
Gaus mean : 43.8925
Gaus Width: 3.8024
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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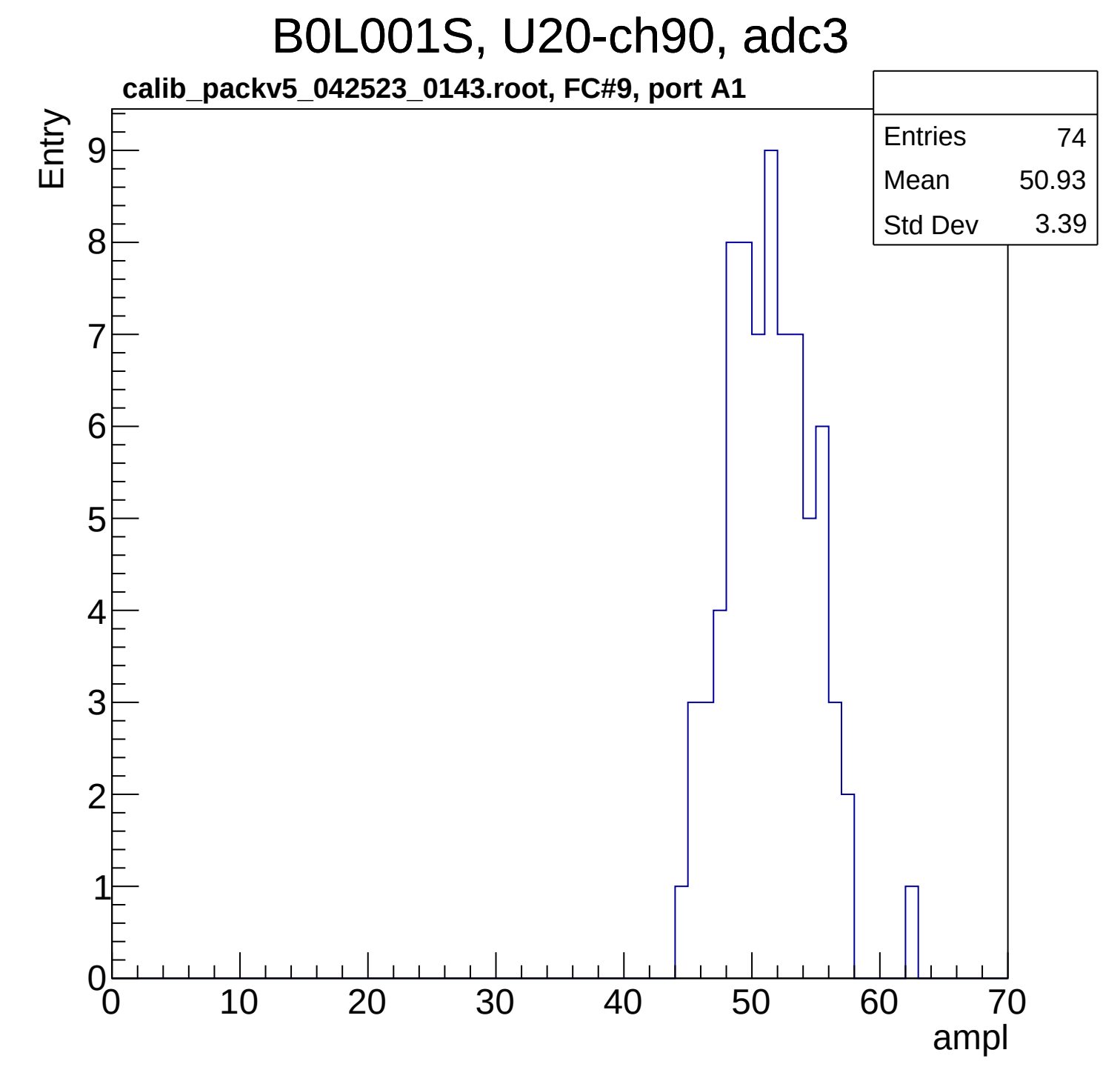
B0L001S, U20-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.93
Std Dev 3.39
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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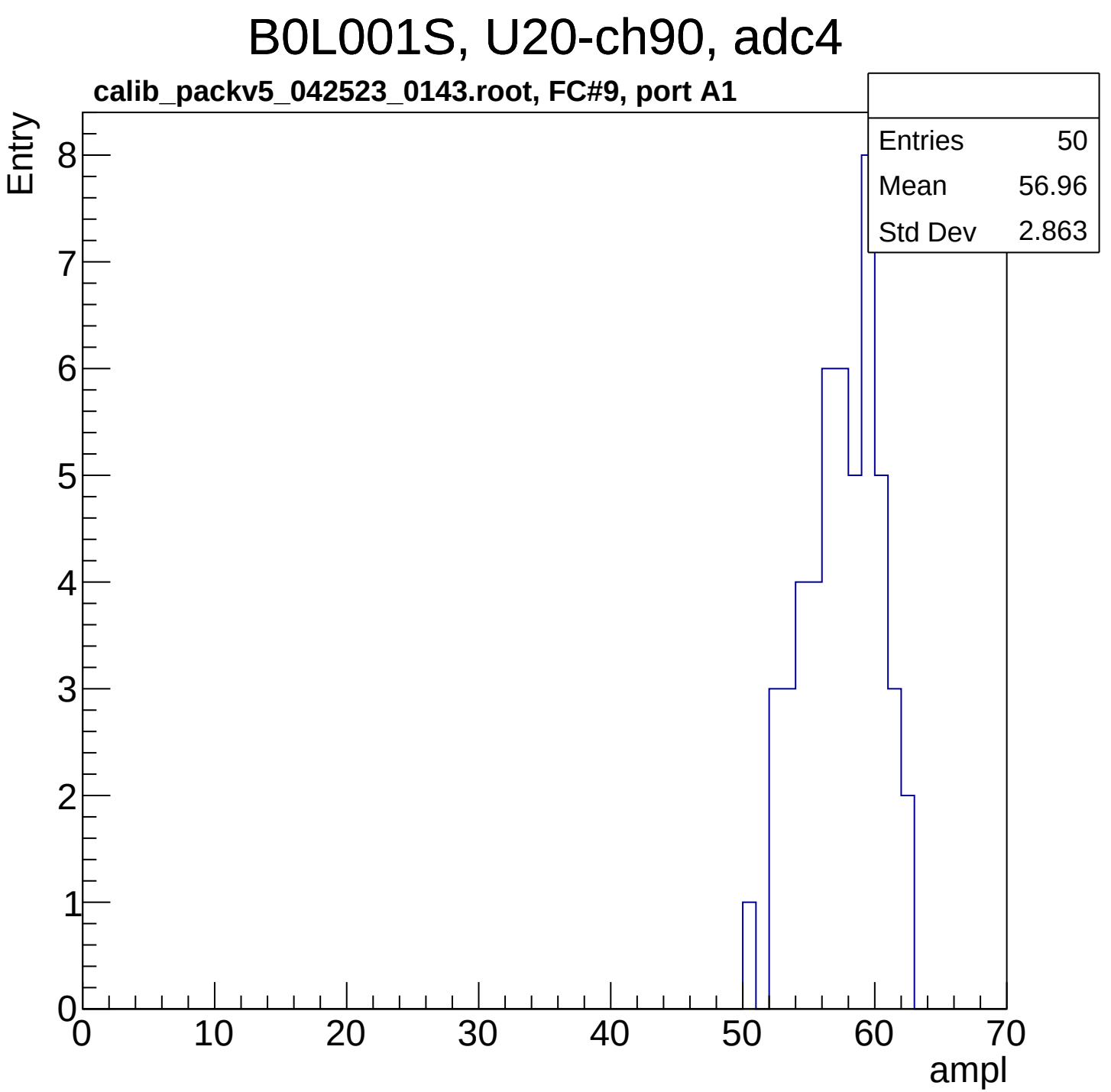
B0L001S, U20-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.96
Std Dev 2.863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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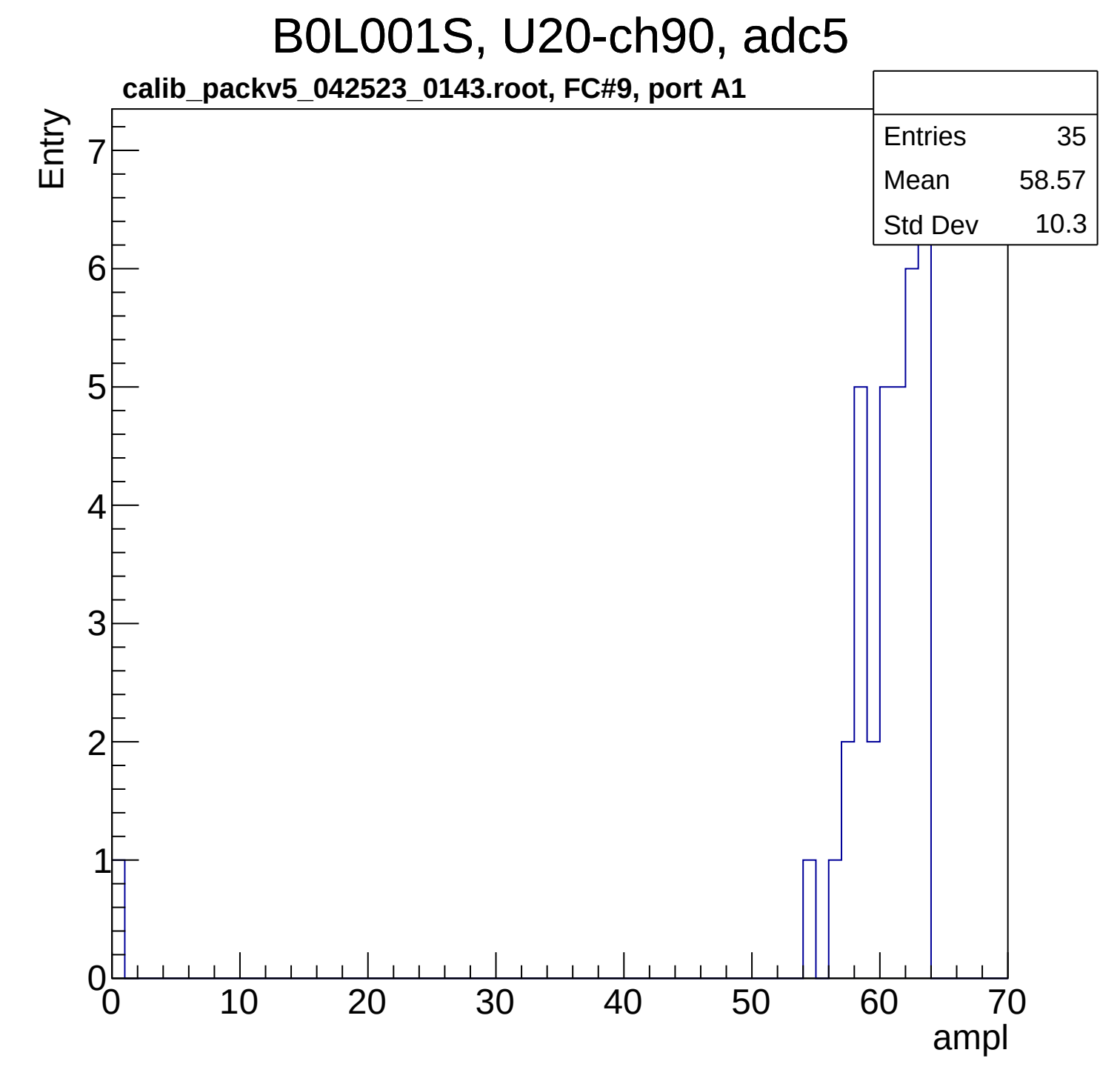
B0L001S, U20-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.57
Std Dev 10.3
Entry
ampl
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2
3
4
5
6
7
0
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60
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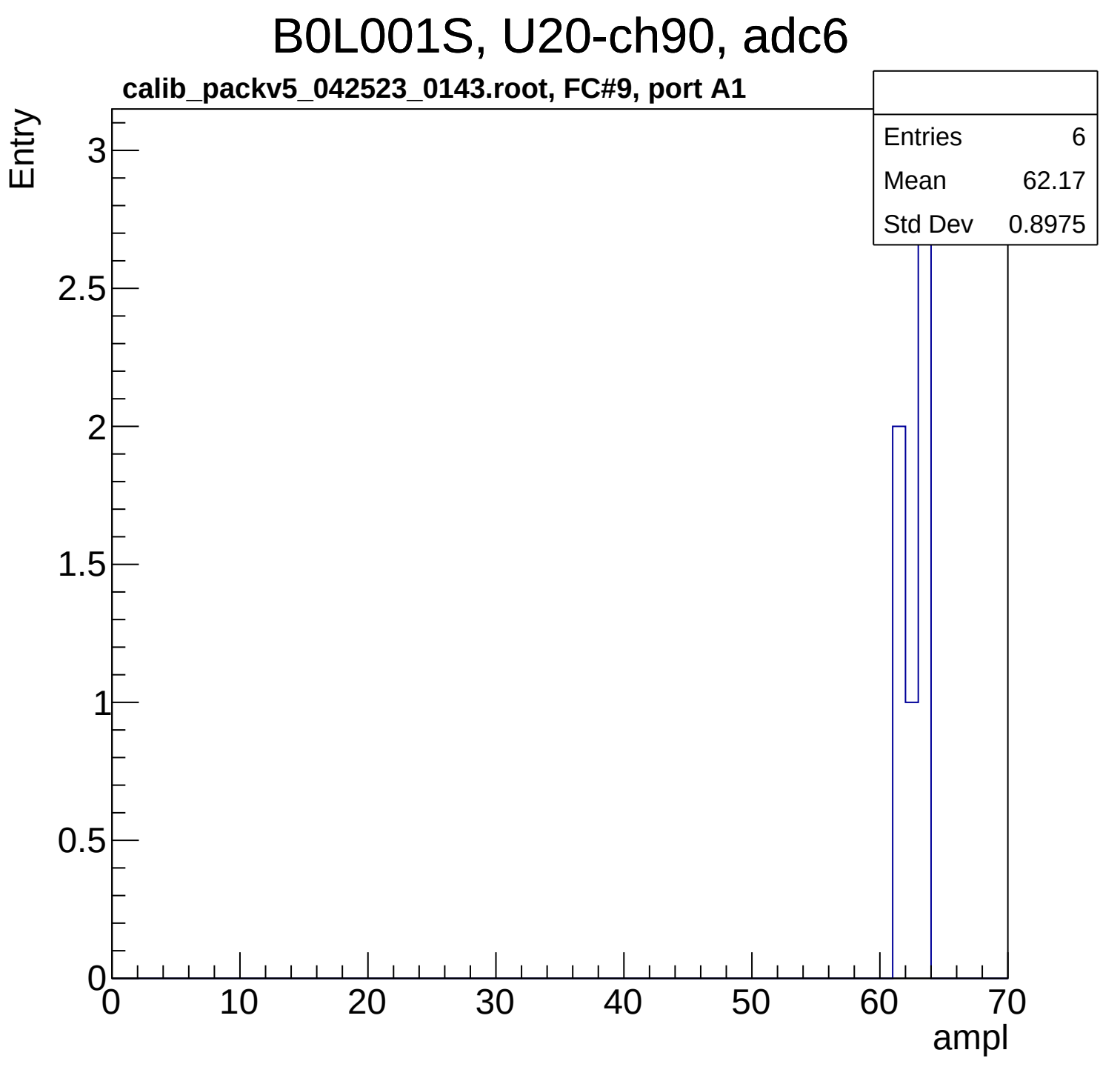
B0L001S, U20-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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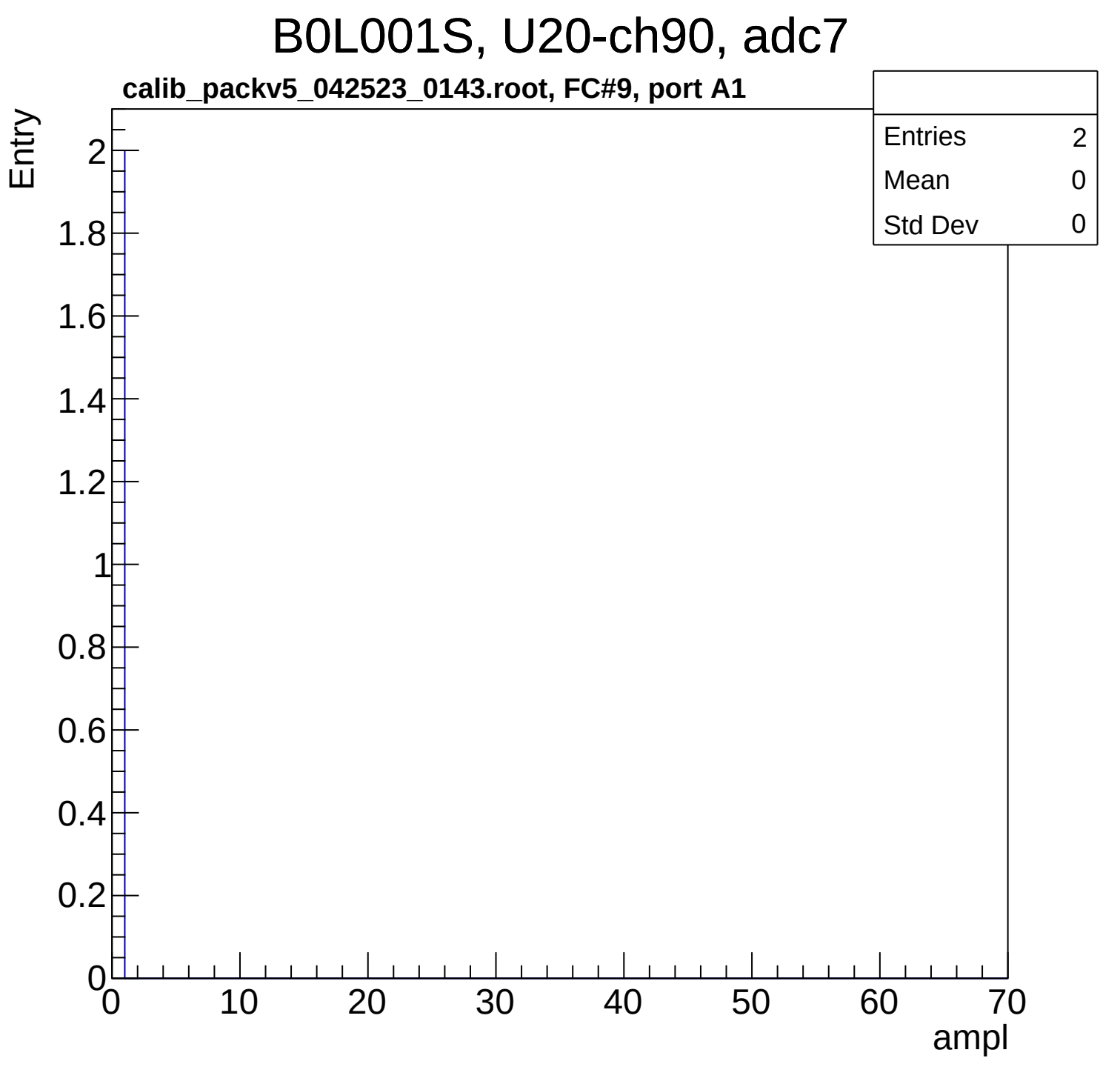
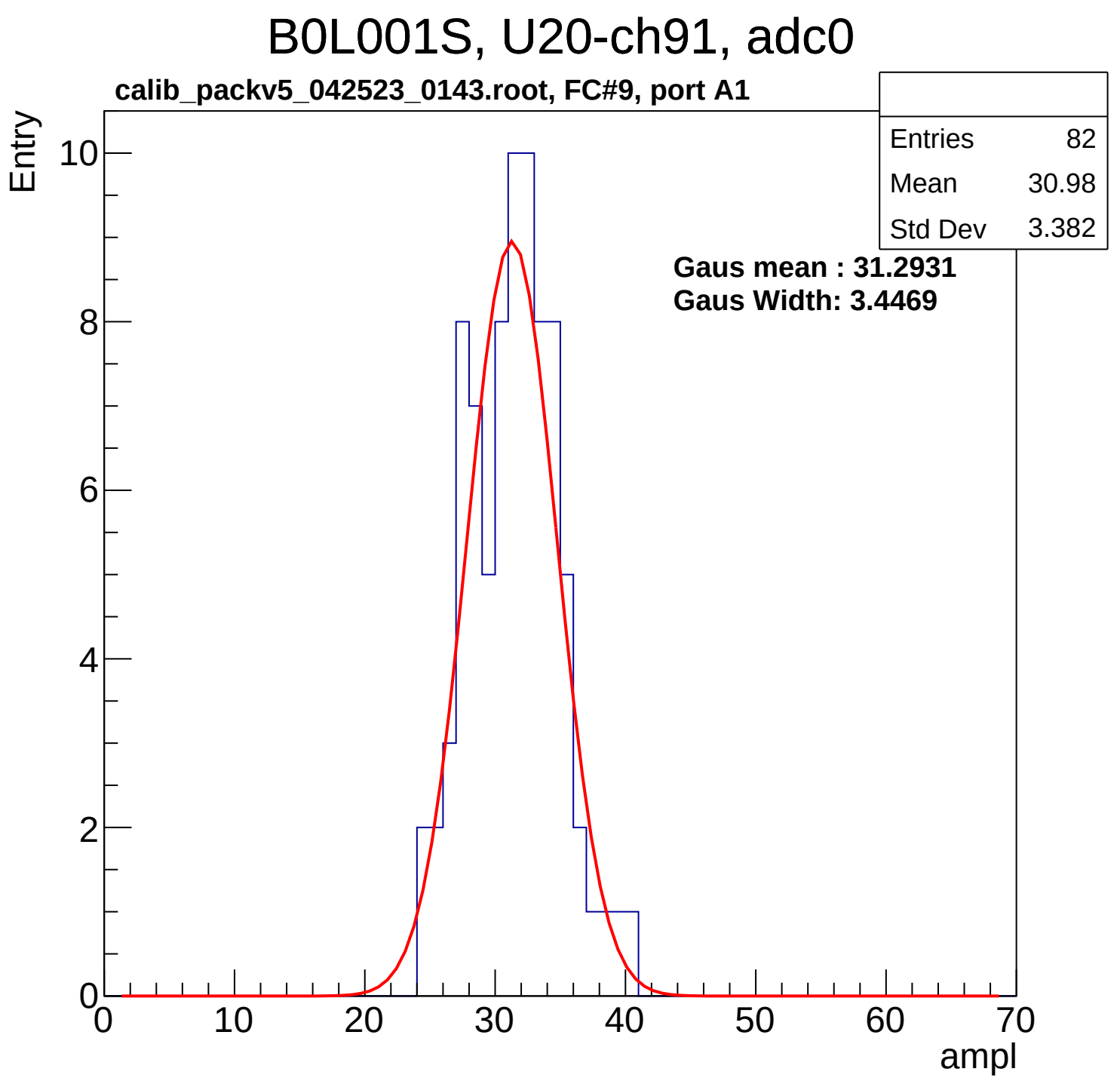
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30.98
Std Dev 3.382
Gaus mean : 31.2931
Gaus Width: 3.4469
Entry
ampl
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2
4
6
8
10
0
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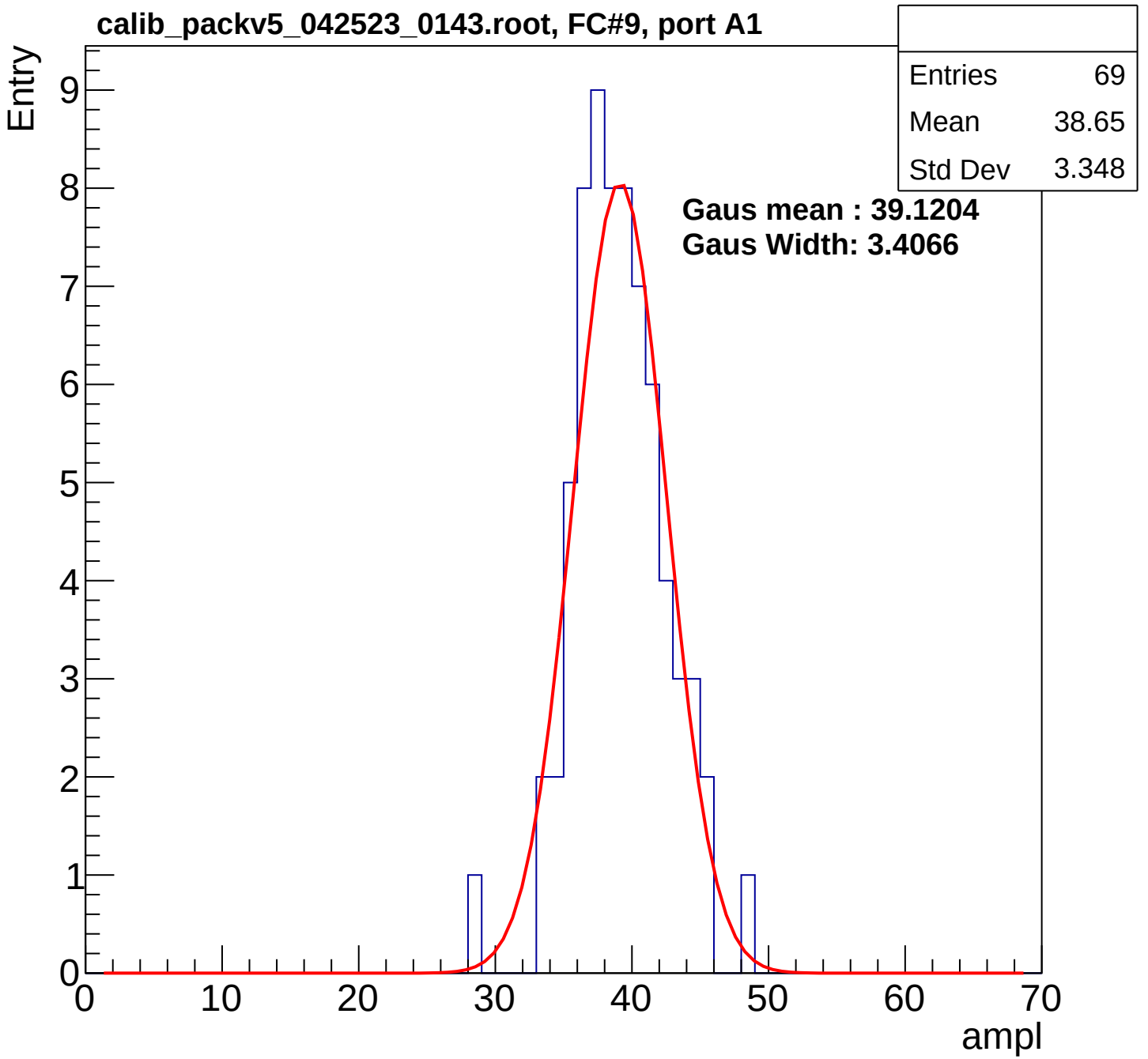
20
30
40
50
60
70


B0L001S, U20-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.65
Std Dev 3.348
Gaus mean : 39.1204
Gaus Width: 3.4066
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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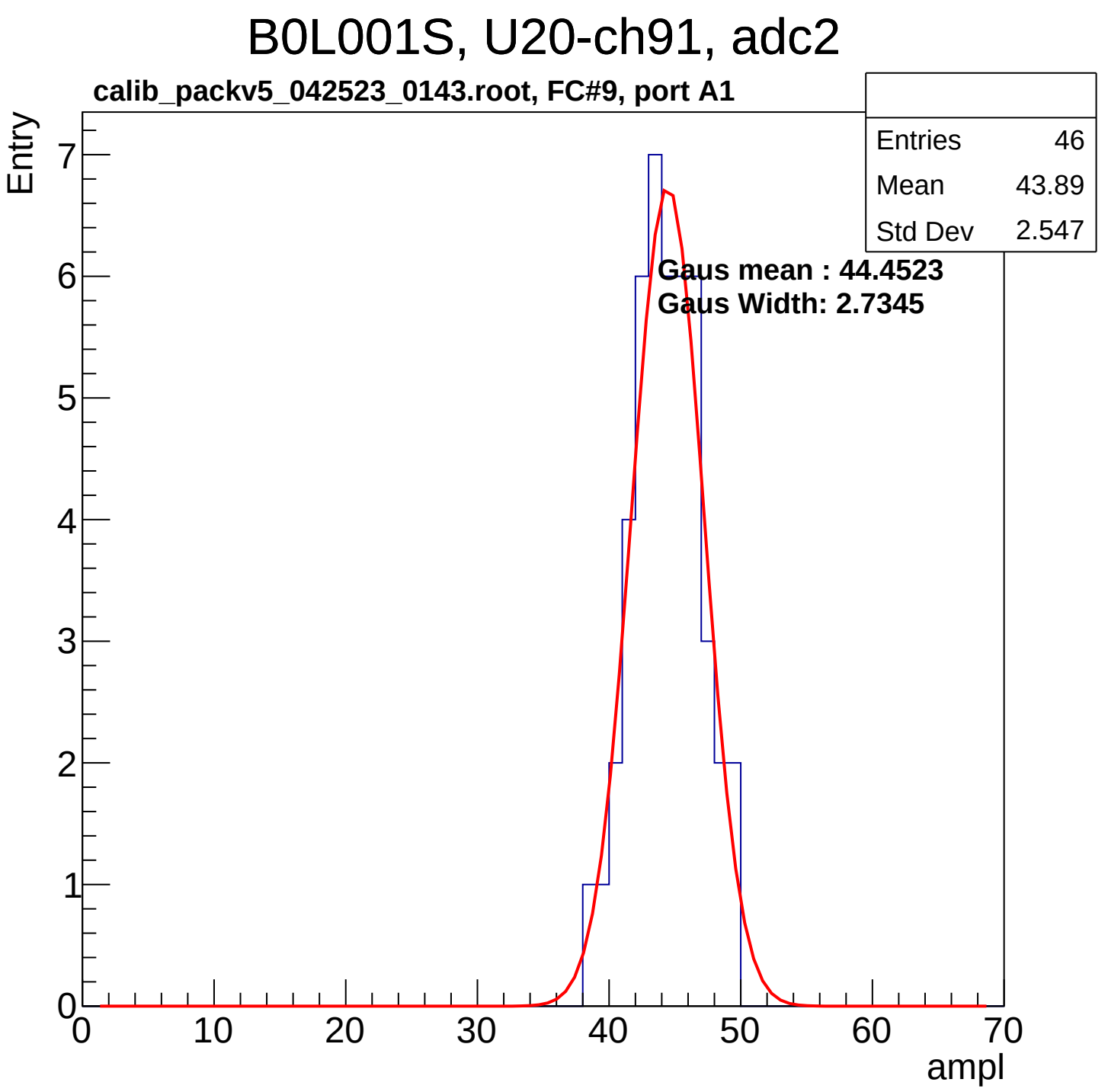
B0L001S, U20-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 43.89
Std Dev 2.547
Gaus mean : 44.4523
Gaus Width: 2.7345
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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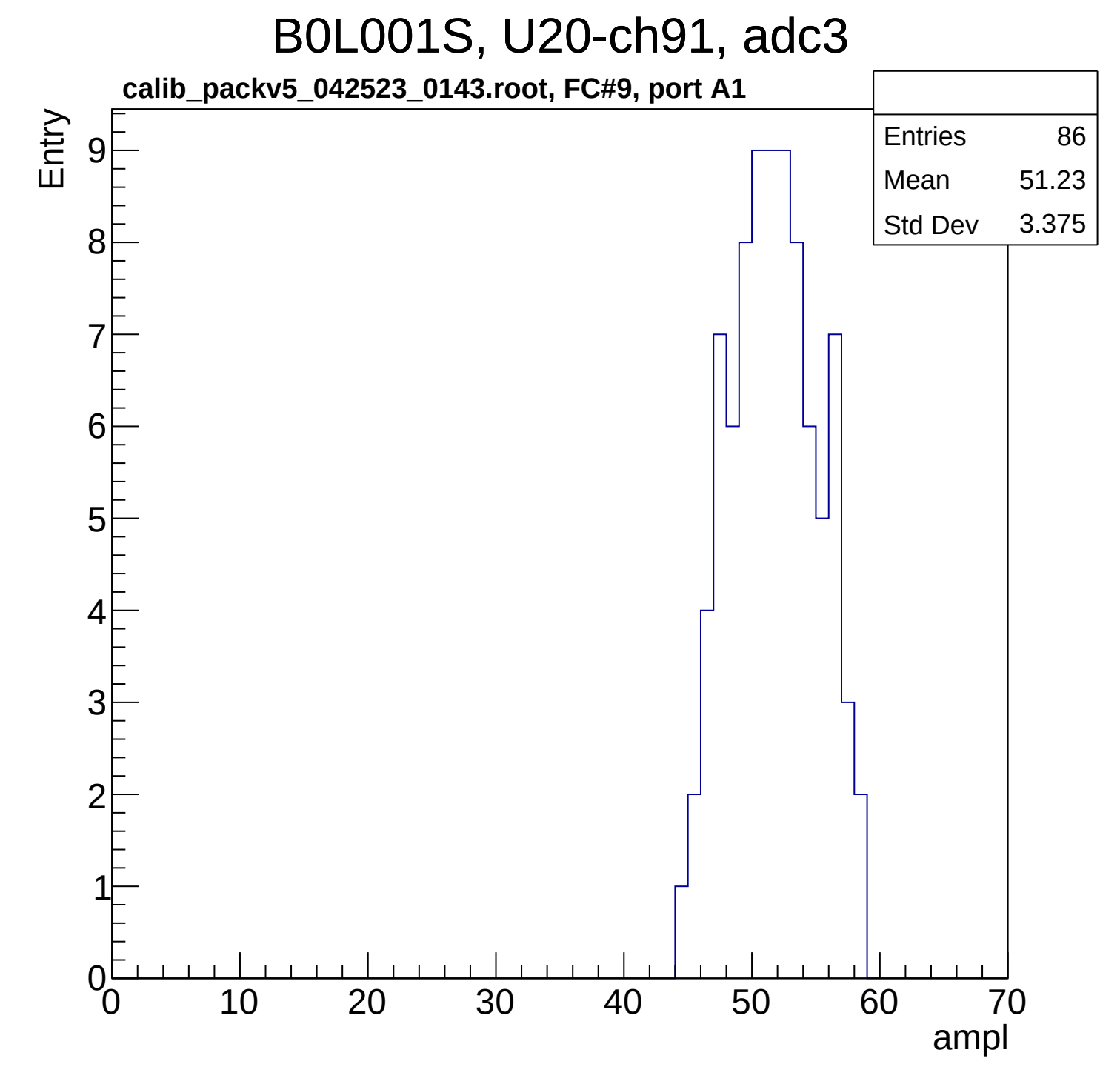
B0L001S, U20-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 51.23
Std Dev 3.375
Entry
ampl
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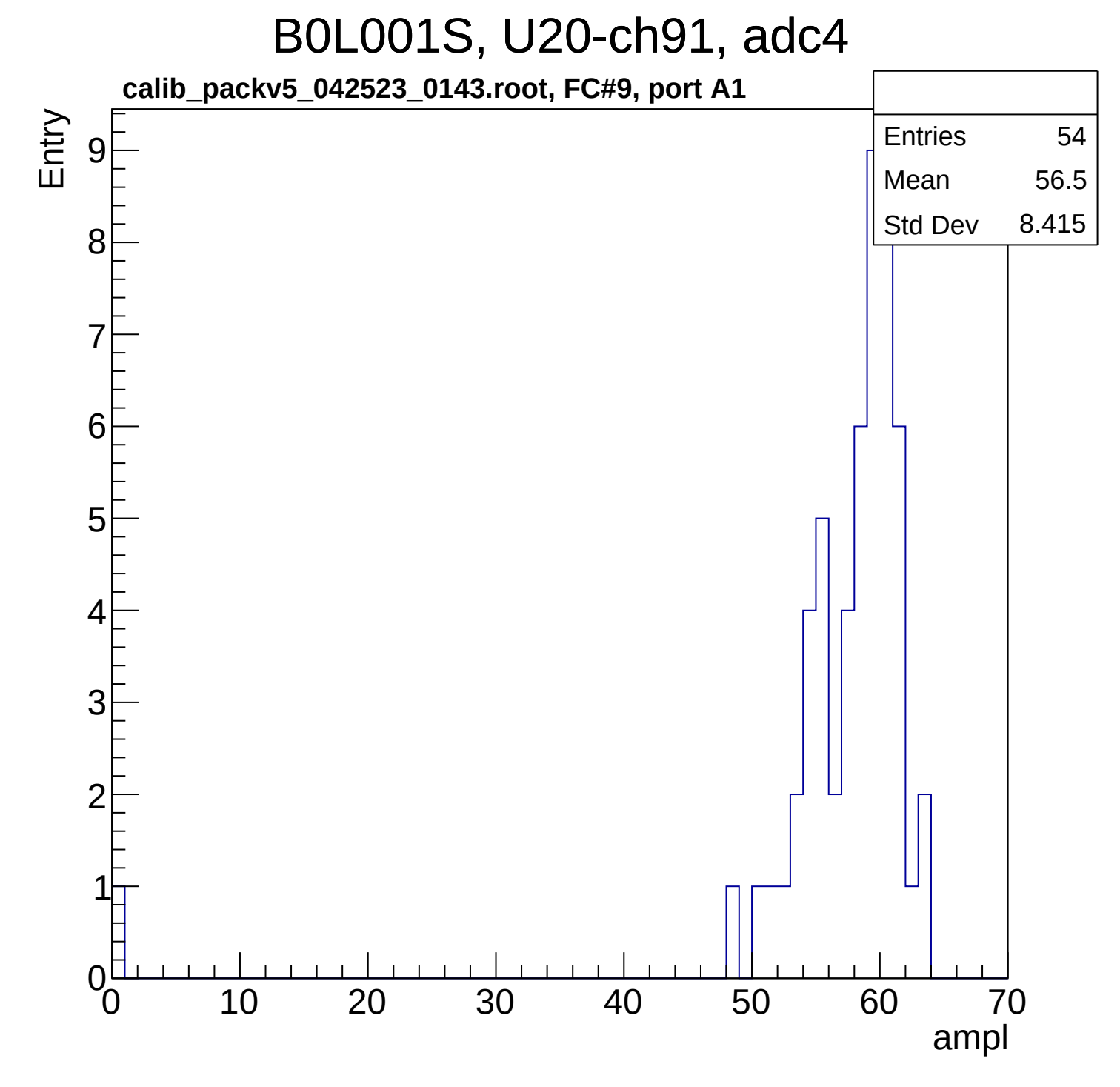
B0L001S, U20-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.5
Std Dev 8.415
Entry
ampl
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2
3
4
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6
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60
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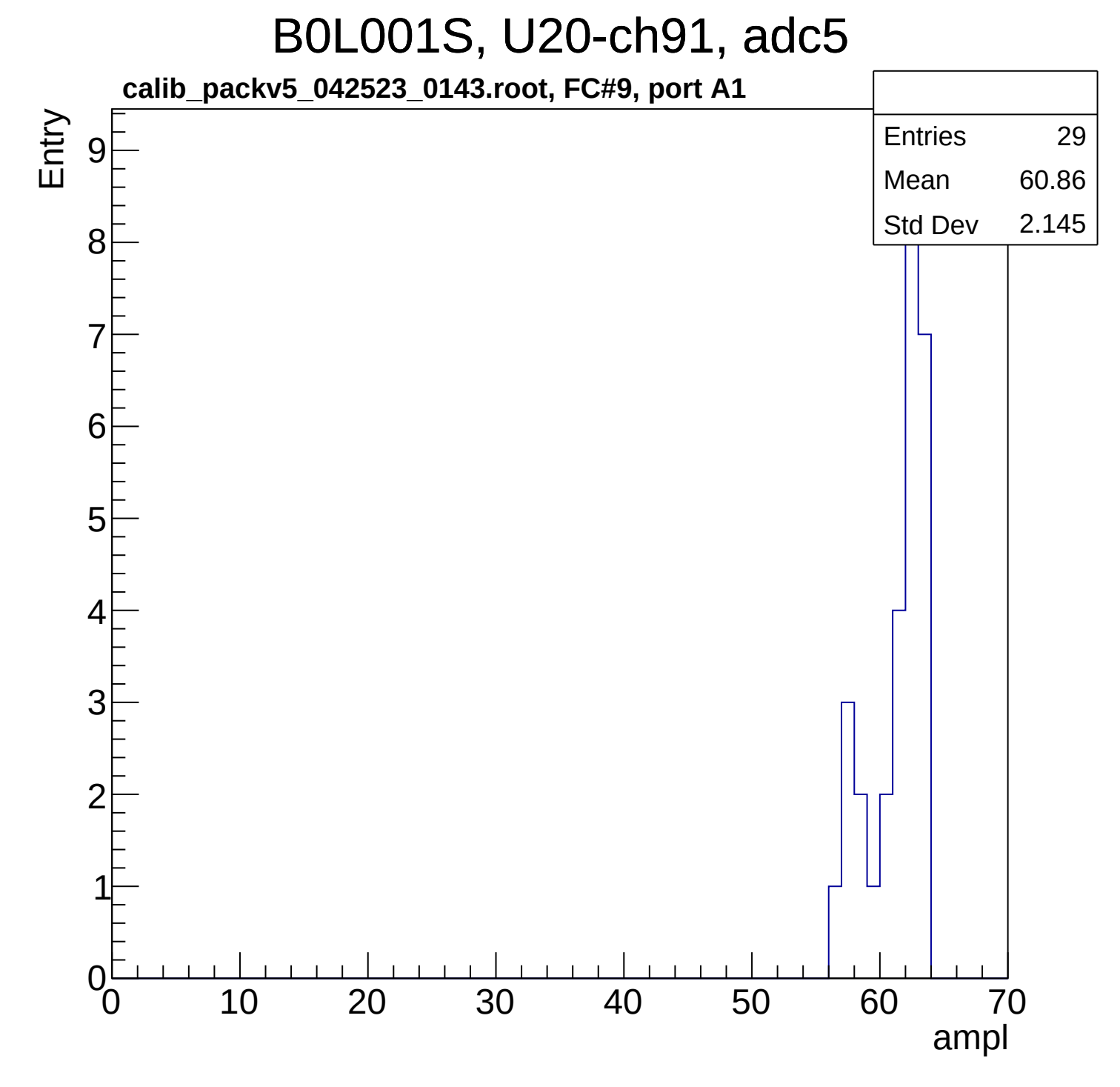
B0L001S, U20-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.86
Std Dev 2.145
Entry
ampl
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8
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0
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60
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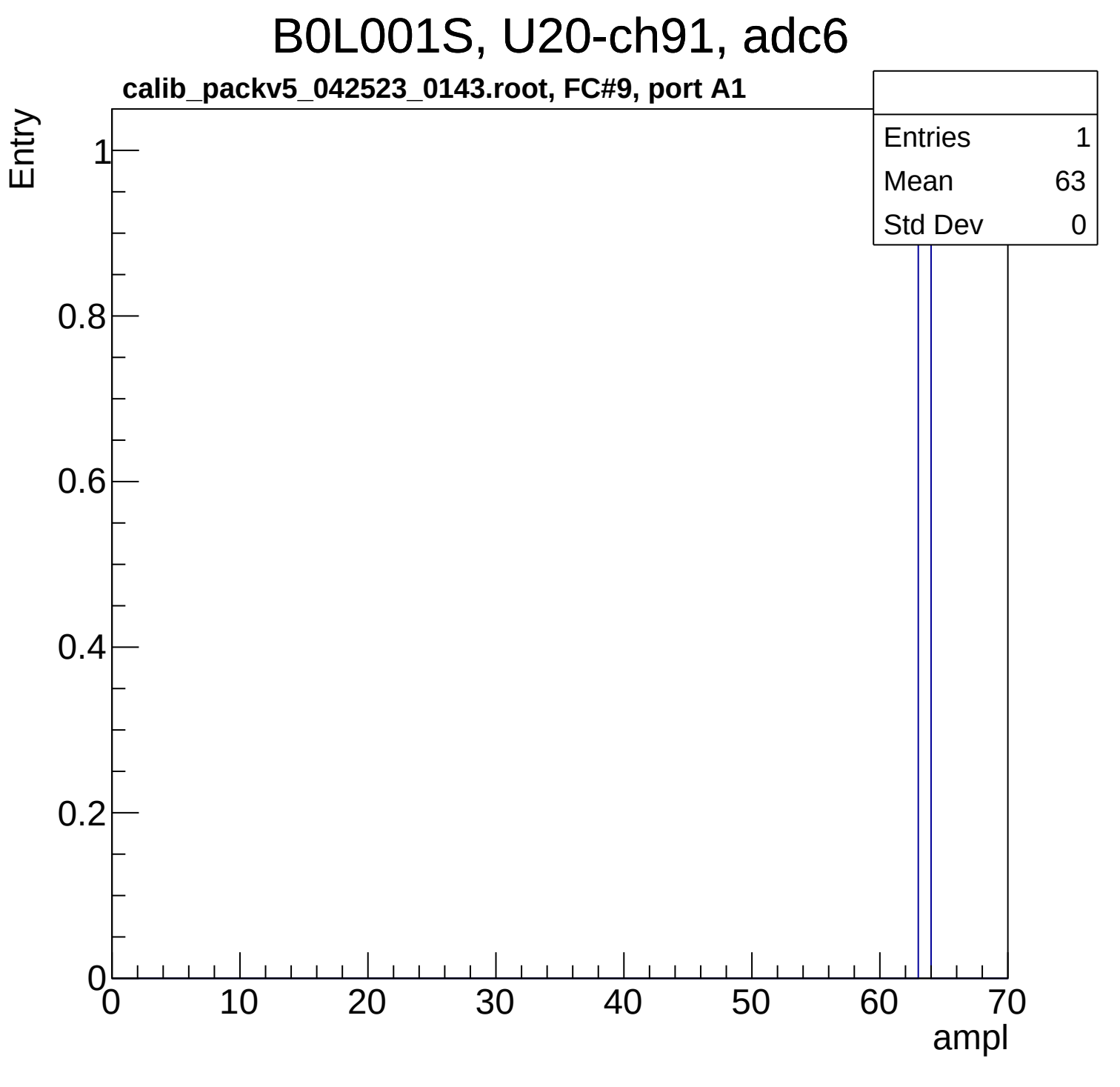
B0L001S, U20-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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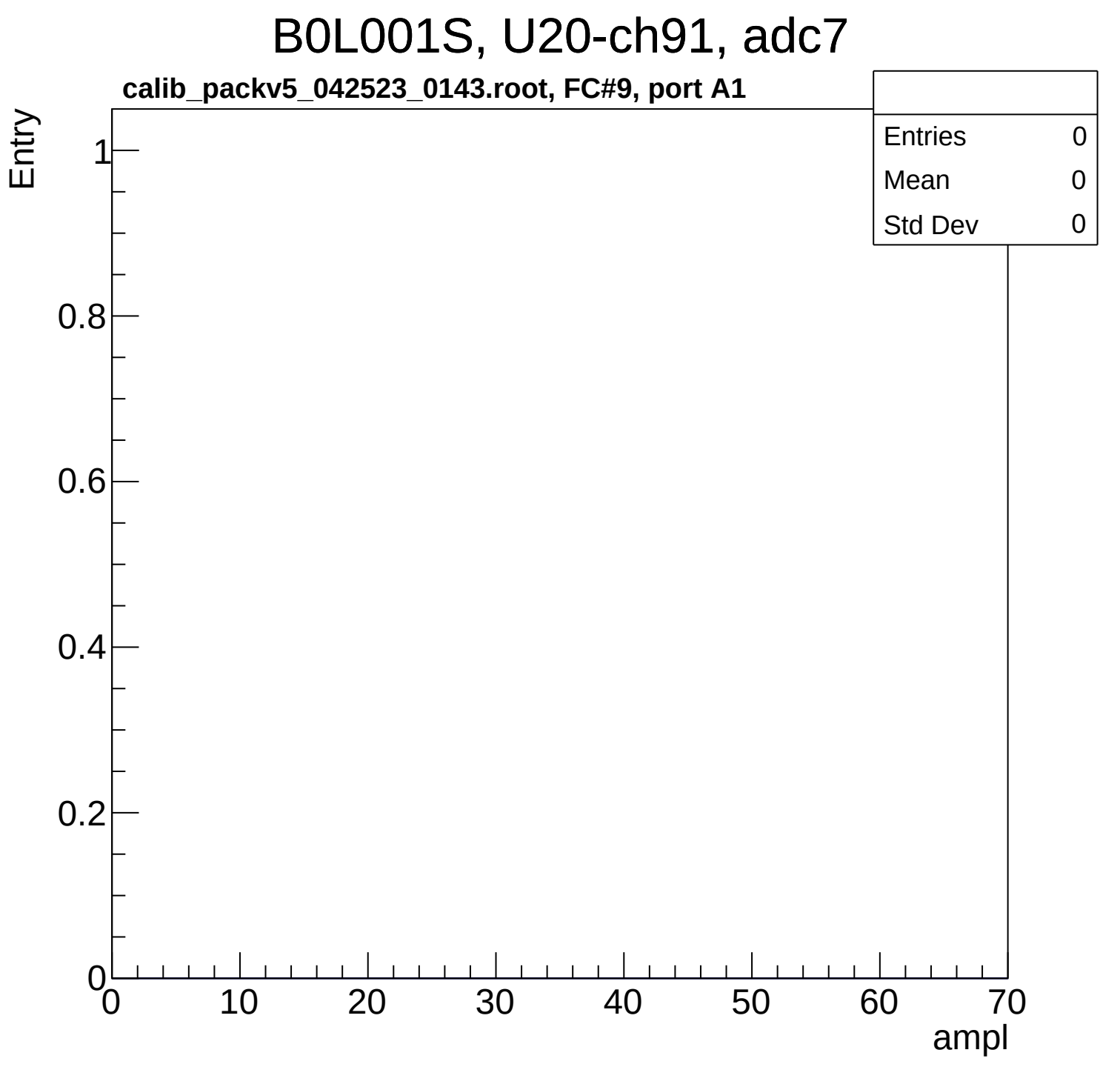
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
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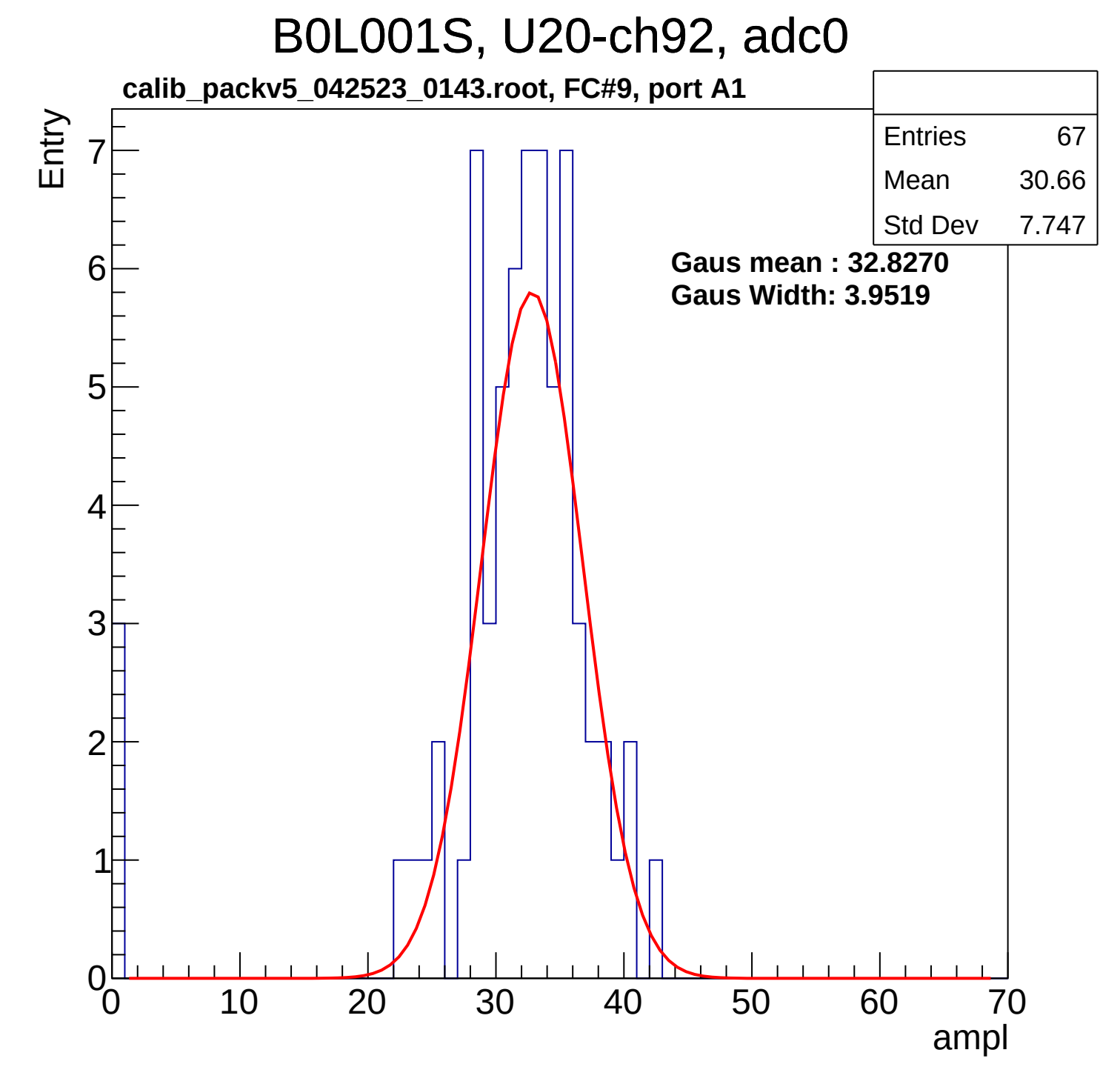
50
60
70
ampl


B0L001S, U20-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.66
Std Dev 7.747
Gaus mean : 32.8270
Gaus Width: 3.9519
Entry
ampl
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1
2
3
4
5
6
7
0
10
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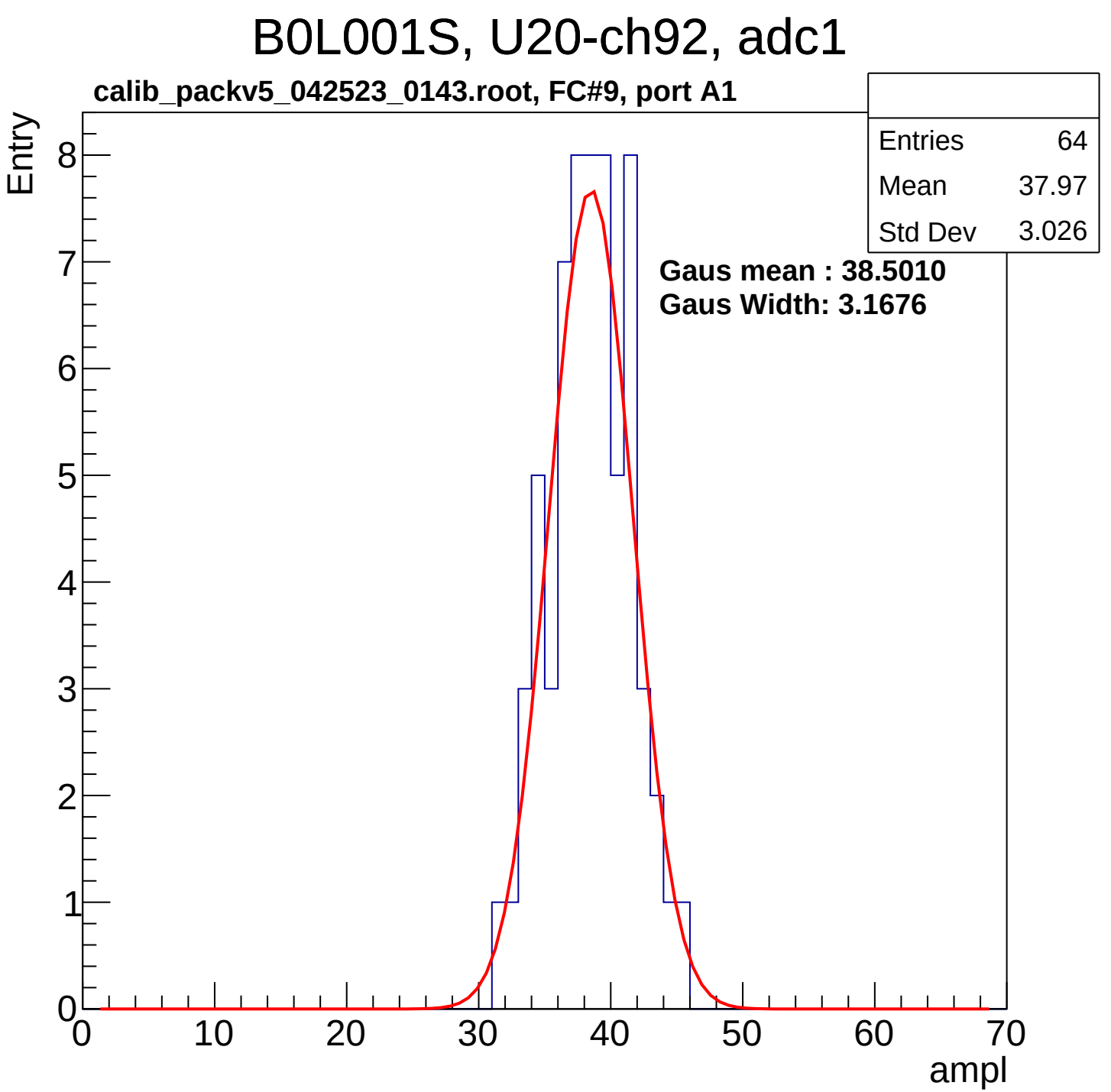
B0L001S, U20-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.97
Std Dev 3.026
Gaus mean : 38.5010
Gaus Width: 3.1676
Entry
ampl
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2
3
4
5
6
7
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0
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60
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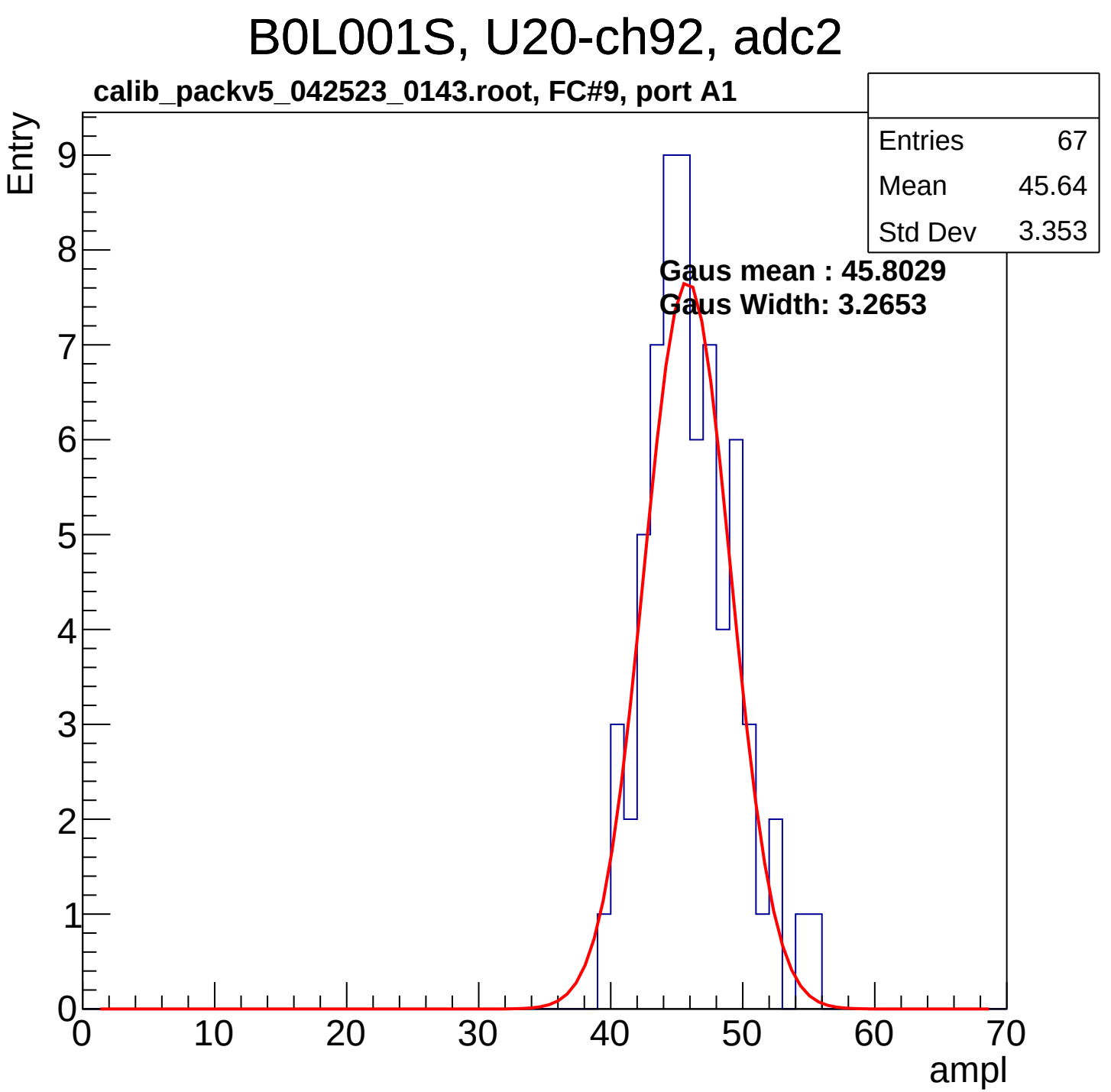
B0L001S, U20-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.64
Std Dev 3.353
Gaus mean : 45.8029
Gaus Width: 3.2653
Entry
ampl
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2
3
4
5
6
7
8
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0
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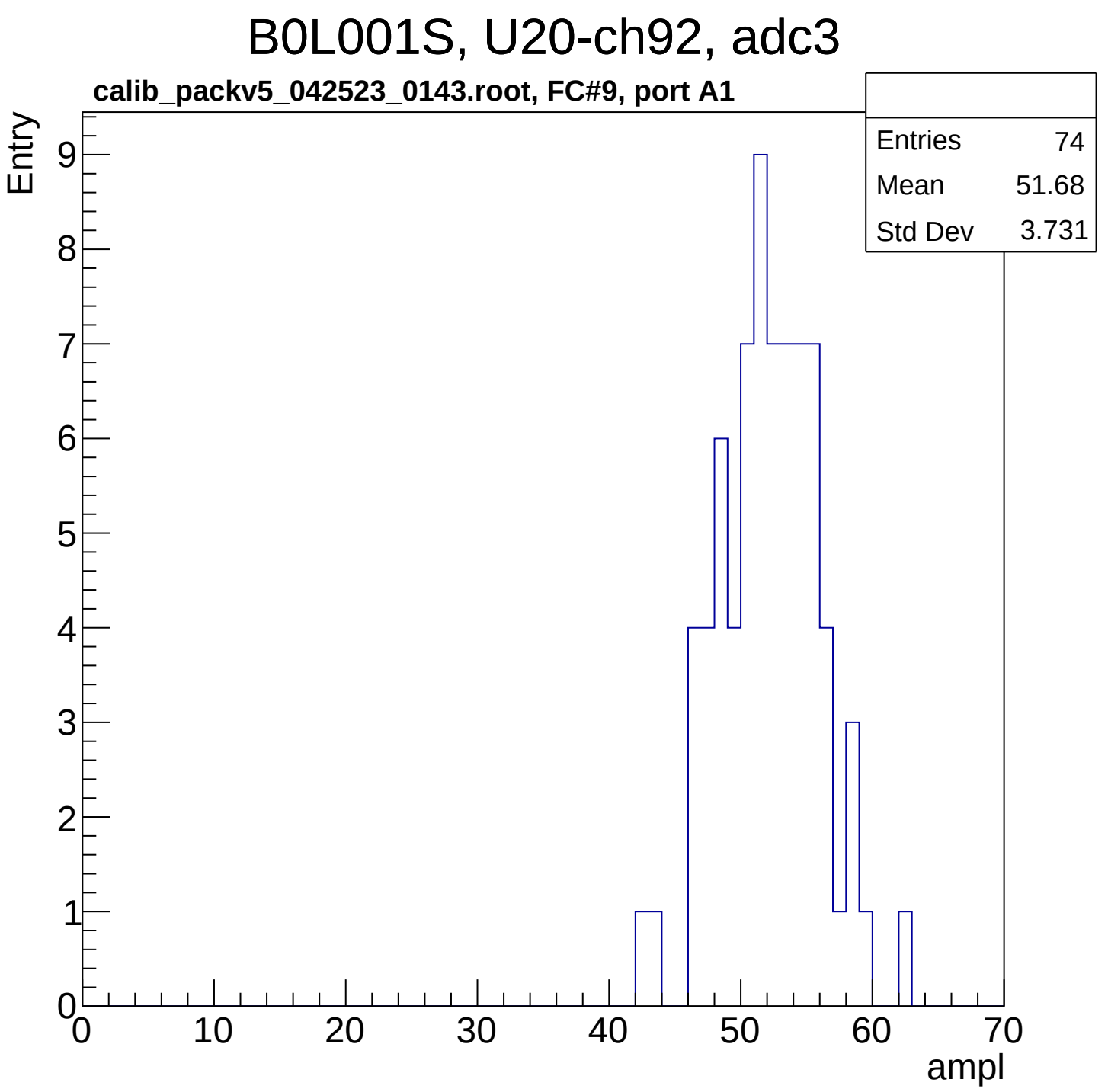
B0L001S, U20-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.68
Std Dev 3.731
Entry
ampl
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6
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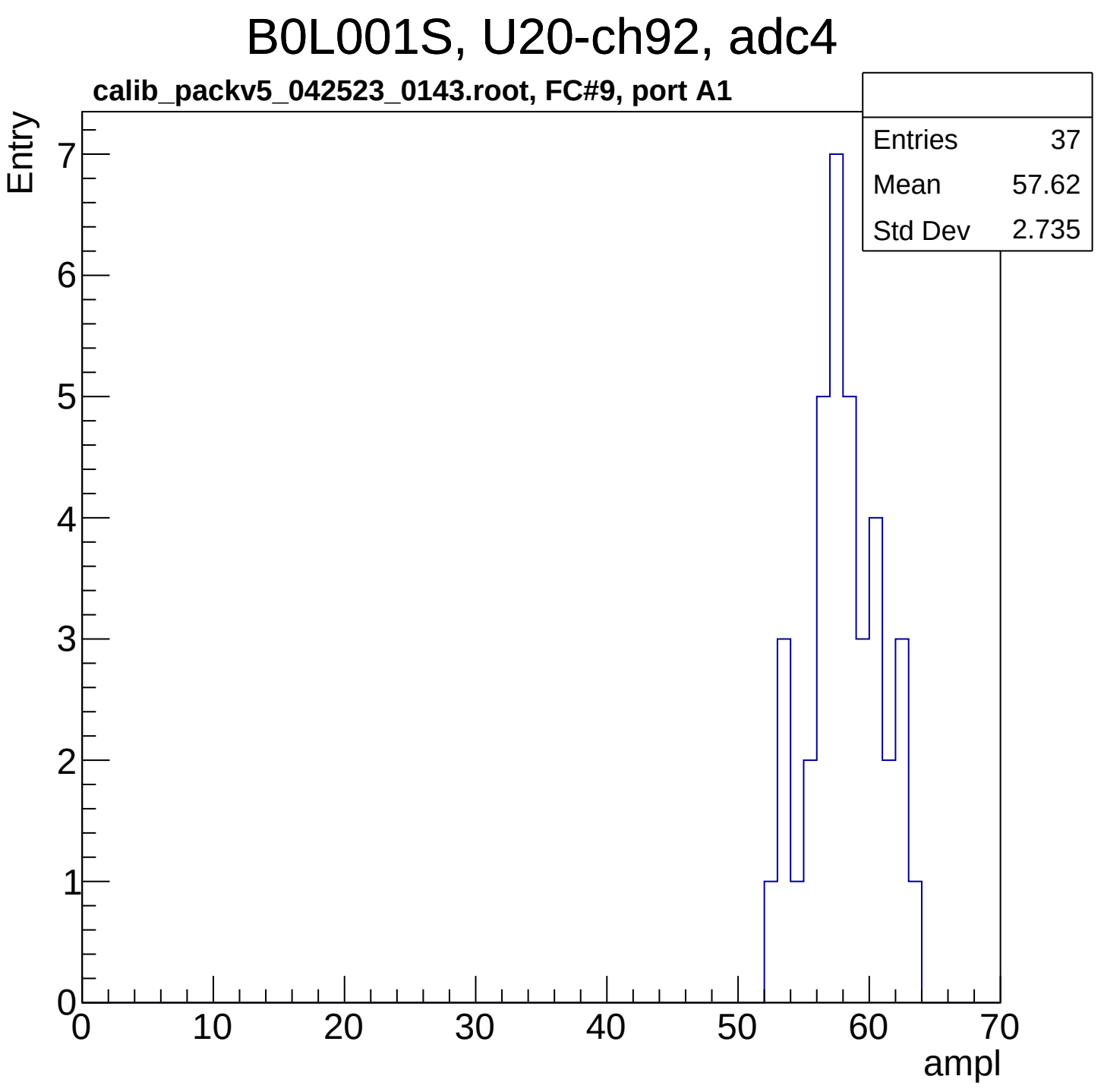
B0L001S, U20-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 57.62
Std Dev 2.735
Entry
ampl
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3
4
5
6
7
0
10
20
30
40
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60
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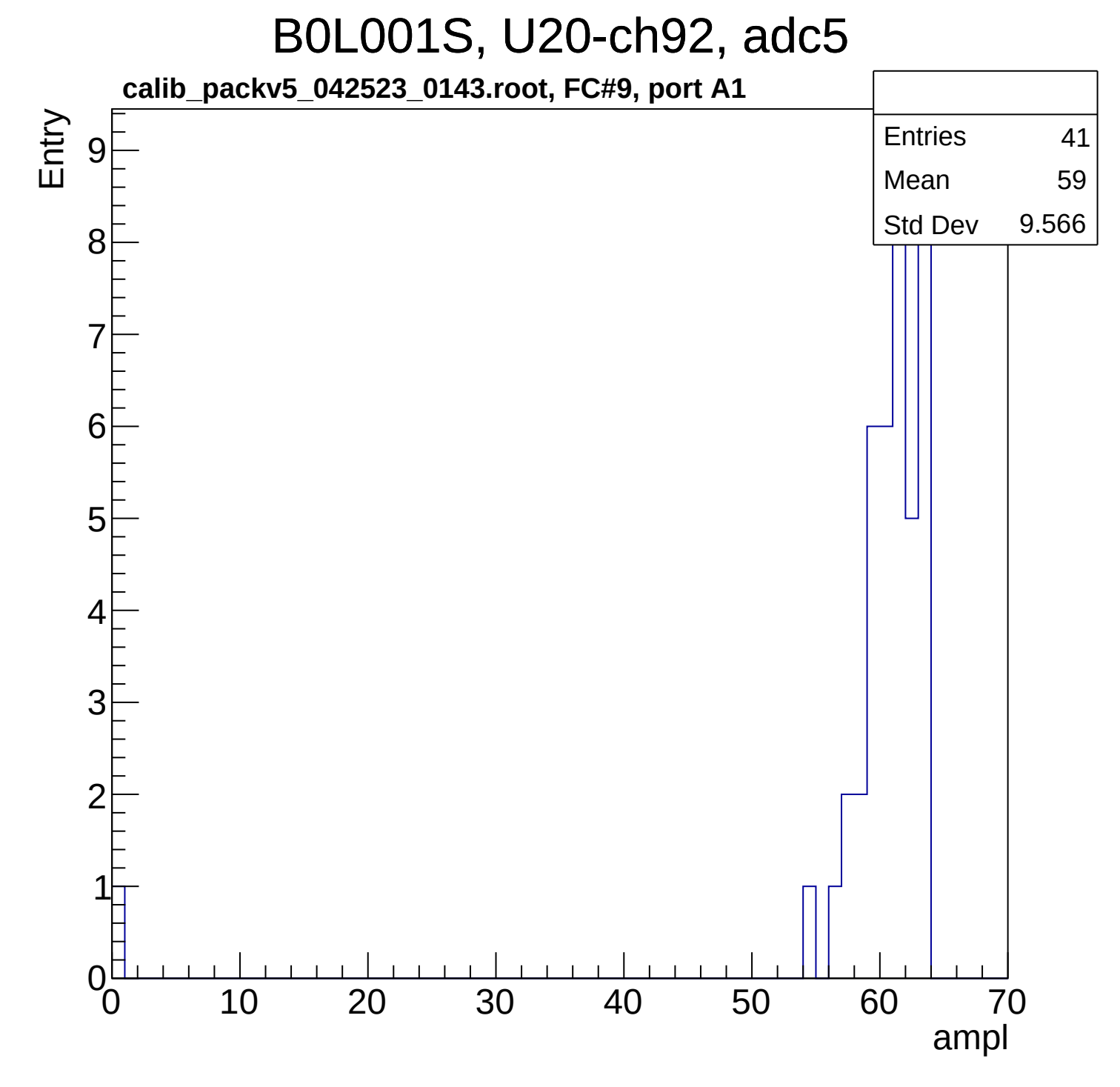
B0L001S, U20-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59
Std Dev 9.566
Entry
ampl
0
1
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6
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0
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60
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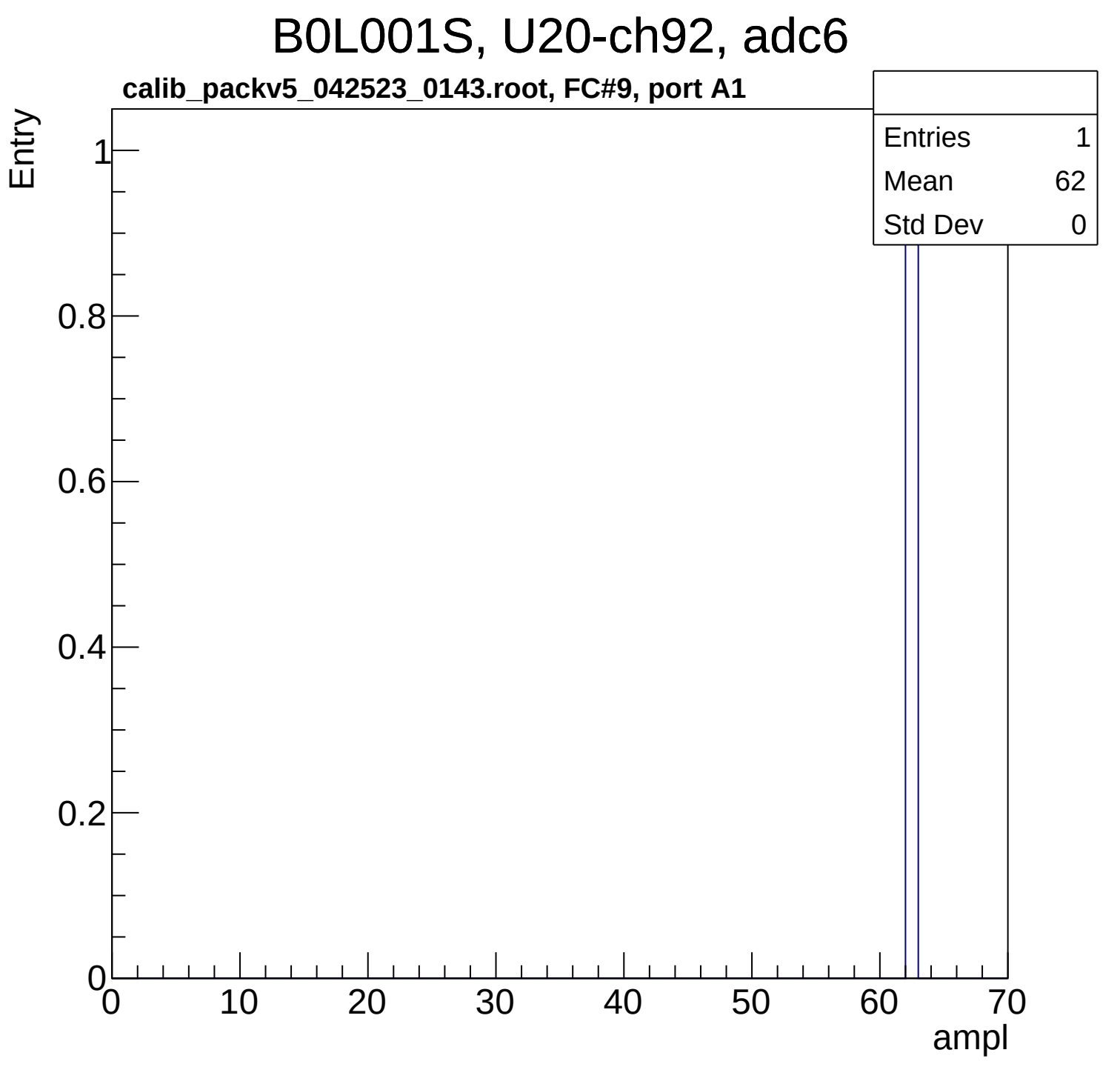
B0L001S, U20-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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50
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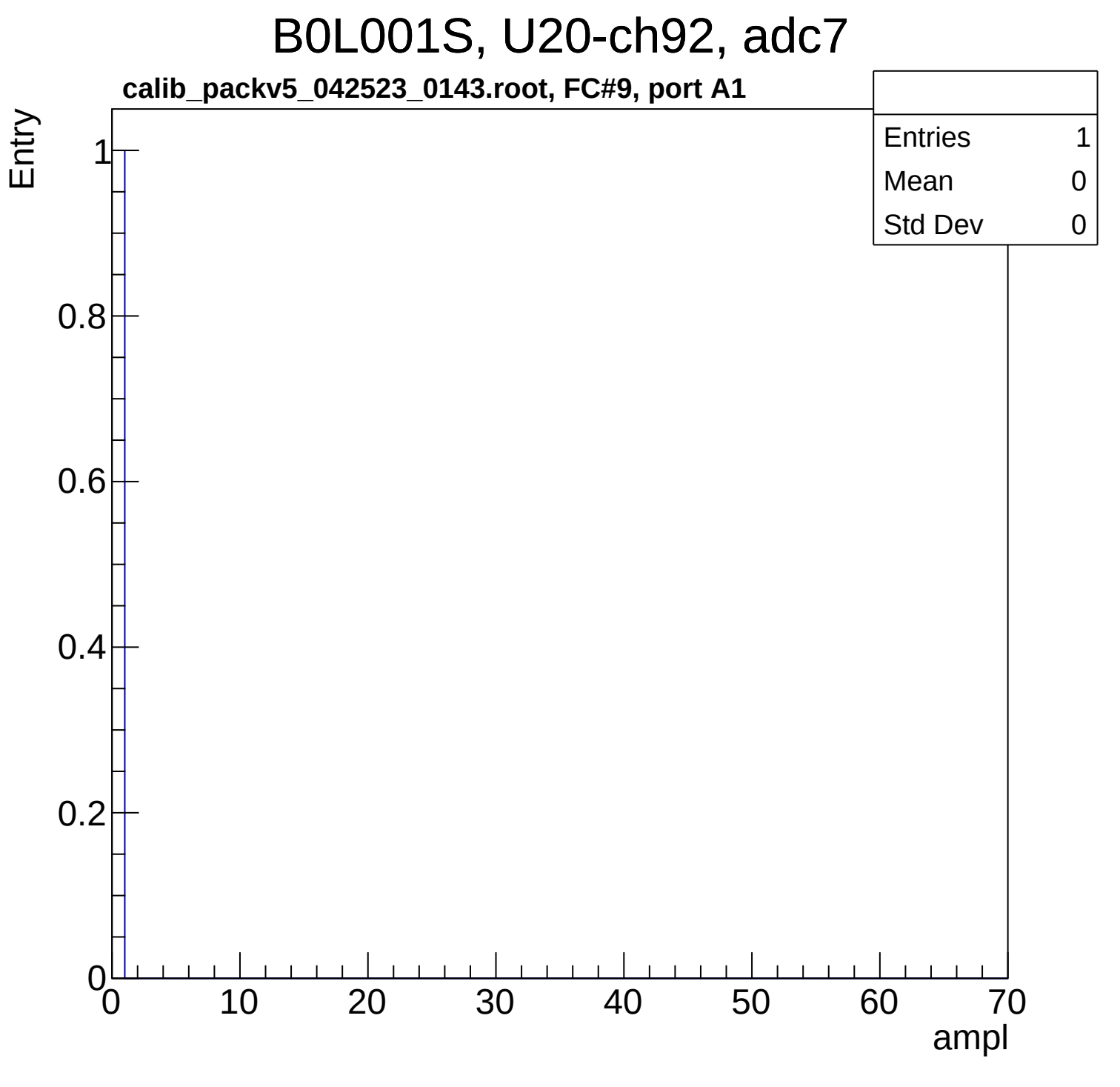
70
ampl


B0L001S, U20-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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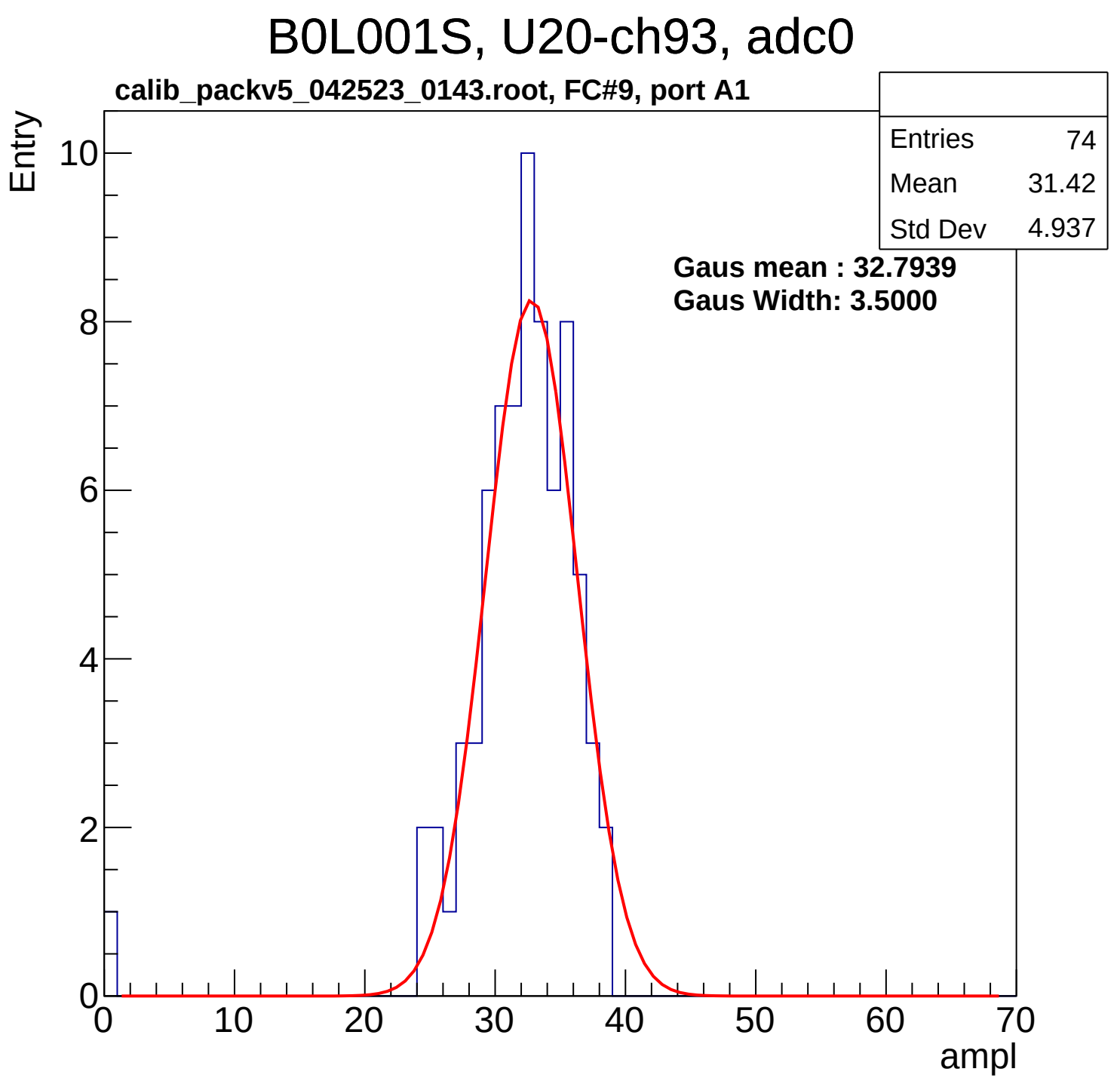
50
60
70
ampl


B0L001S, U20-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 31.42
Std Dev 4.937
Gaus mean : 32.7939
Gaus Width: 3.5000
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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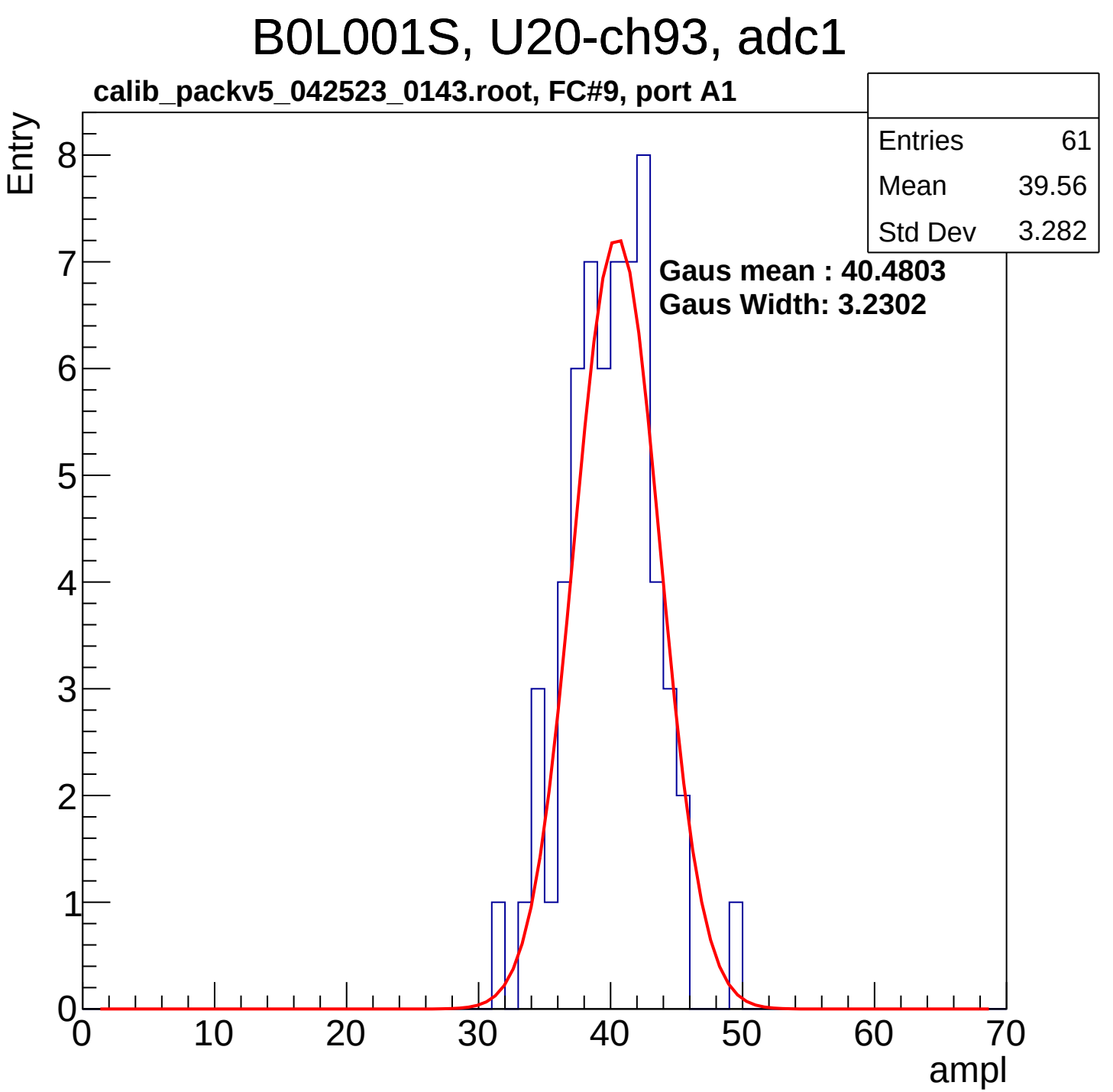
B0L001S, U20-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 39.56
Std Dev 3.282
Gaus mean : 40.4803
Gaus Width: 3.2302
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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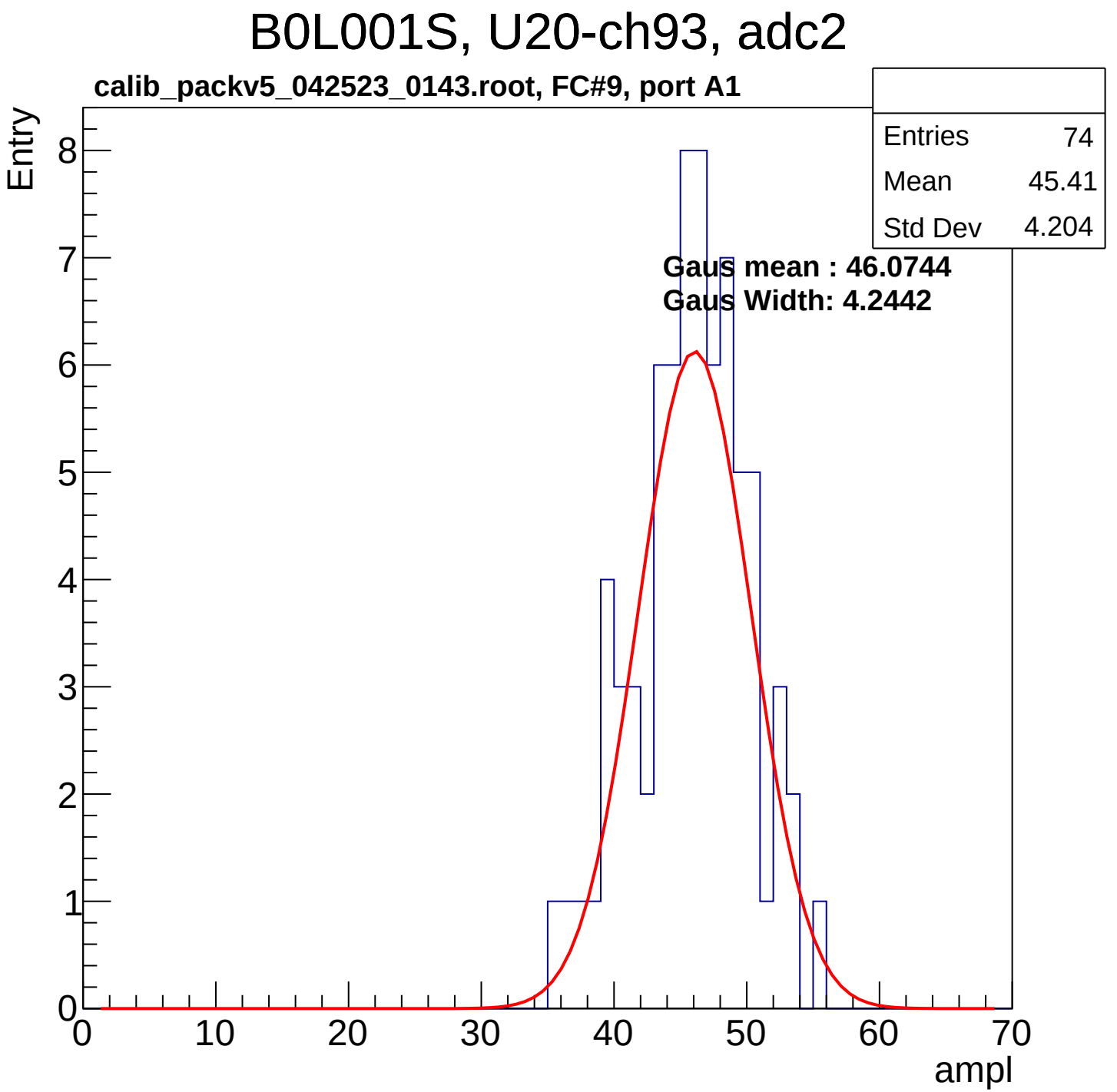
B0L001S, U20-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 45.41
Std Dev 4.204
Gaus mean : 46.0744
Gaus Width: 4.2442
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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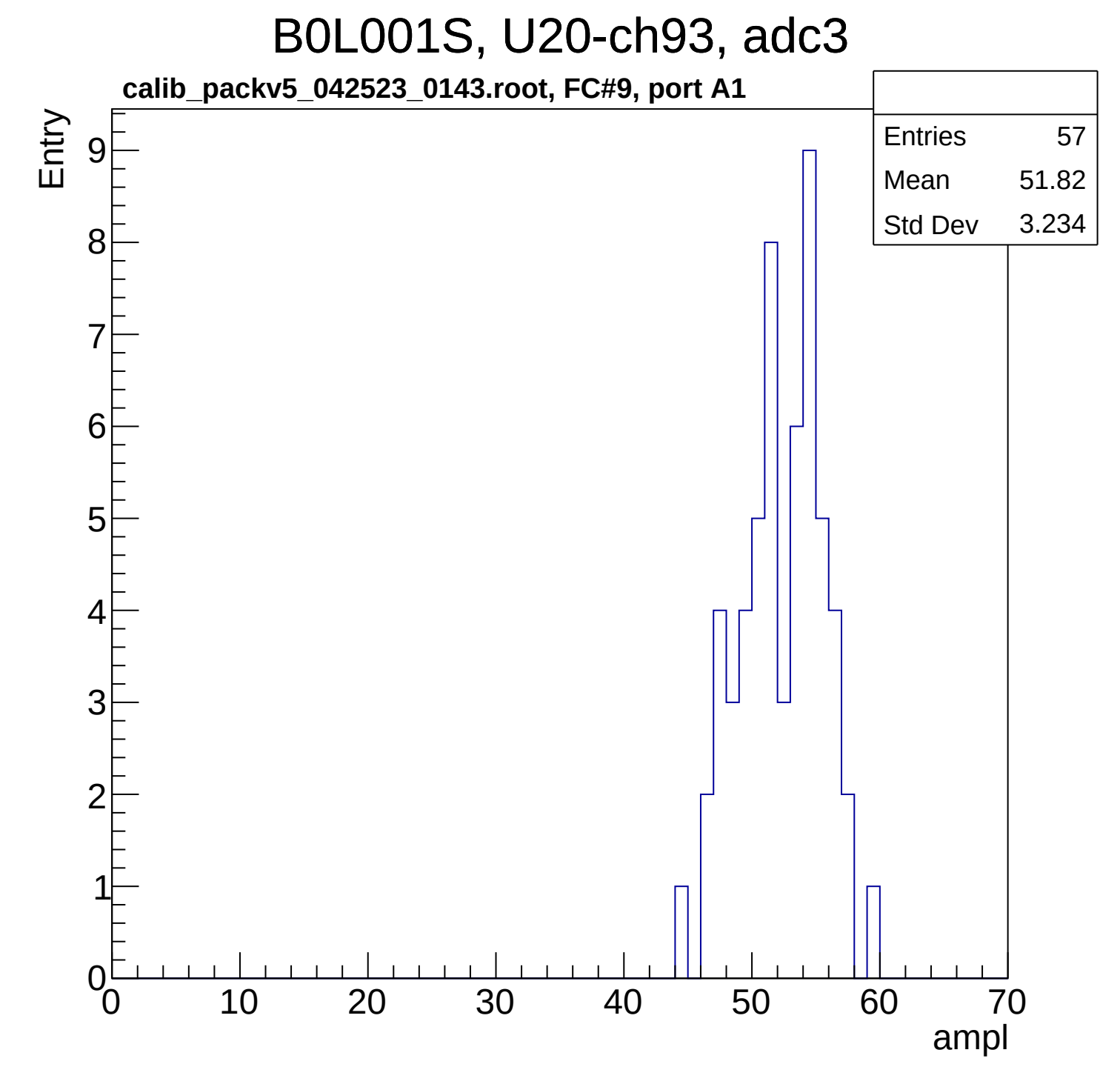
B0L001S, U20-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.82
Std Dev 3.234
Entry
ampl
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2
3
4
5
6
7
8
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0
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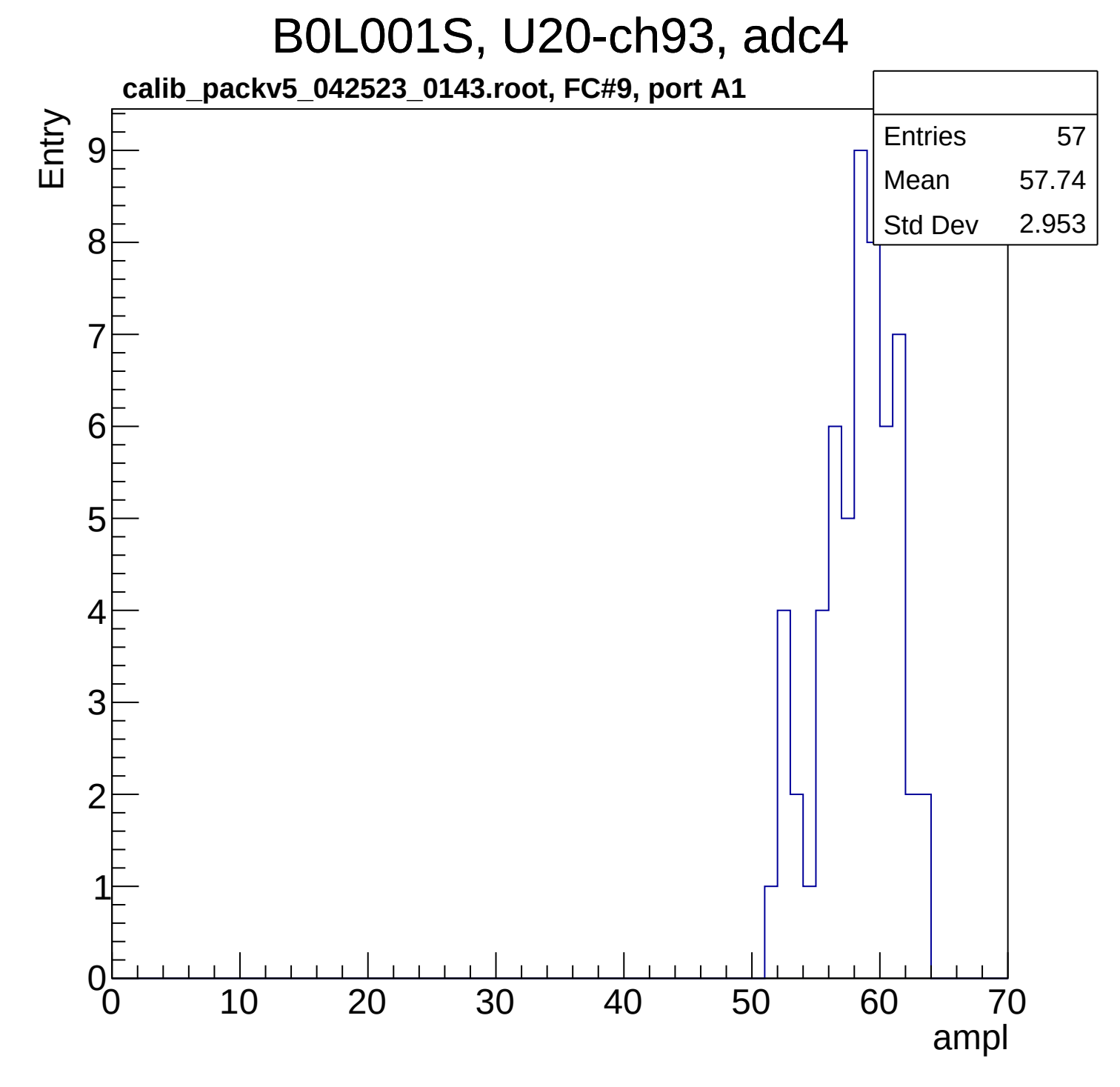
B0L001S, U20-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.74
Std Dev 2.953
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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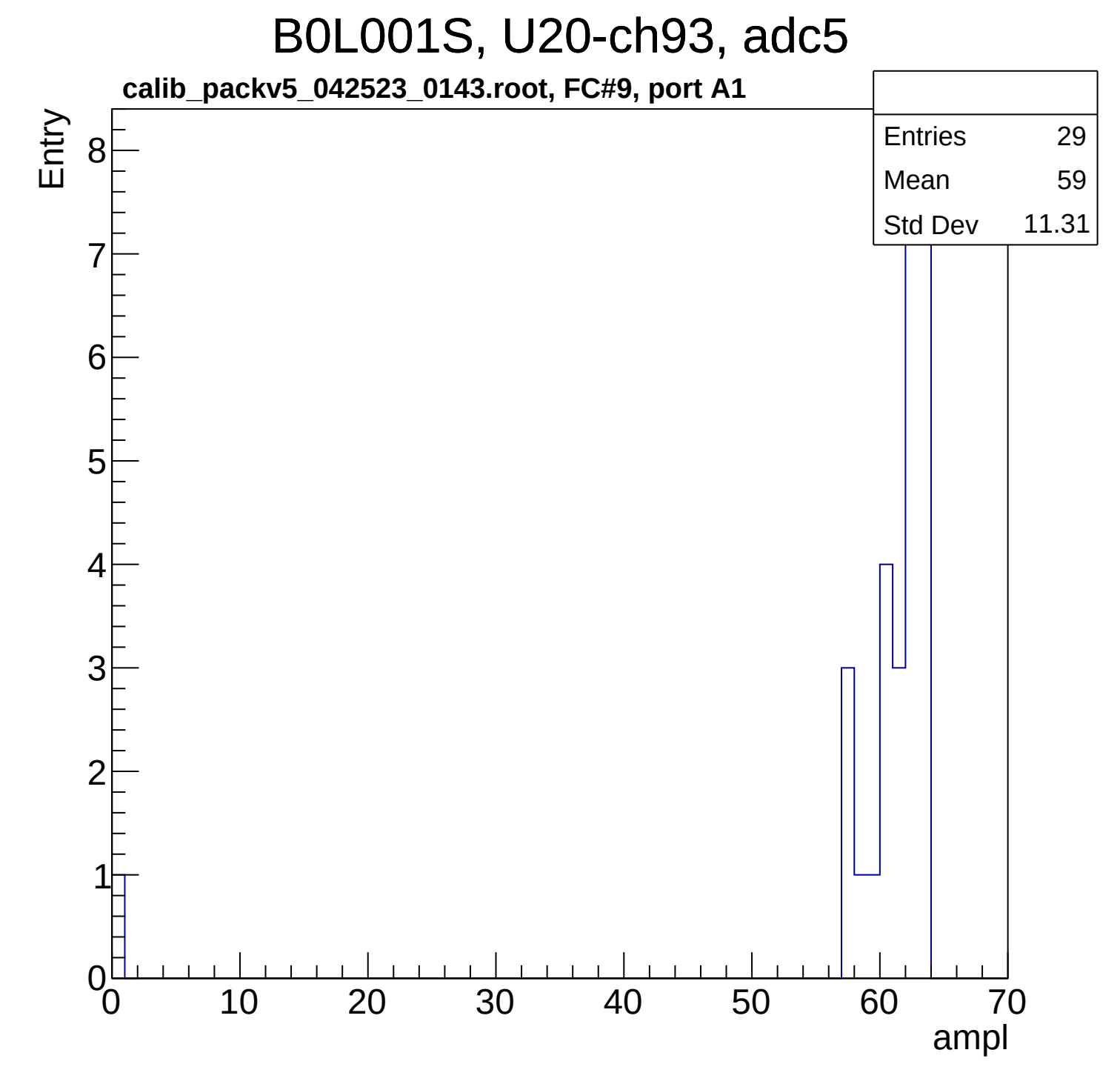
B0L001S, U20-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59
Std Dev 11.31
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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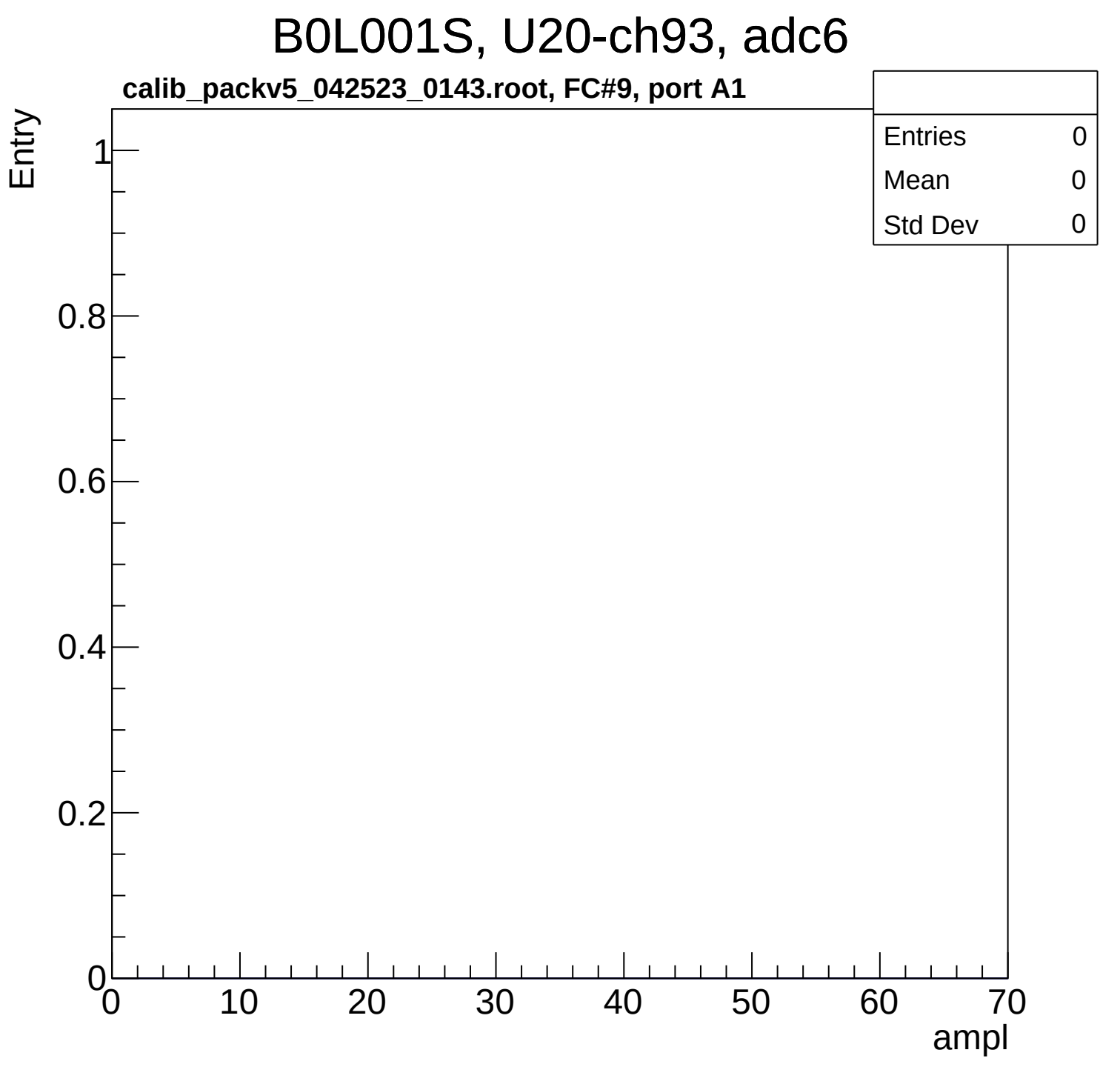
B0L001S, U20-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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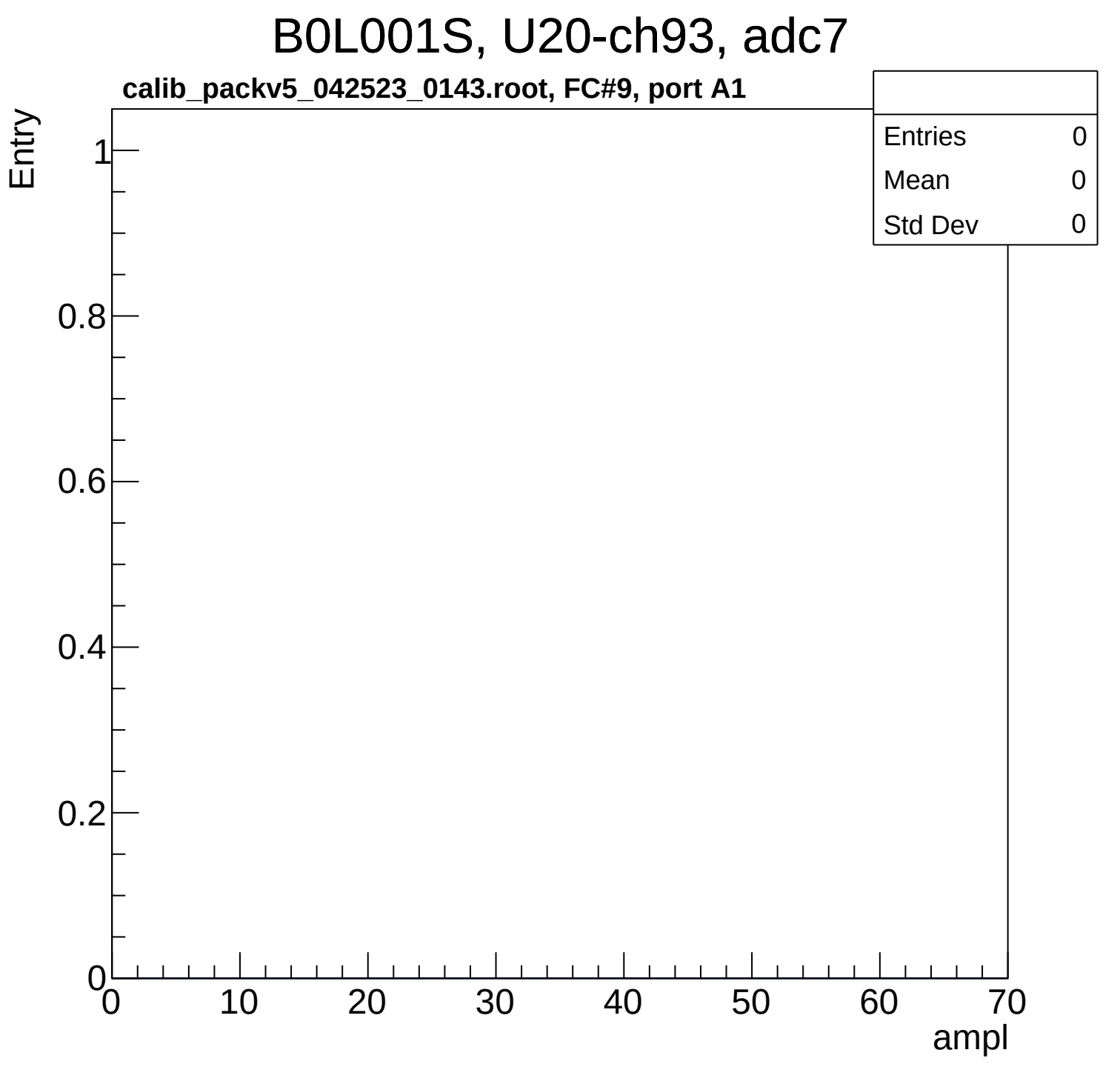
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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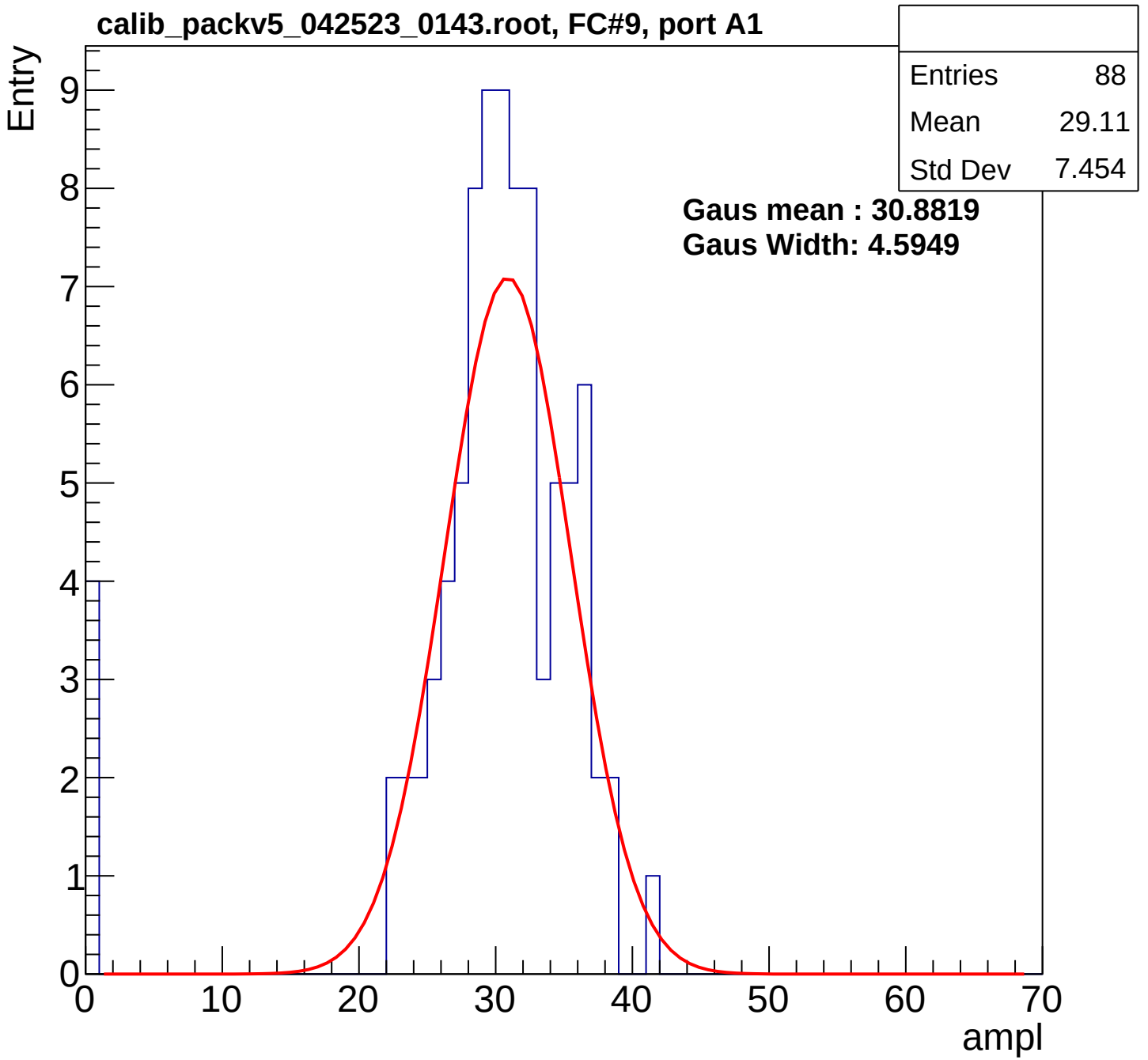
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 29.11
Std Dev 7.454
Gaus mean : 30.8819
Gaus Width: 4.5949
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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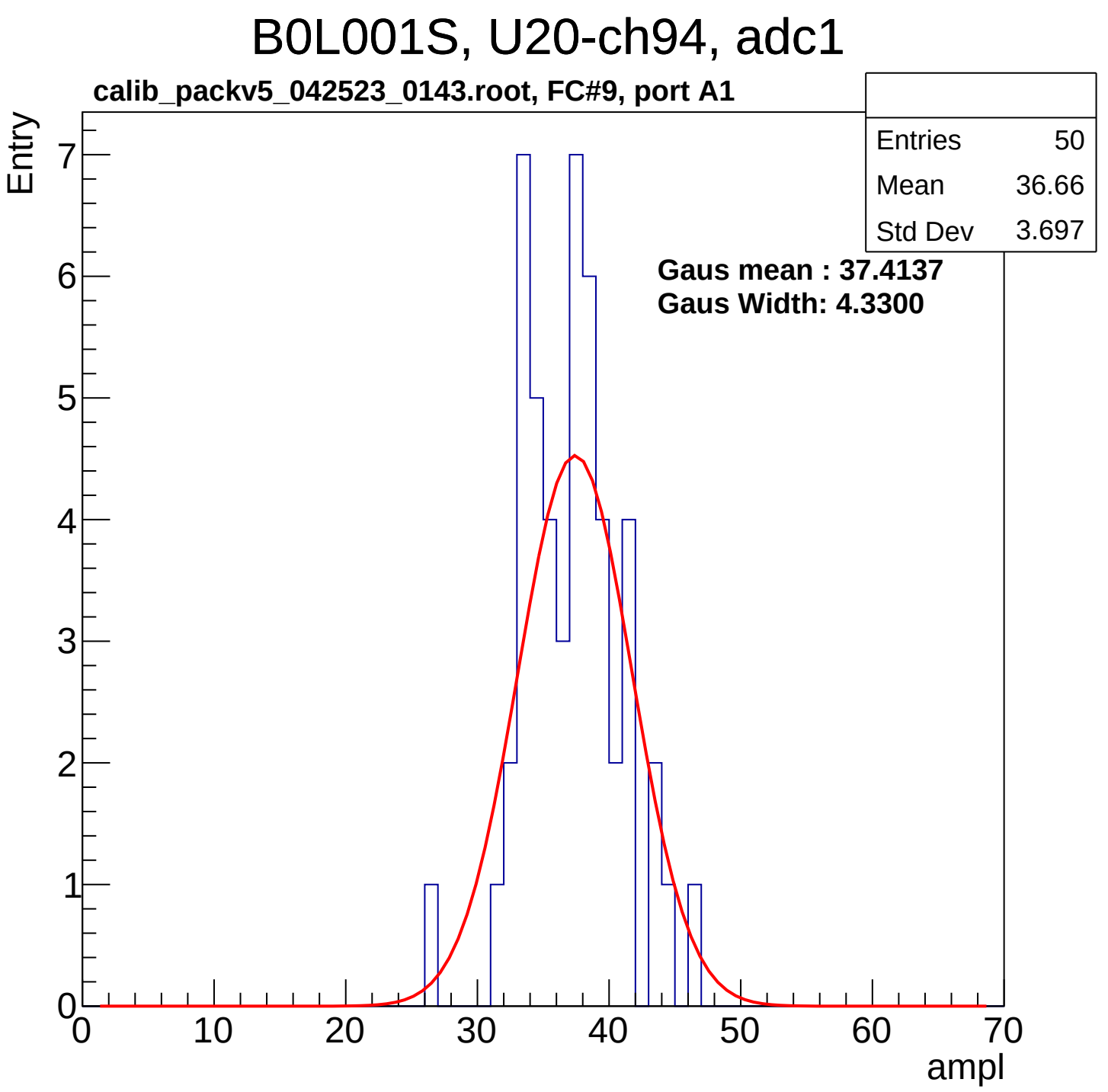
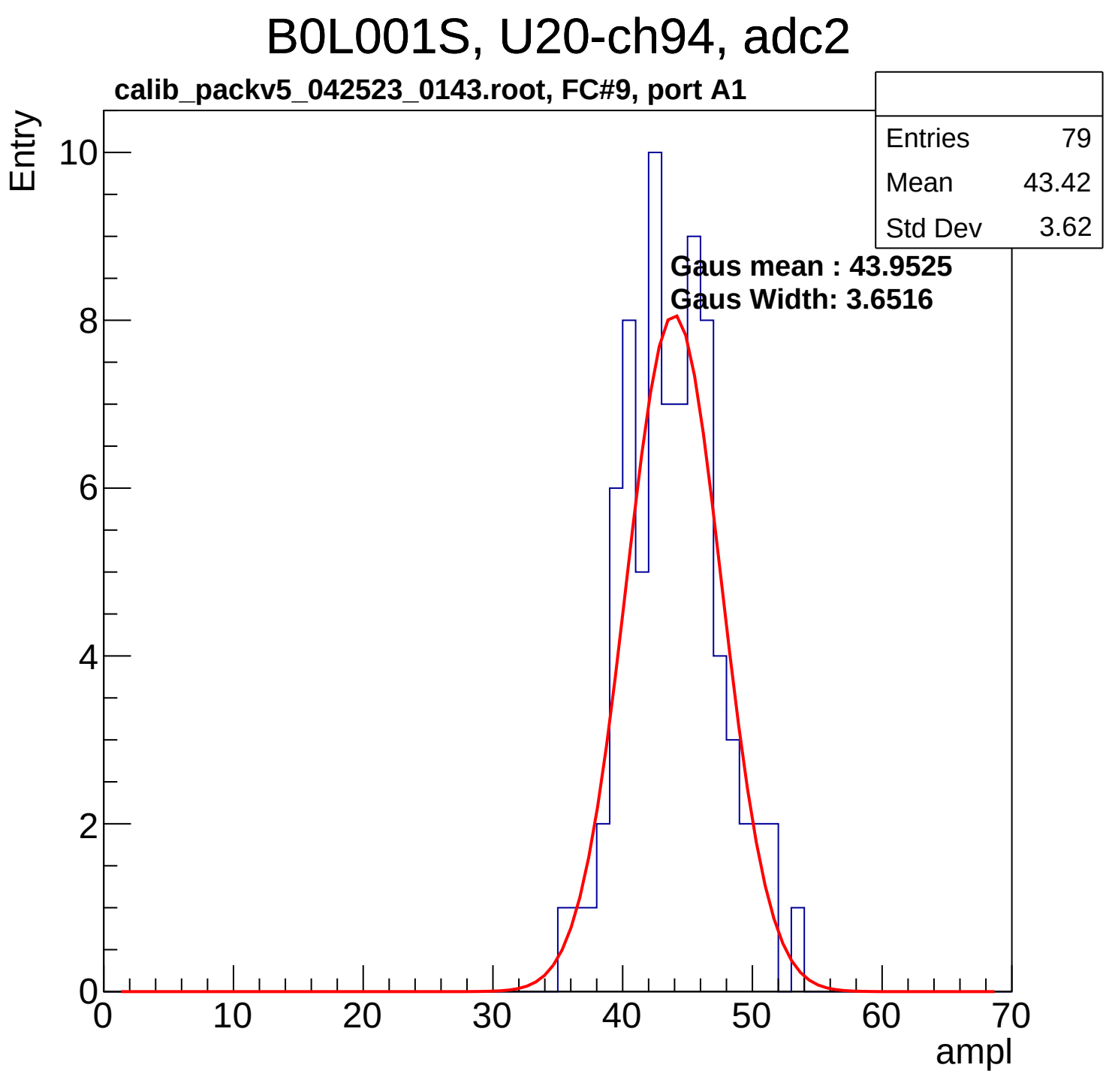

B0L001S, U20-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 36.66
Std Dev 3.697
Gaus mean : 37.4137
Gaus Width: 4.3300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 43.42
Std Dev 3.62
Gaus mean : 43.9525
Gaus Width: 3.6516
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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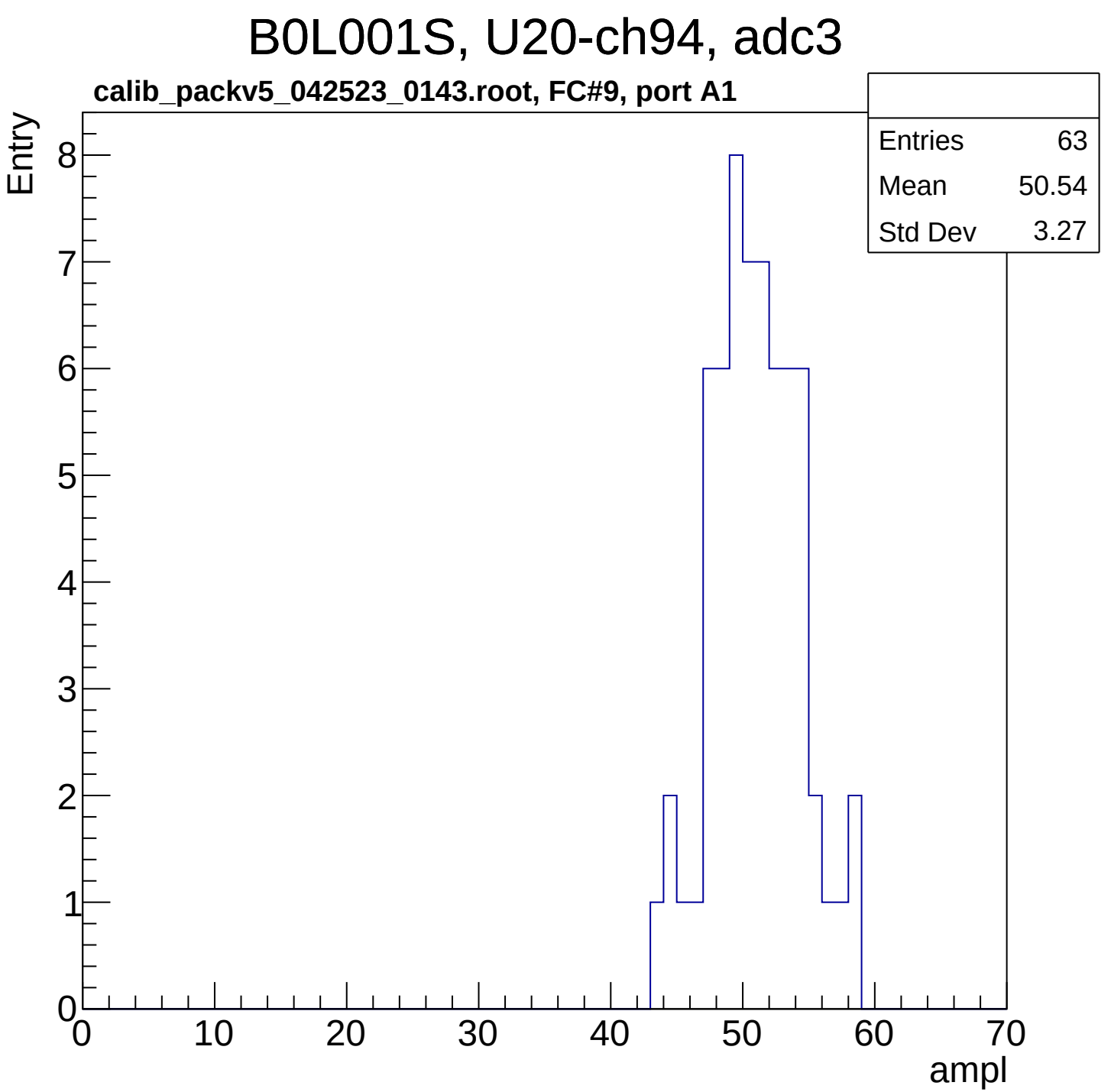
B0L001S, U20-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.54
Std Dev 3.27
Entry
ampl
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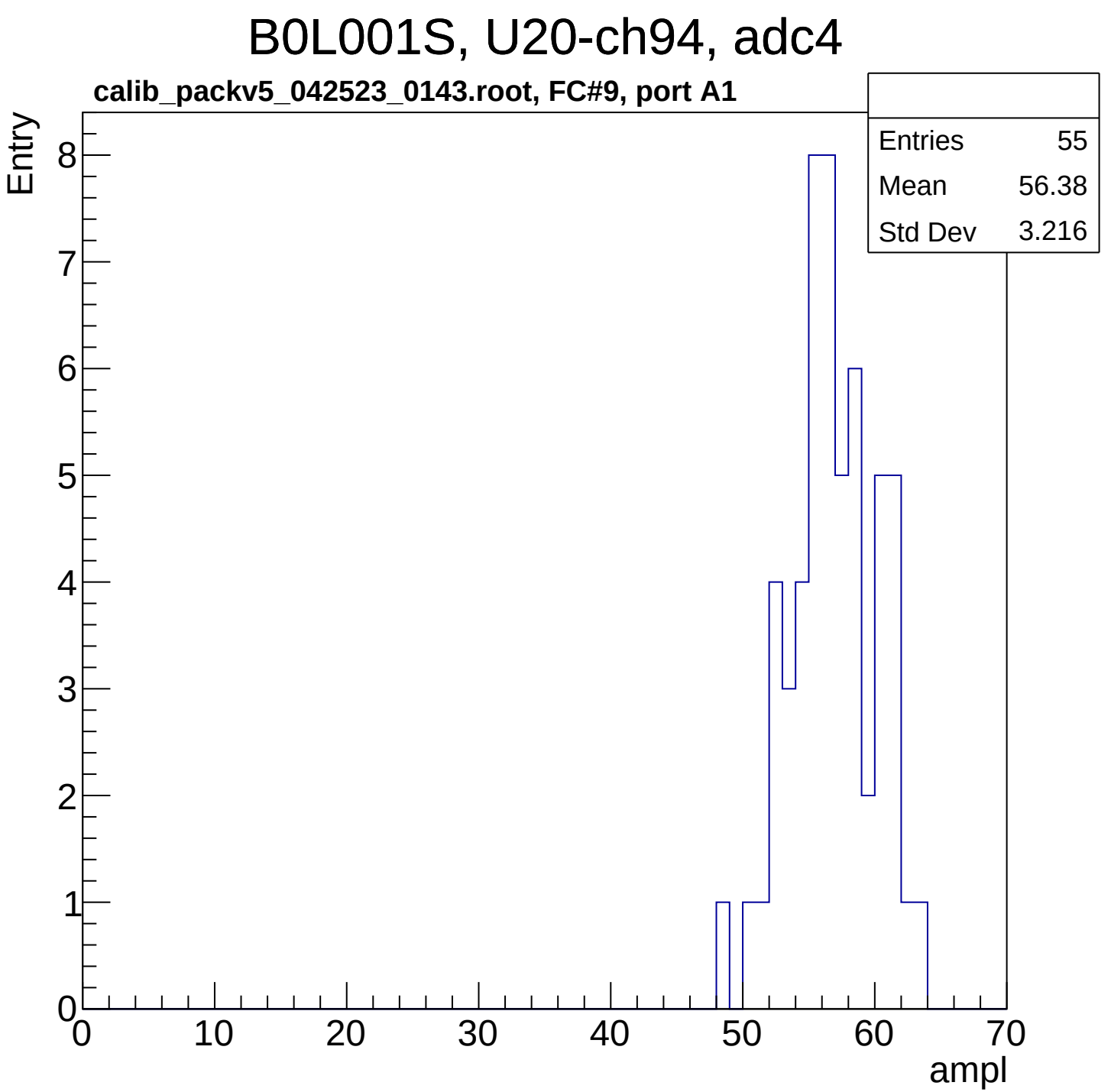
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.38
Std Dev 3.216
Entry
ampl
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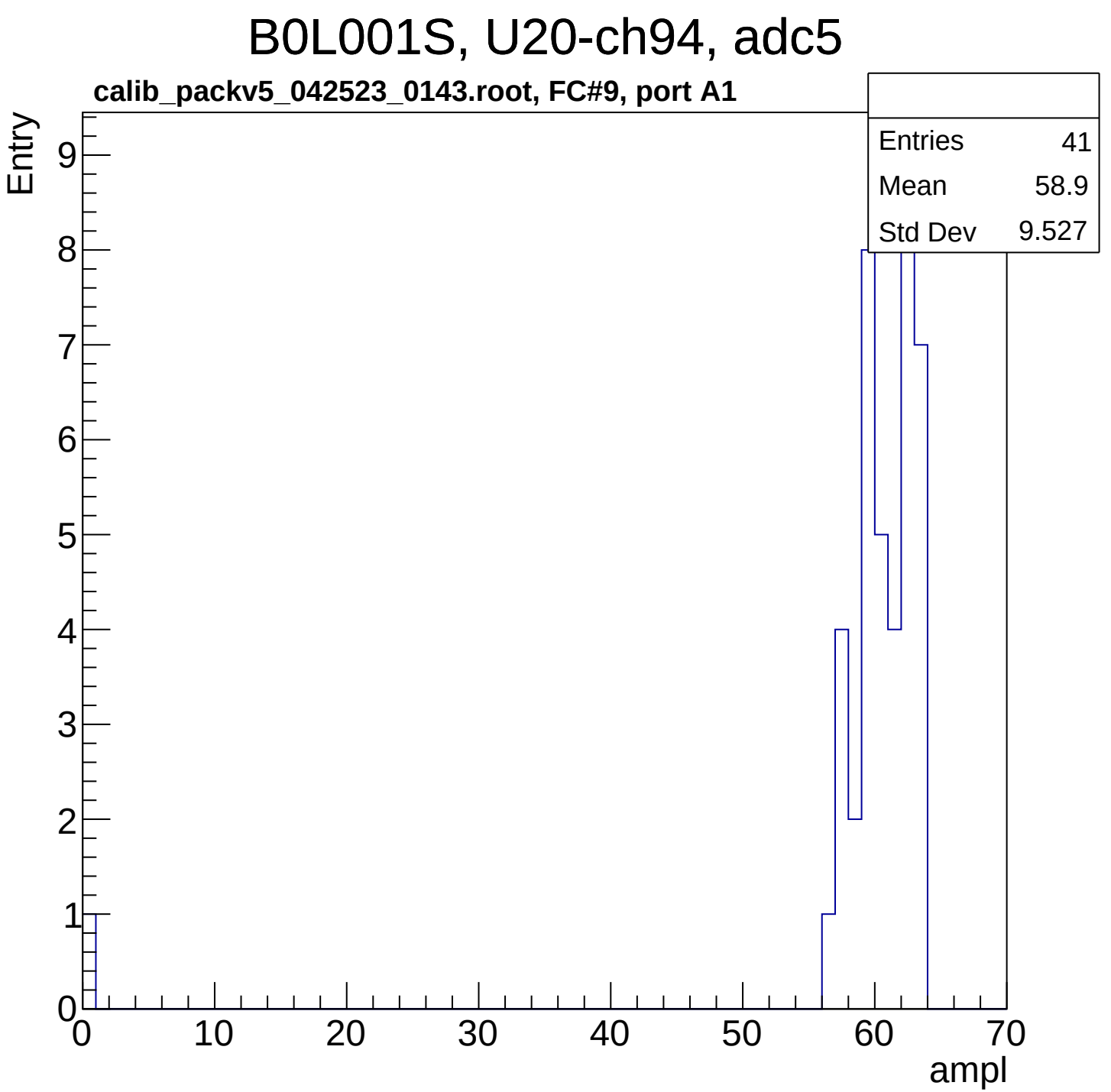
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.9
Std Dev 9.527
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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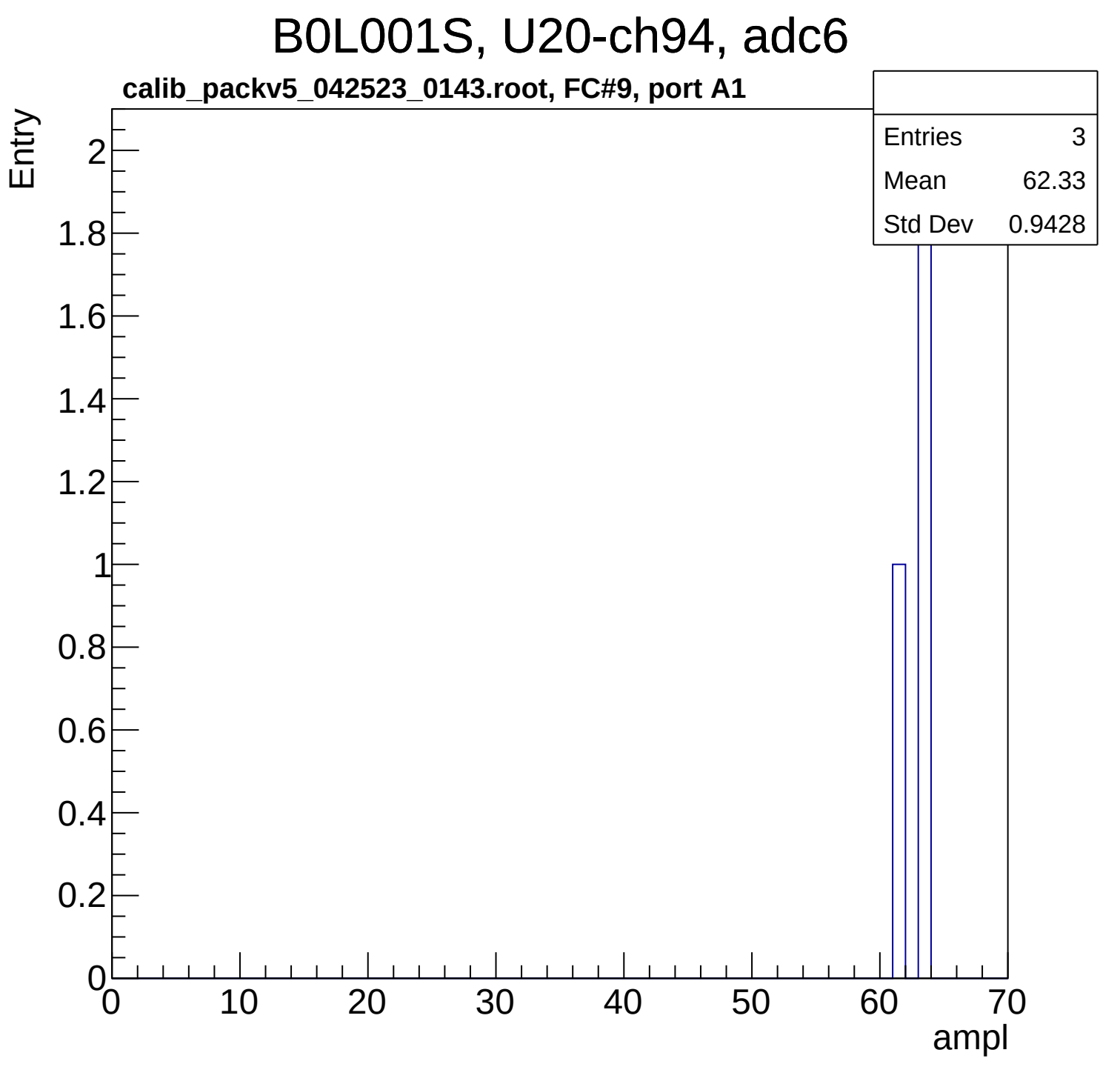
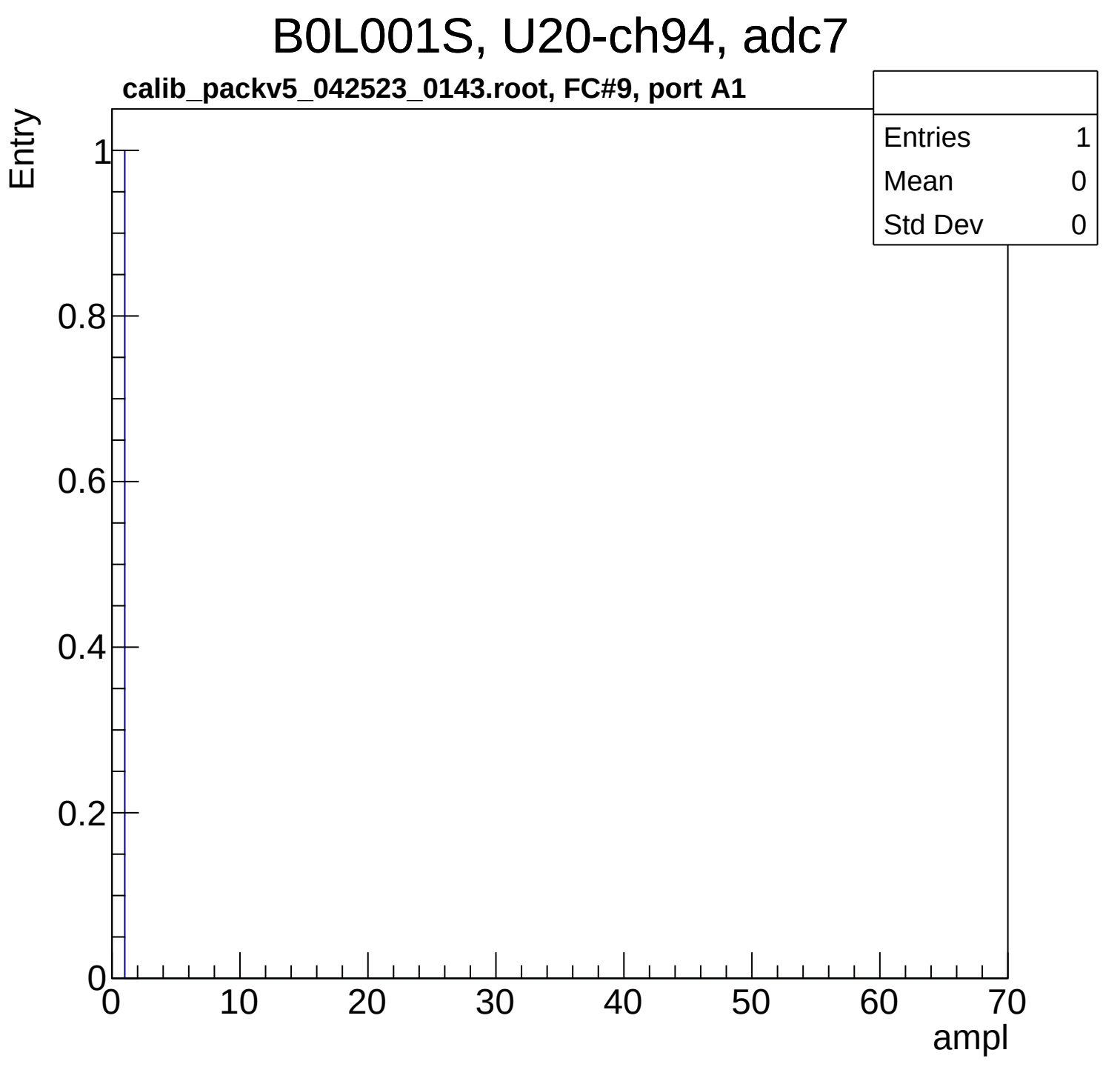
40
50
60
70


B0L001S, U20-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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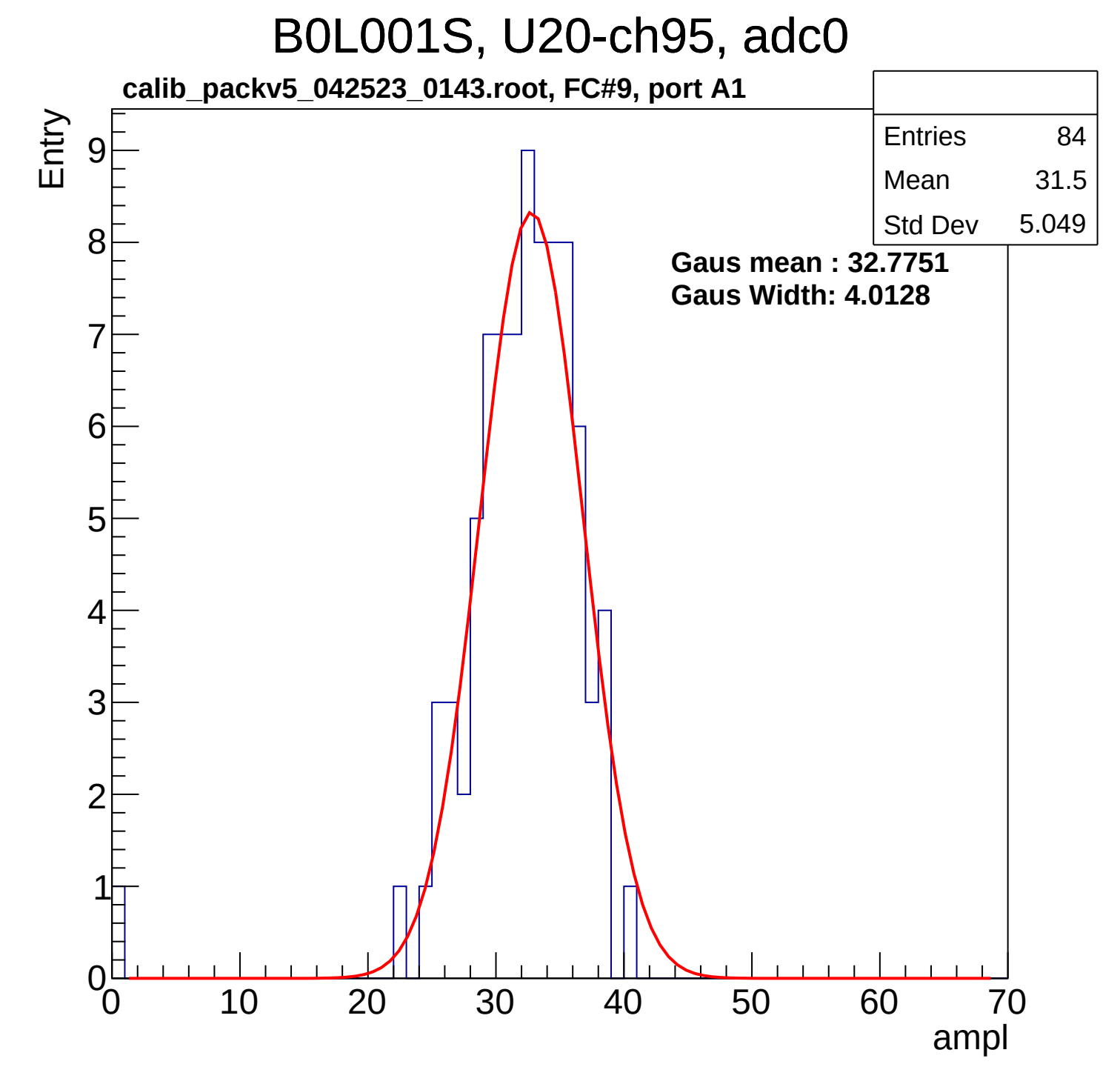
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 31.5
Std Dev 5.049
Gaus mean : 32.7751
Gaus Width: 4.0128
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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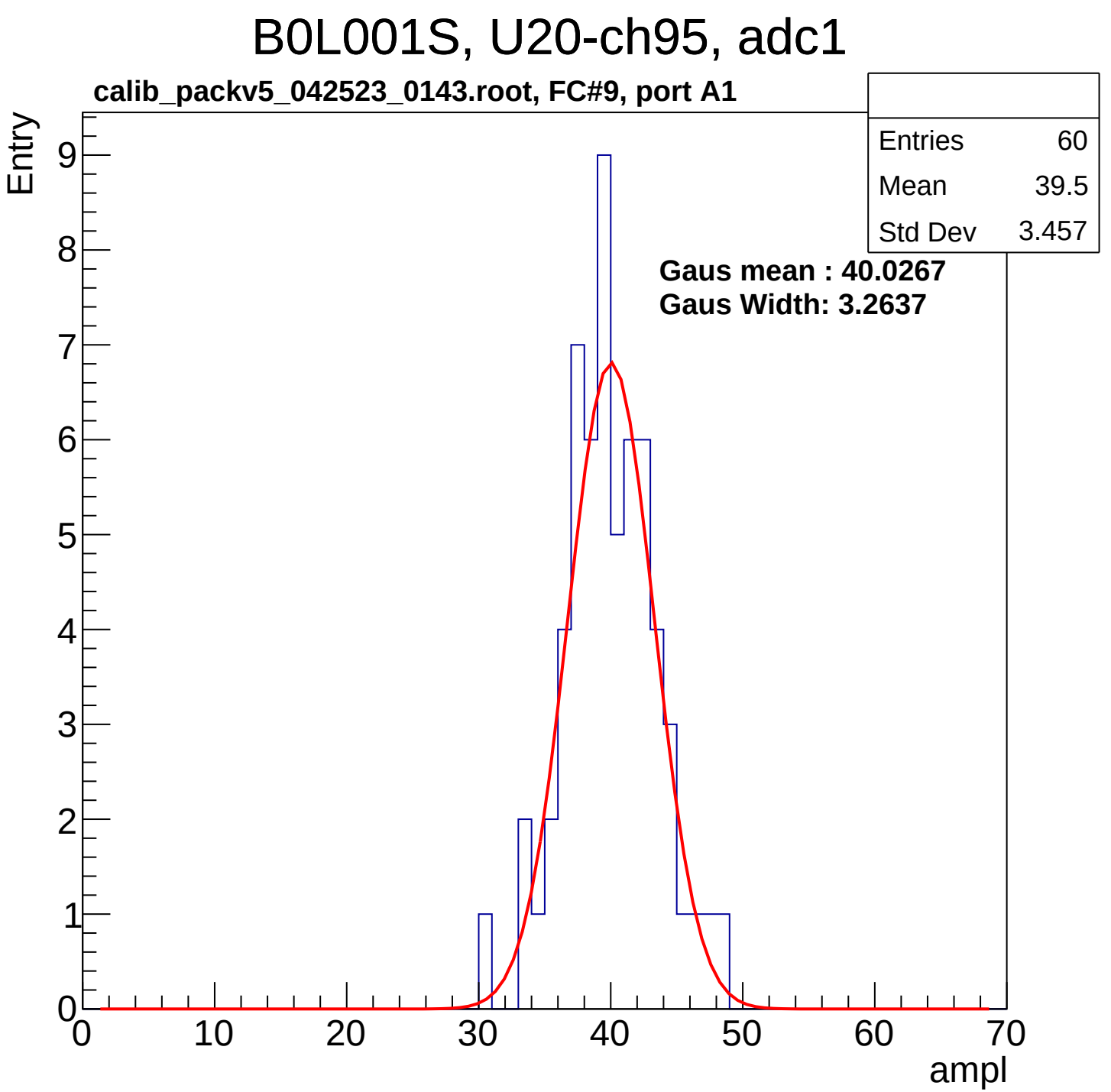
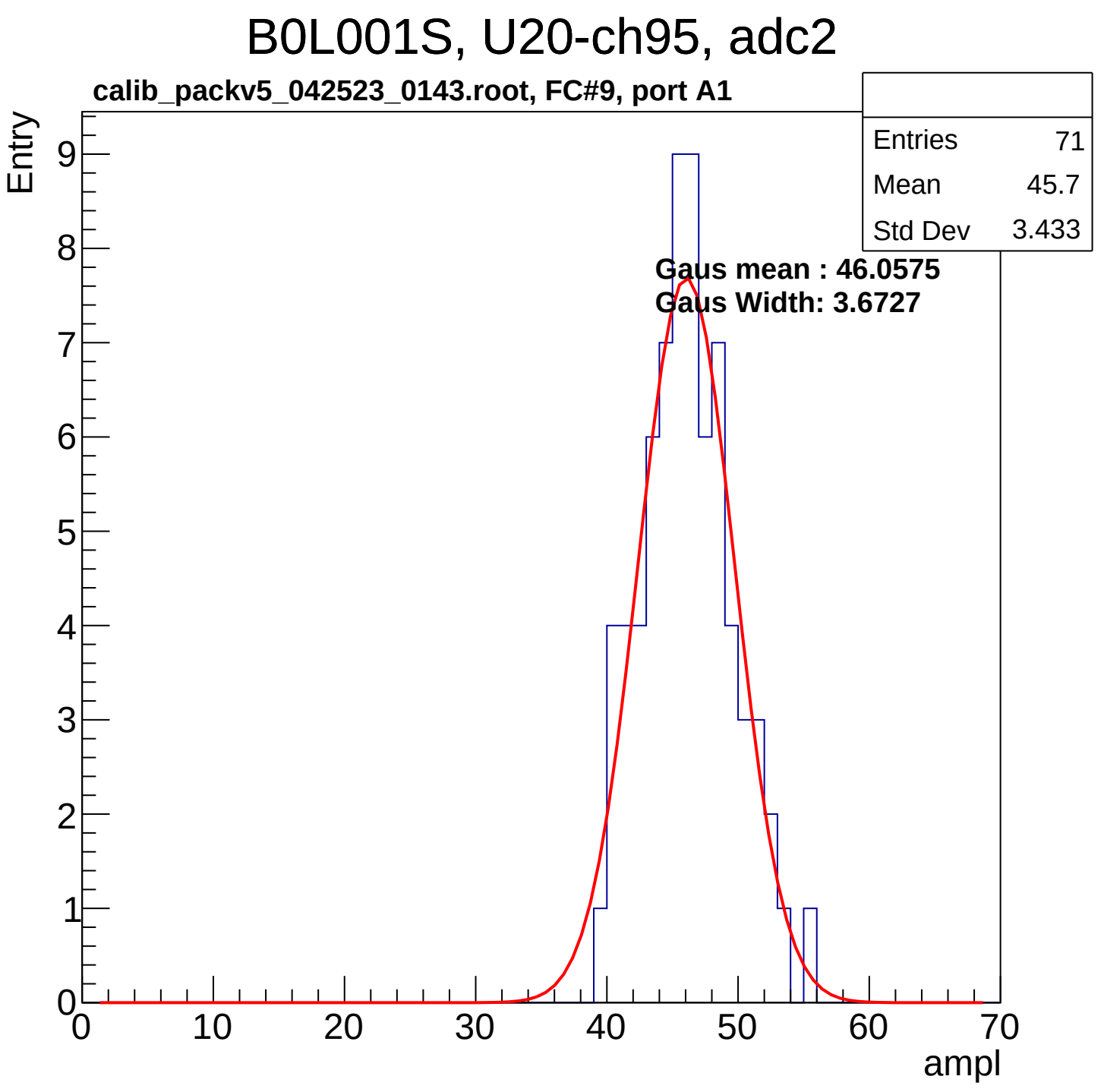
60
70


B0L001S, U20-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 39.5
Std Dev 3.457
Gaus mean : 40.0267
Gaus Width: 3.2637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.7
Std Dev 3.433
Gaus mean : 46.0575
Gaus Width: 3.6727
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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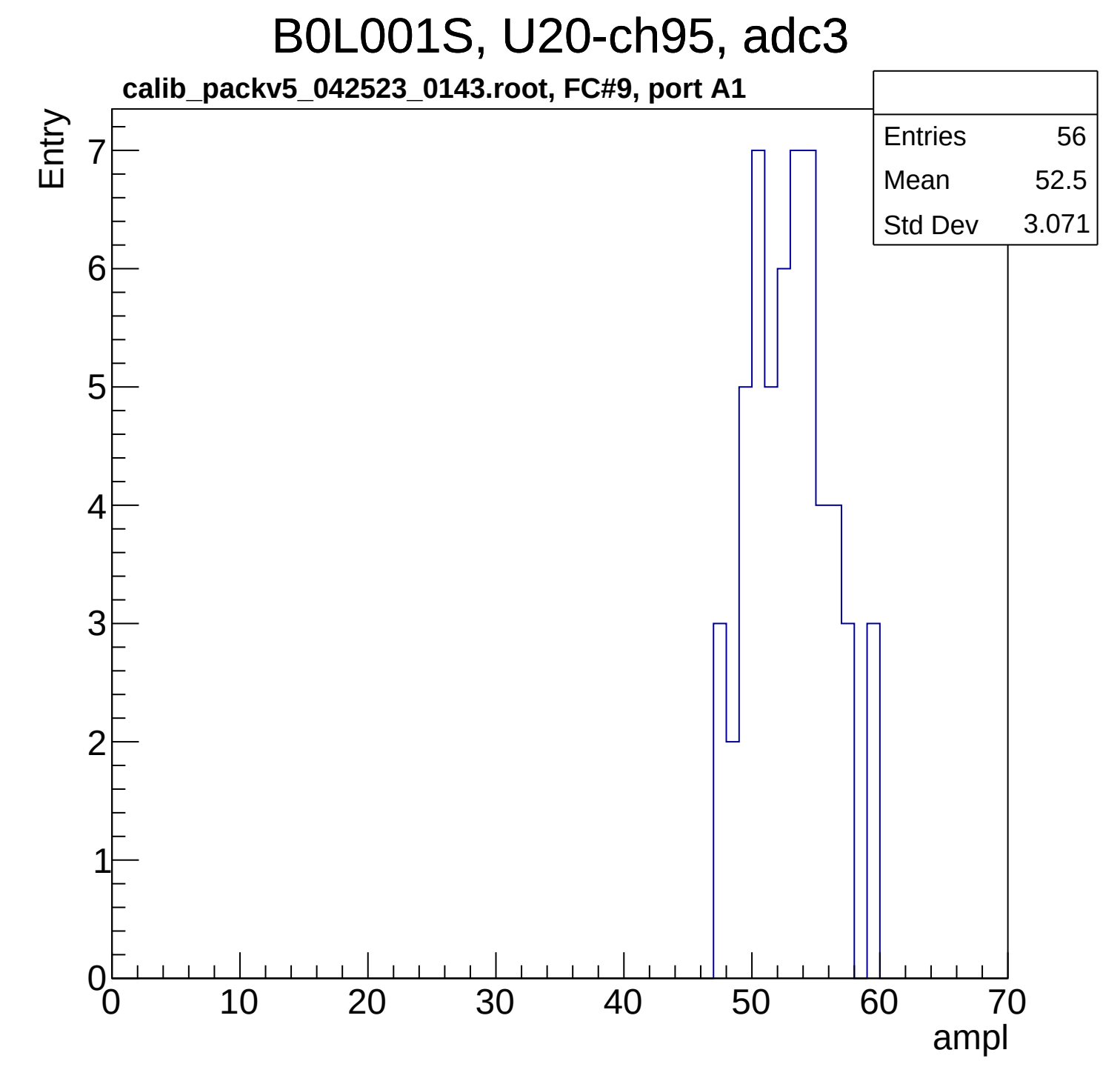
B0L001S, U20-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 52.5
Std Dev 3.071
Entry
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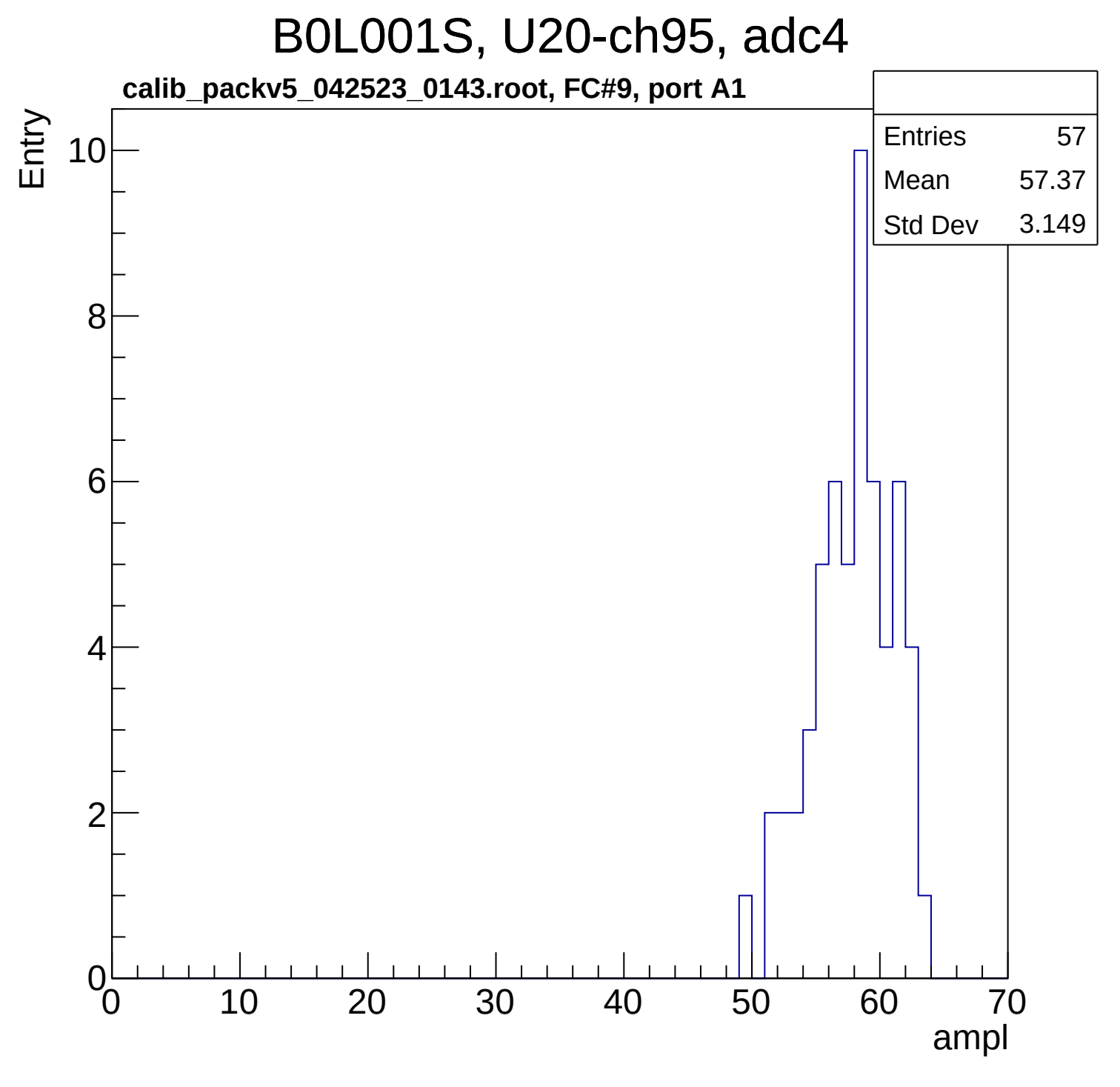
50
60
70
ampl


B0L001S, U20-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.37
Std Dev 3.149
Entry
ampl
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2
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8
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0
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20
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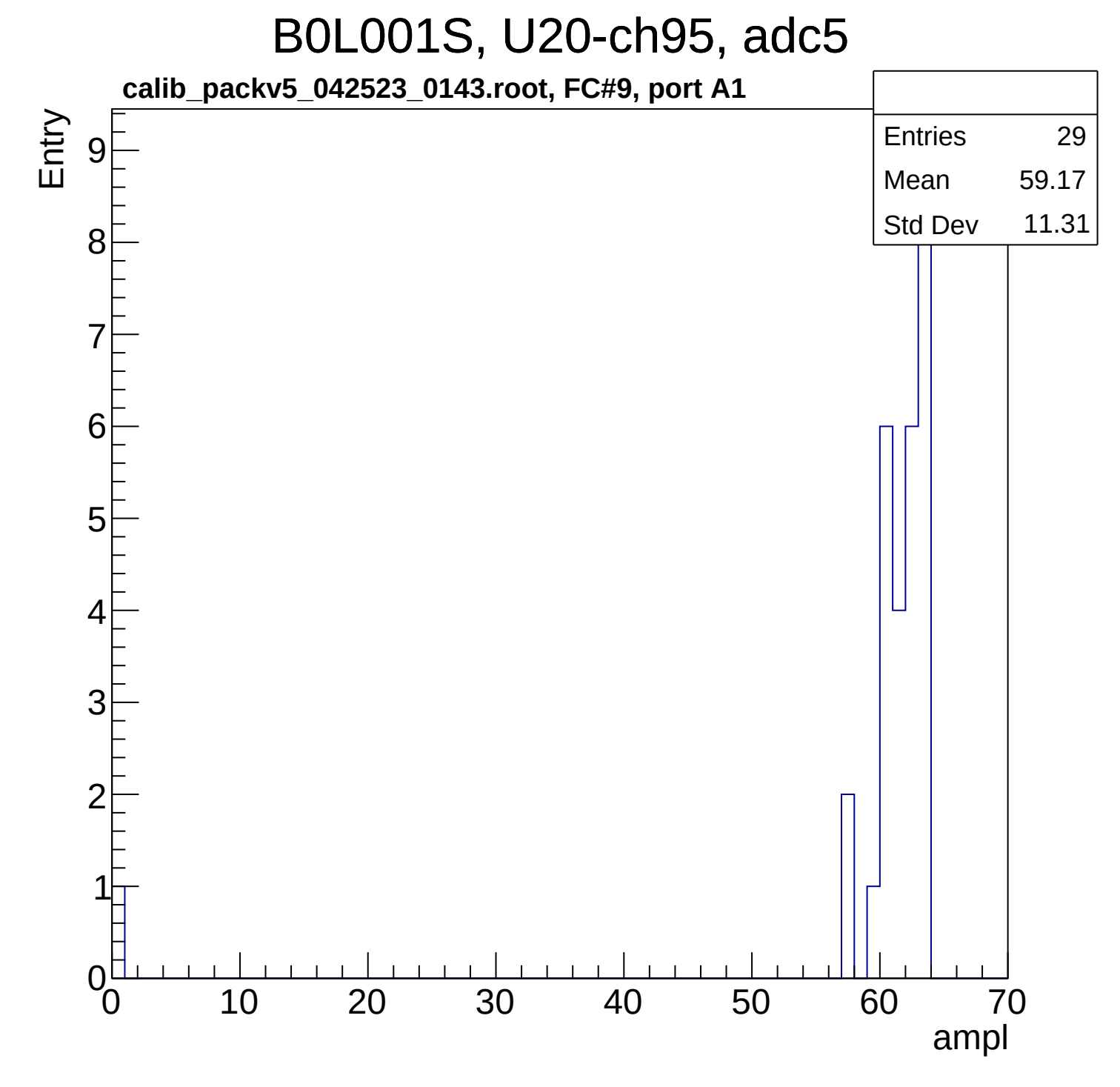
40
50
60
70


B0L001S, U20-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59.17
Std Dev 11.31
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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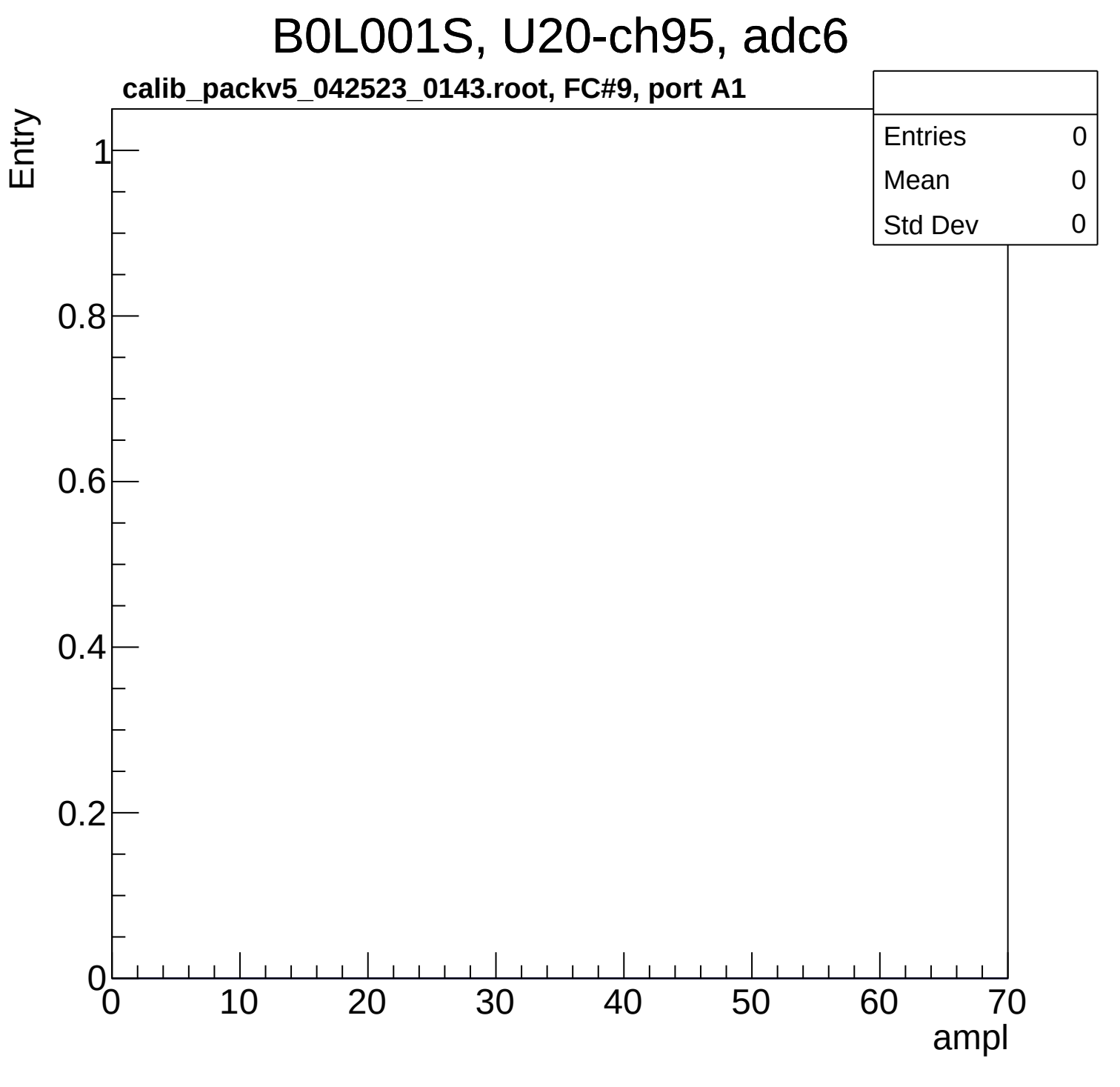
B0L001S, U20-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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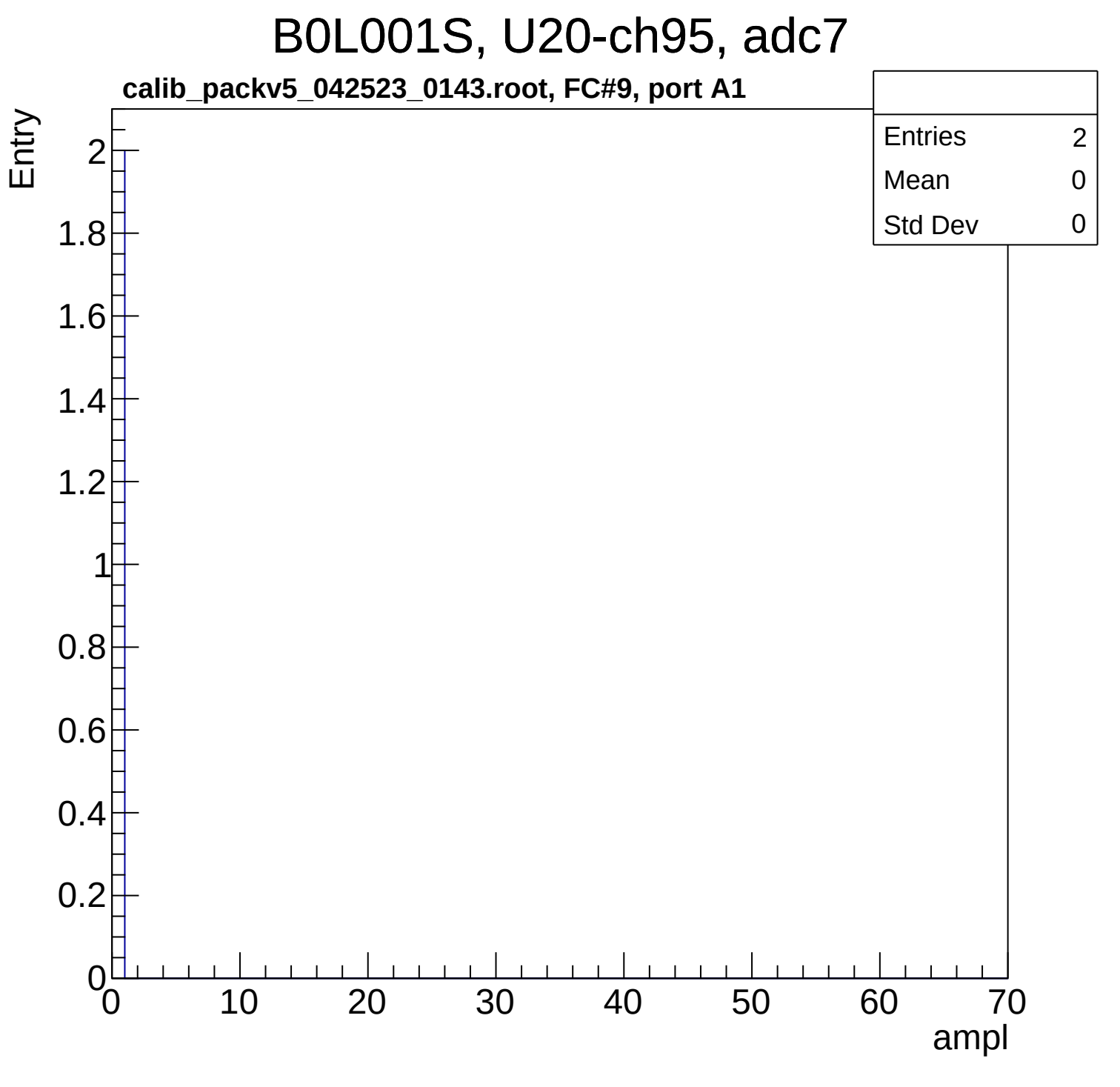
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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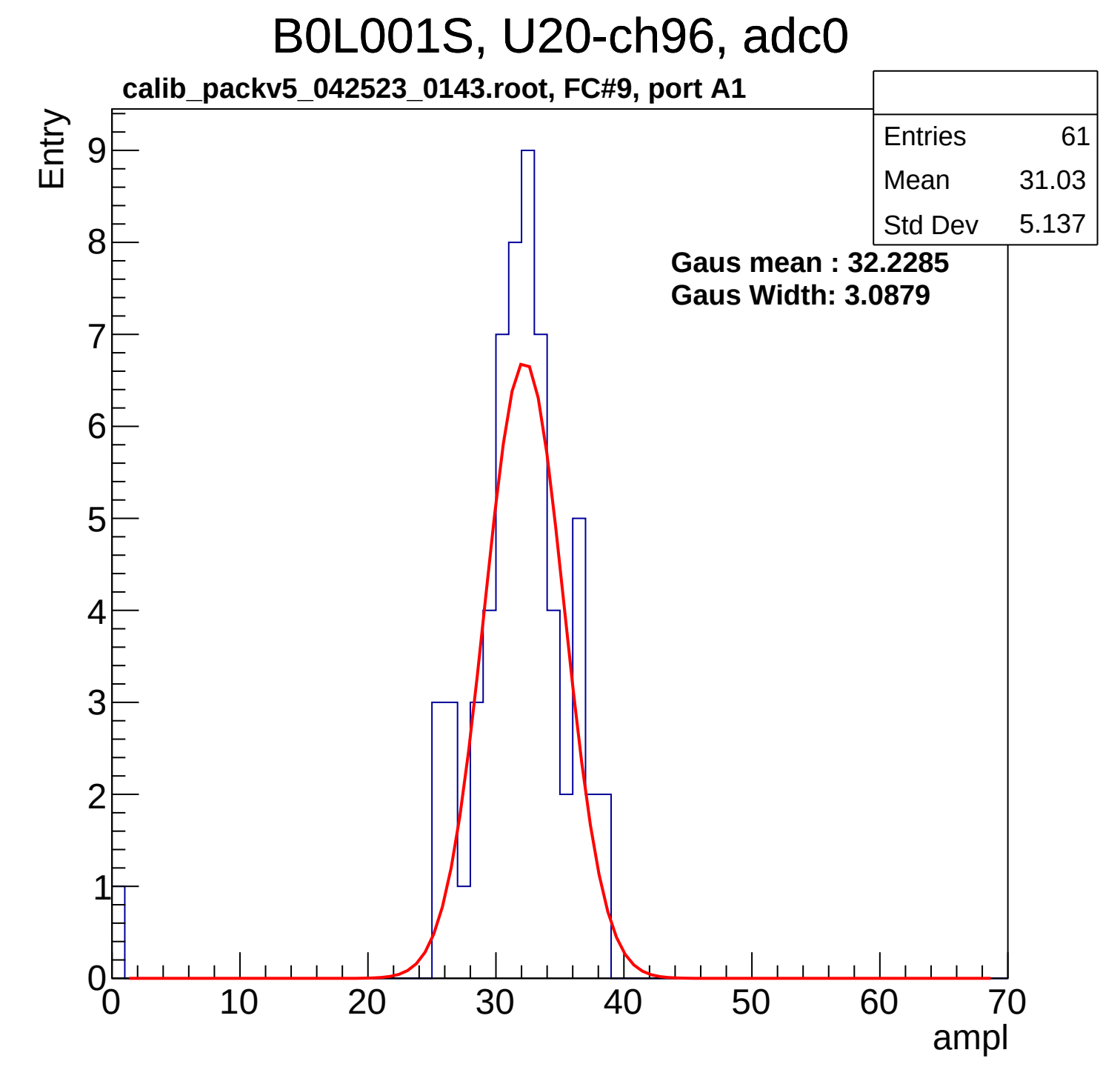
50
60
70
ampl


B0L001S, U20-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.03
Std Dev 5.137
Gaus mean : 32.2285
Gaus Width: 3.0879
Entry
ampl
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3
4
5
6
7
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0
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60
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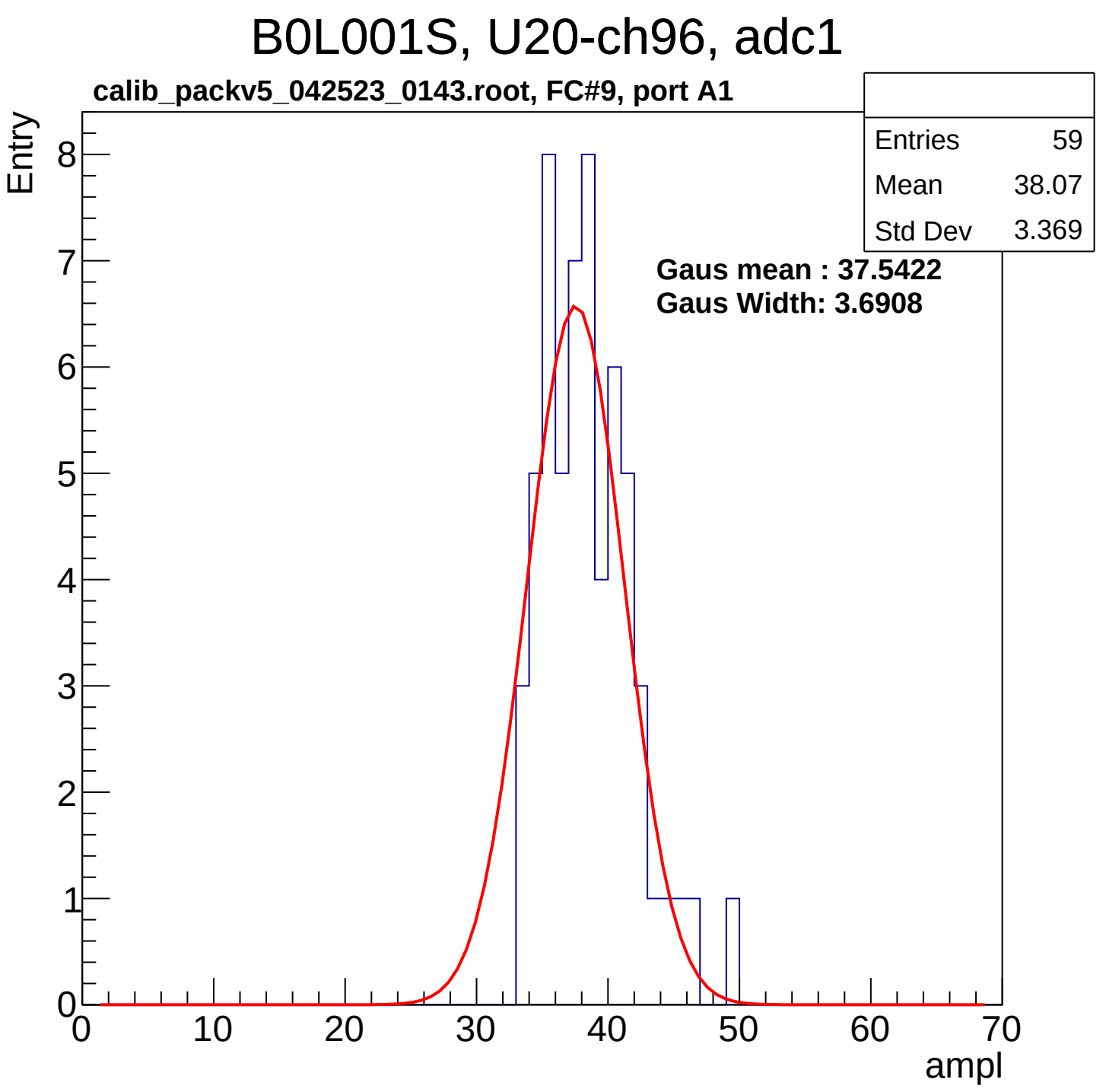
B0L001S, U20-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.07
Std Dev 3.369
Gaus mean : 37.5422
Gaus Width: 3.6908
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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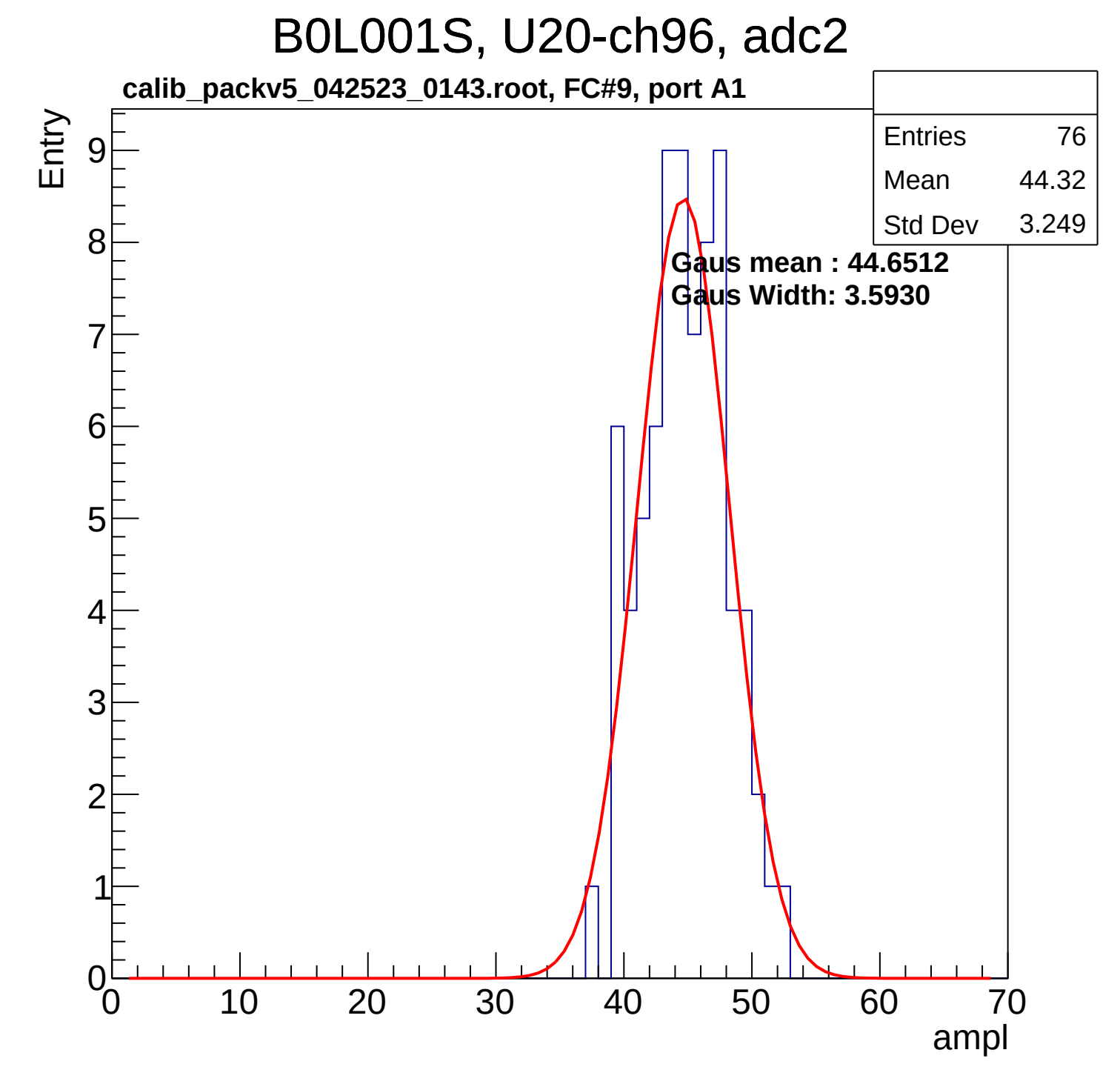
B0L001S, U20-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.32
Std Dev 3.249
Gaus mean : 44.6512
Gaus Width: 3.5930
Entry
ampl
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2
3
4
5
6
7
8
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0
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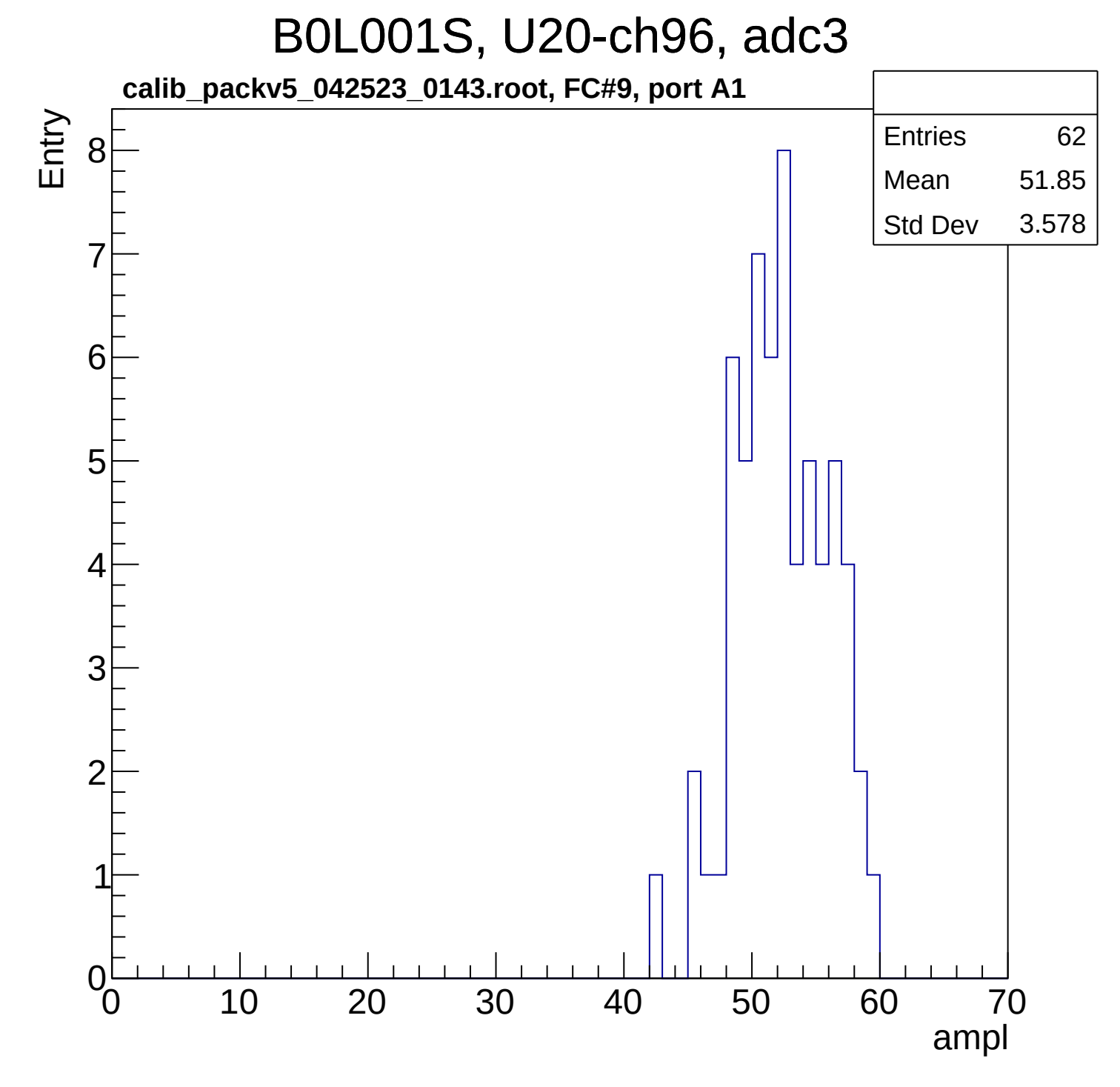
B0L001S, U20-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.85
Std Dev 3.578
Entry
ampl
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3
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5
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7
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0
10
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40
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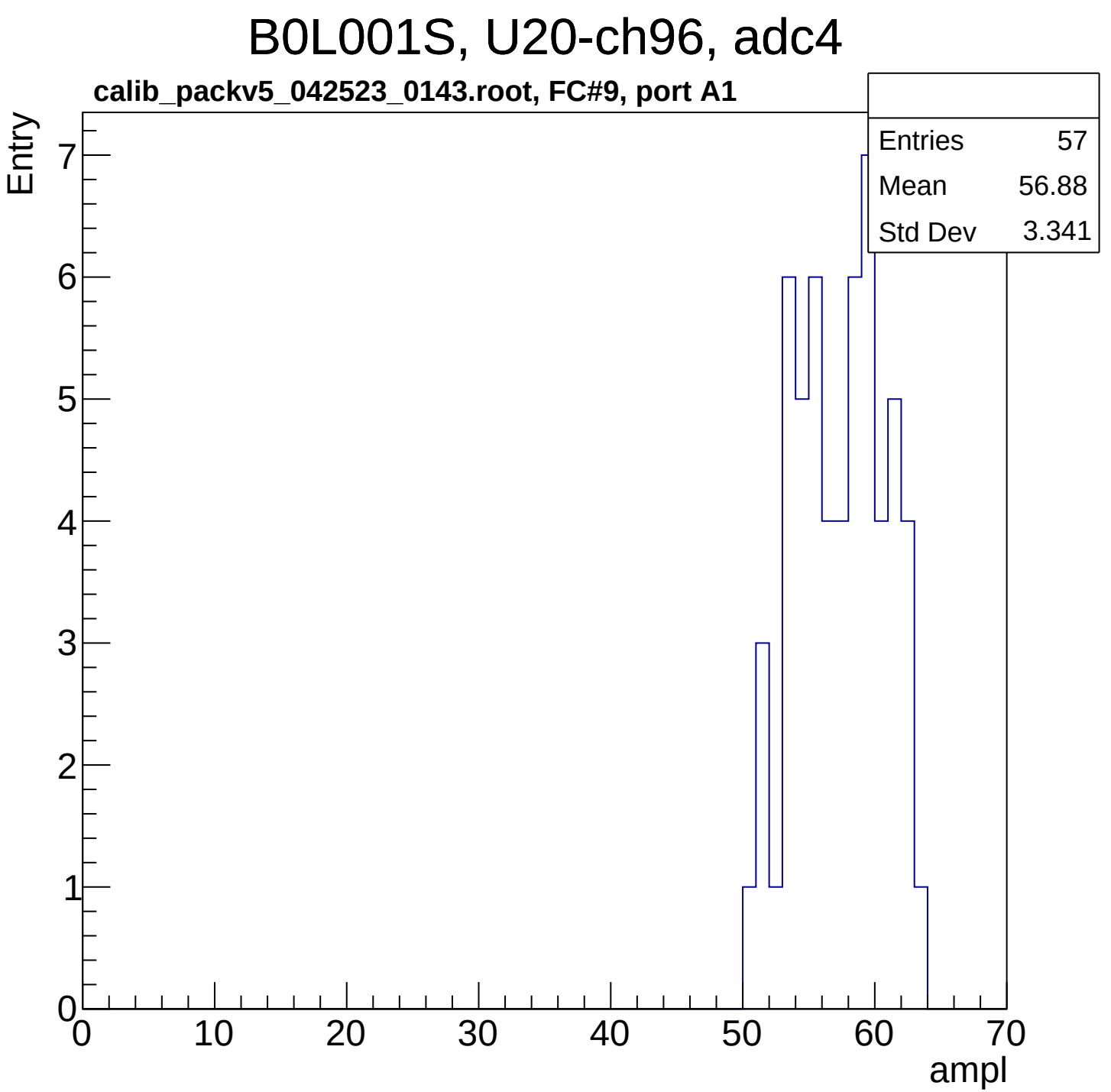
B0L001S, U20-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.88
Std Dev 3.341
Entry
ampl
0
1
2
3
4
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6
7
0
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20
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40
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60
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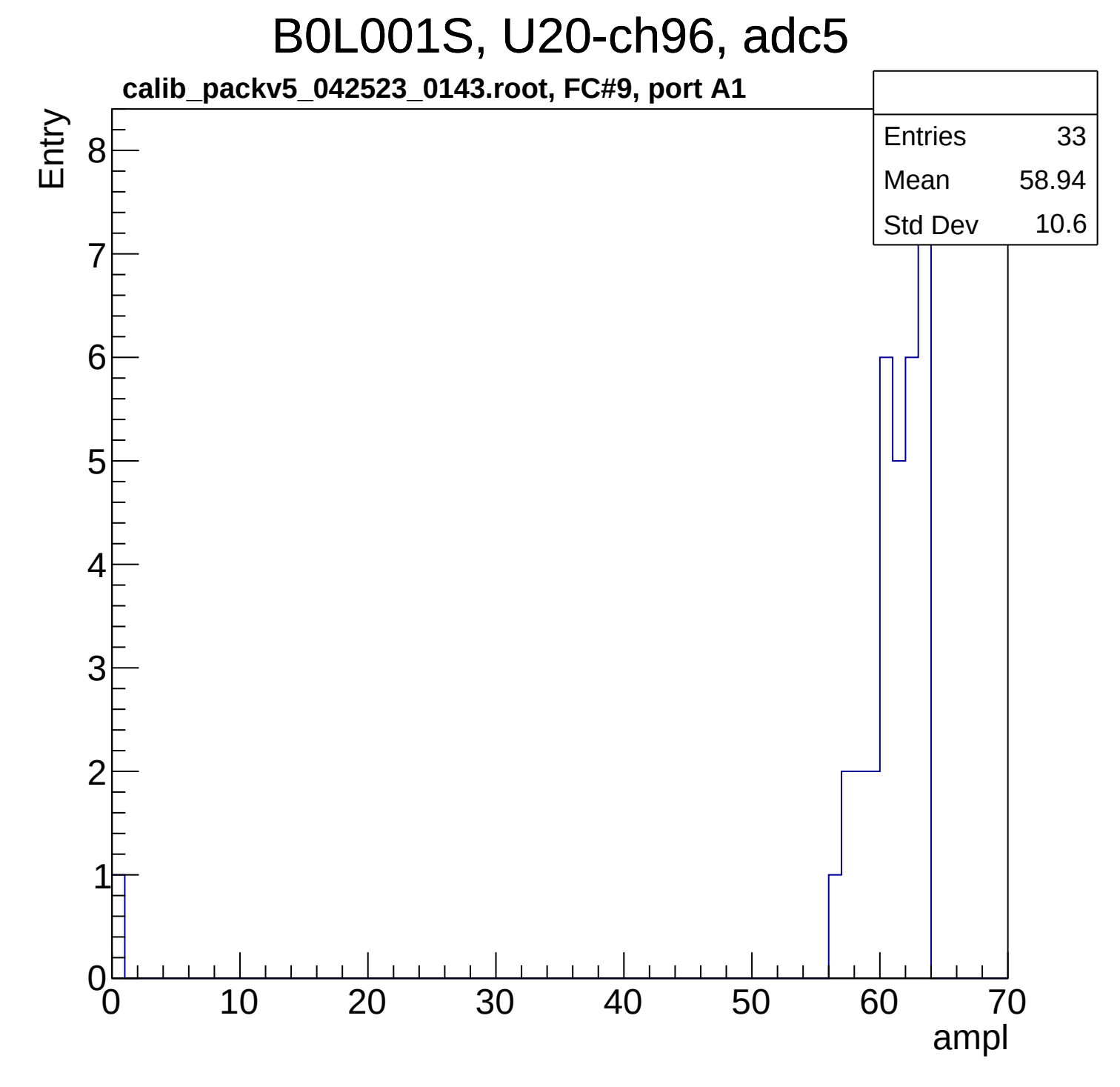
B0L001S, U20-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.94
Std Dev 10.6
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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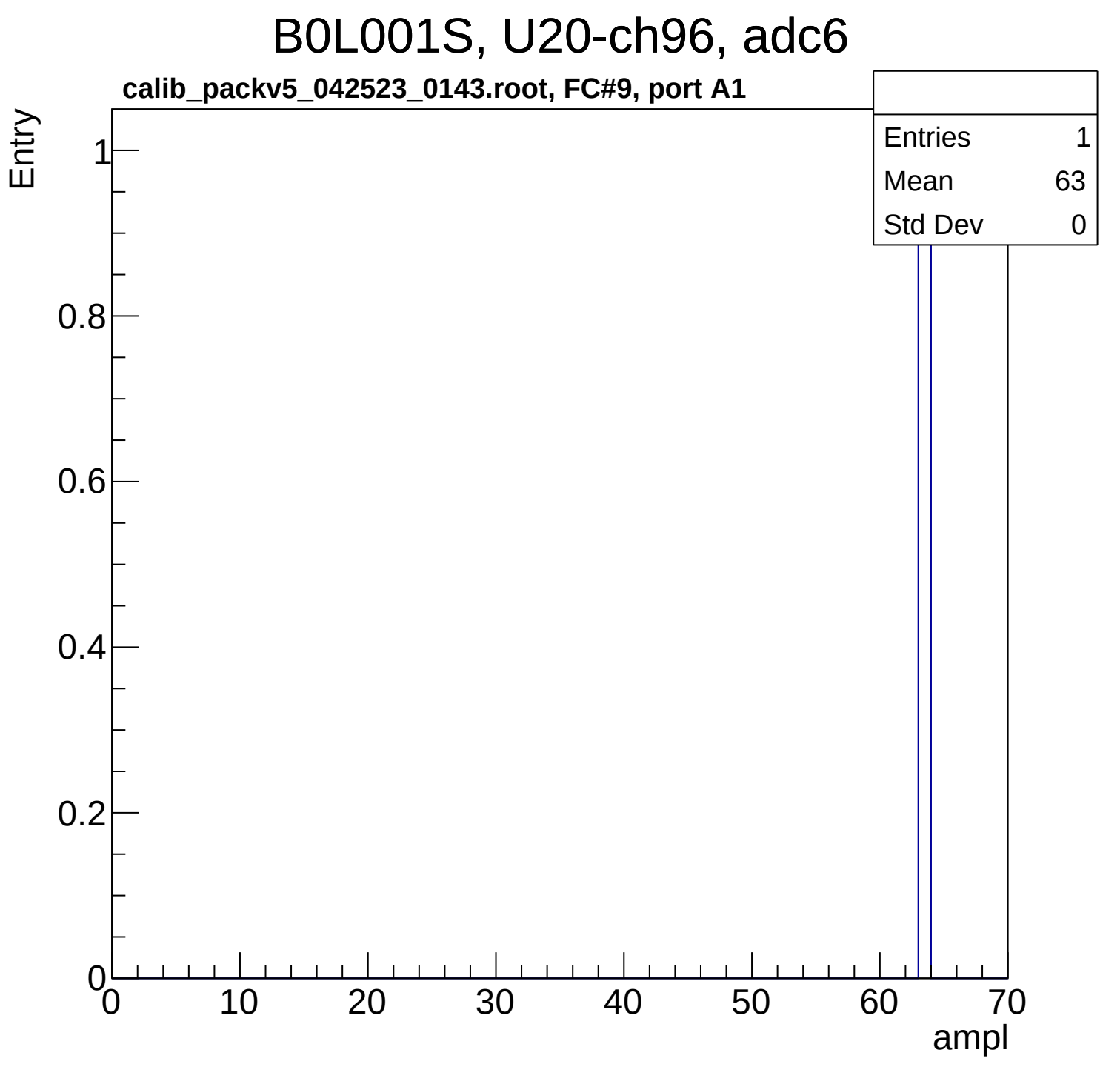
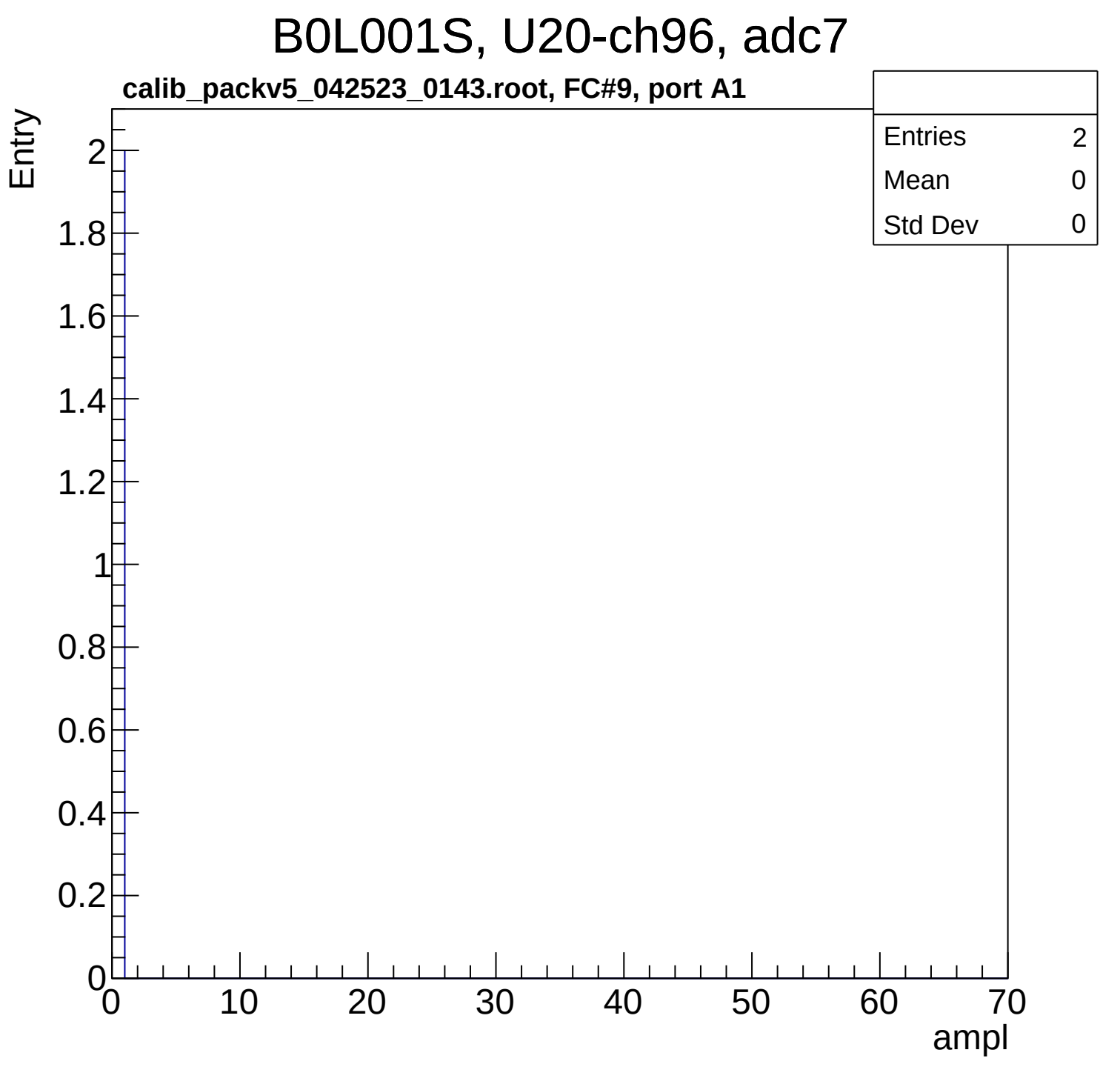
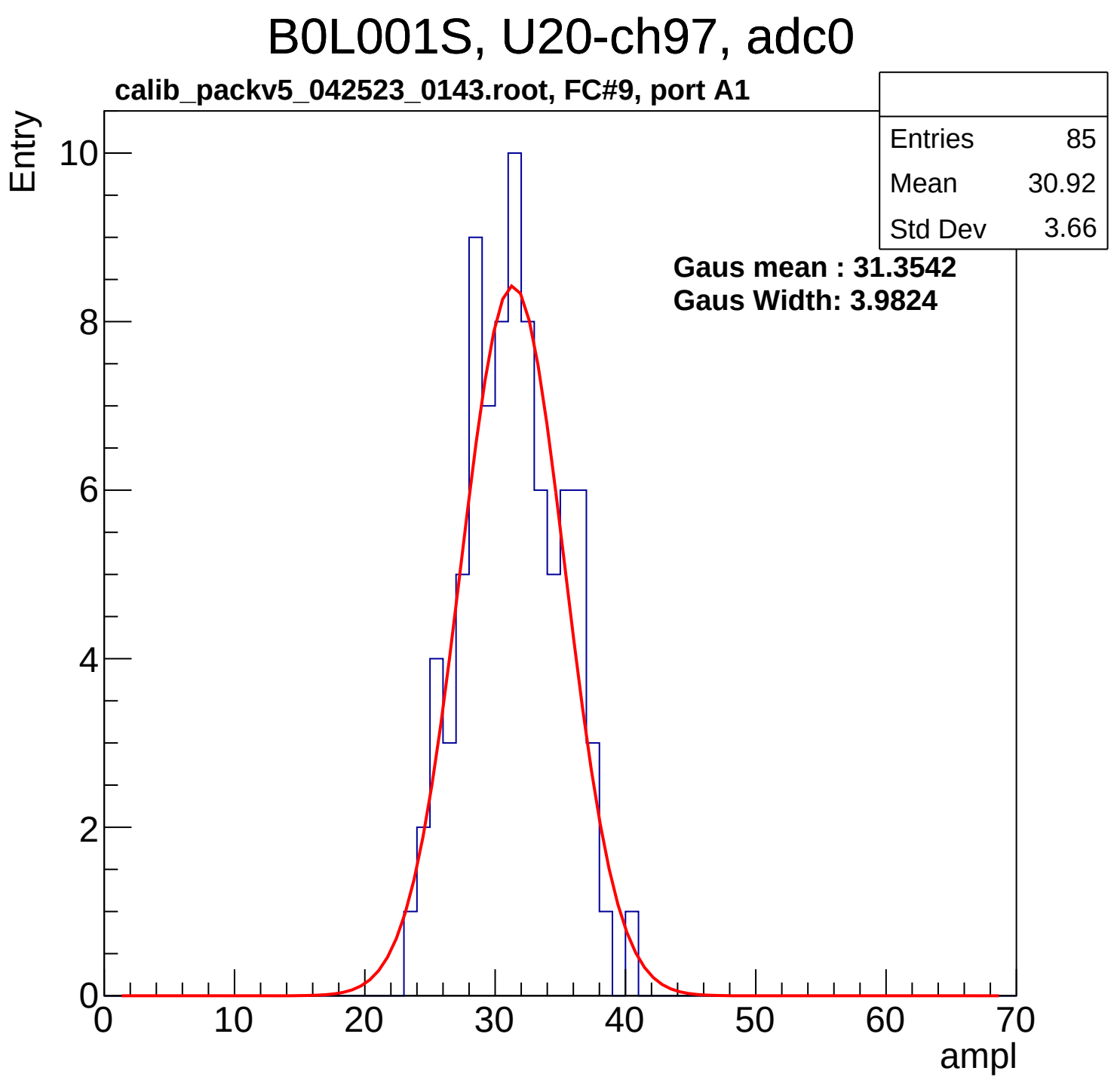

B0L001S, U20-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 30.92
Std Dev 3.66
Gaus mean : 31.3542
Gaus Width: 3.9824
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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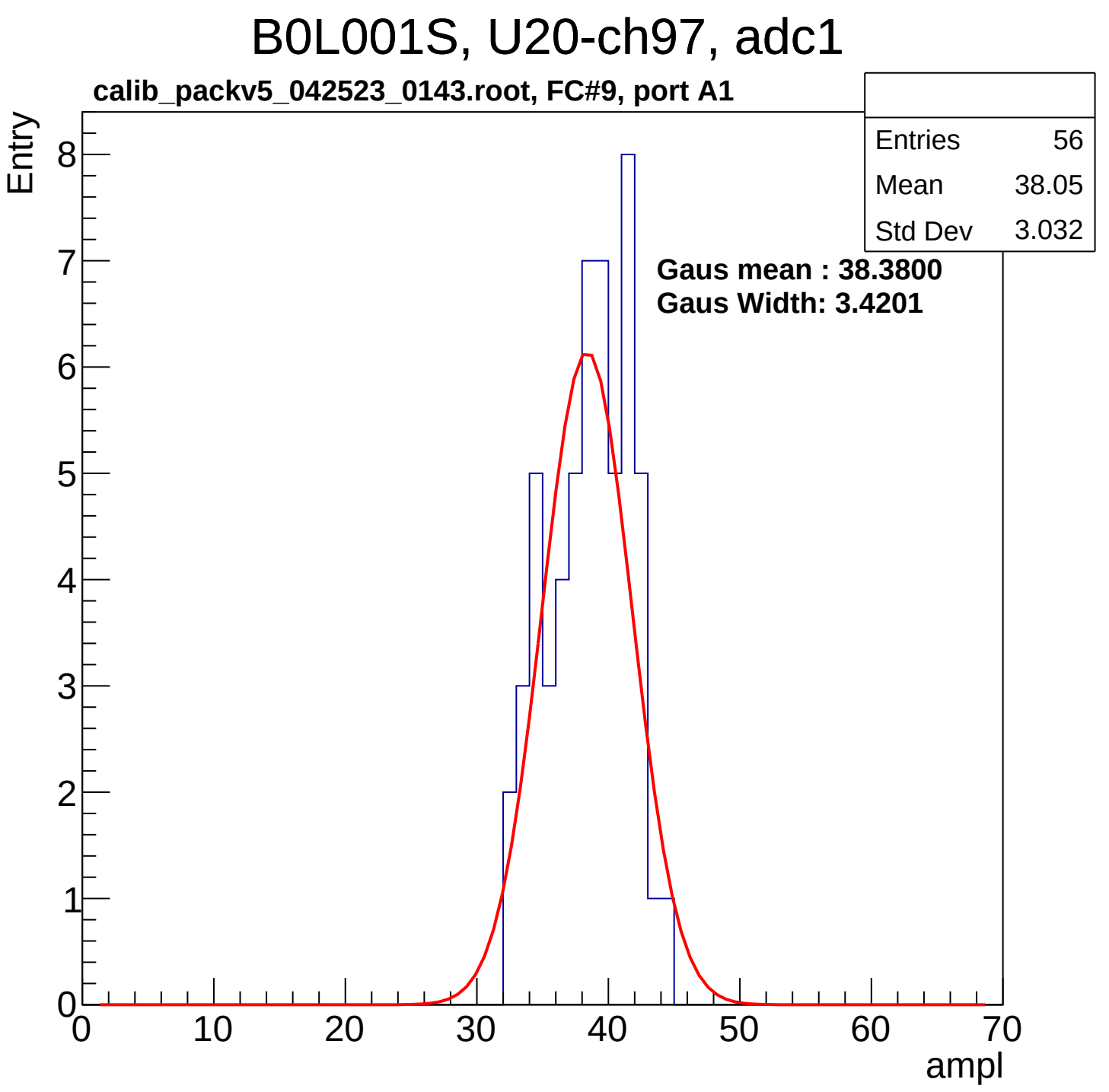
B0L001S, U20-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 38.05
Std Dev 3.032
Gaus mean : 38.3800
Gaus Width: 3.4201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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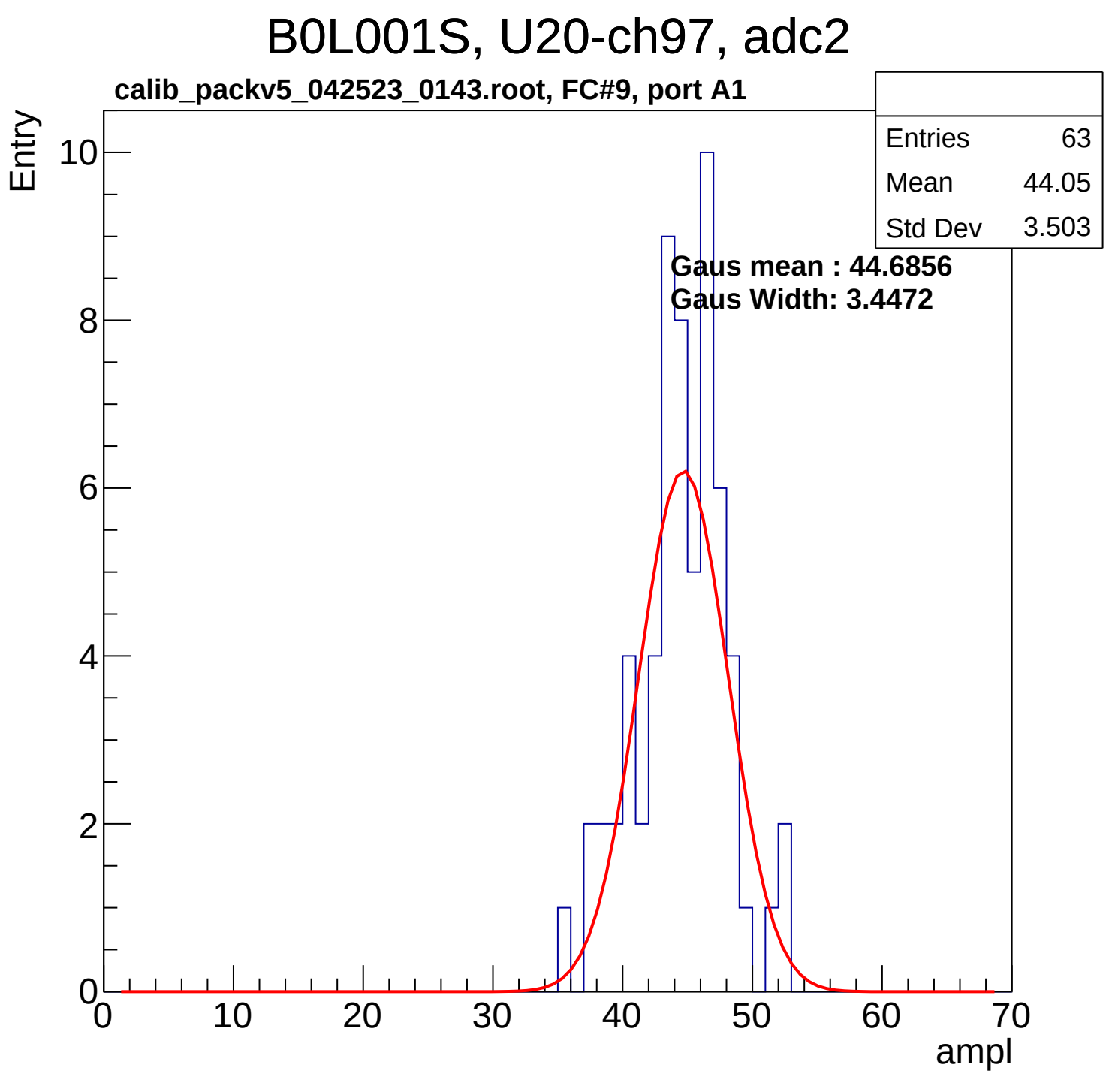
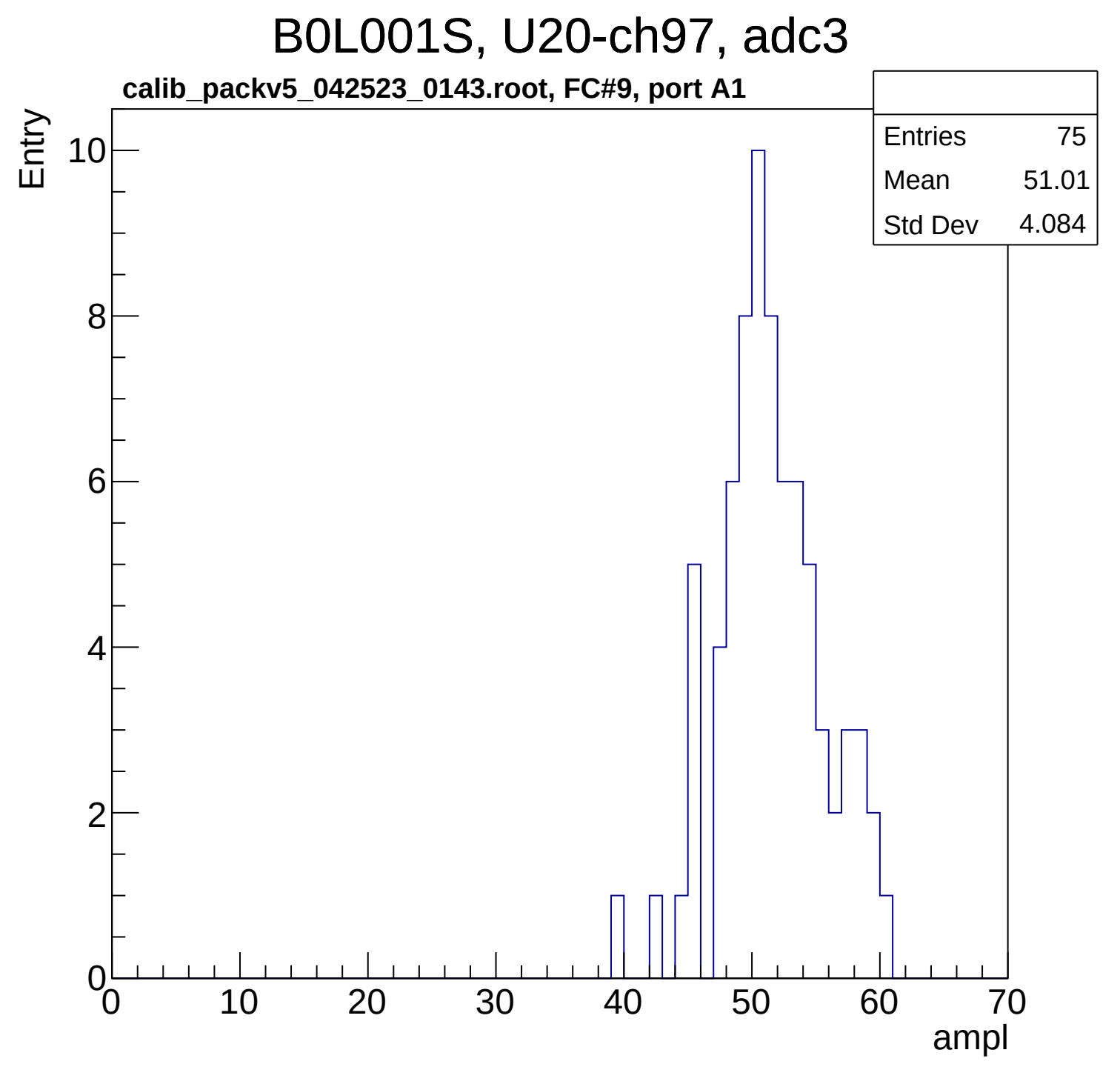
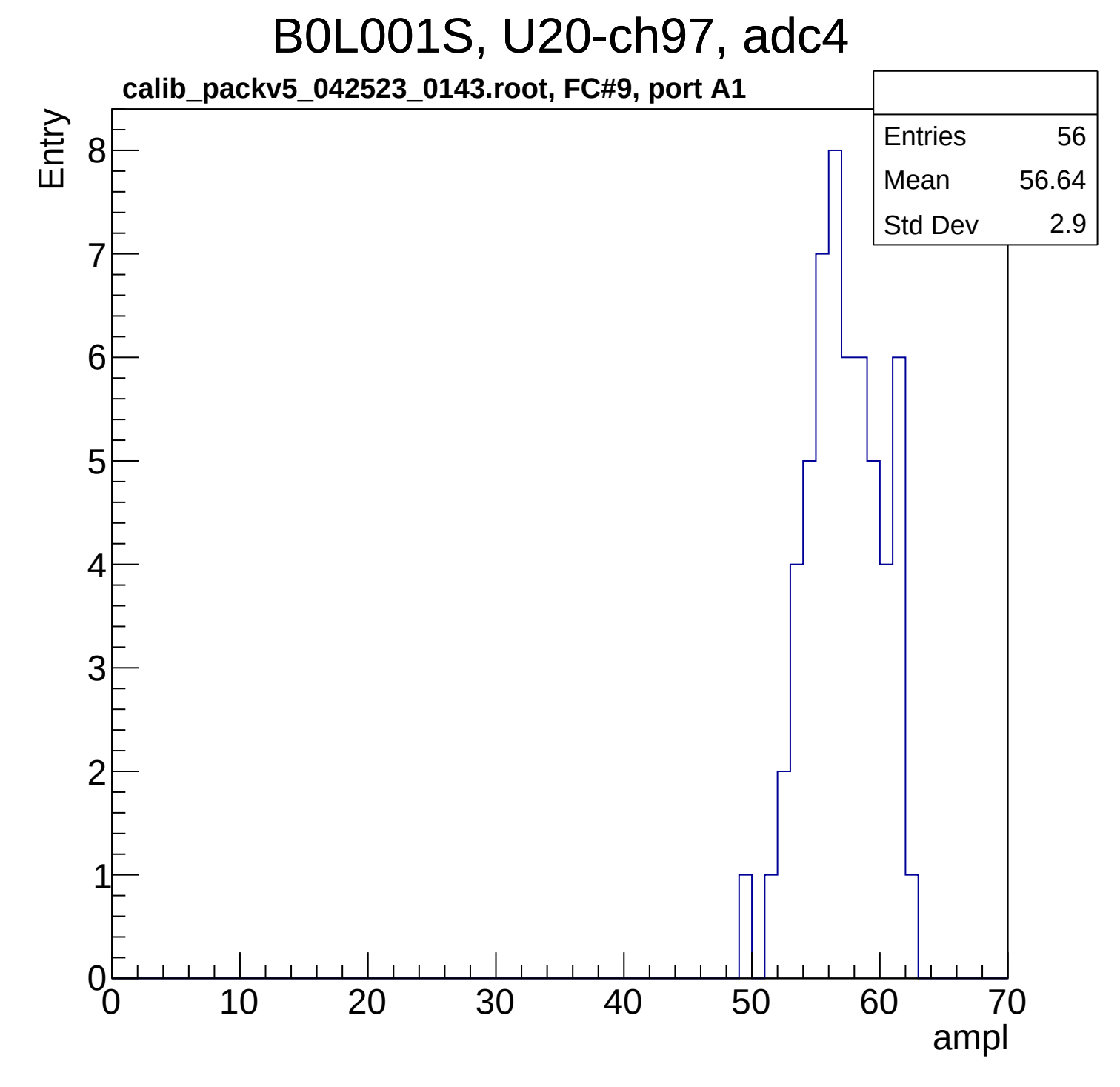
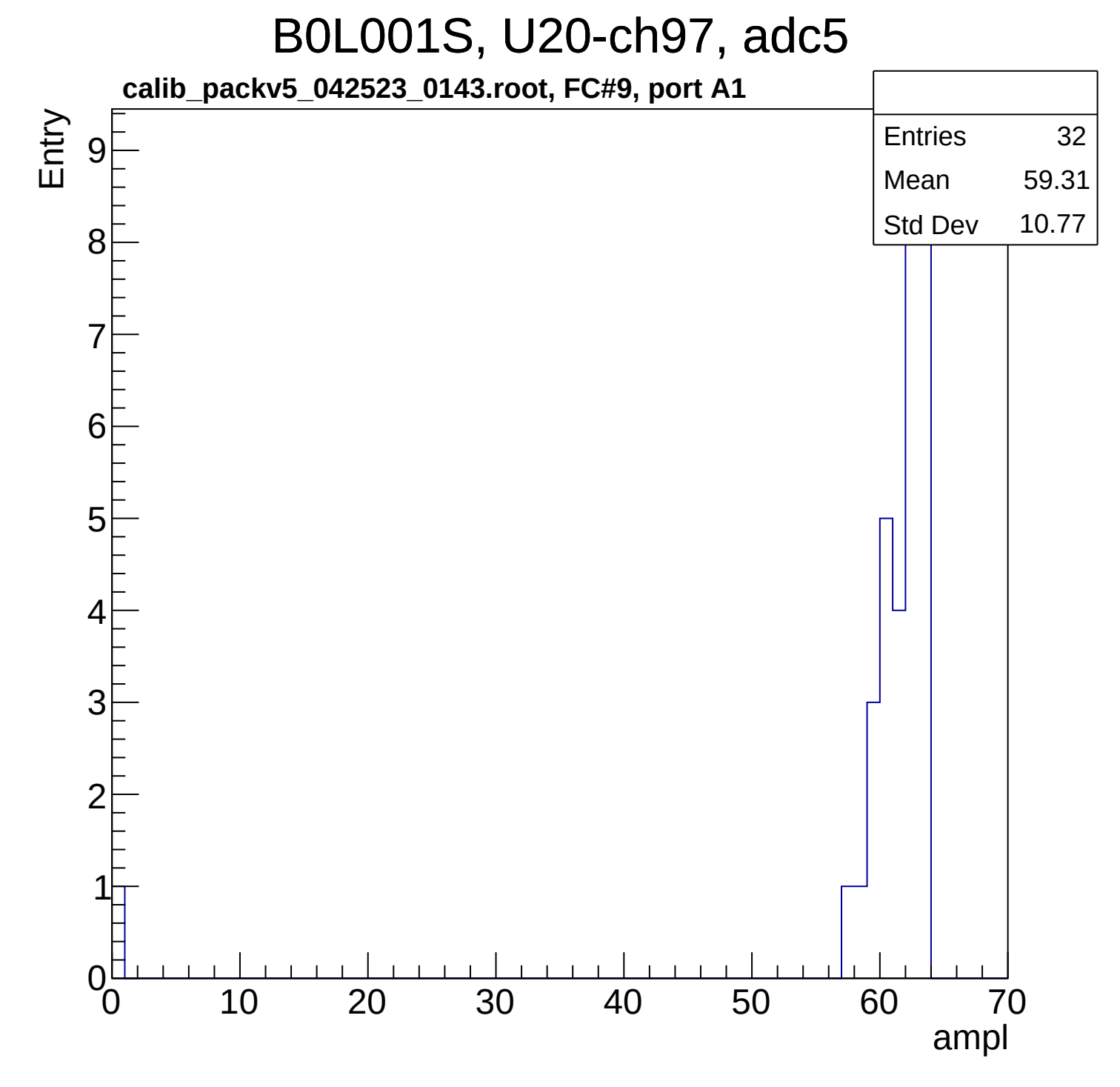
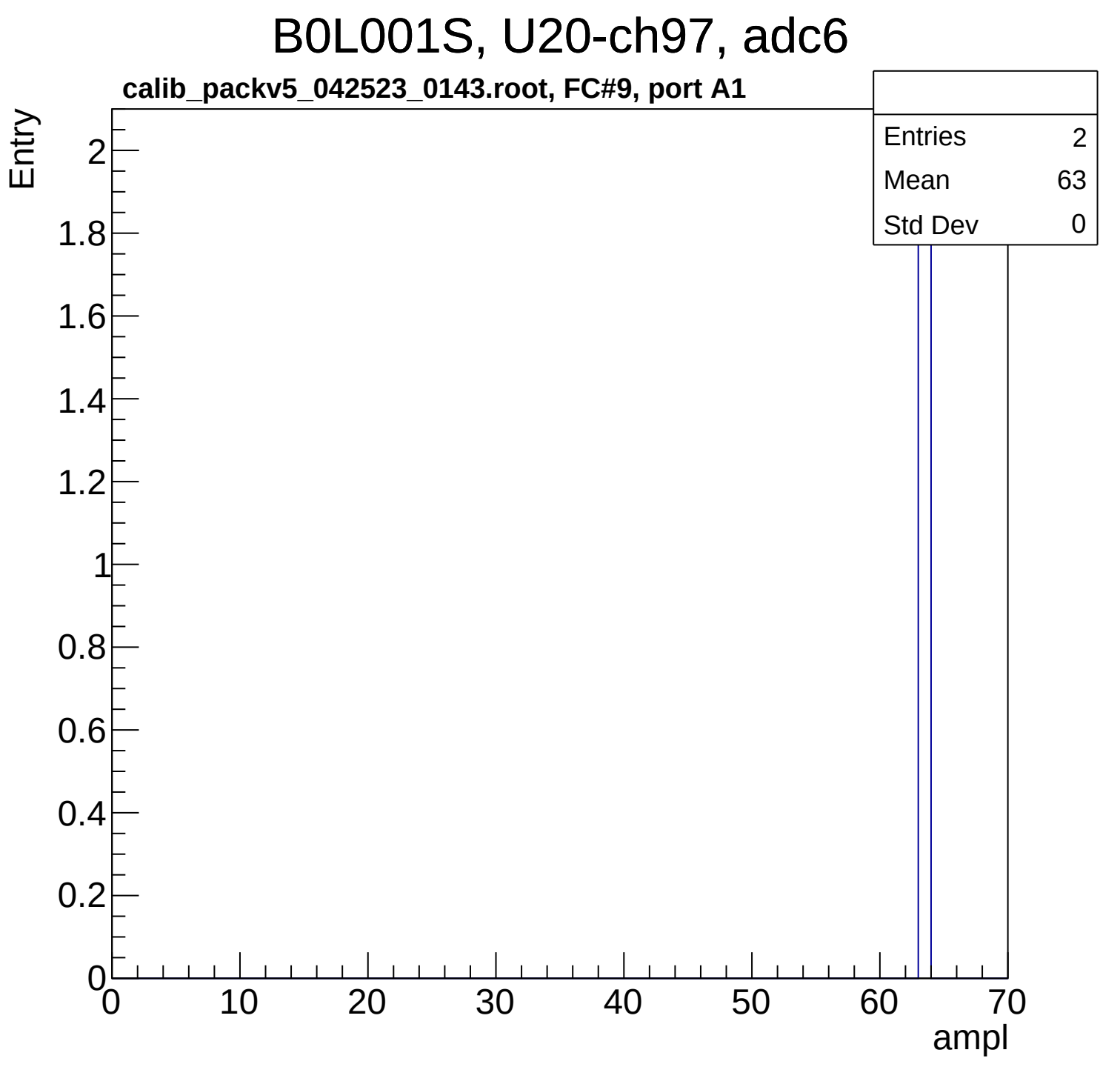
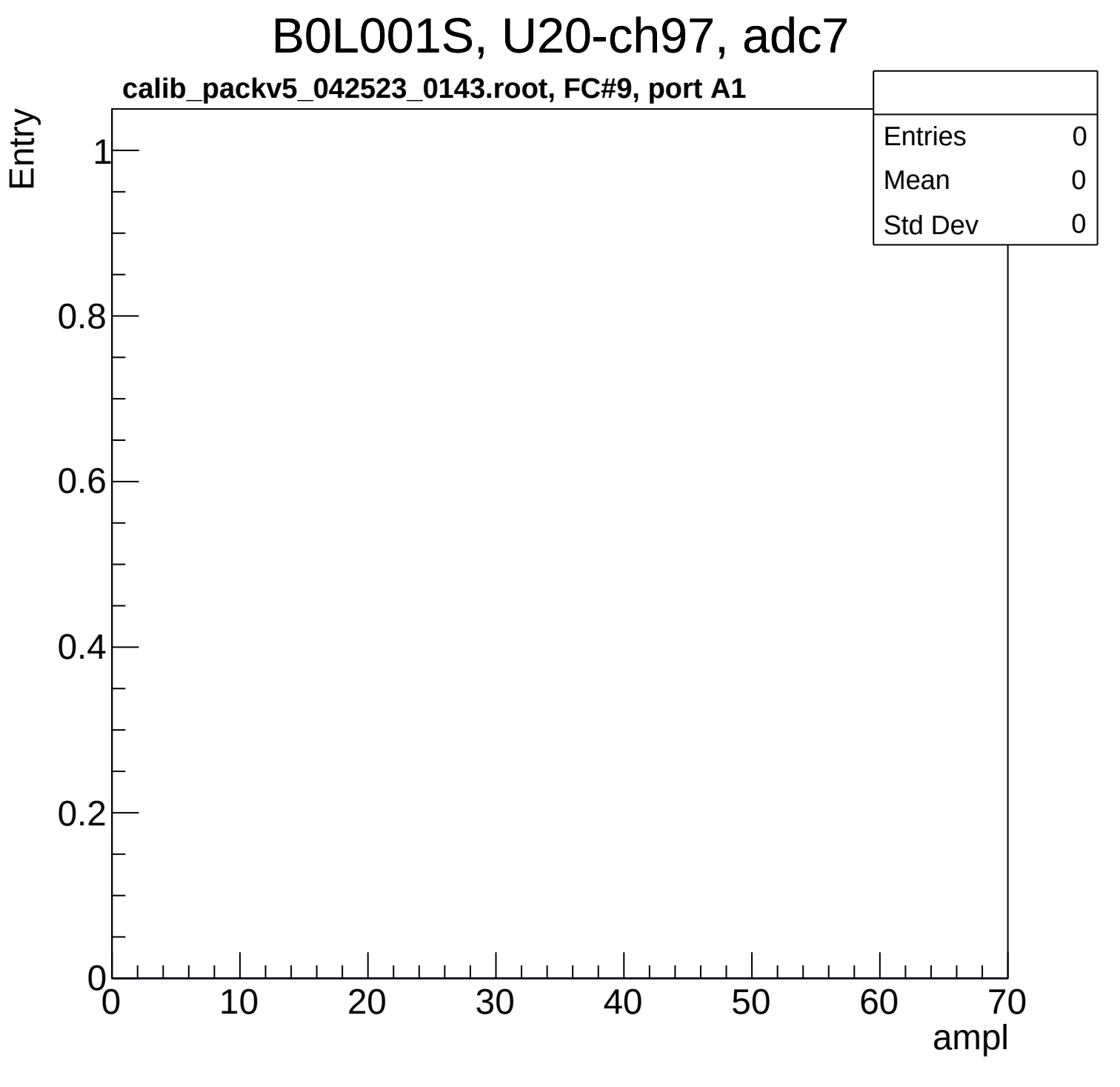

B0L001S, U20-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.05
Std Dev 3.503
Gaus mean : 44.6856
Gaus Width: 3.4472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

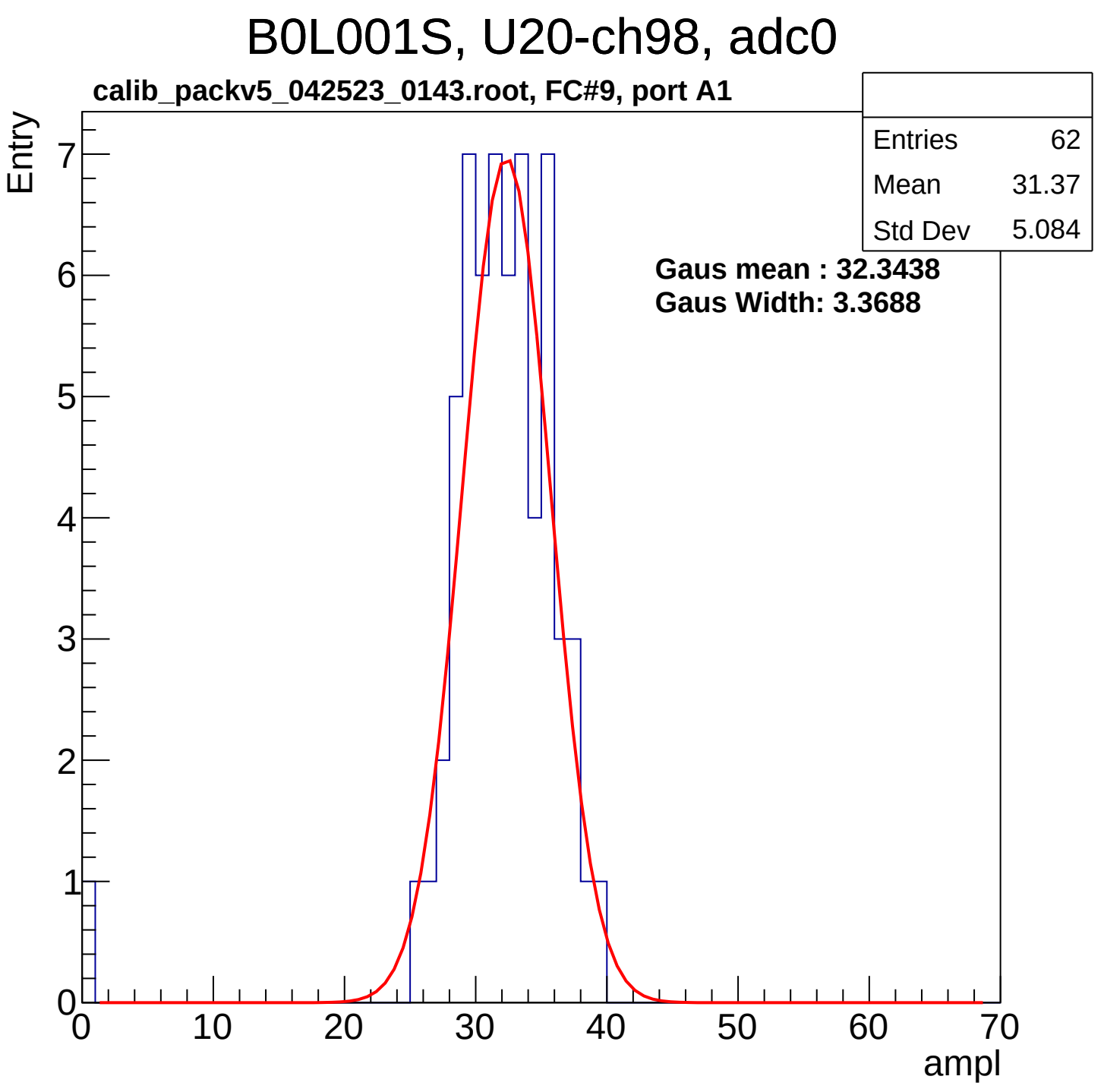

B0L001S, U20-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.01
Std Dev 4.084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

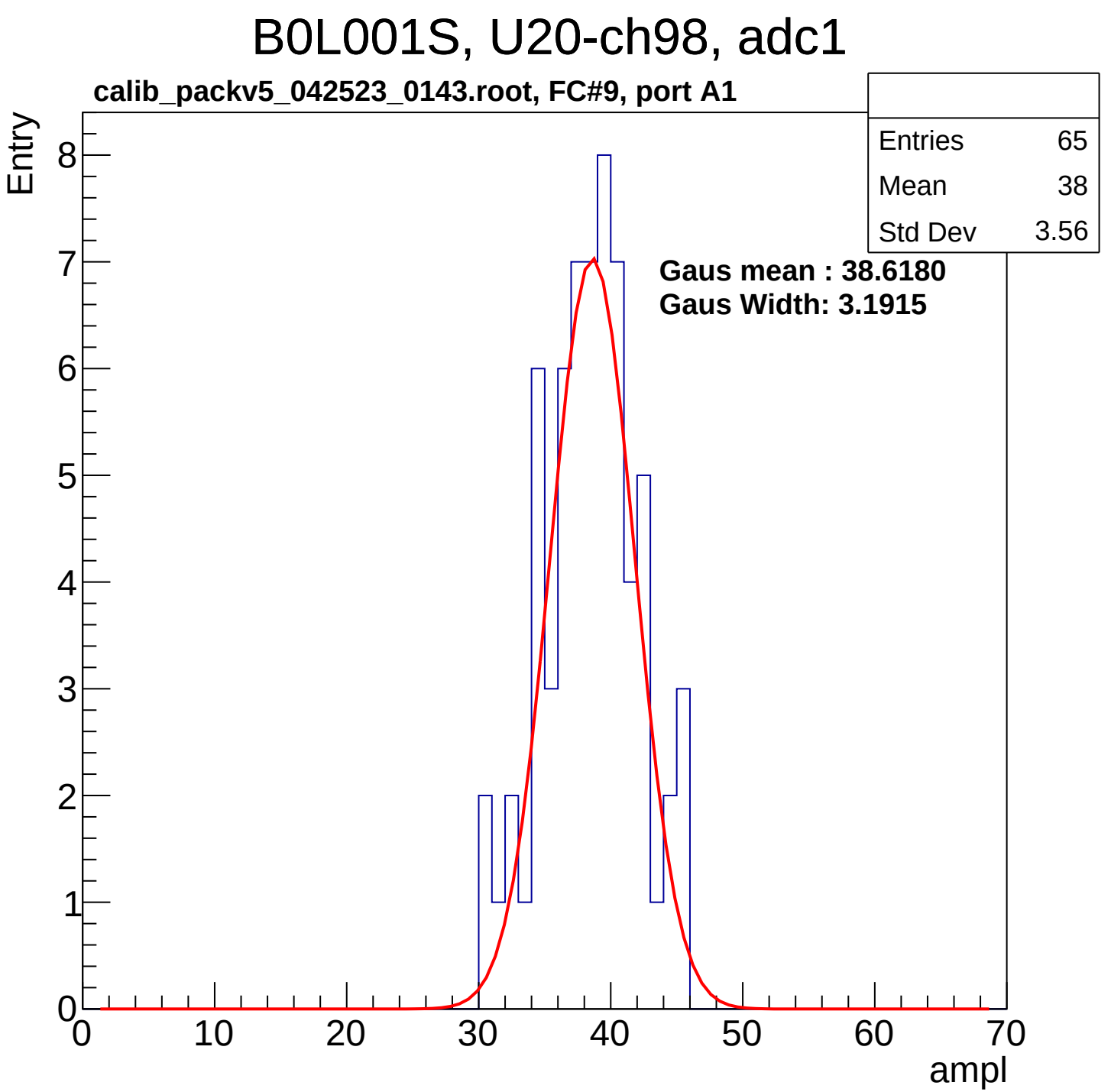

B0L001S, U20-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.64
Std Dev 2.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

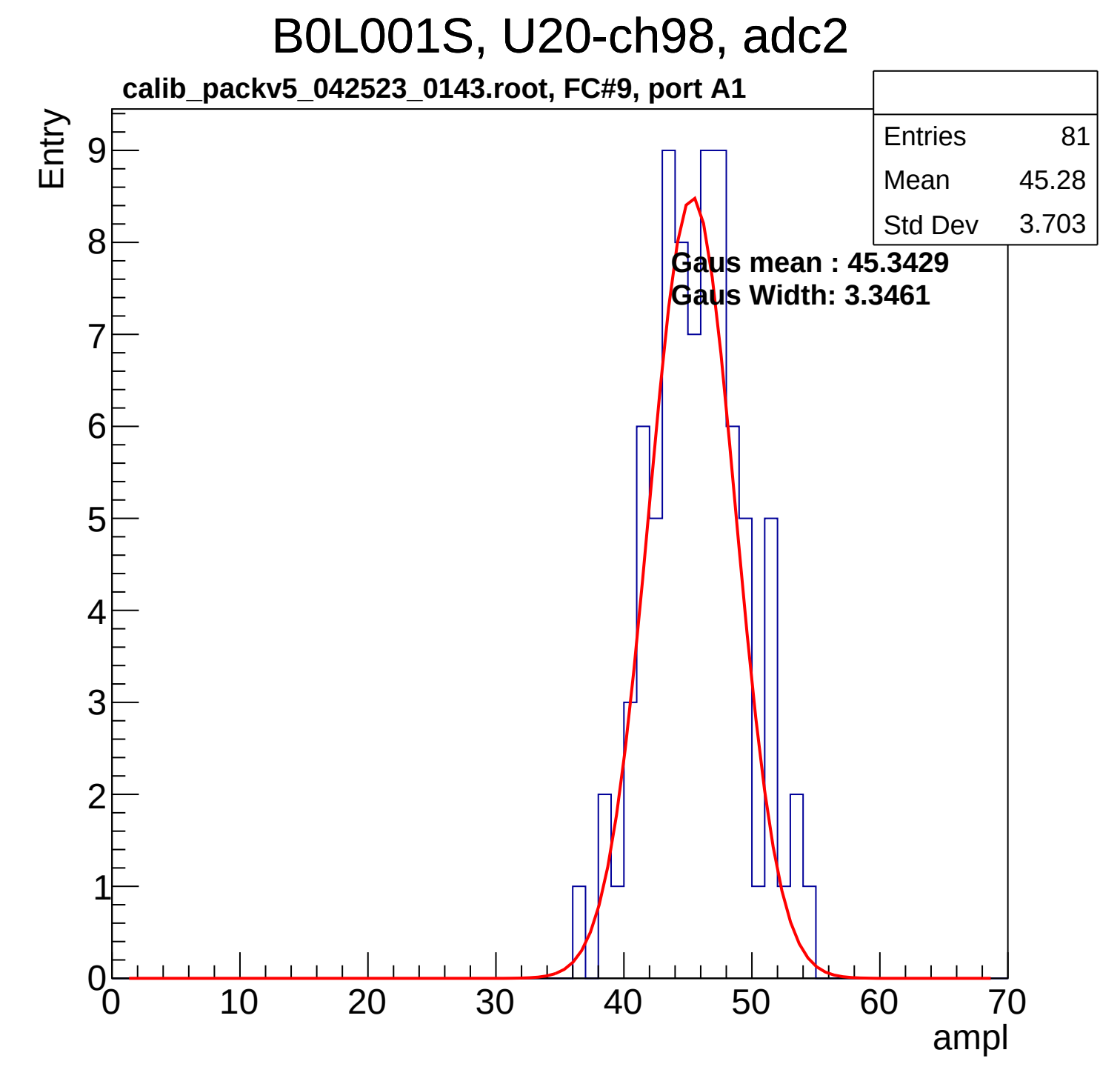

B0L001S, U20-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.31
Std Dev 10.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.37
Std Dev 5.084
Gaus mean : 32.3438
Gaus Width: 3.3688
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38
Std Dev 3.56
Gaus mean : 38.6180
Gaus Width: 3.1915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 45.28
Std Dev 3.703
Gaus mean : 45.3429
Gaus Width: 3.3461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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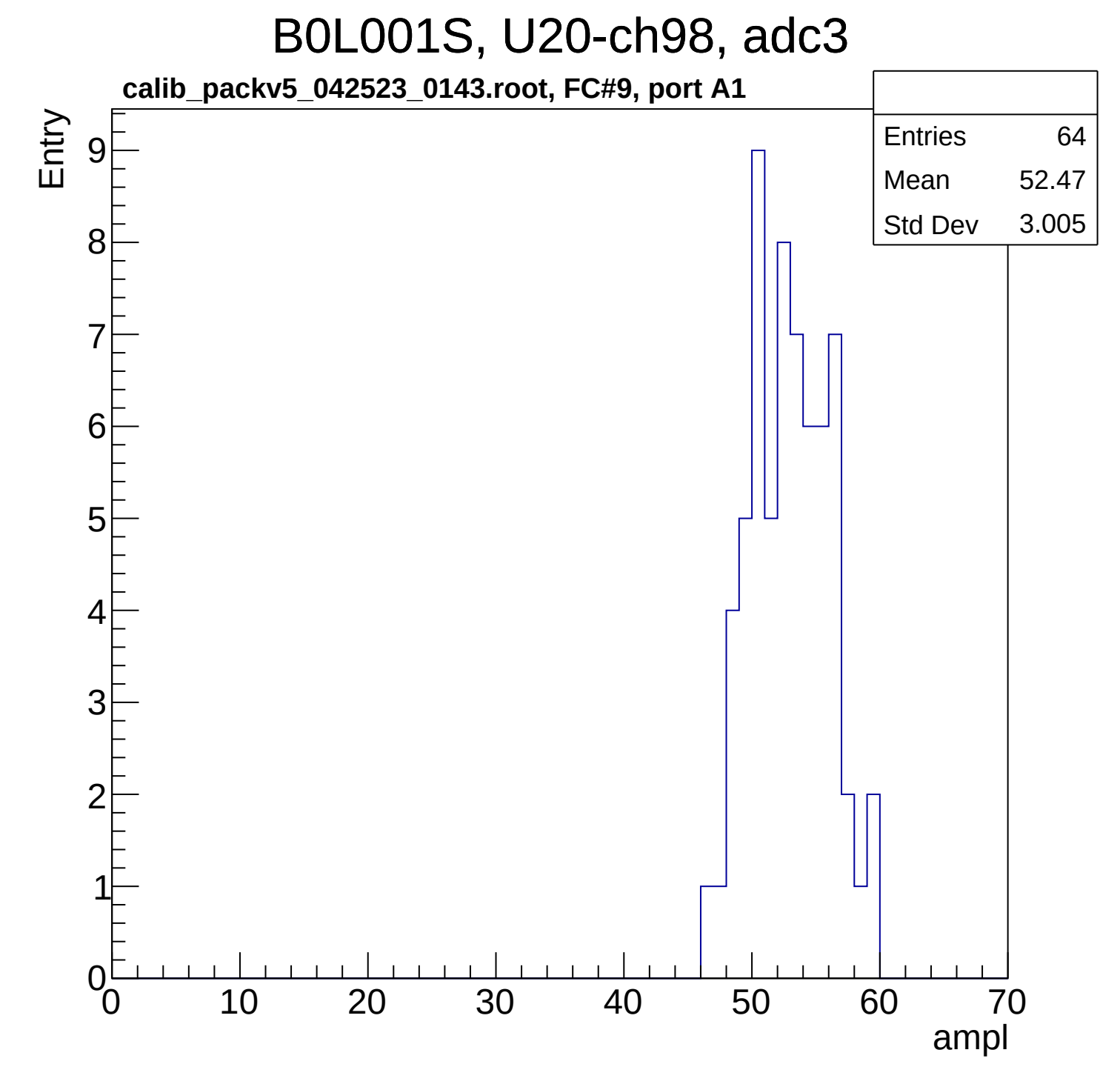
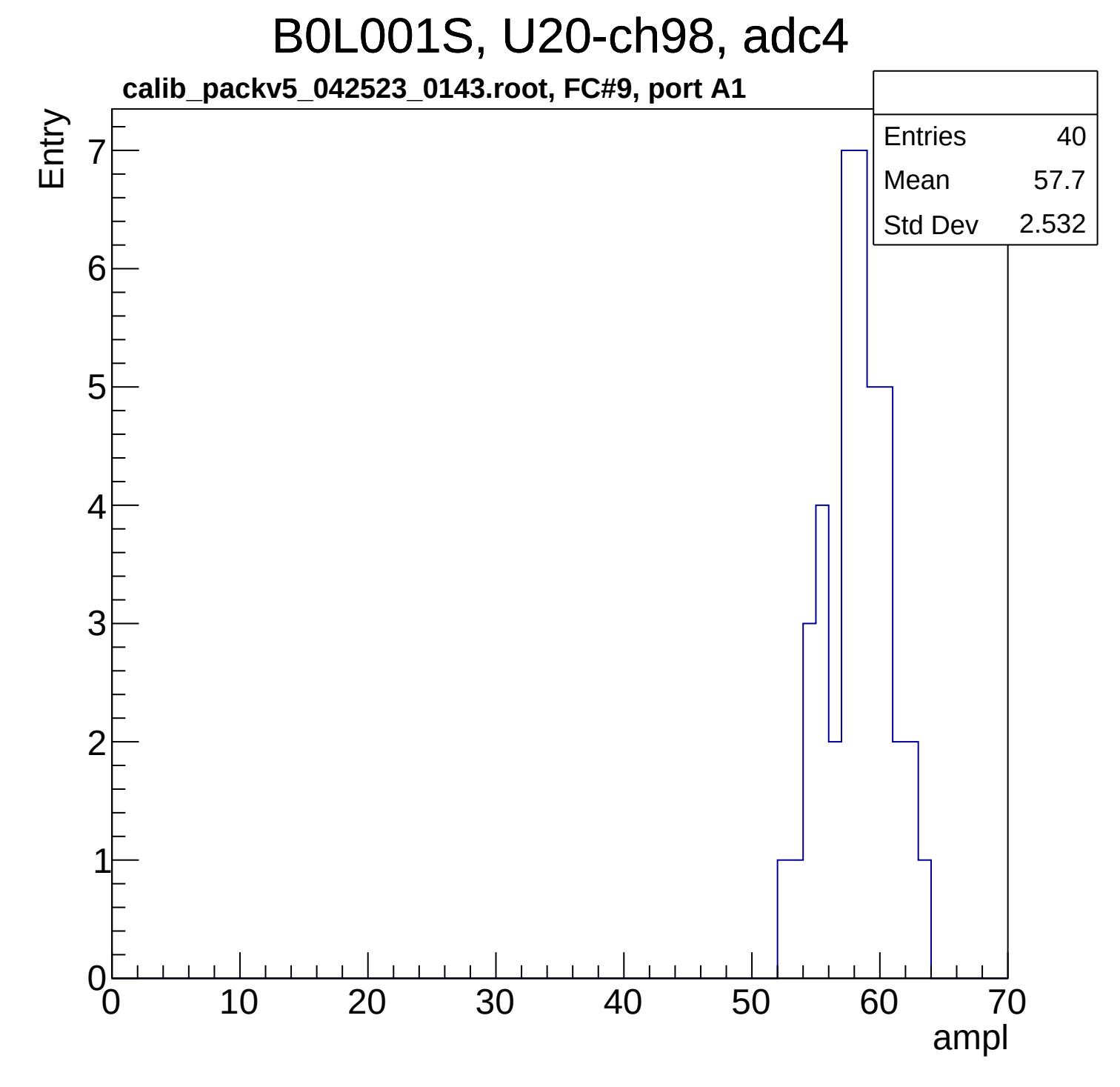
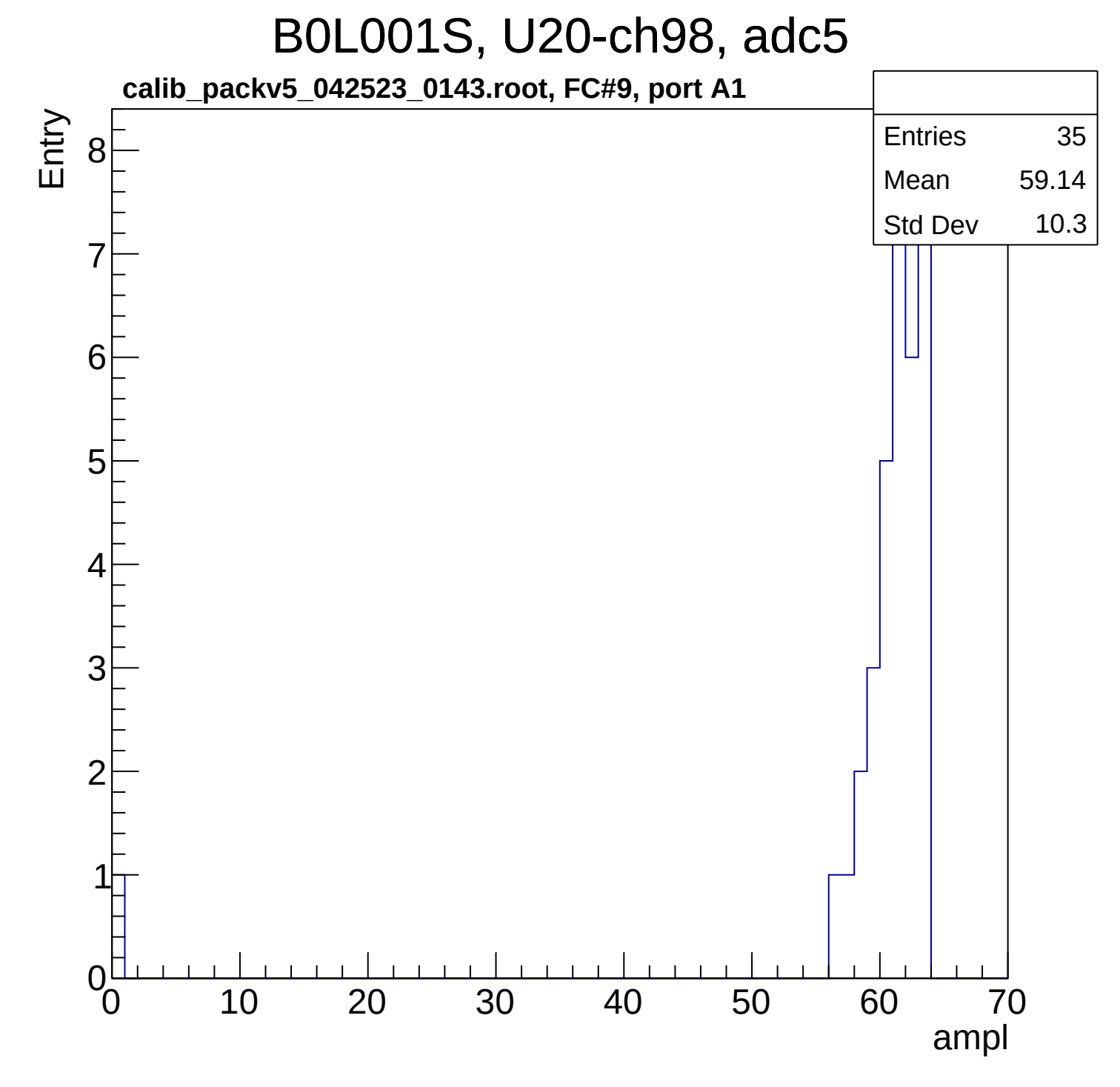
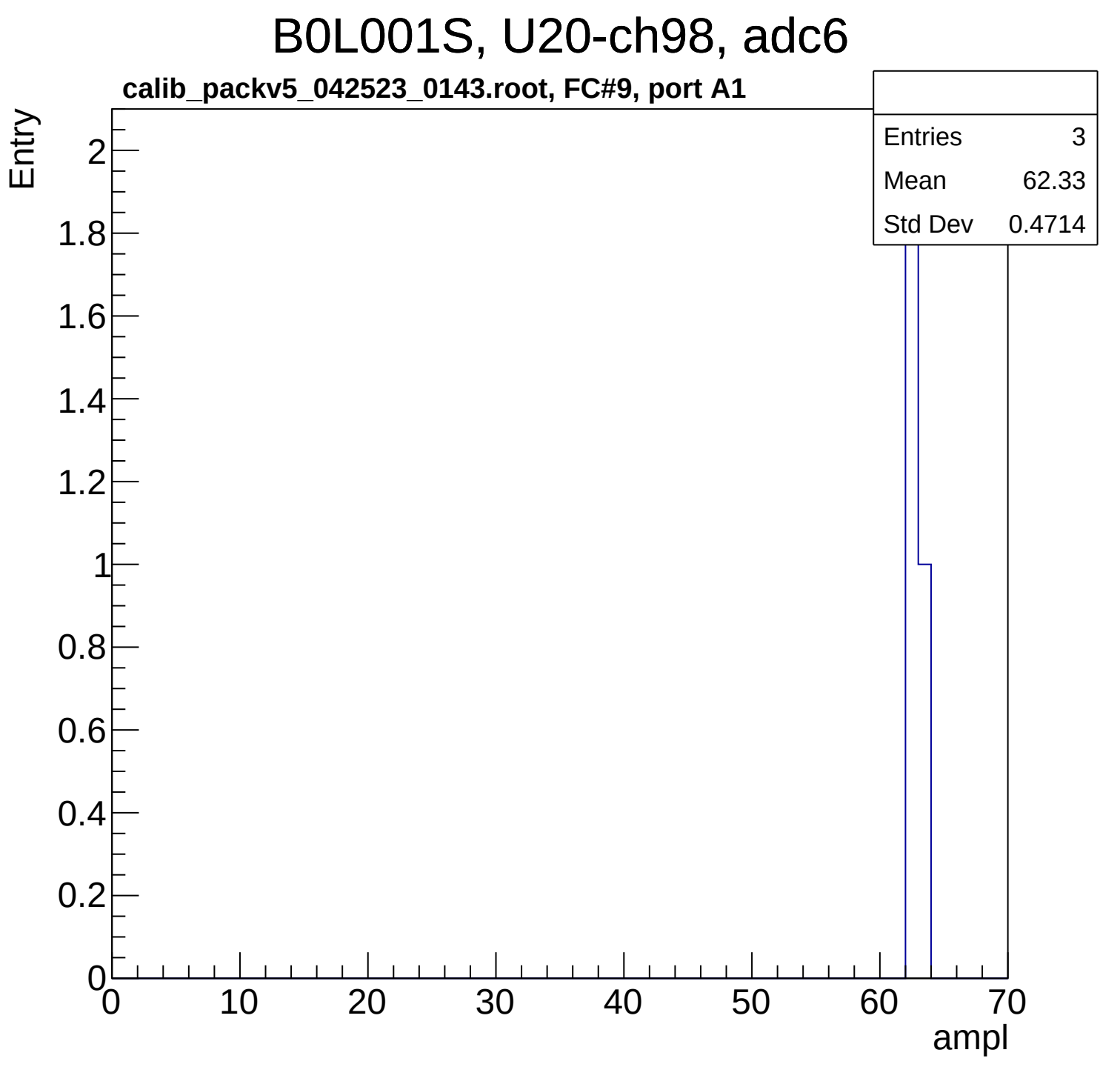
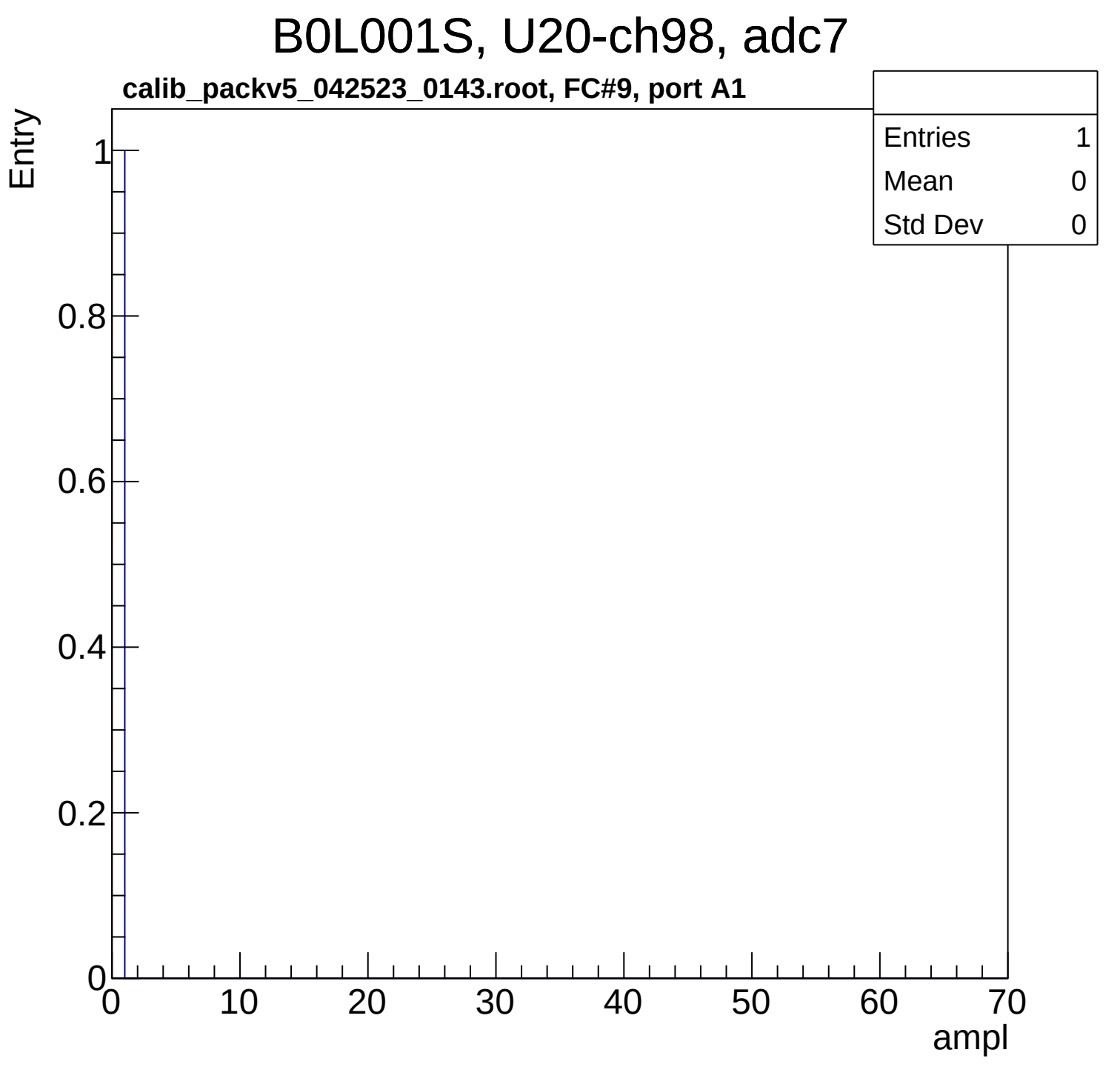
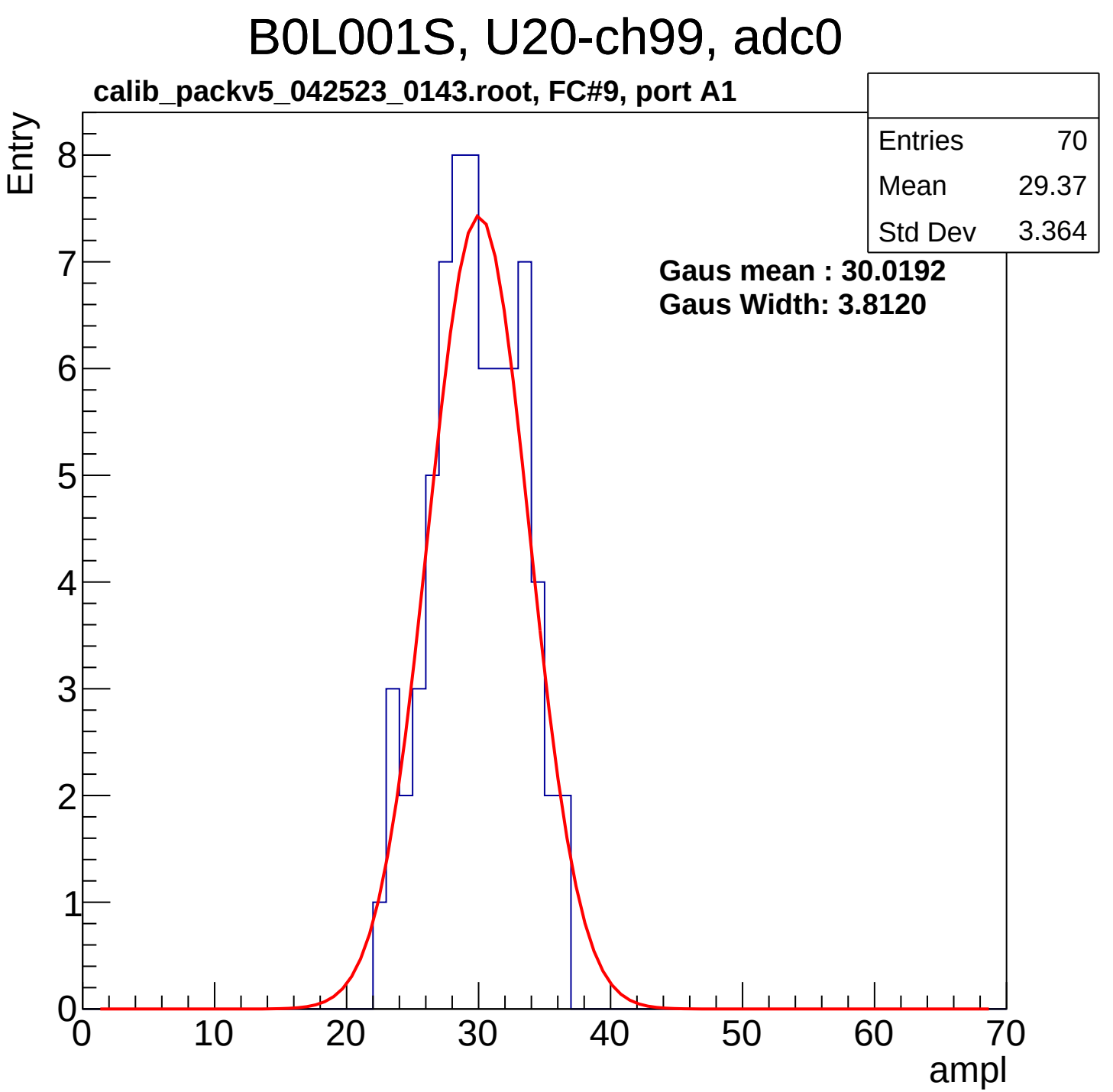

B0L001S, U20-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.47
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

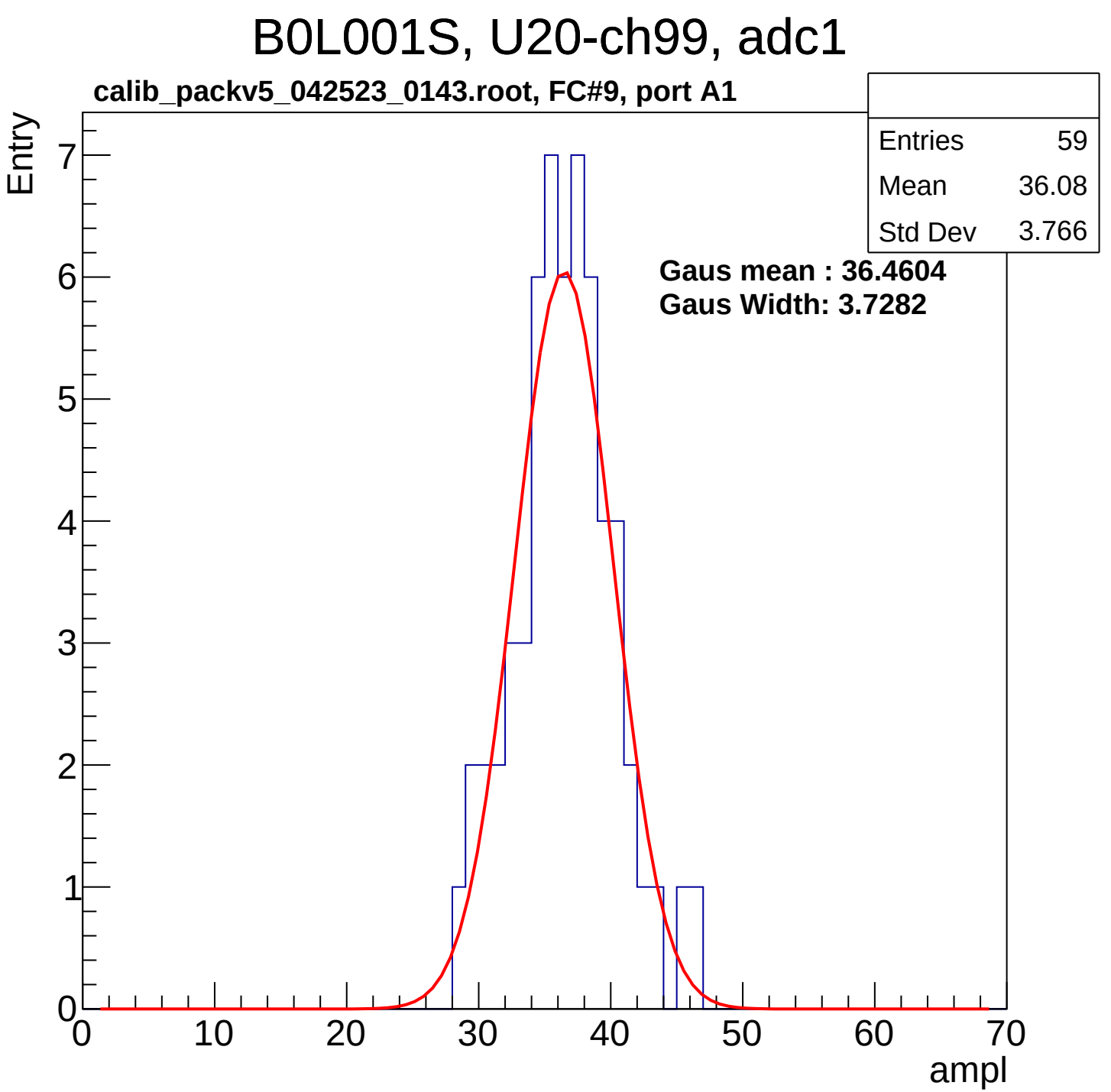

B0L001S, U20-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 57.7
Std Dev 2.532
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

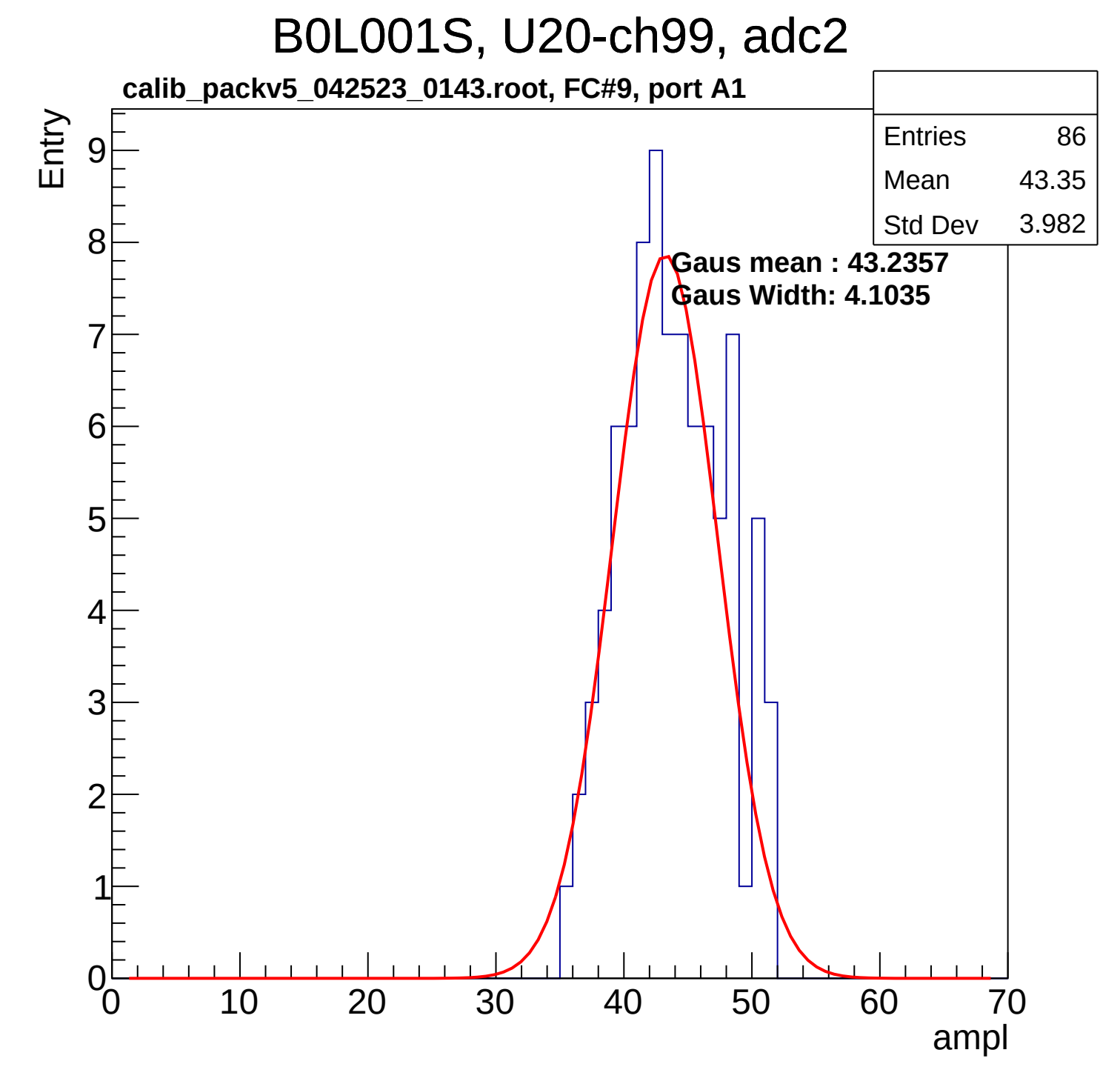

B0L001S, U20-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.14
Std Dev 10.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.37
Std Dev 3.364
Gaus mean : 30.0192
Gaus Width: 3.8120
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 36.08
Std Dev 3.766
Gaus mean : 36.4604
Gaus Width: 3.7282
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 43.35
Std Dev 3.982
Gaus mean : 43.2357
Gaus Width: 4.1035
Entry
ampl
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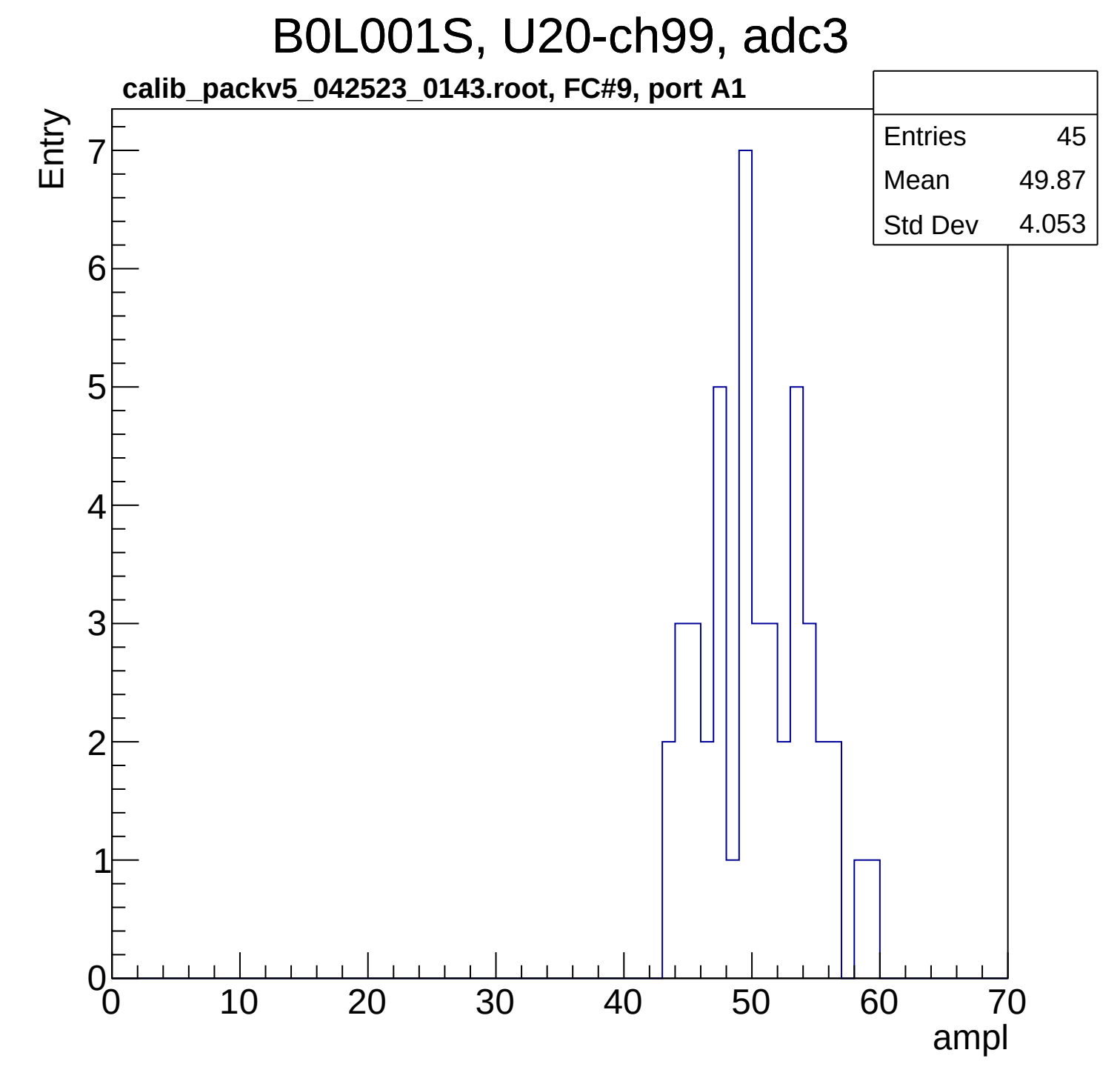
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 49.87
Std Dev 4.053
Entry
ampl
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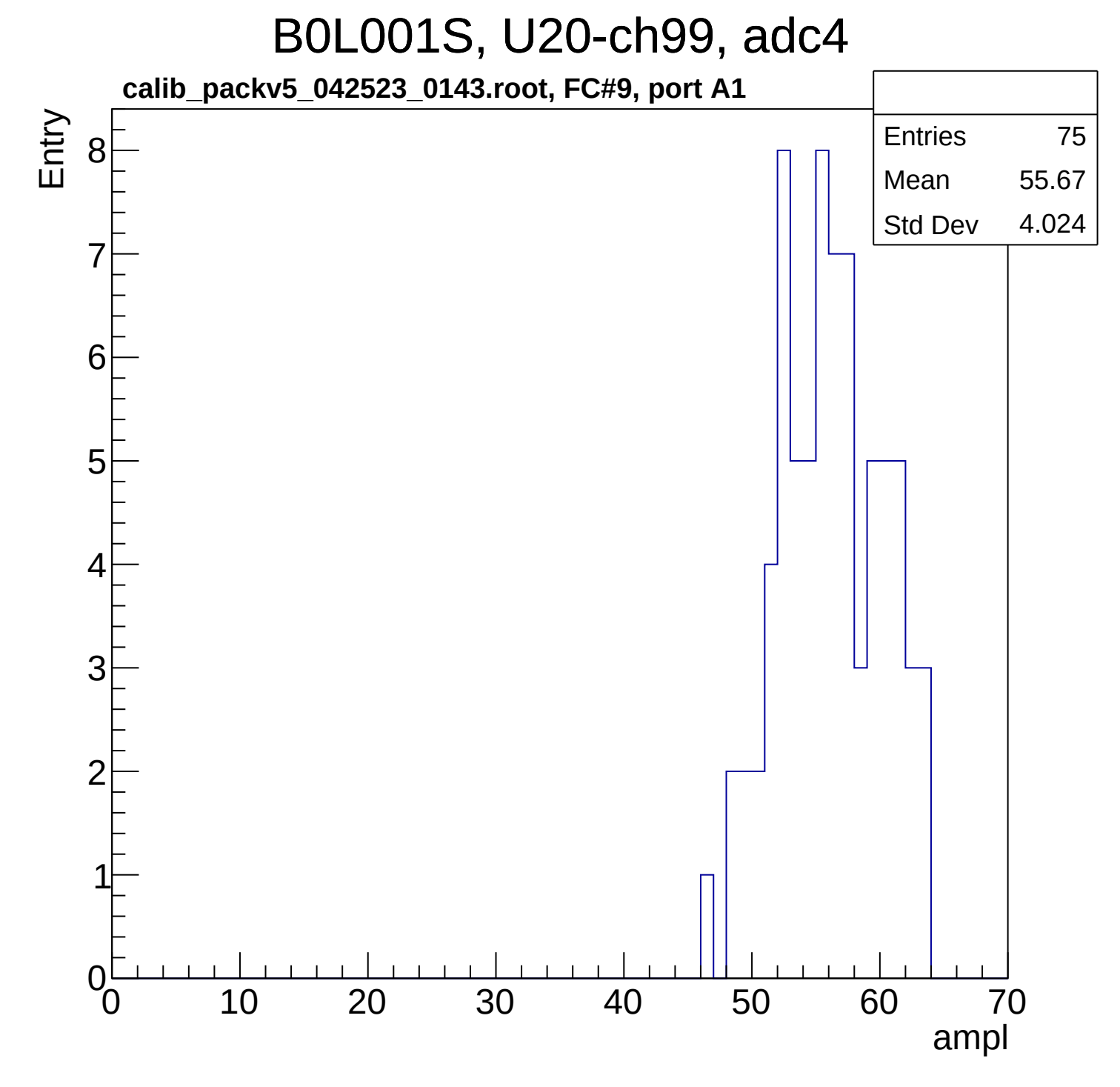
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 55.67
Std Dev 4.024
Entry
ampl
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2
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4
5
6
7
8
0
10
20
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40
50
60
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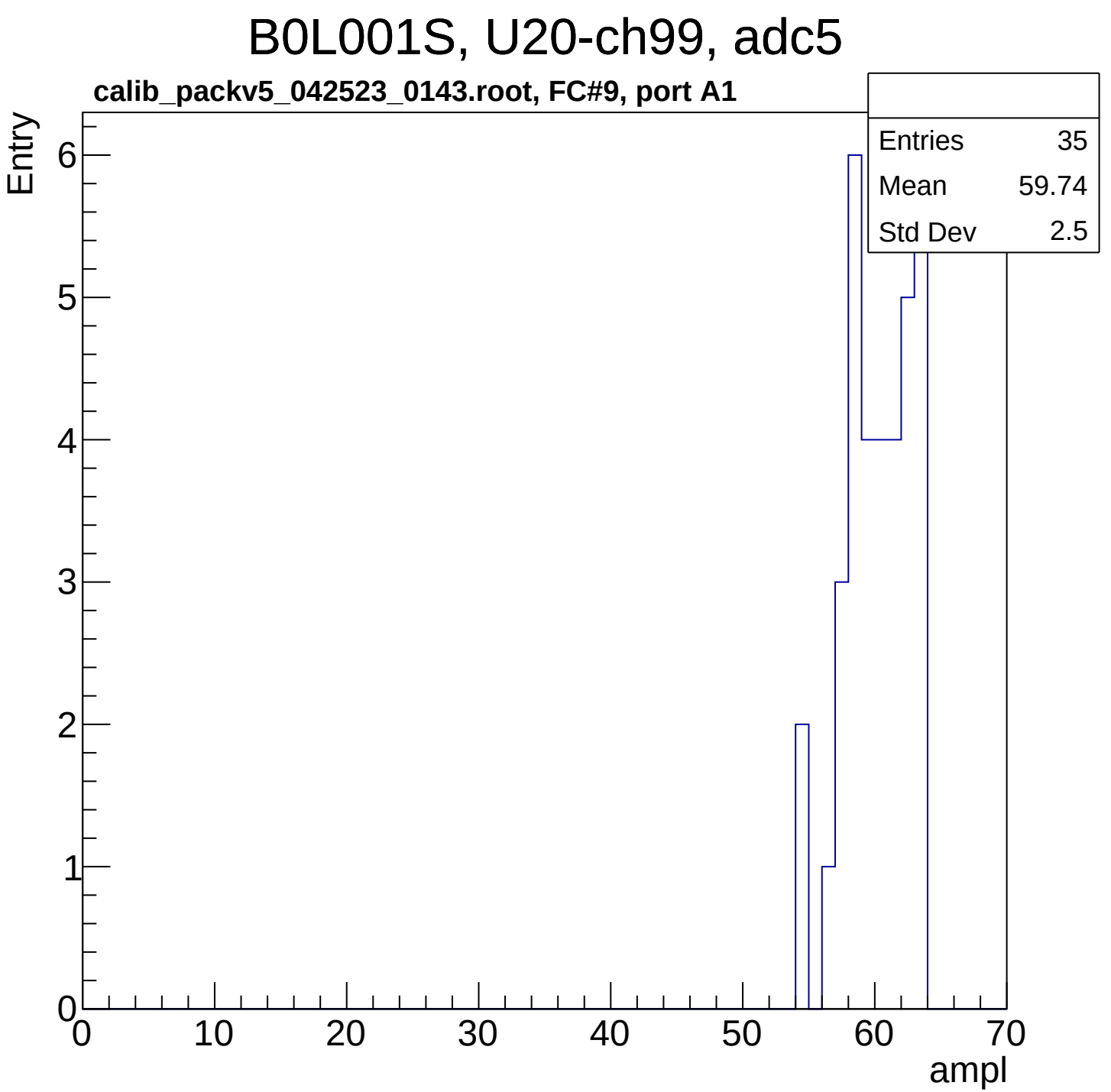
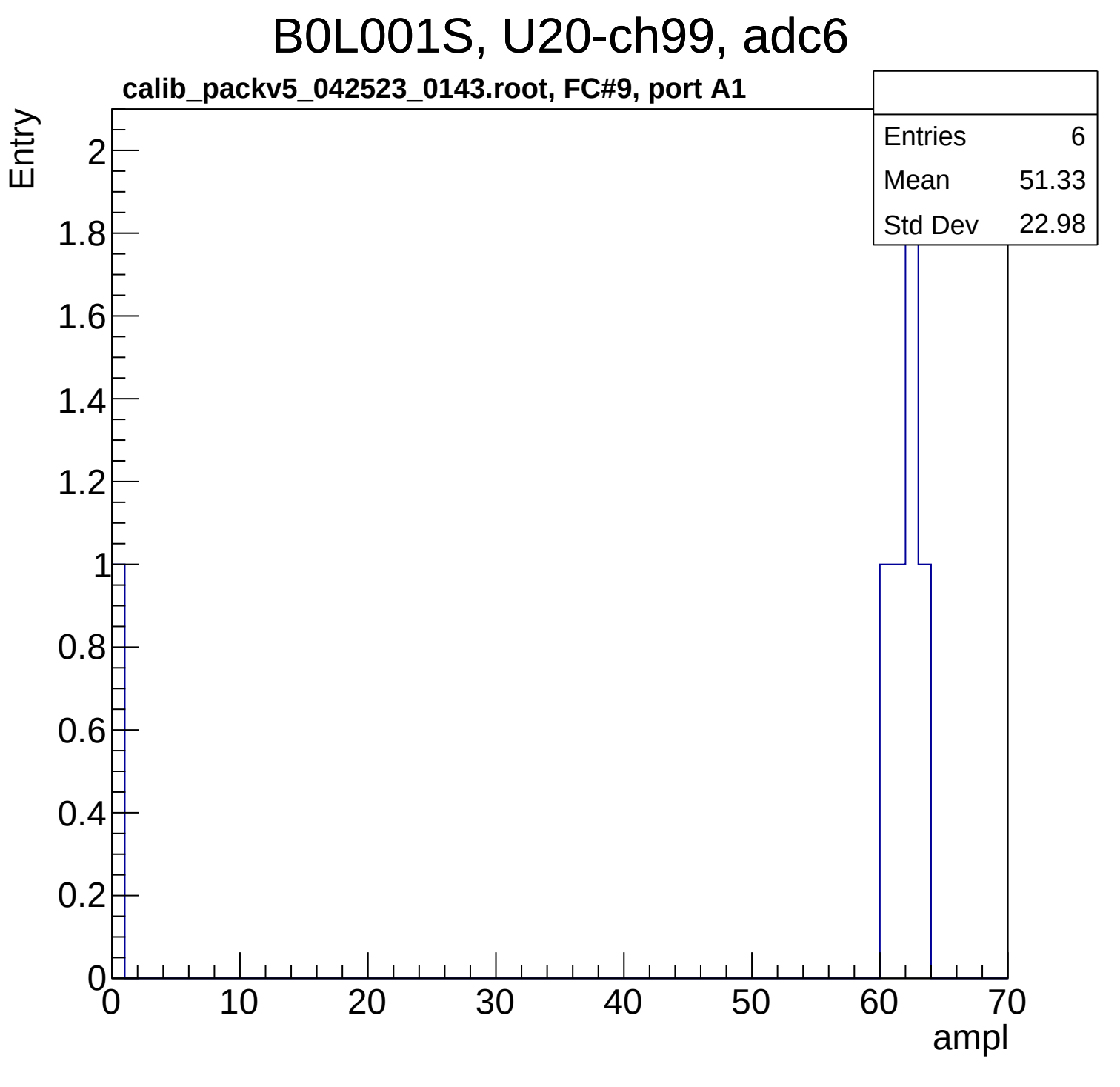

B0L001S, U20-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.74
Std Dev 2.5
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.33
Std Dev 22.98
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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20
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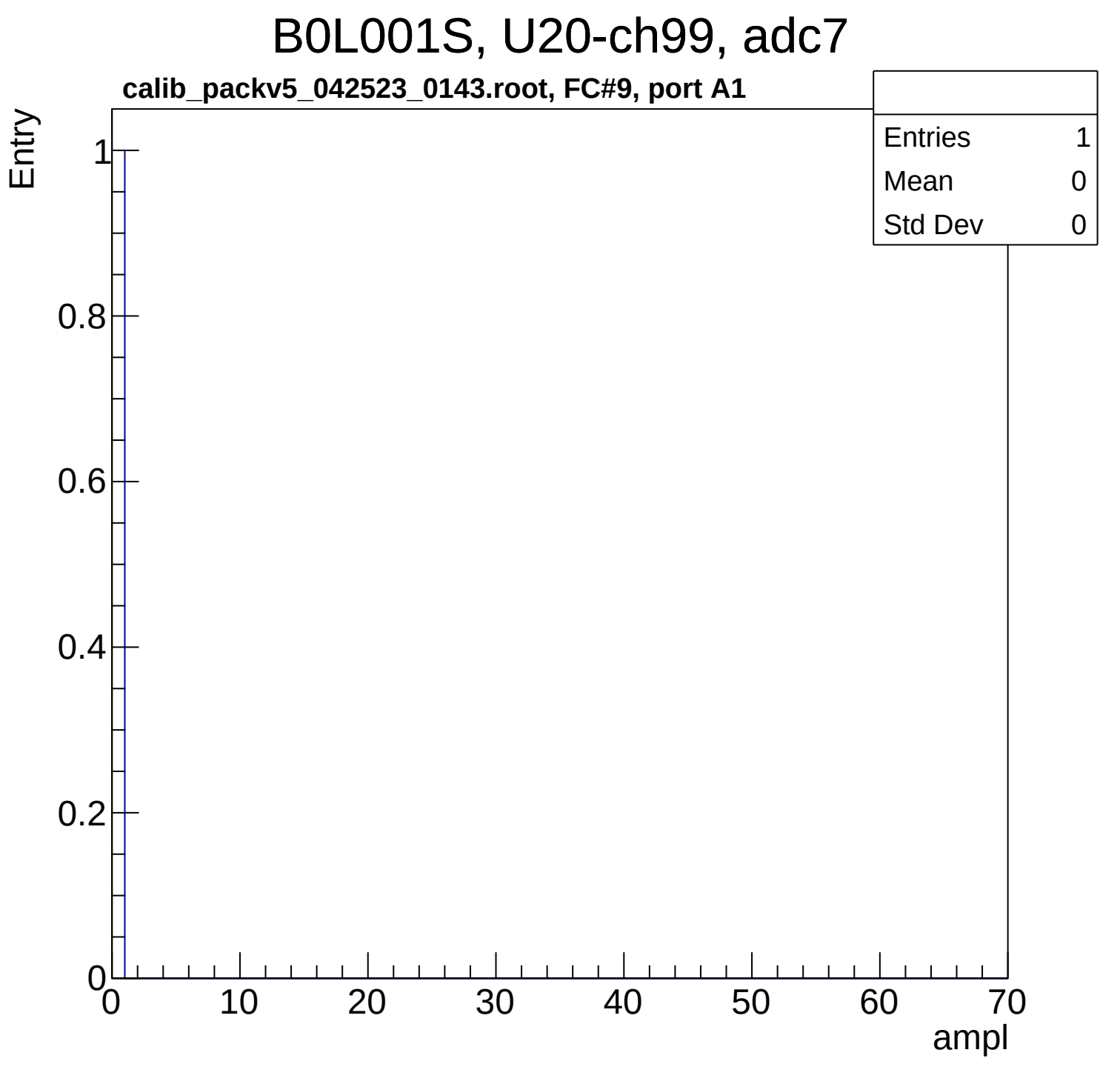
40
50
60
70


B0L001S, U20-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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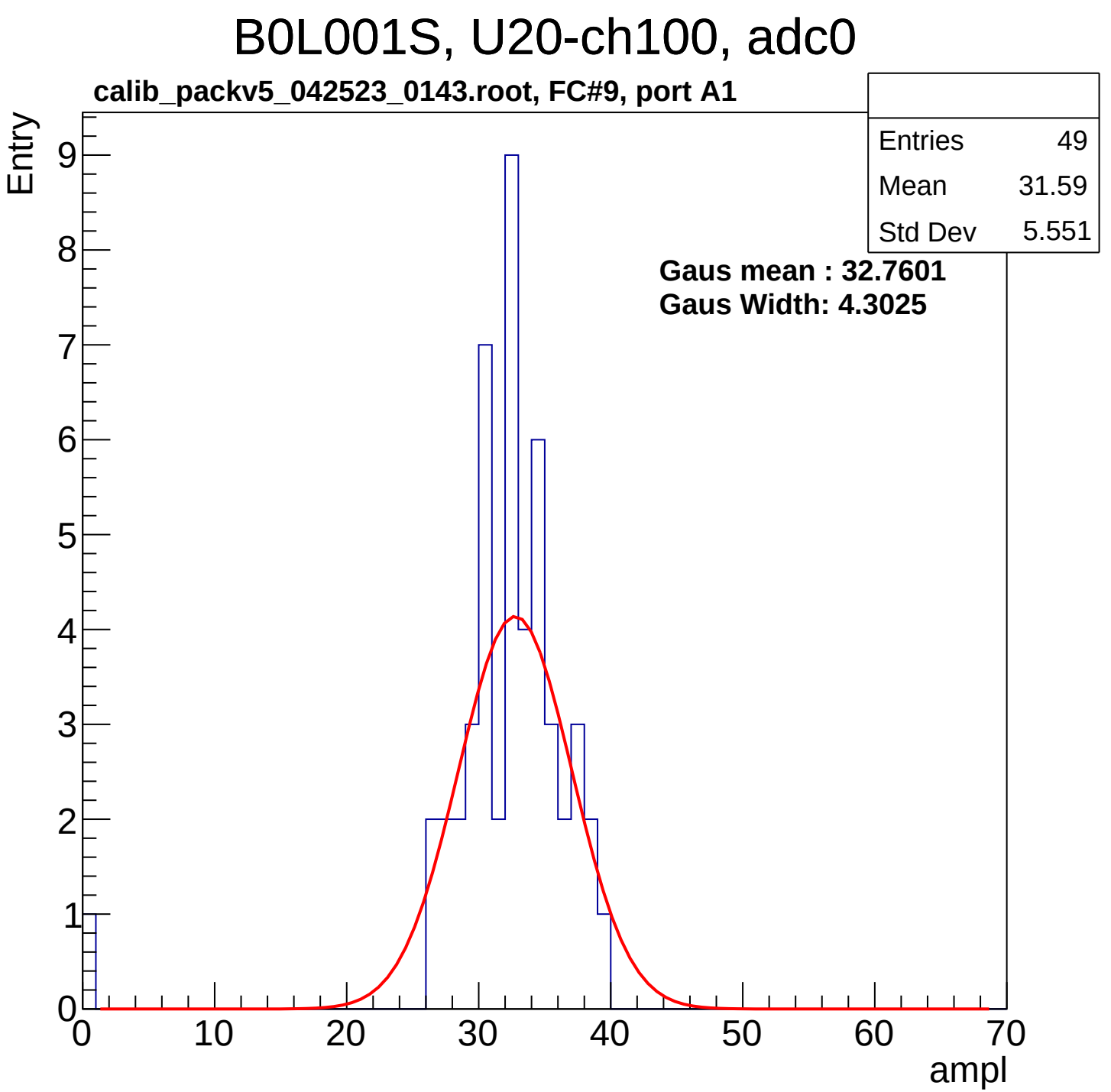
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 31.59
Std Dev 5.551
Gaus mean : 32.7601
Gaus Width: 4.3025
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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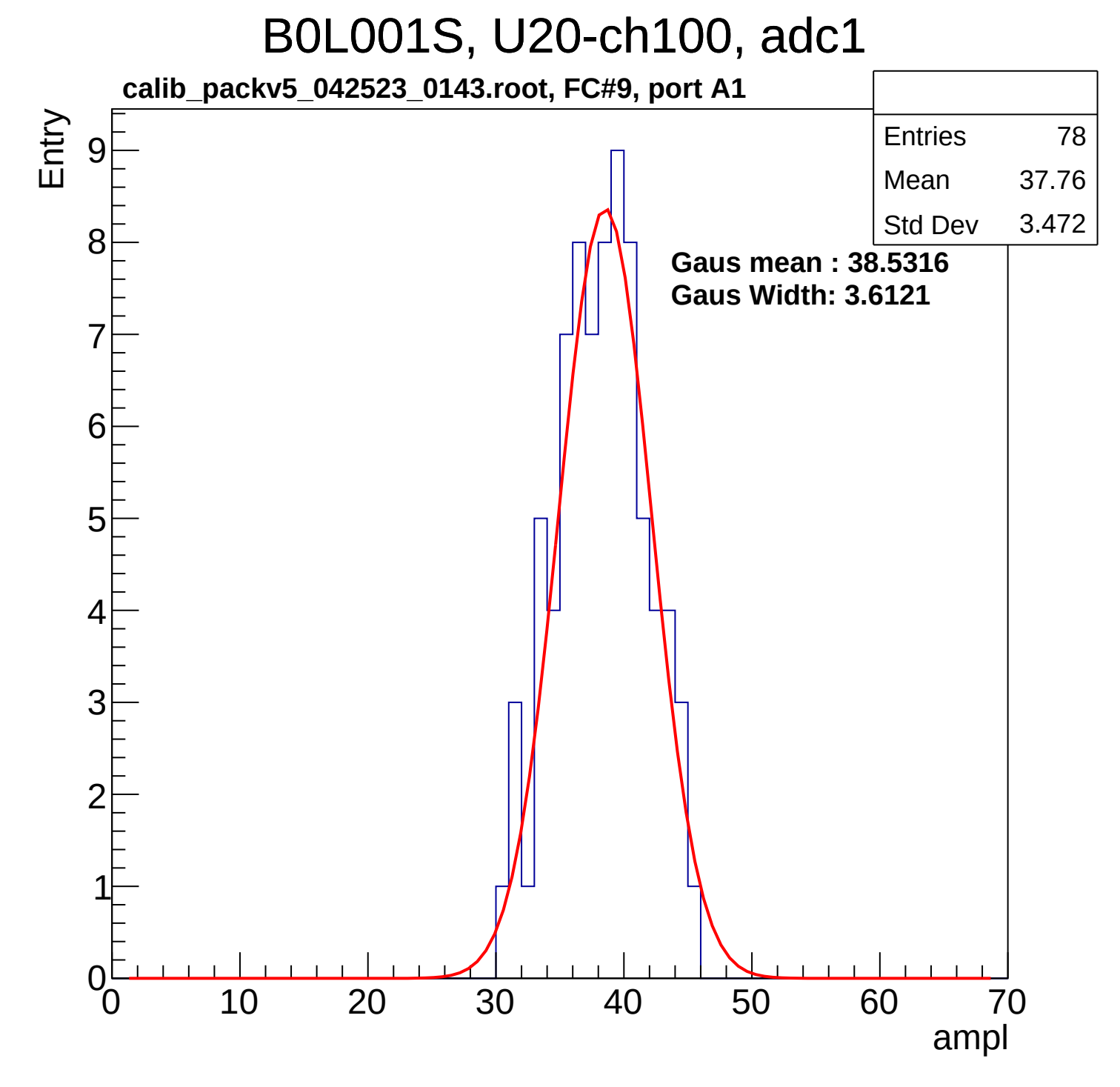
B0L001S, U20-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.76
Std Dev 3.472
Gaus mean : 38.5316
Gaus Width: 3.6121
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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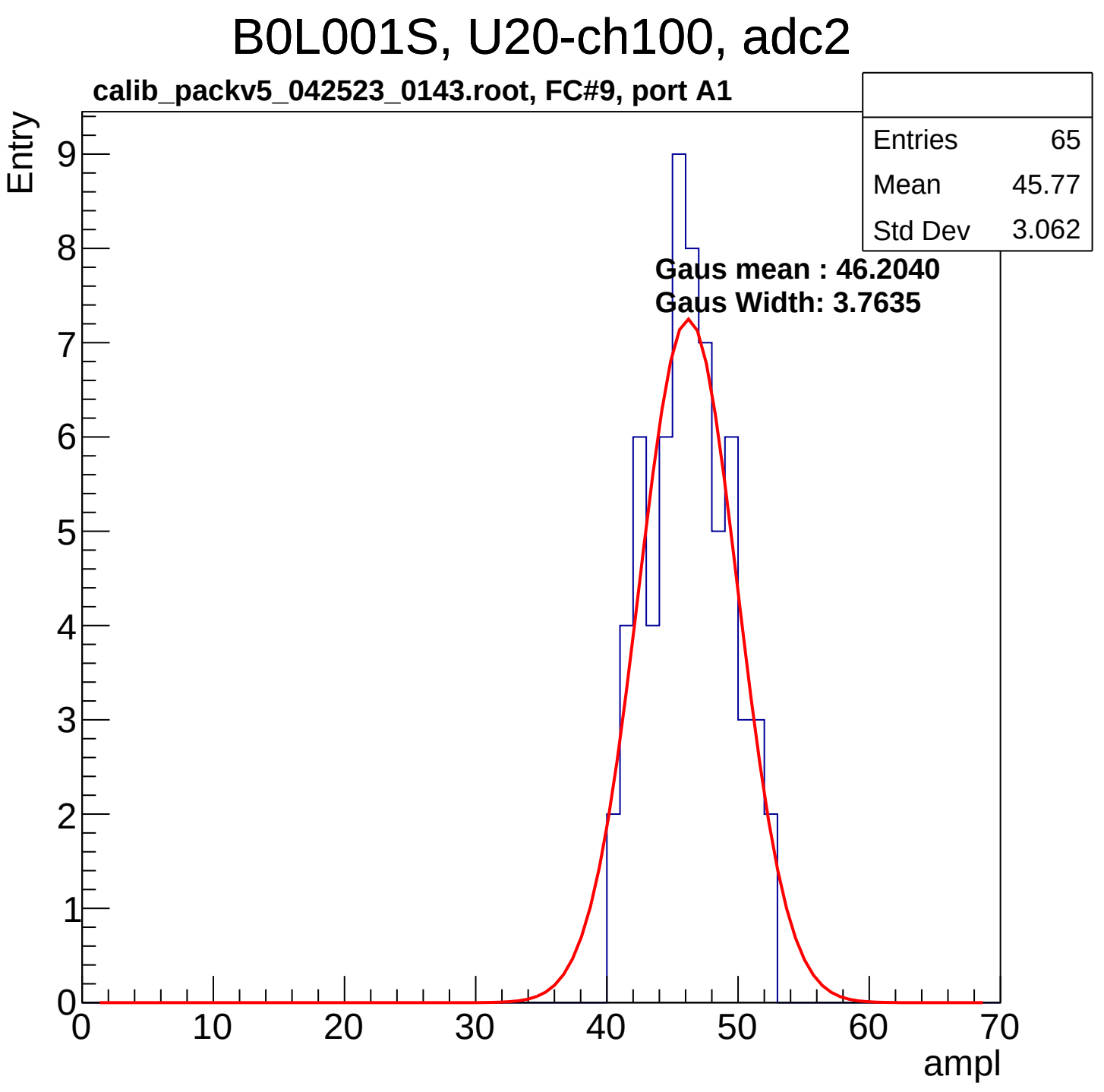
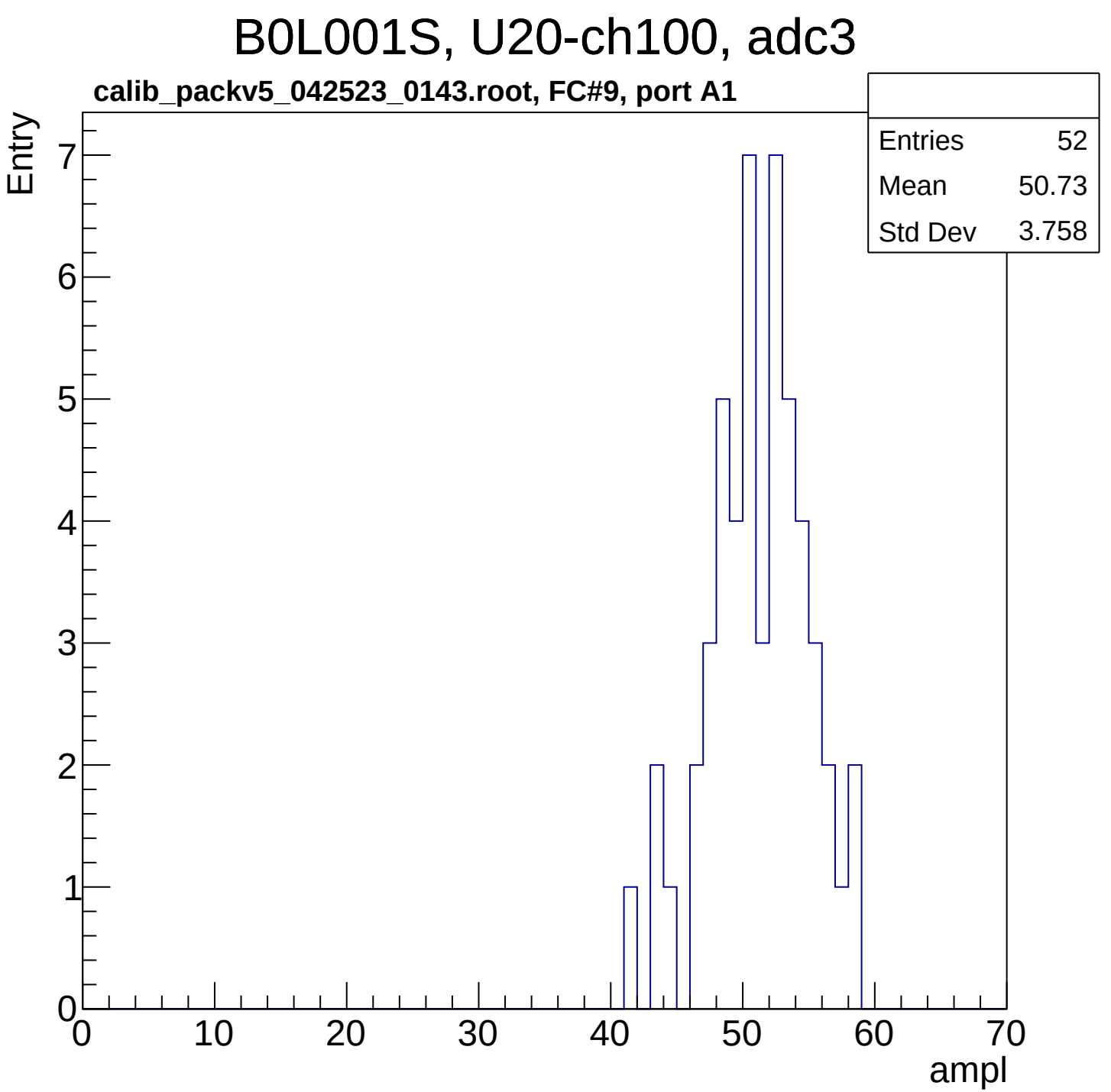
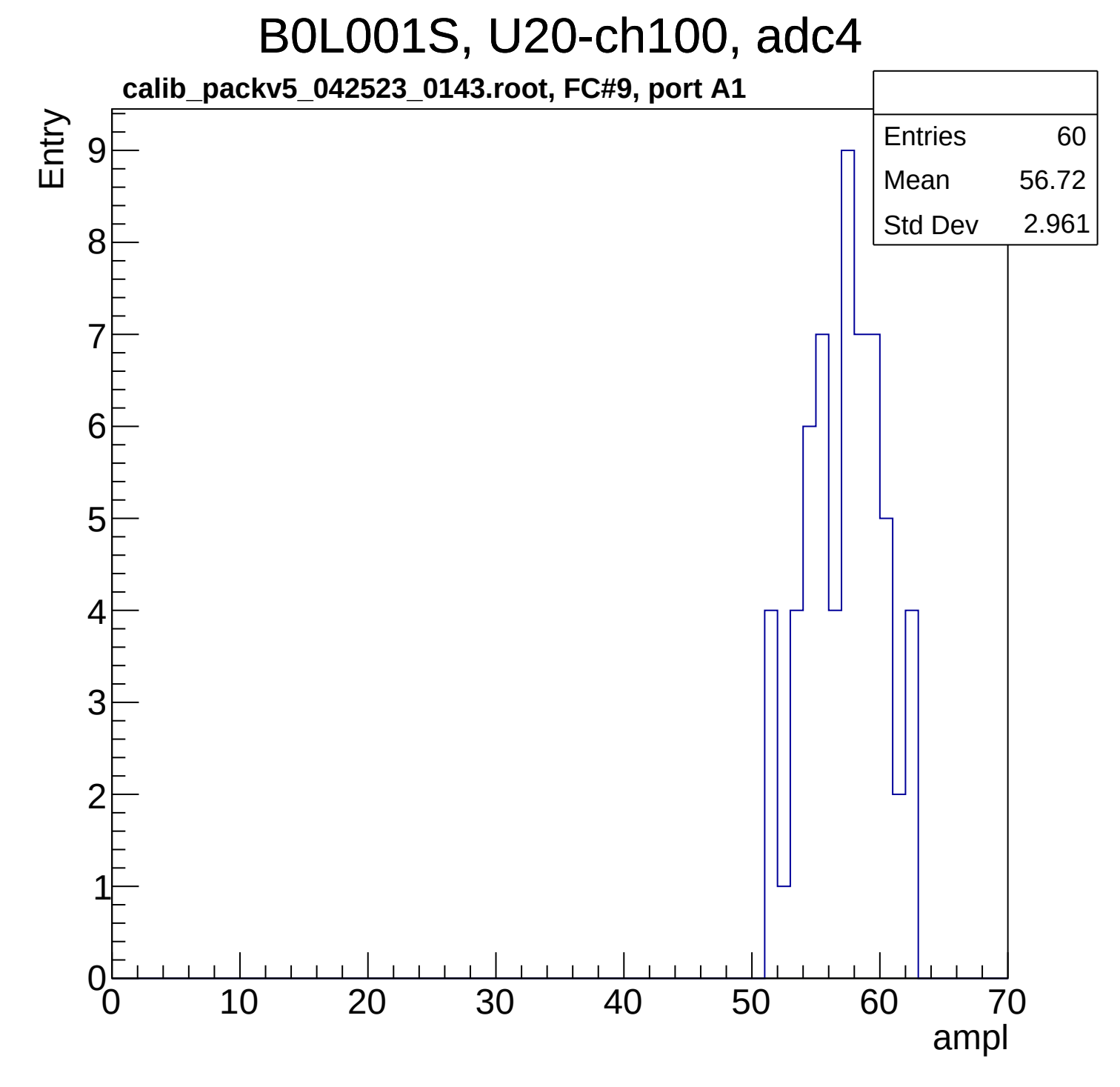
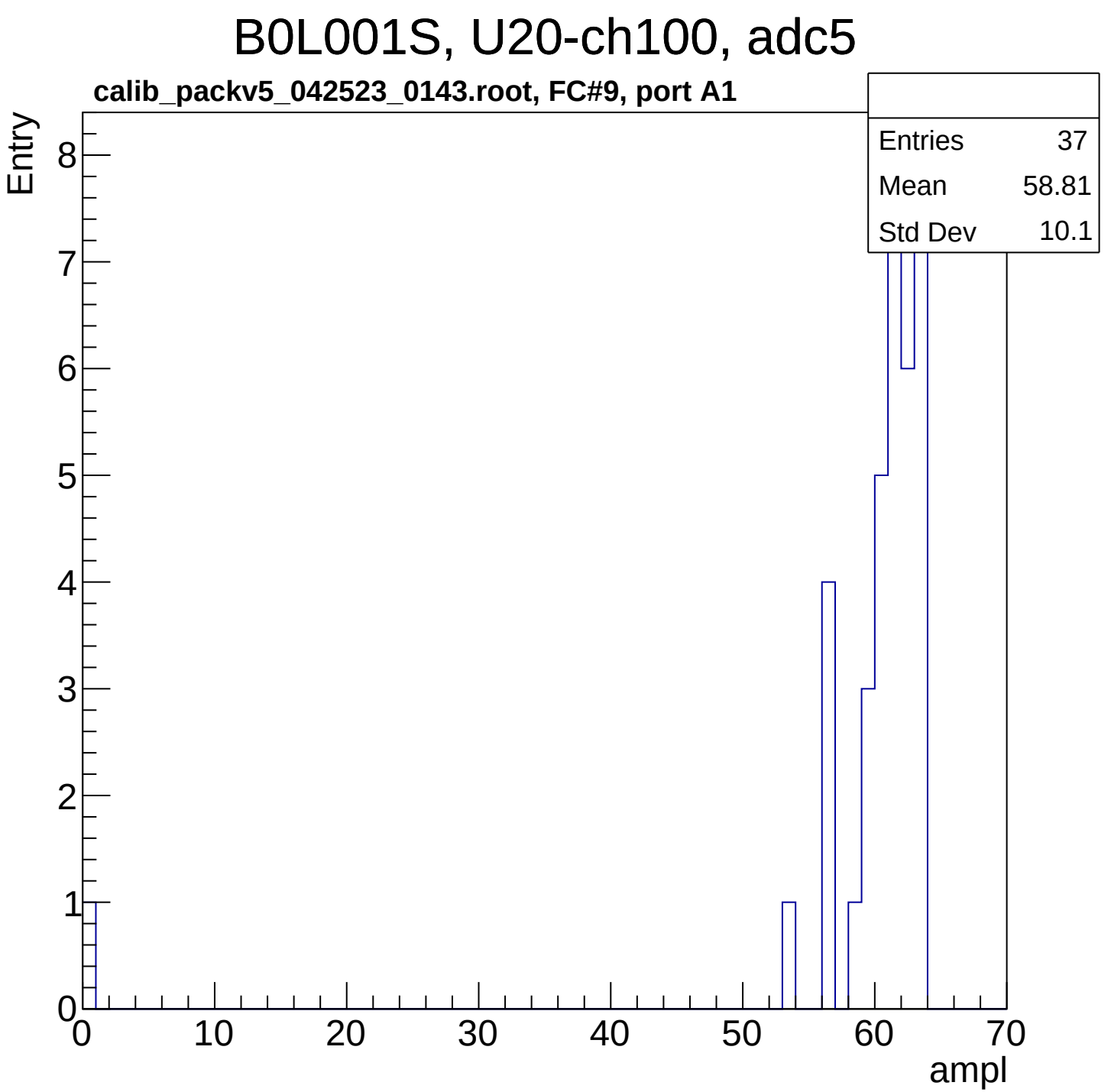
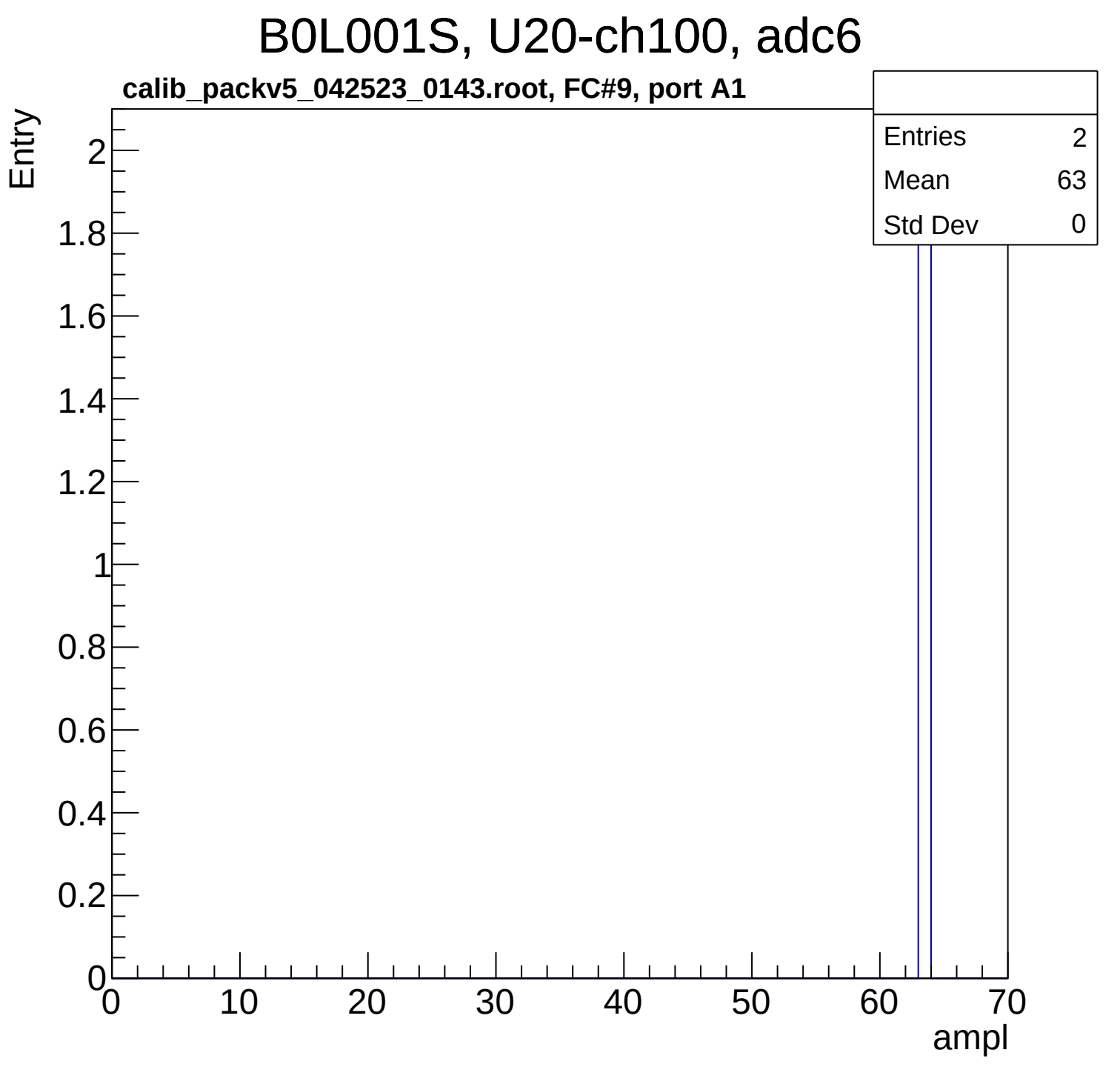
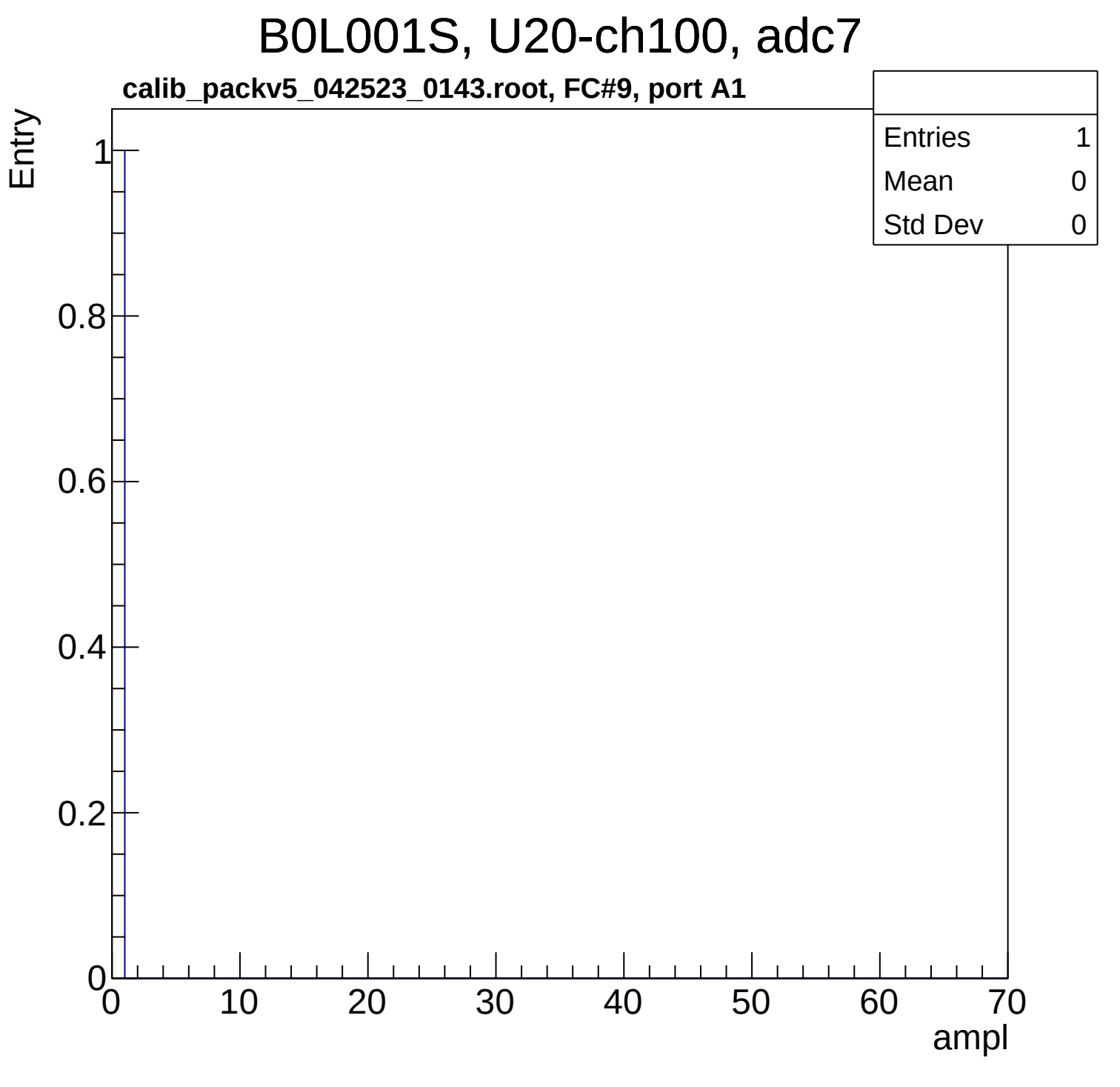

B0L001S, U20-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.77
Std Dev 3.062
Gaus mean : 46.2040
Gaus Width: 3.7635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

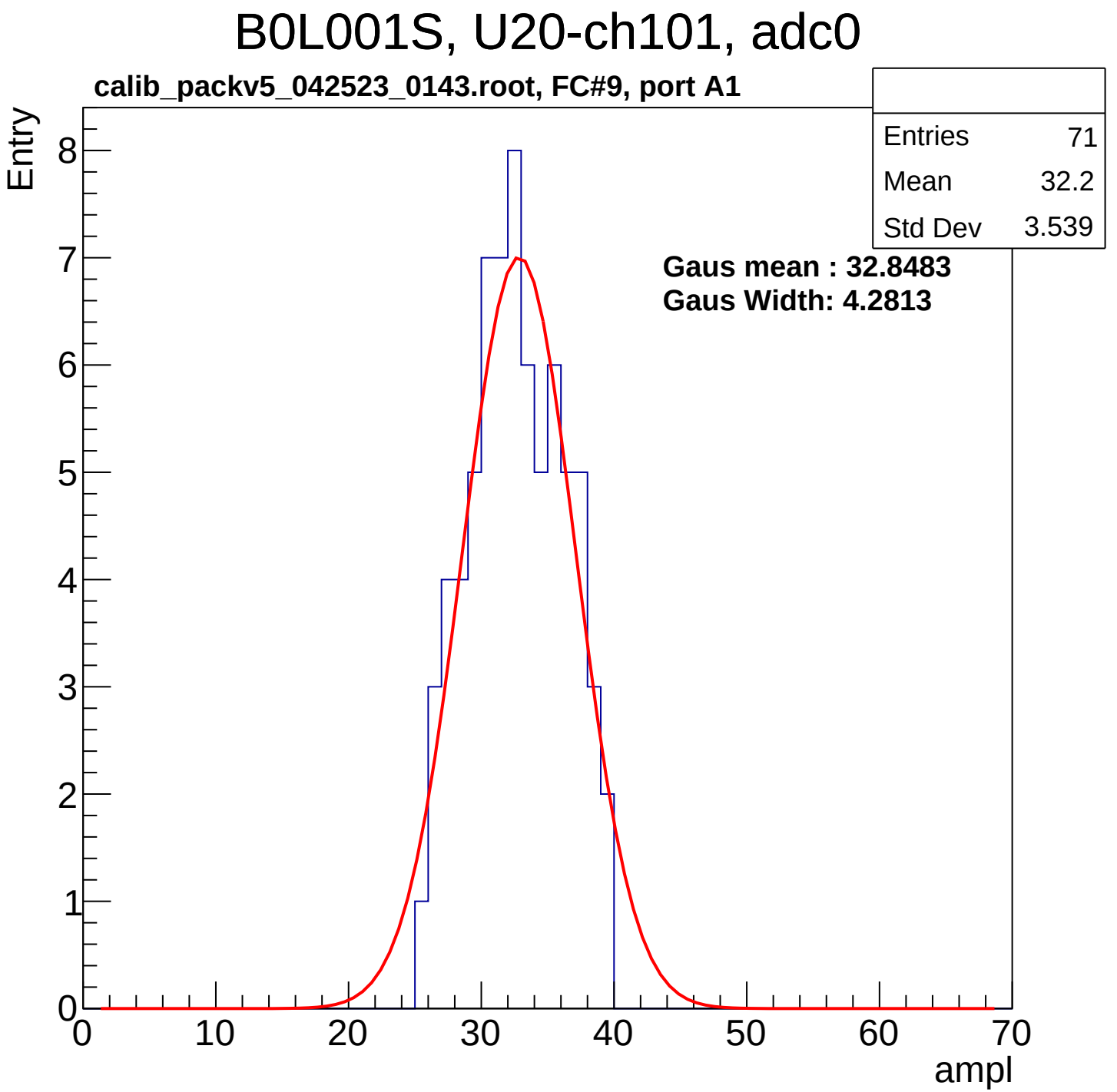

B0L001S, U20-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.73
Std Dev 3.758
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

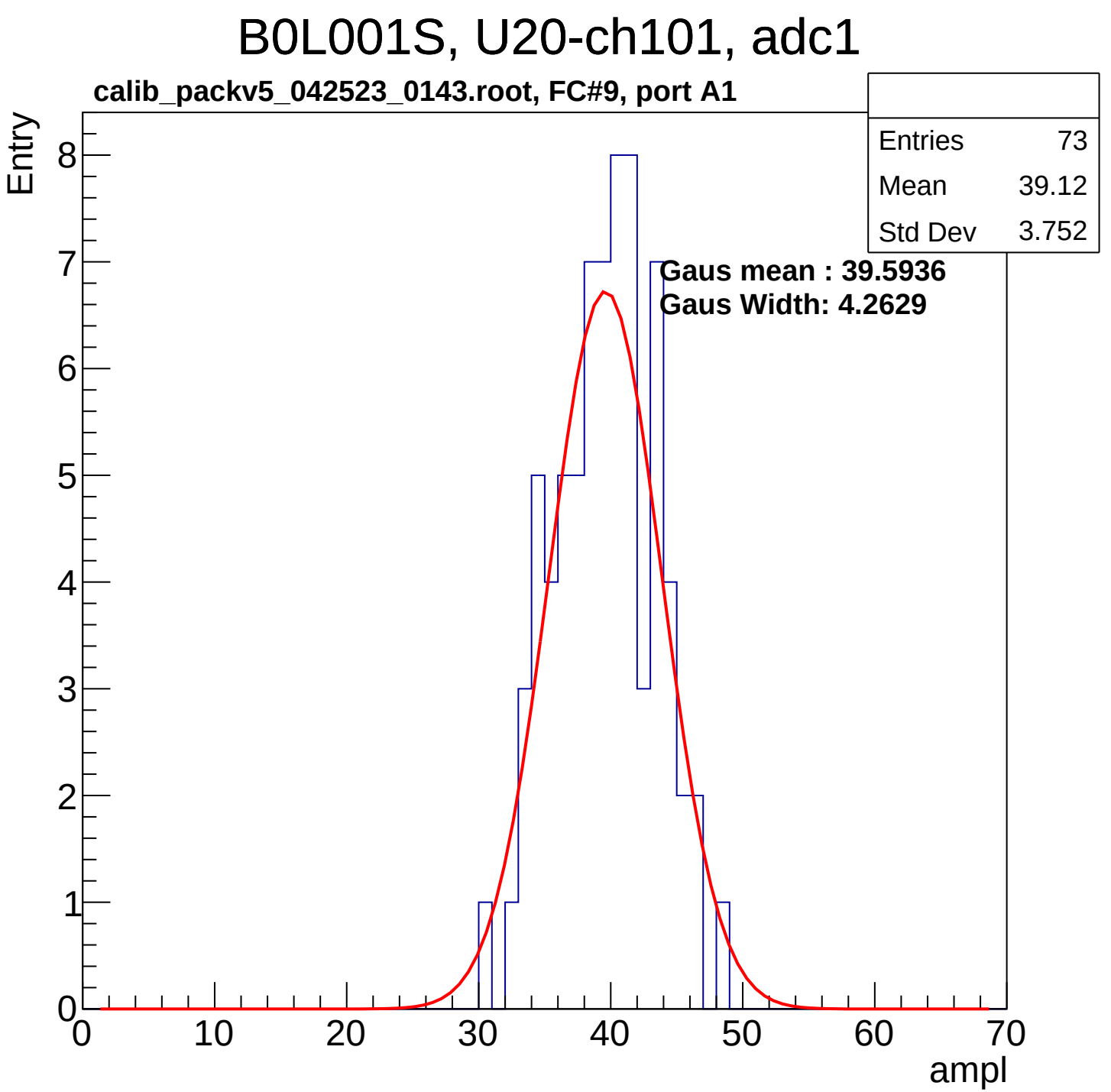

B0L001S, U20-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.72
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

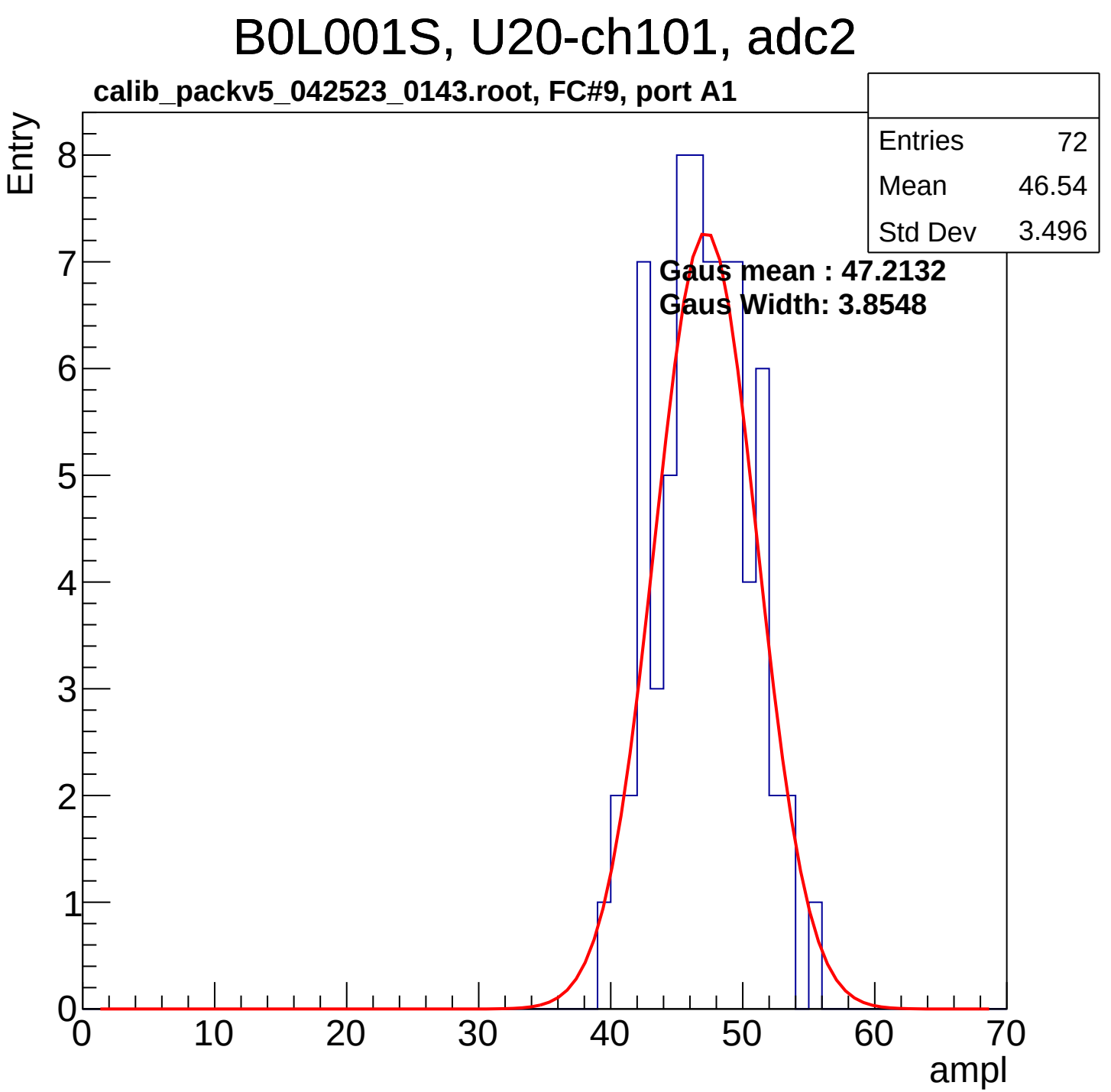

B0L001S, U20-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.81
Std Dev 10.1
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

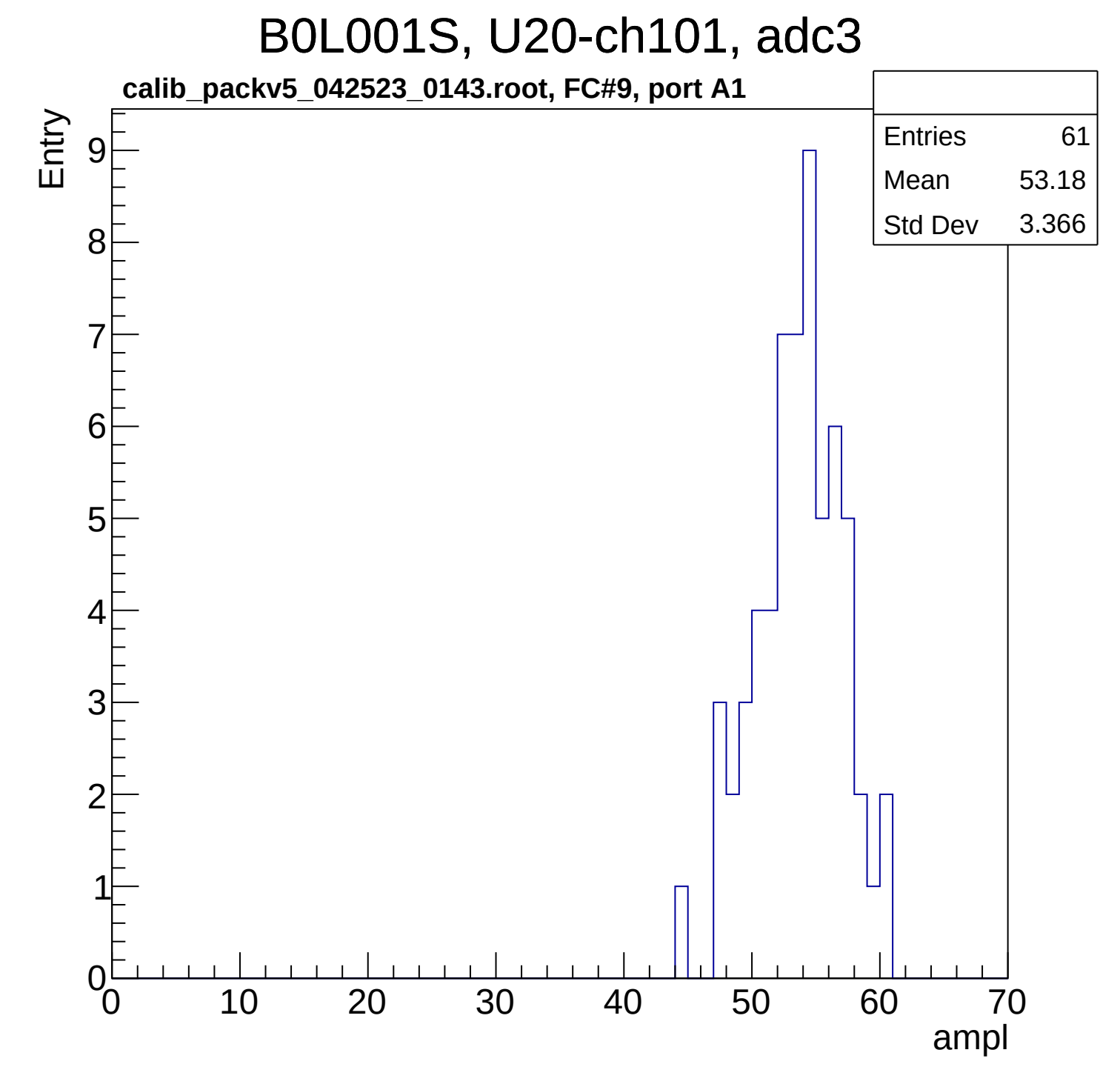

B0L001S, U20-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

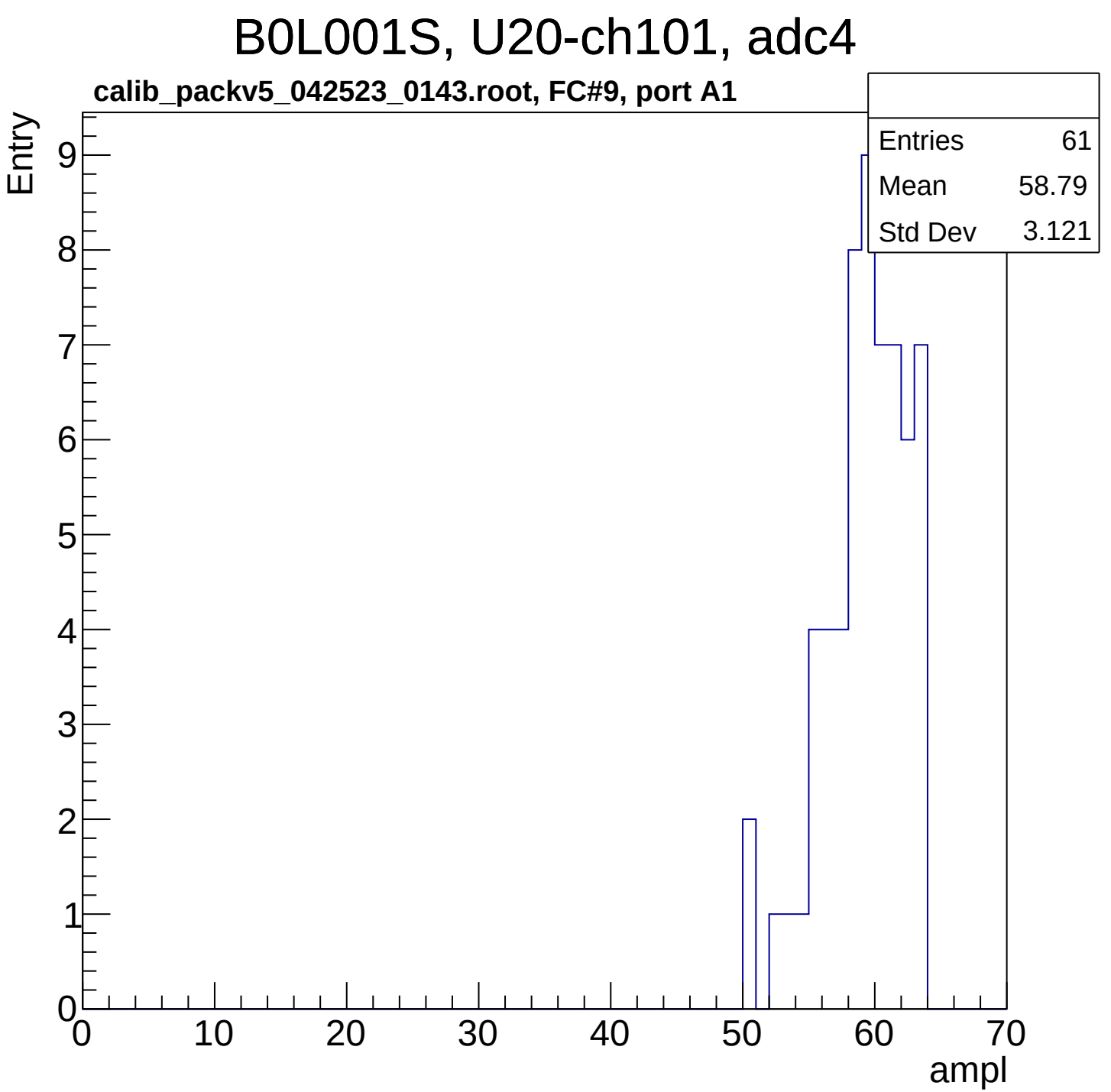

B0L001S, U20-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.2
Std Dev 3.539
Gaus mean : 32.8483
Gaus Width: 4.2813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 39.12
Std Dev 3.752
Gaus mean : 39.5936
Gaus Width: 4.2629
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 46.54
Std Dev 3.496
Gaus mean : 47.2132
Gaus Width: 3.8548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 53.18
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 58.79
Std Dev 3.121
Entry
ampl
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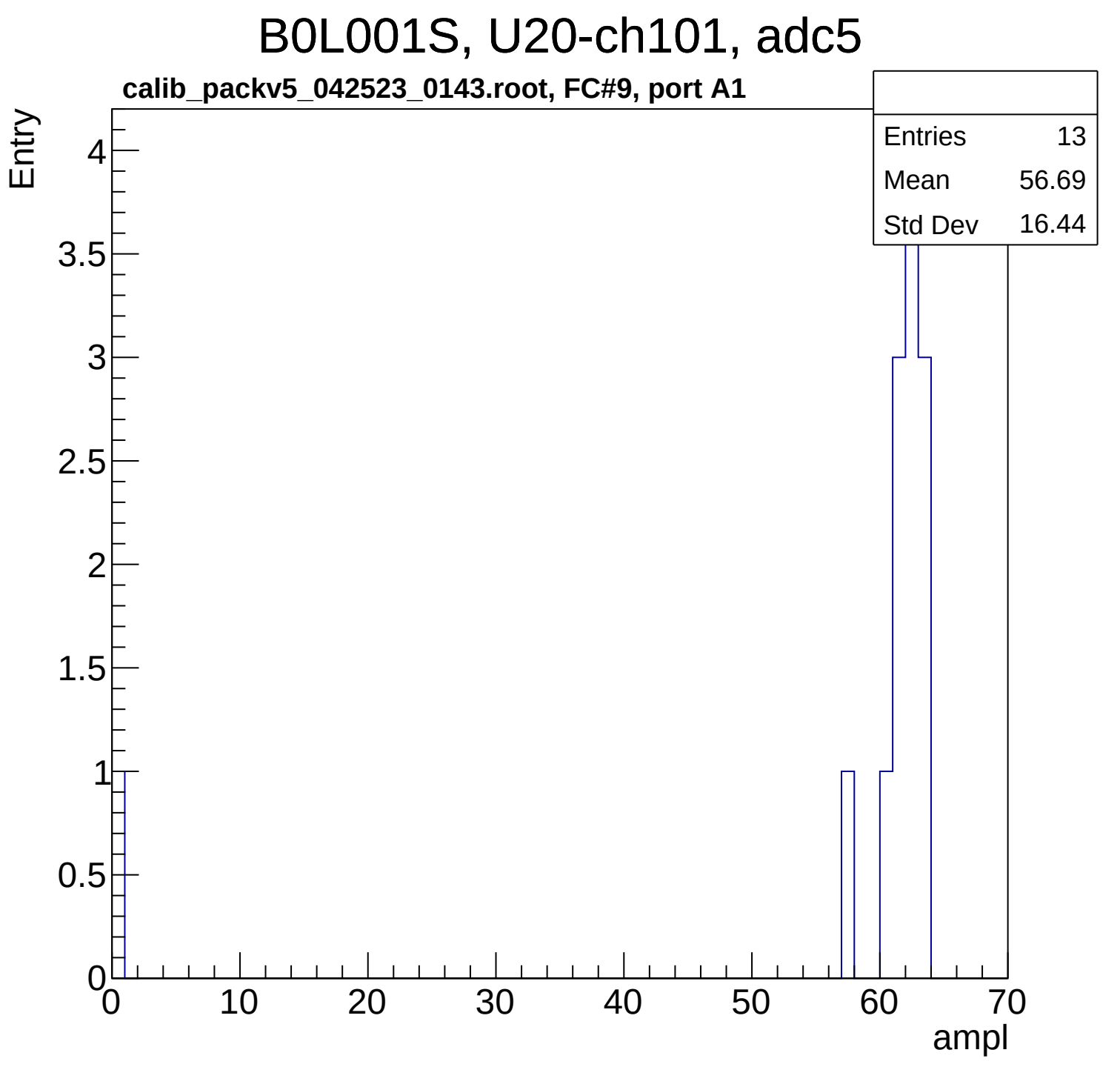
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 56.69
Std Dev 16.44
Entry
ampl
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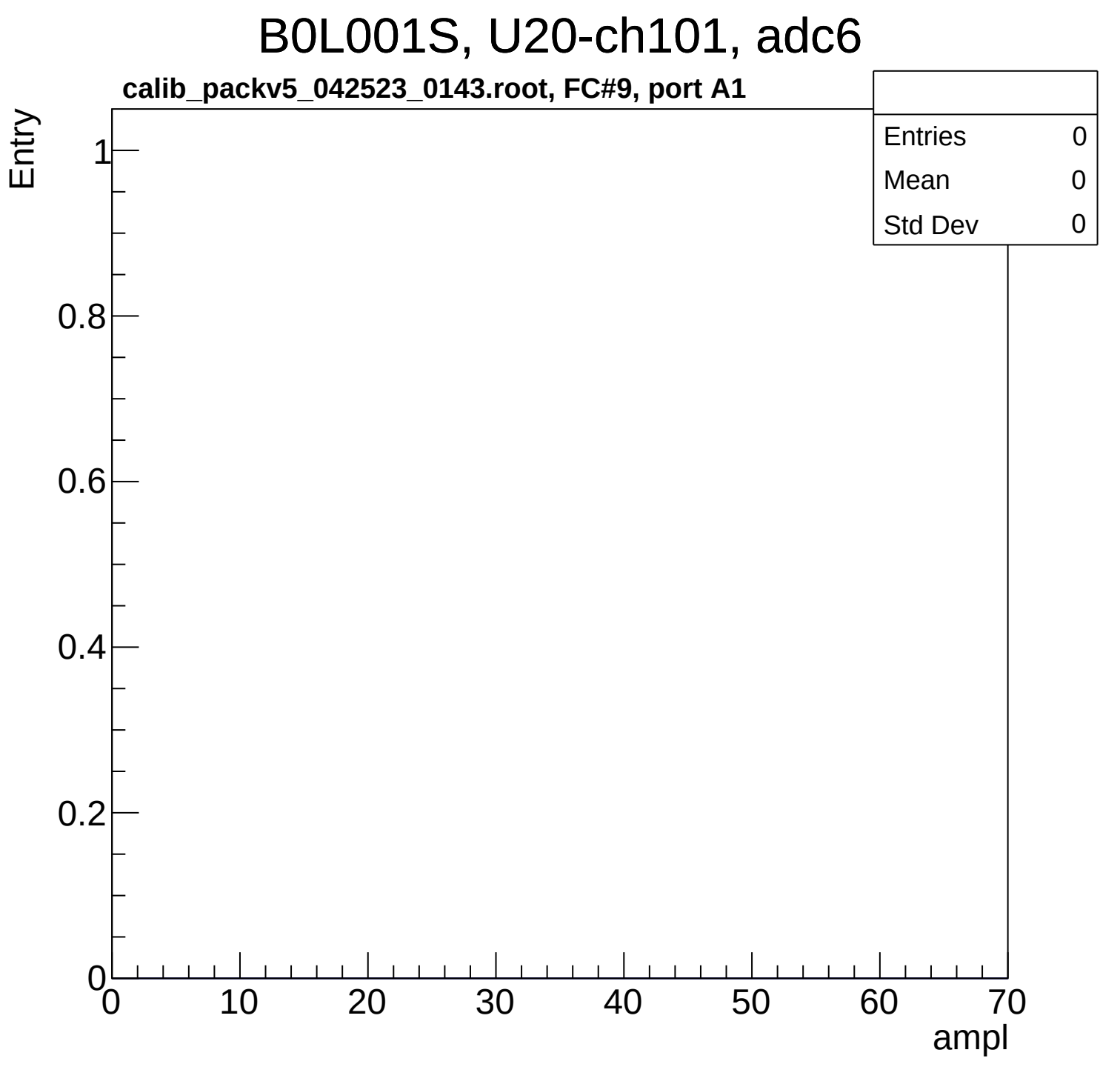
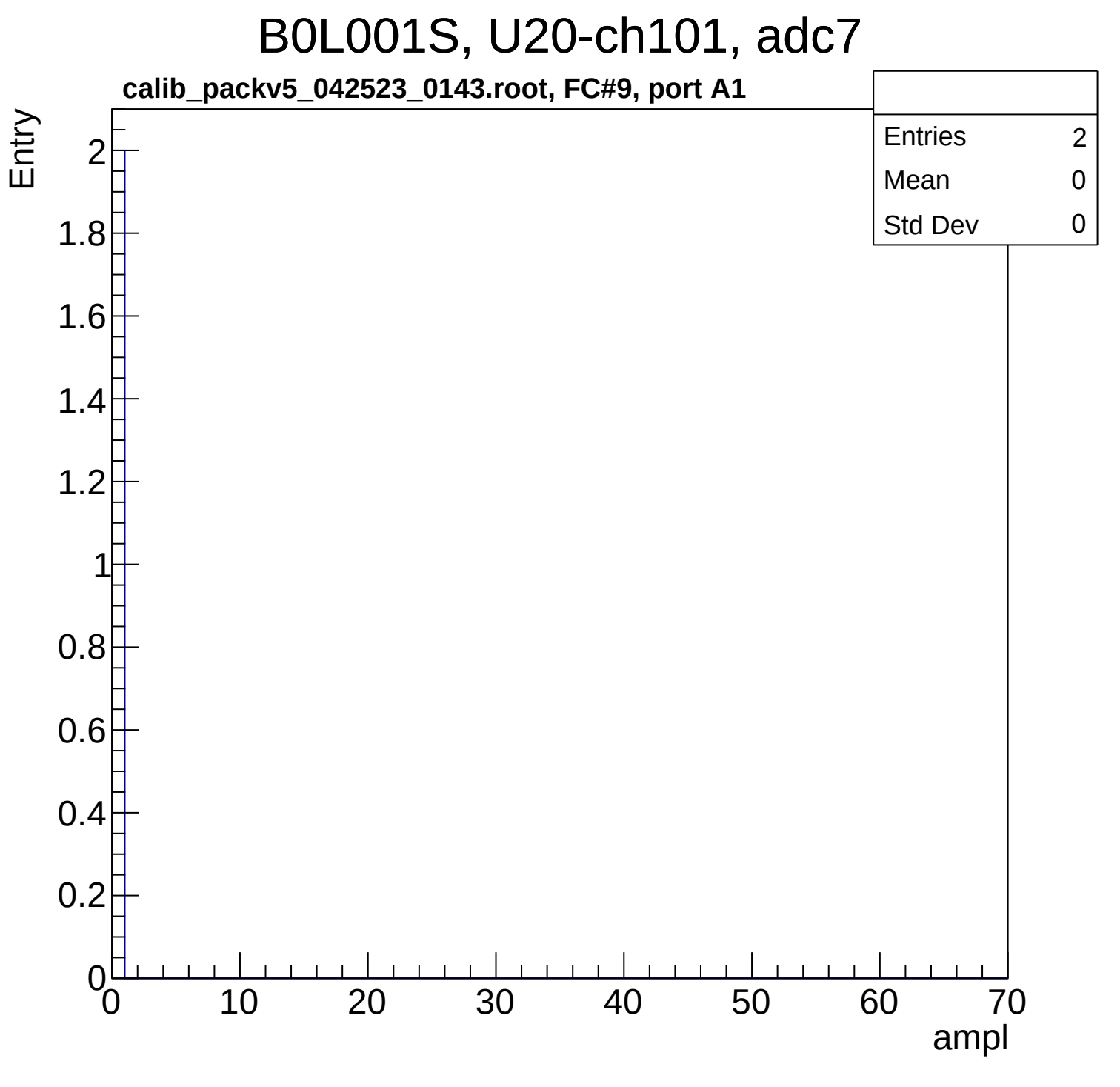
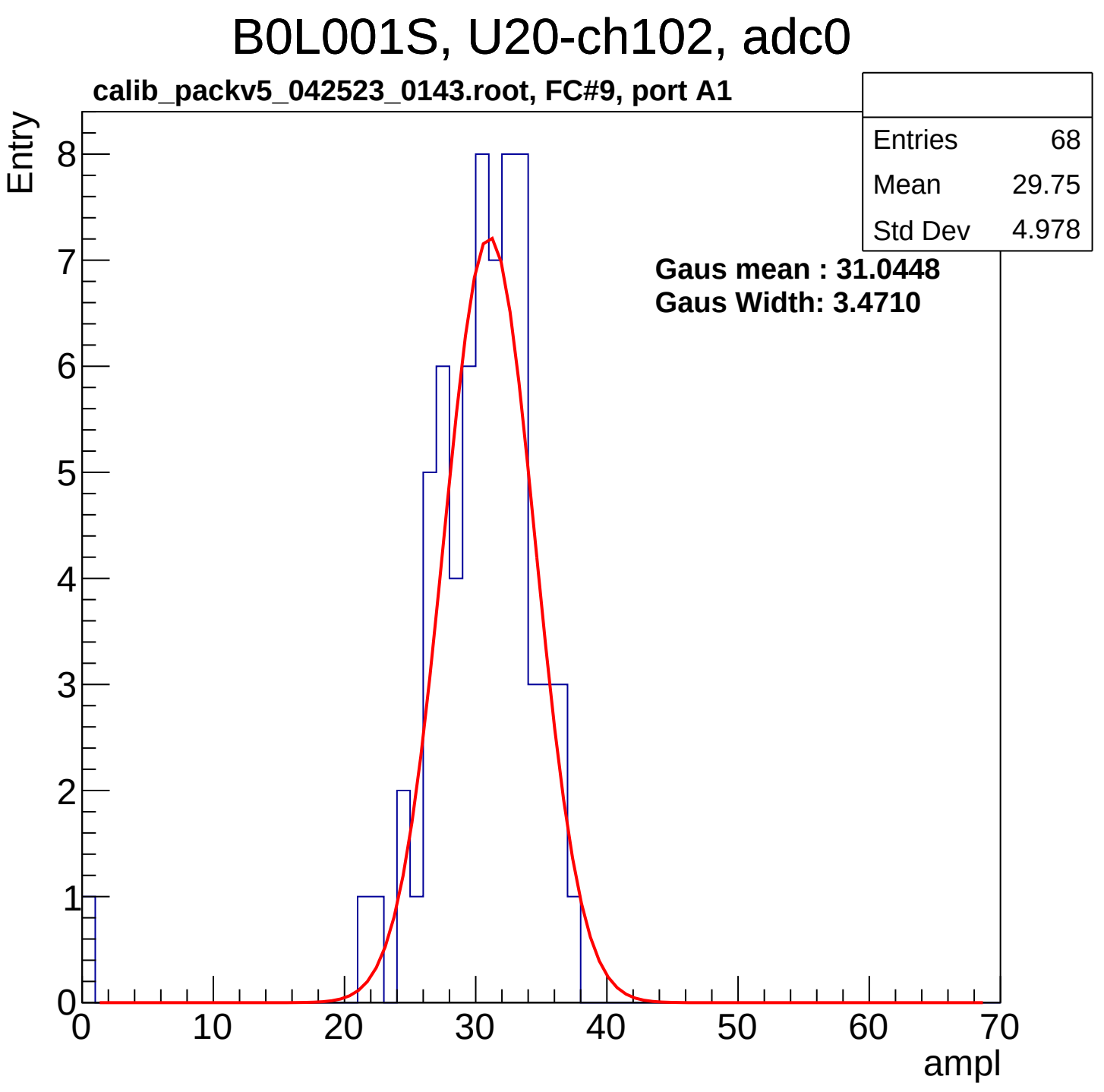
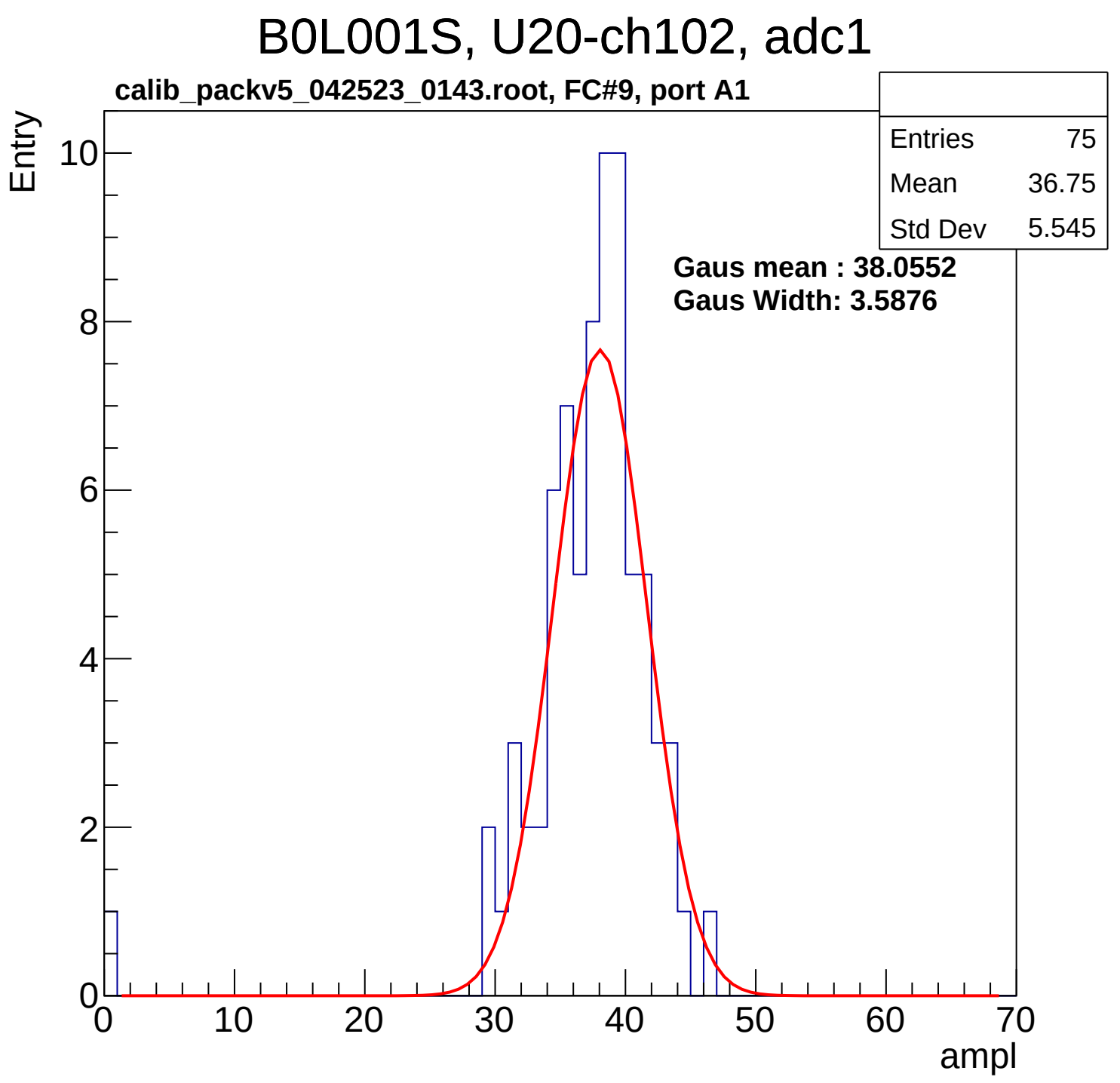
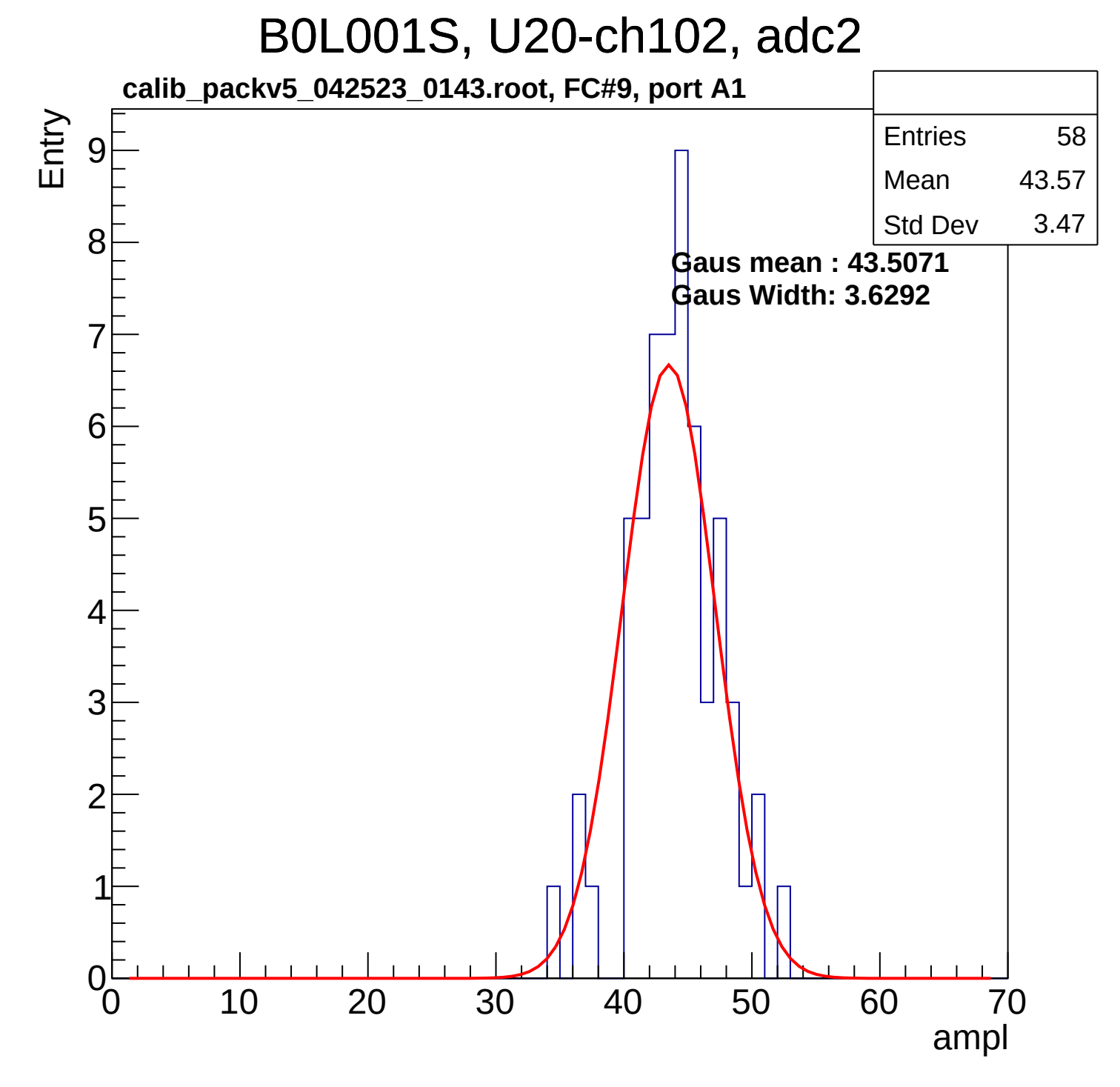
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

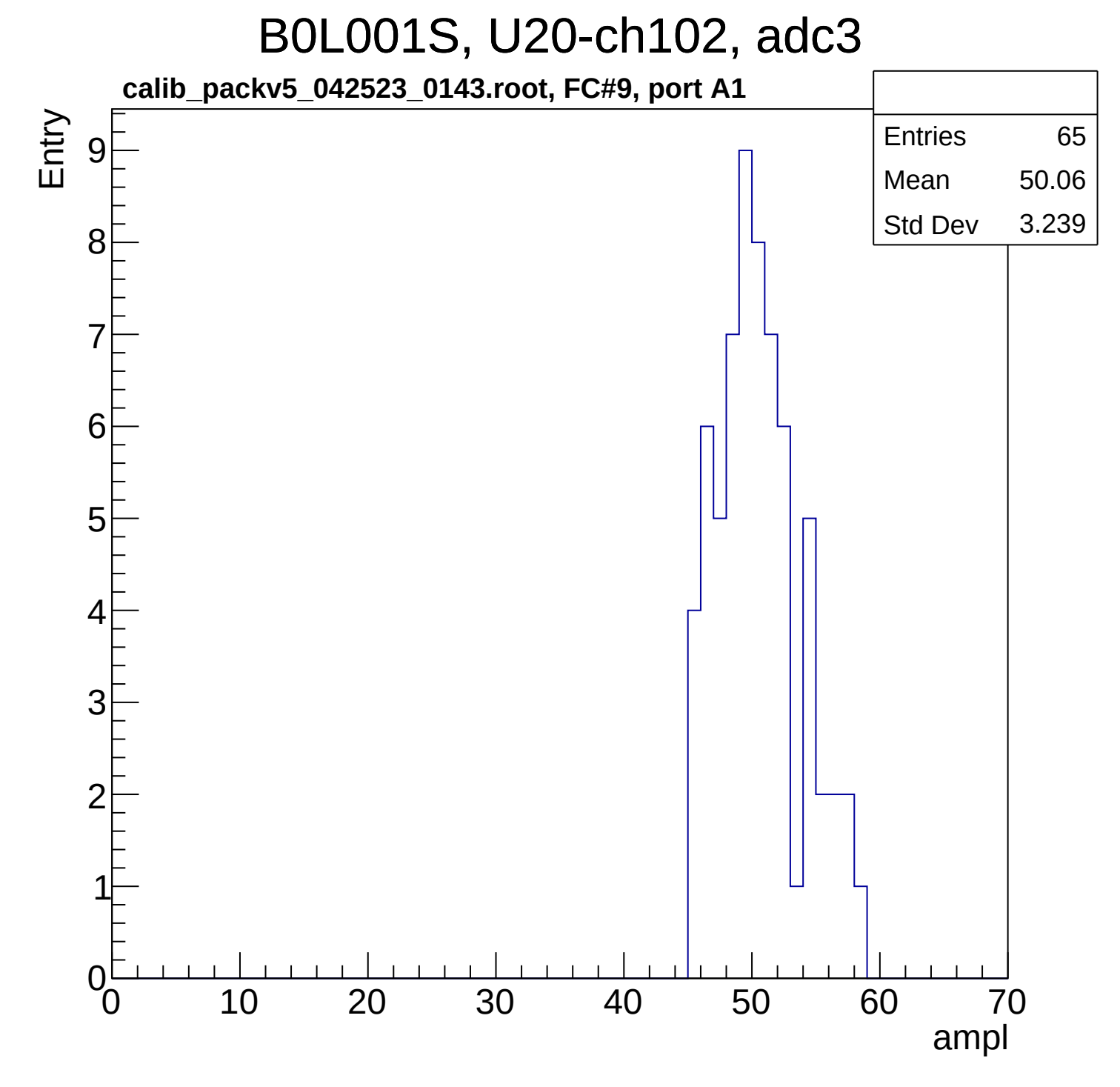

B0L001S, U20-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

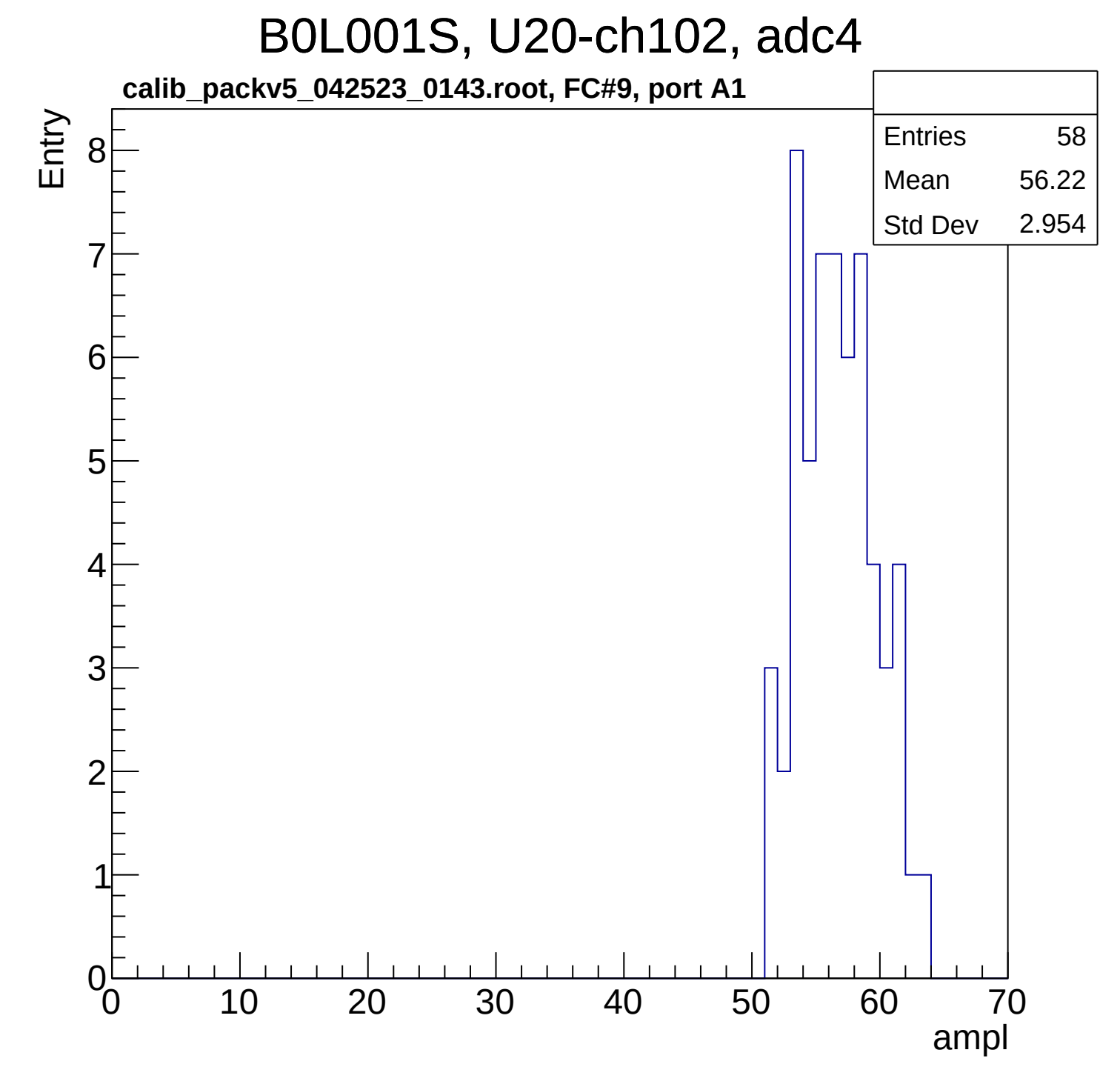

B0L001S, U20-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 29.75
Std Dev 4.978
Gaus mean : 31.0448
Gaus Width: 3.4710
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 36.75
Std Dev 5.545
Gaus mean : 38.0552
Gaus Width: 3.5876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.57
Std Dev 3.47
Gaus mean : 43.5071
Gaus Width: 3.6292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.06
Std Dev 3.239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.22
Std Dev 2.954
Entry
ampl
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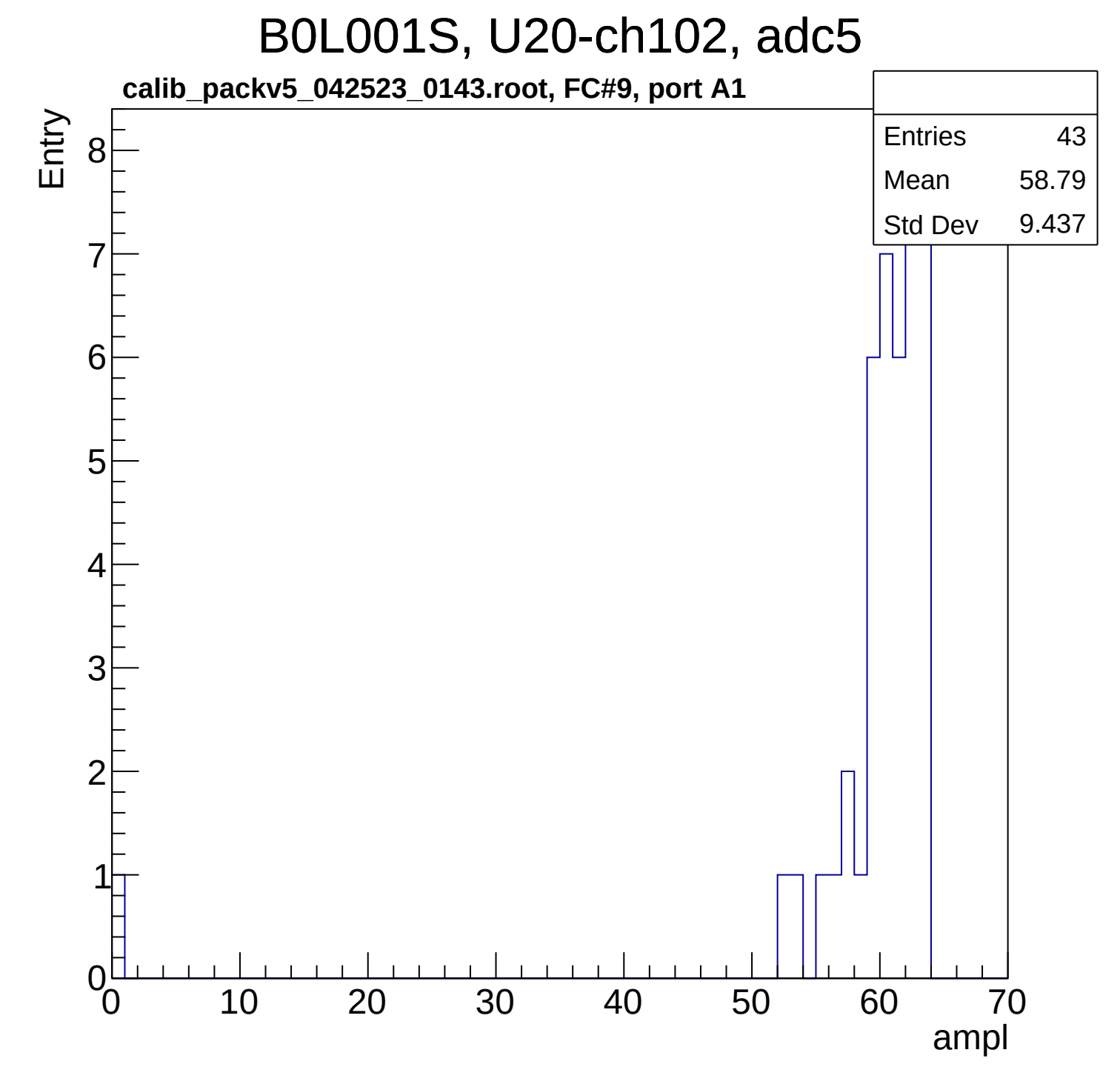
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.79
Std Dev 9.437
Entry
ampl
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2
3
4
5
6
7
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0
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20
30
40
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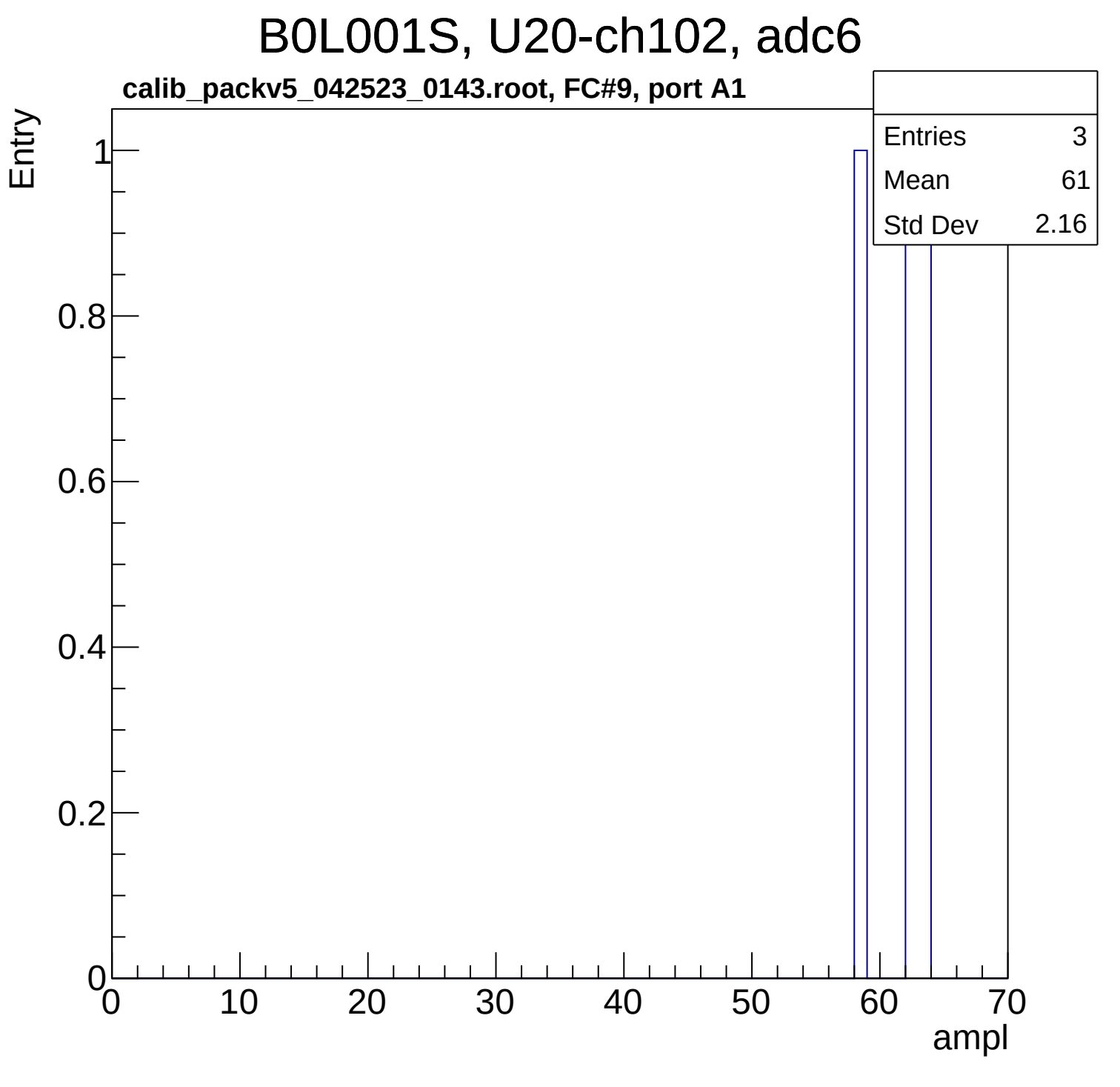
60
70


B0L001S, U20-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 2.16
Entry
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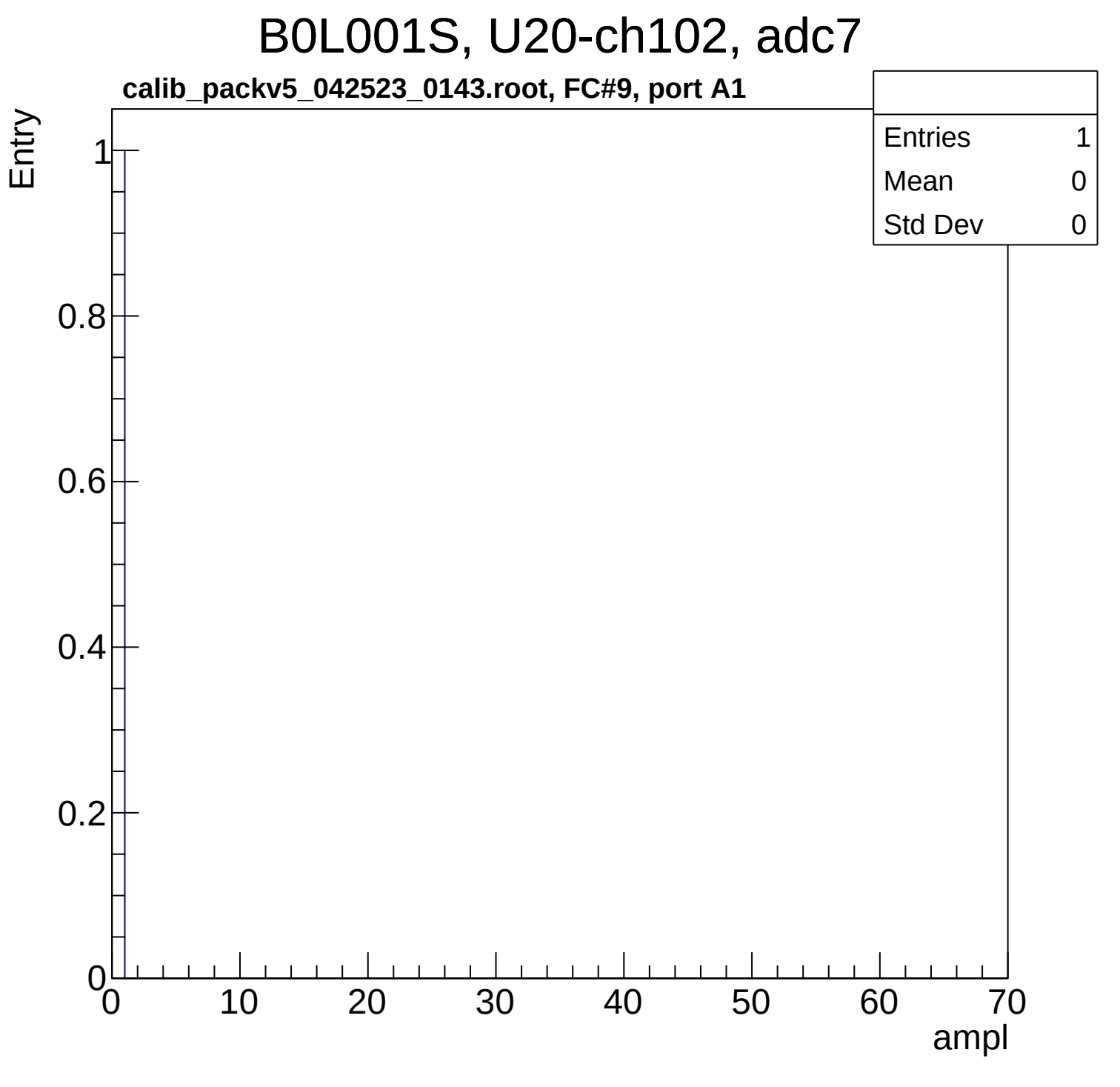
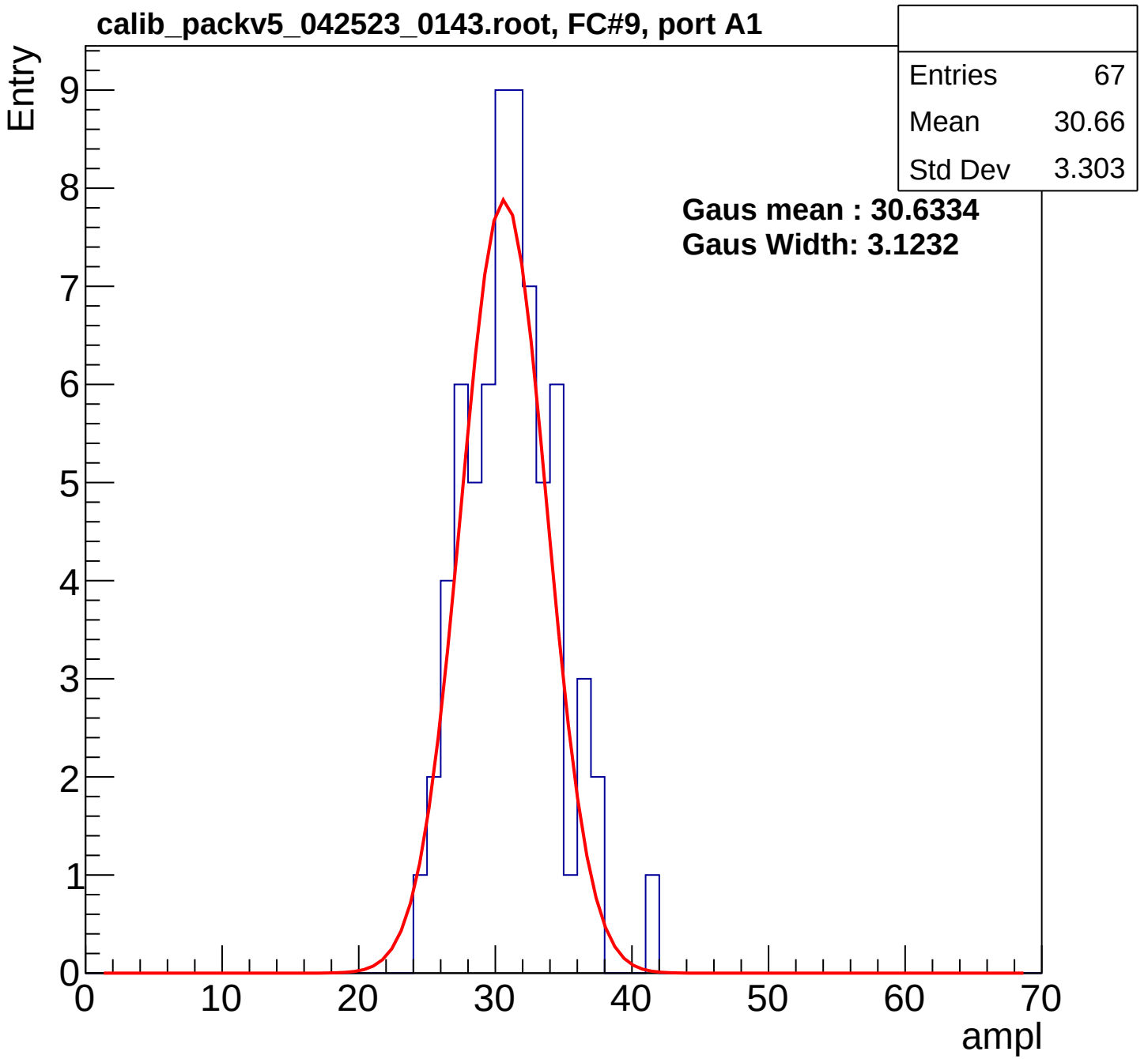
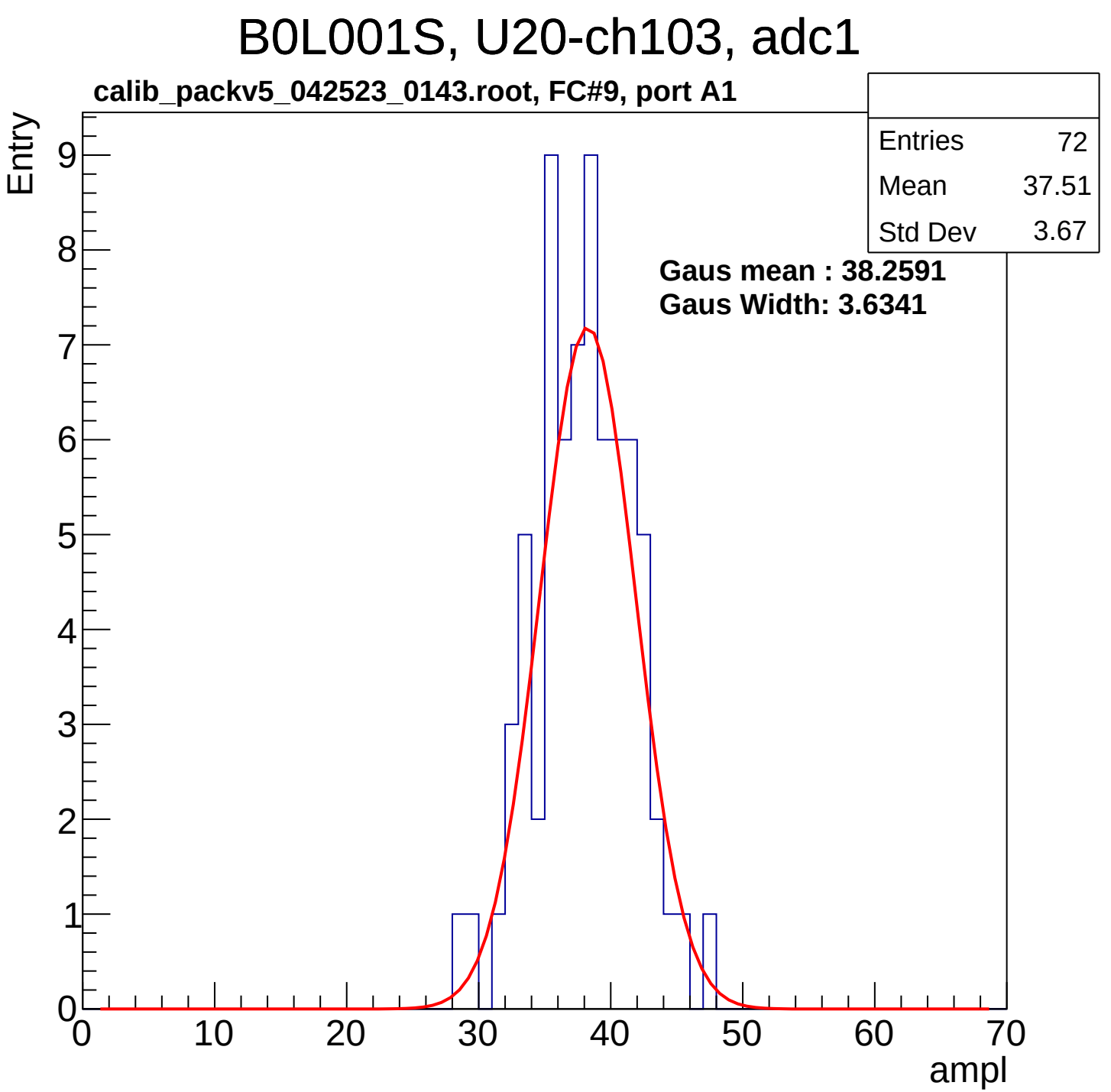
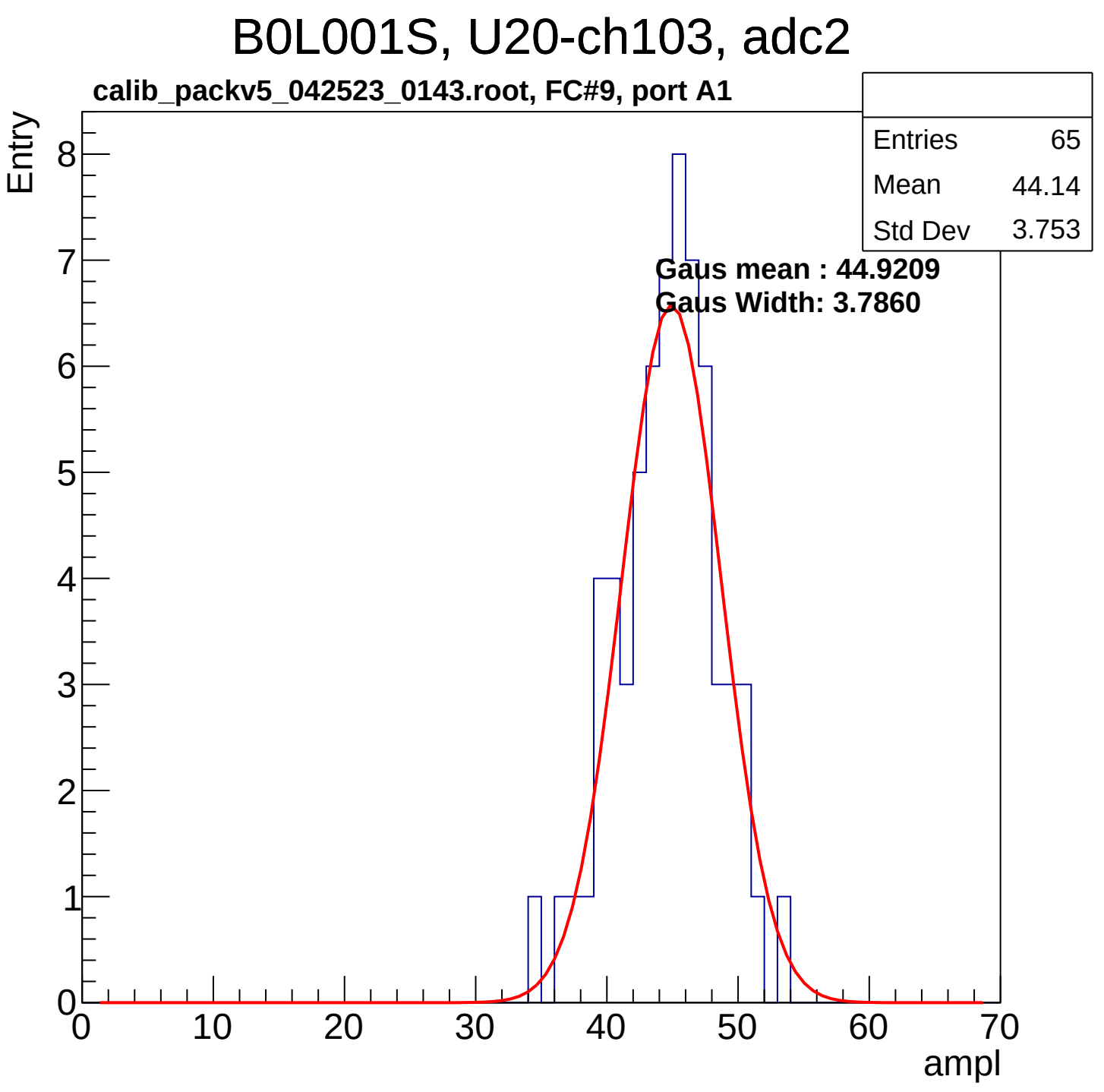
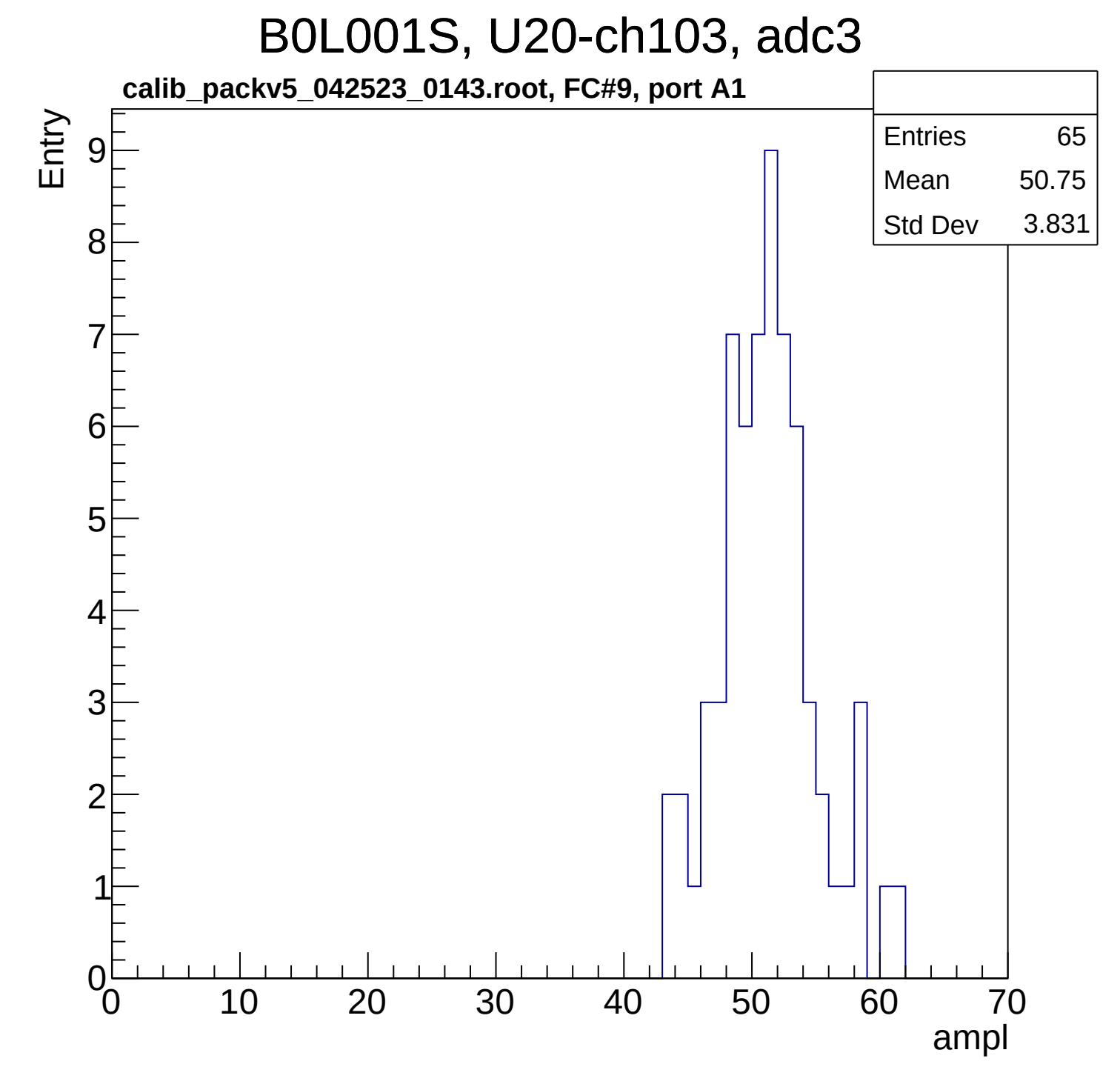
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.66
Std Dev 3.303
Gaus mean : 30.6334
Gaus Width: 3.1232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.51
Std Dev 3.67
Gaus mean : 38.2591
Gaus Width: 3.6341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.14
Std Dev 3.753
Gaus mean : 44.9209
Gaus Width: 3.7860
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.75
Std Dev 3.831
Entry
ampl
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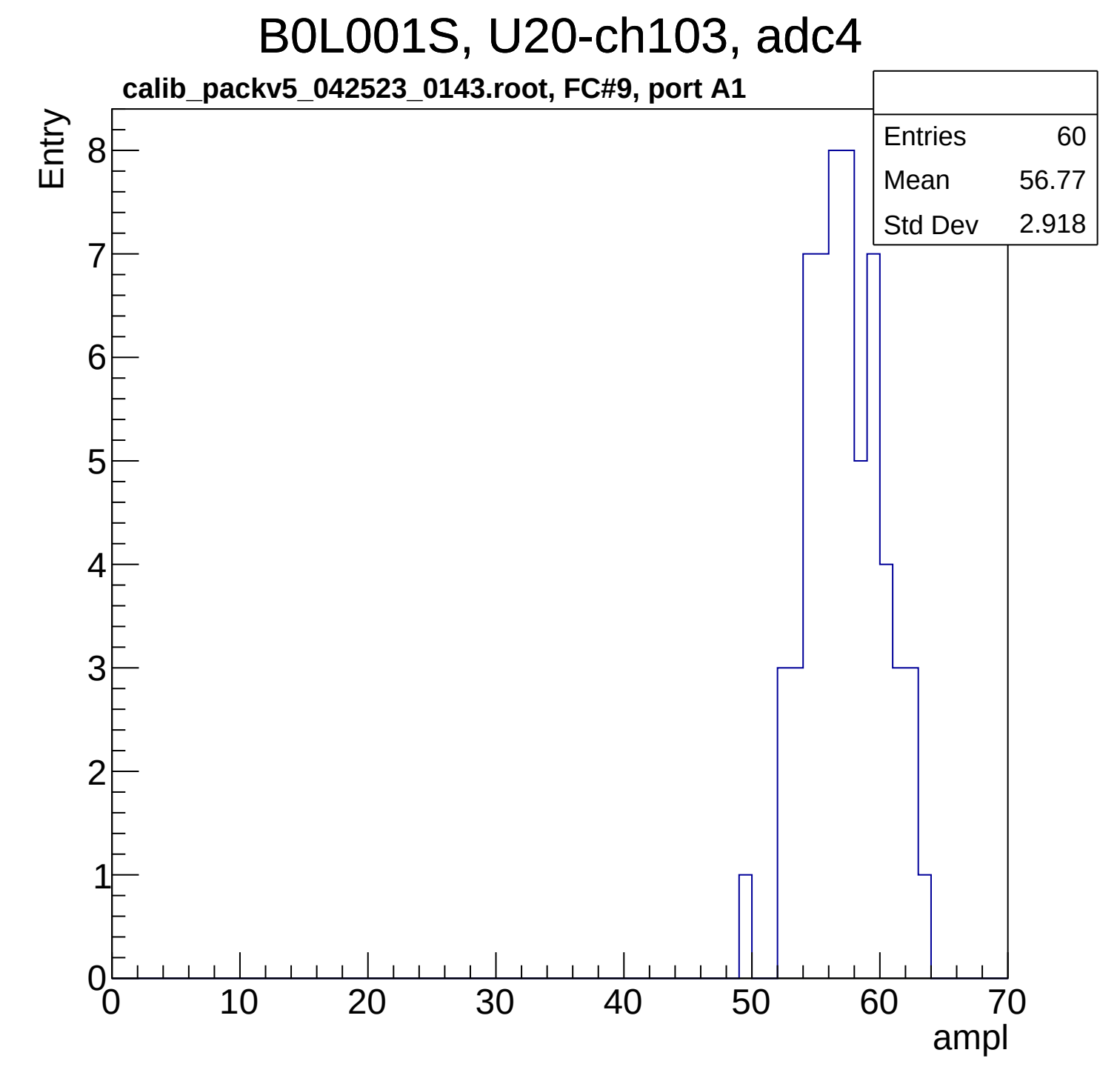
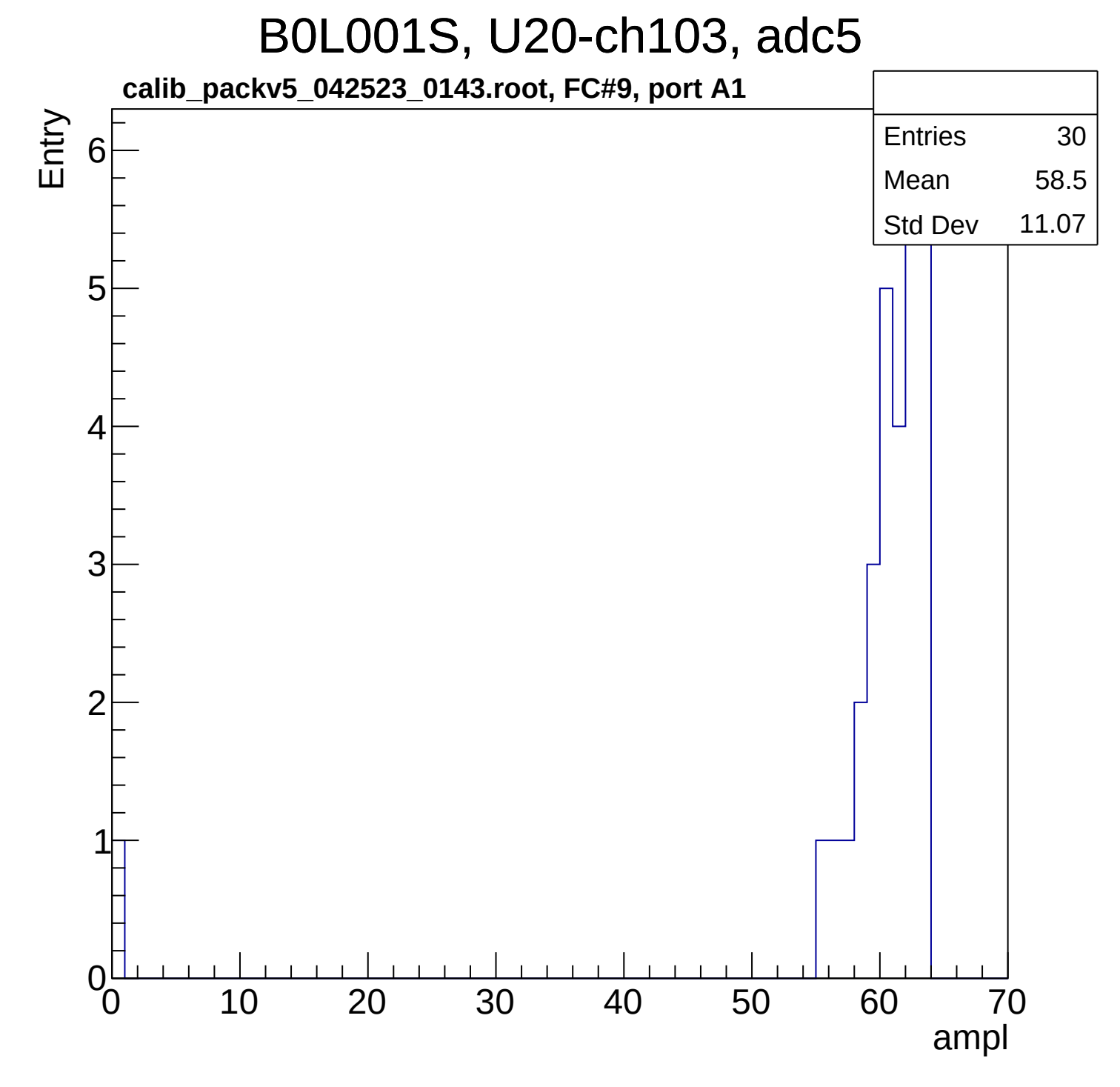
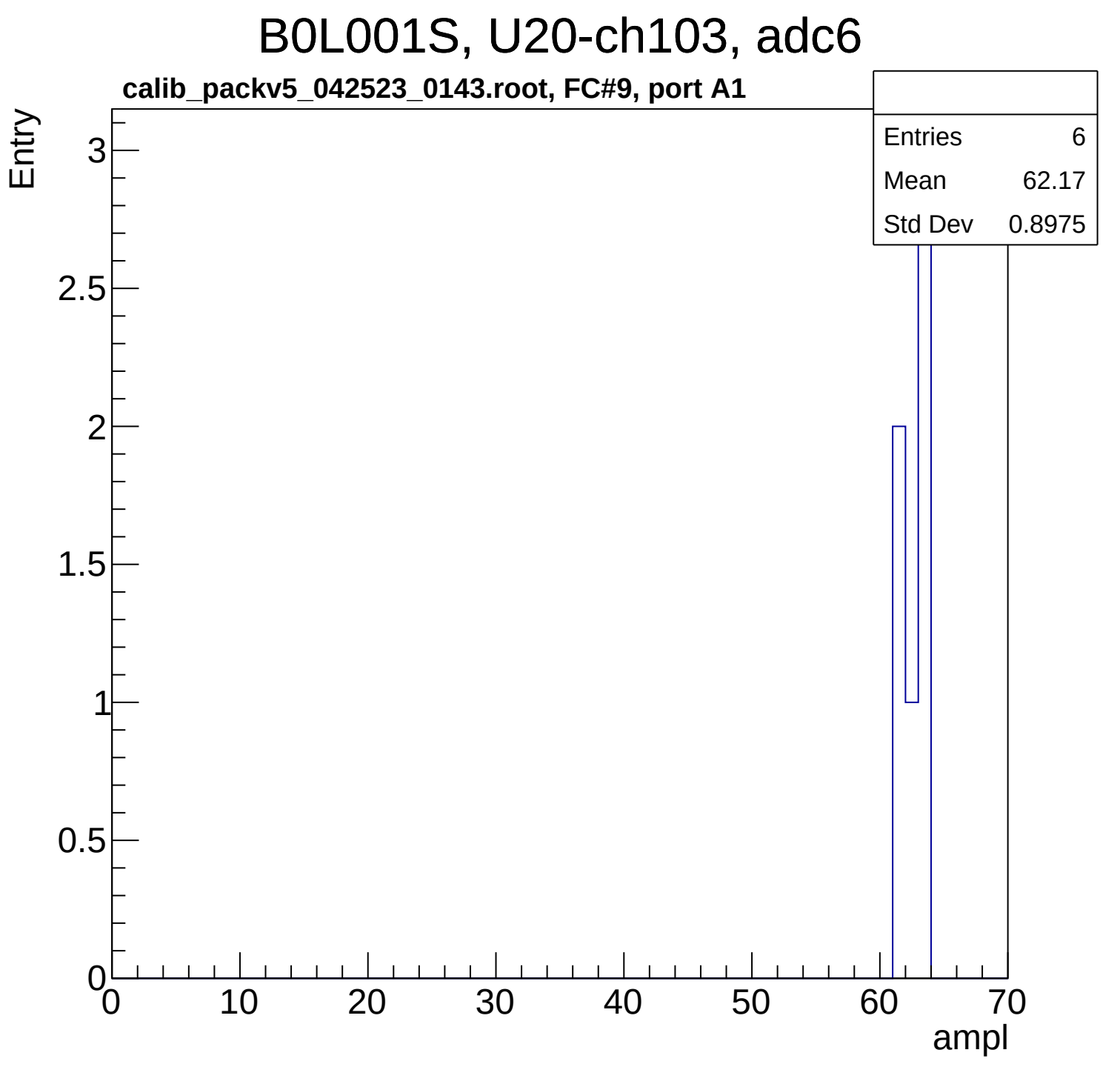
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.77
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.5
Std Dev 11.07
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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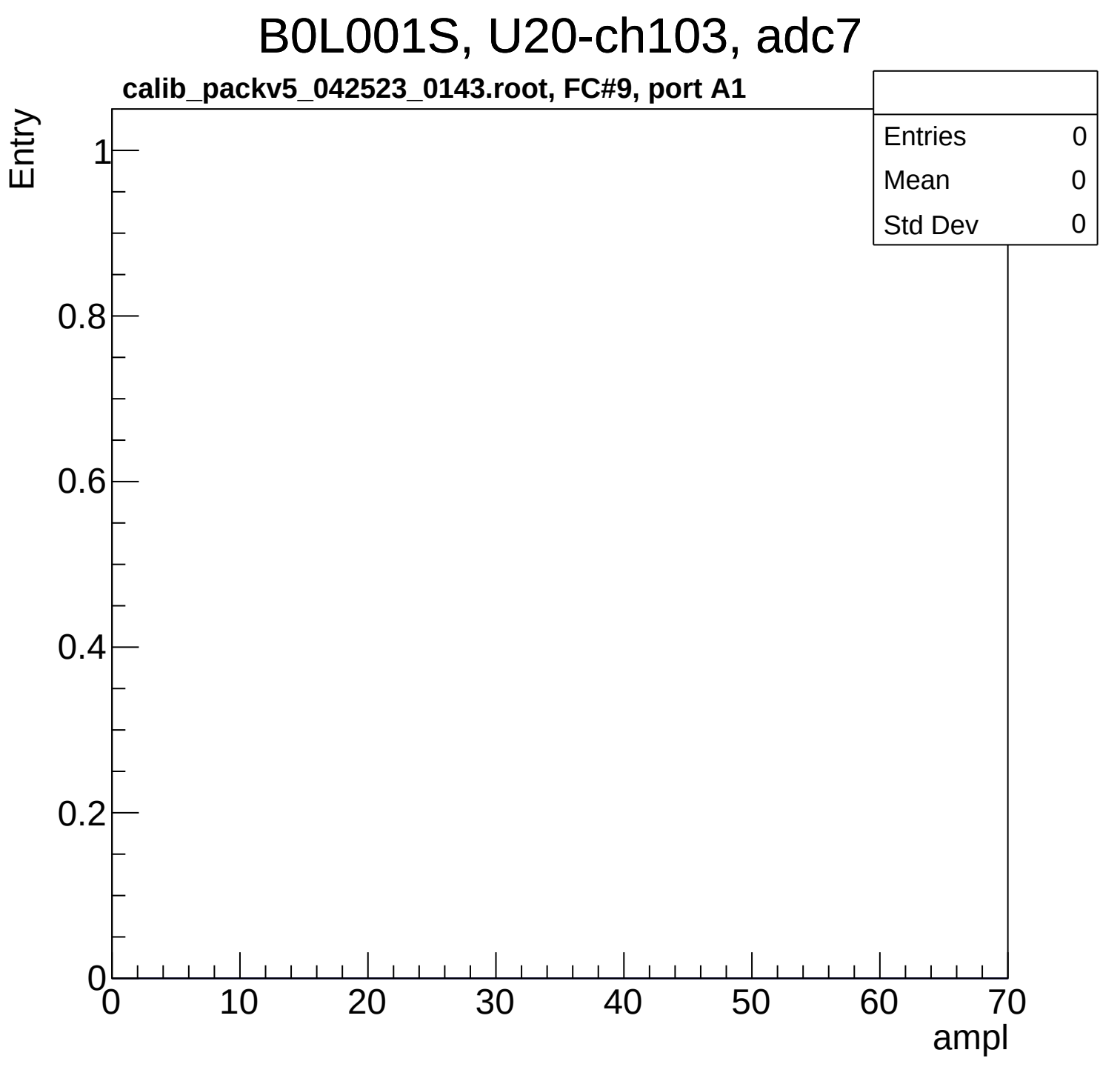
B0L001S, U20-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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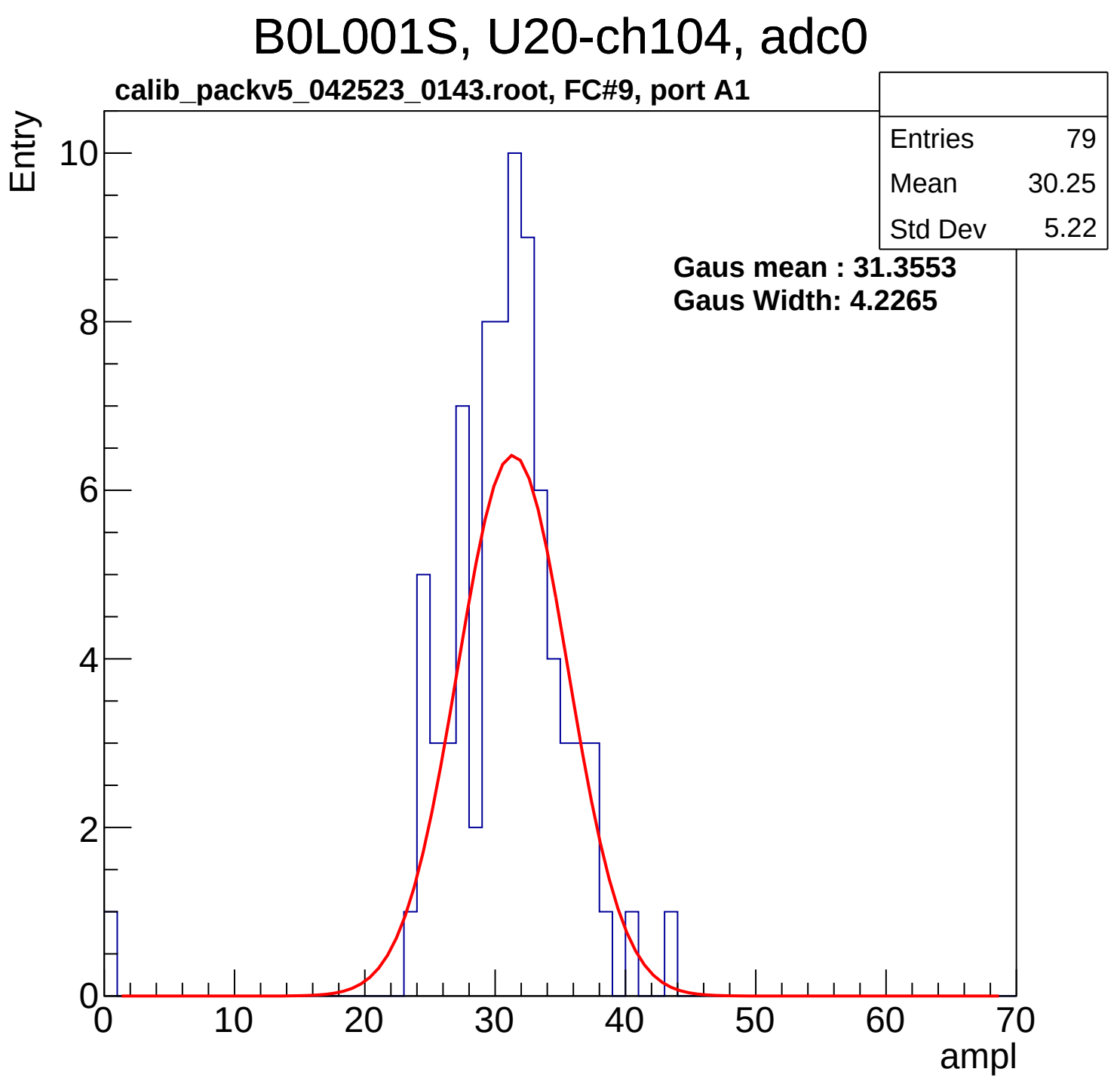
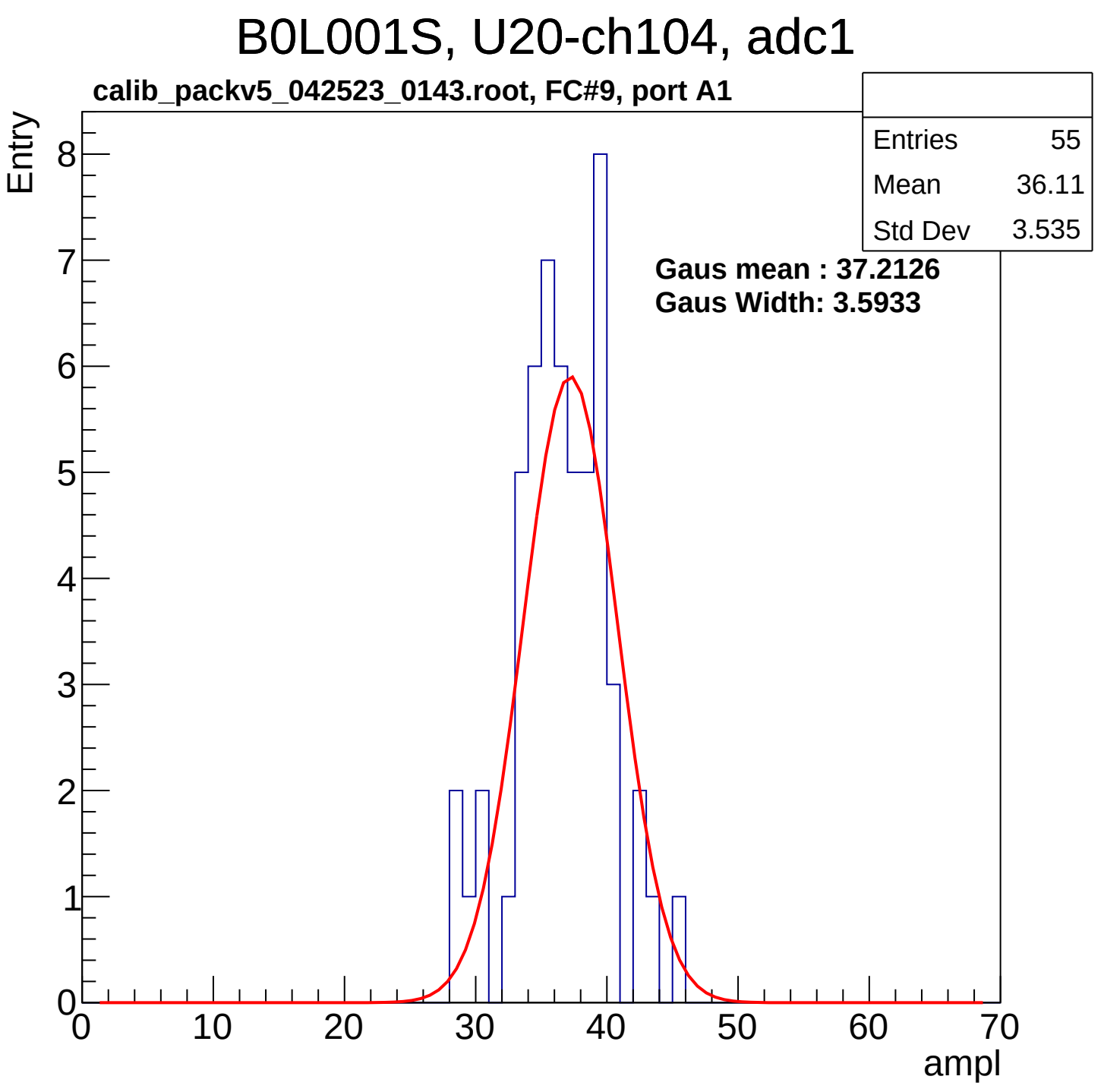
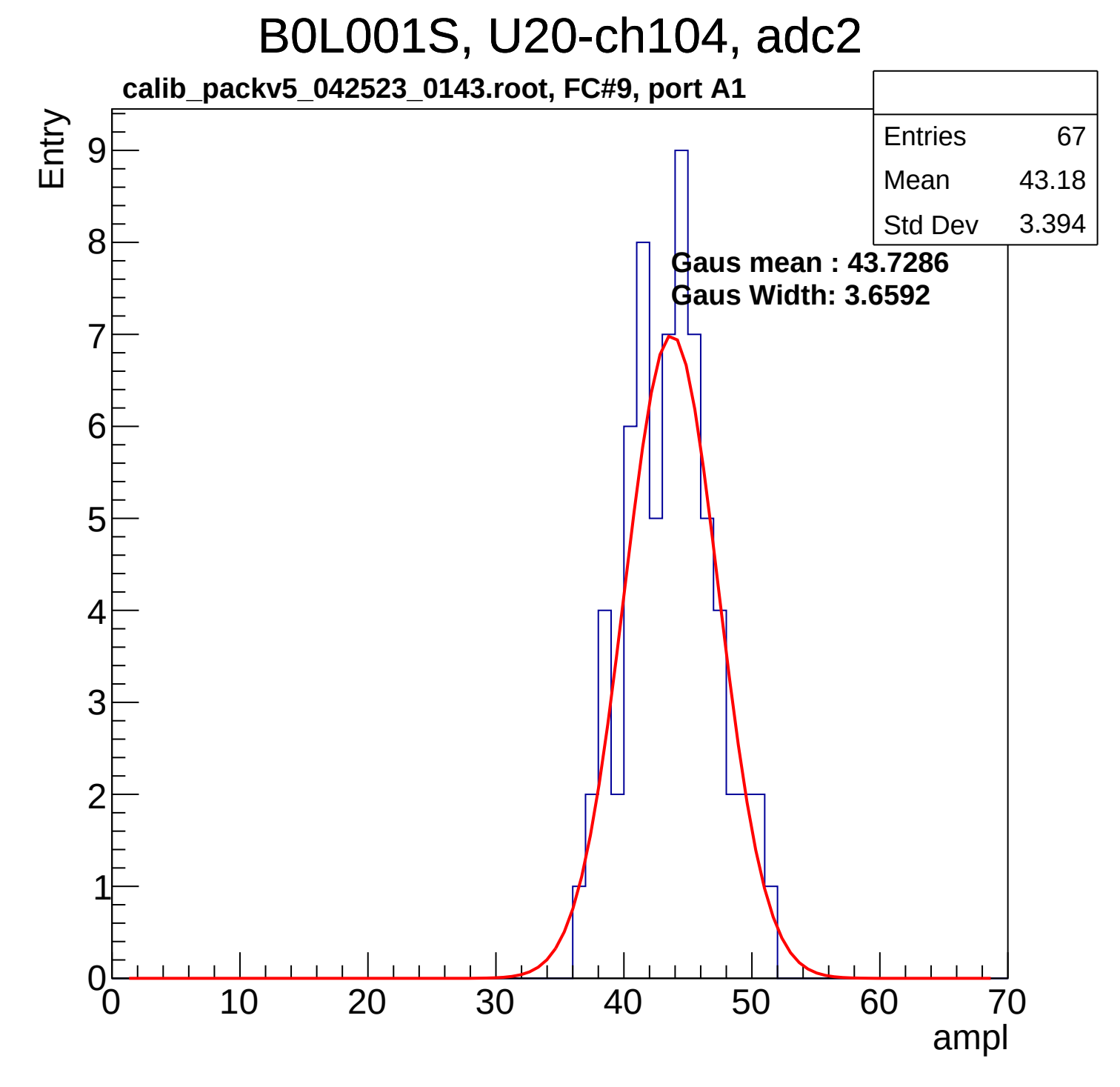
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.25
Std Dev 5.22
Gaus mean : 31.3553
Gaus Width: 4.2265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 36.11
Std Dev 3.535
Gaus mean : 37.2126
Gaus Width: 3.5933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.18
Std Dev 3.394
Gaus mean : 43.7286
Gaus Width: 3.6592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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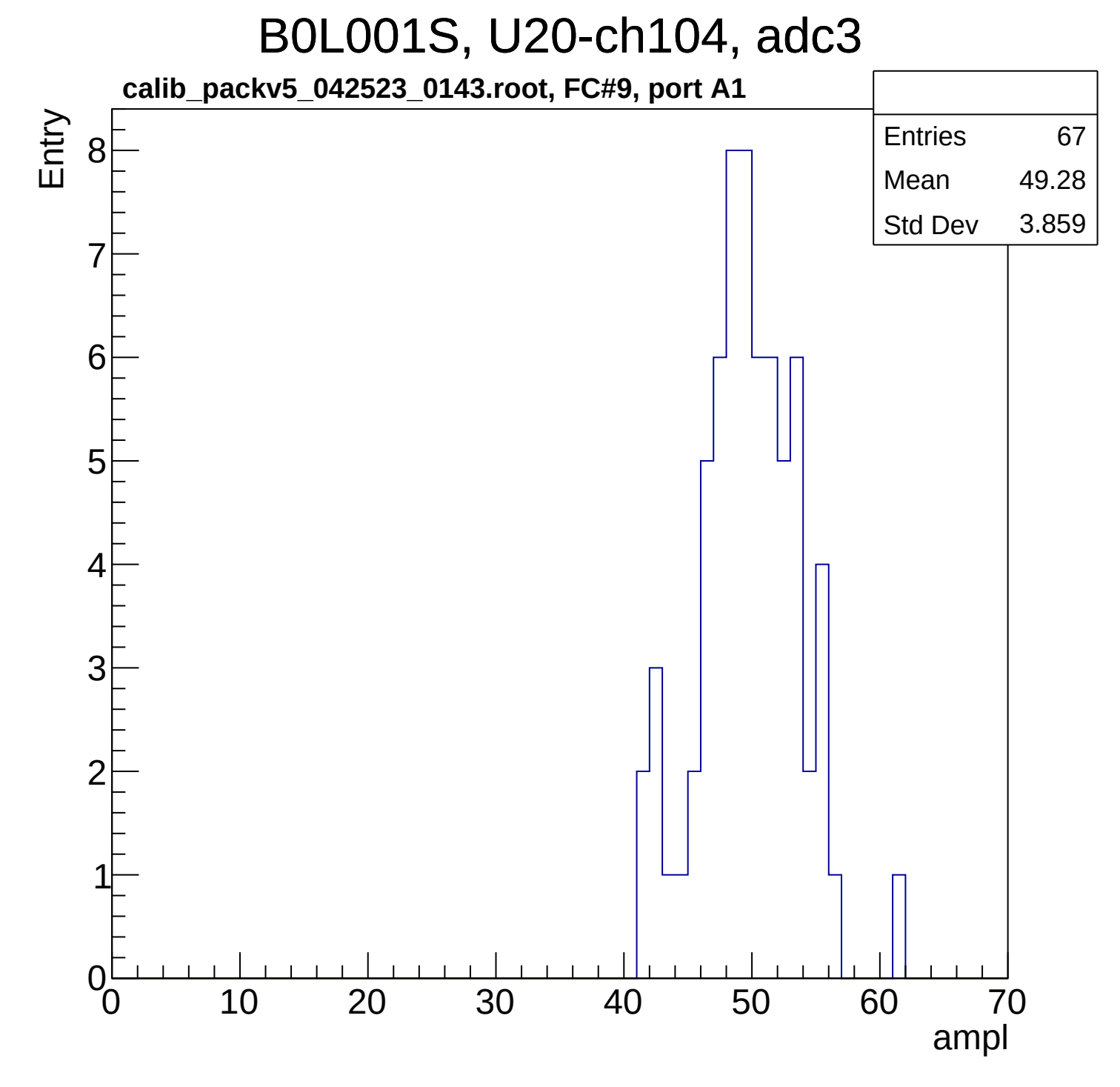
B0L001S, U20-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.28
Std Dev 3.859
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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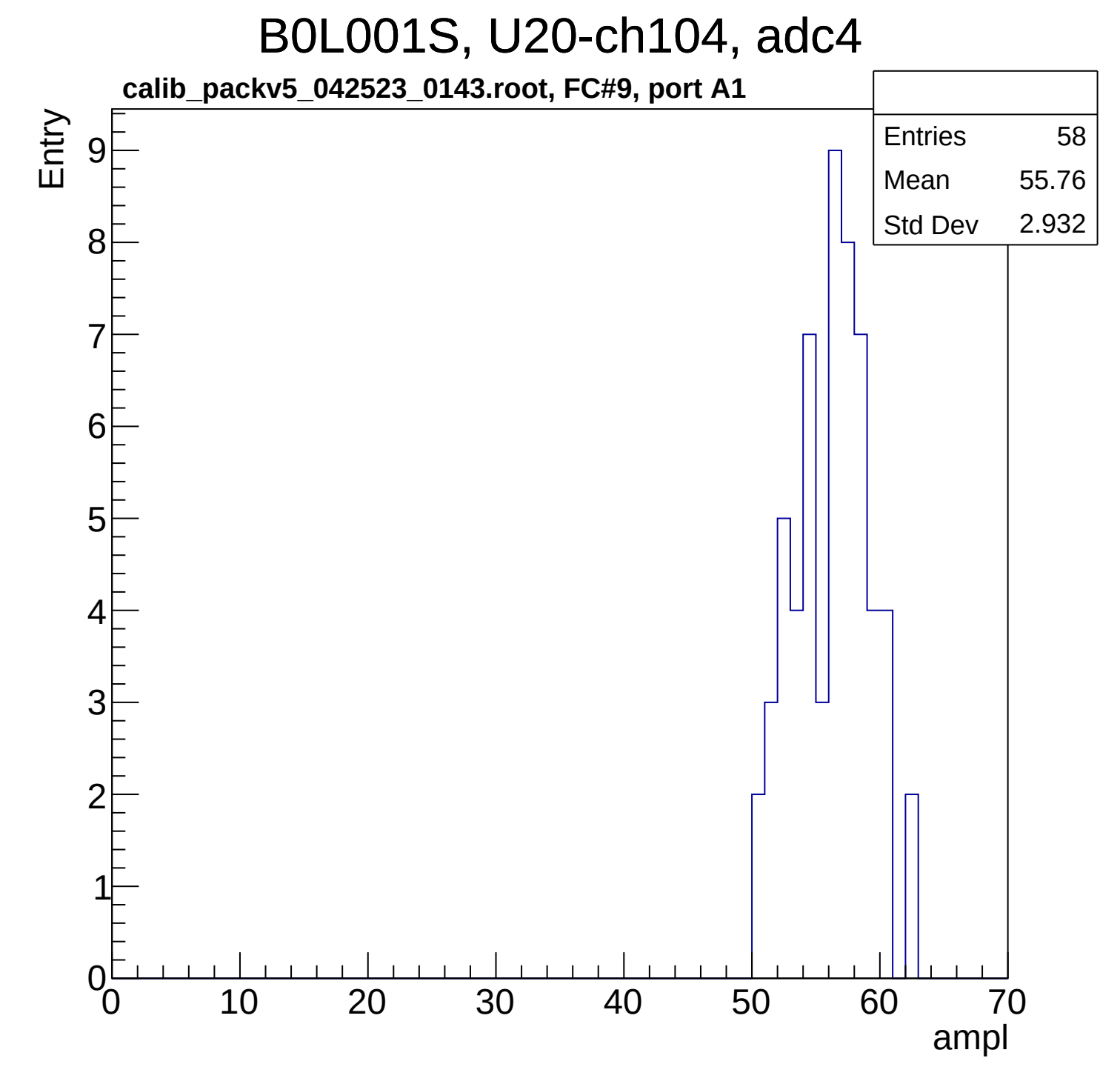
B0L001S, U20-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.76
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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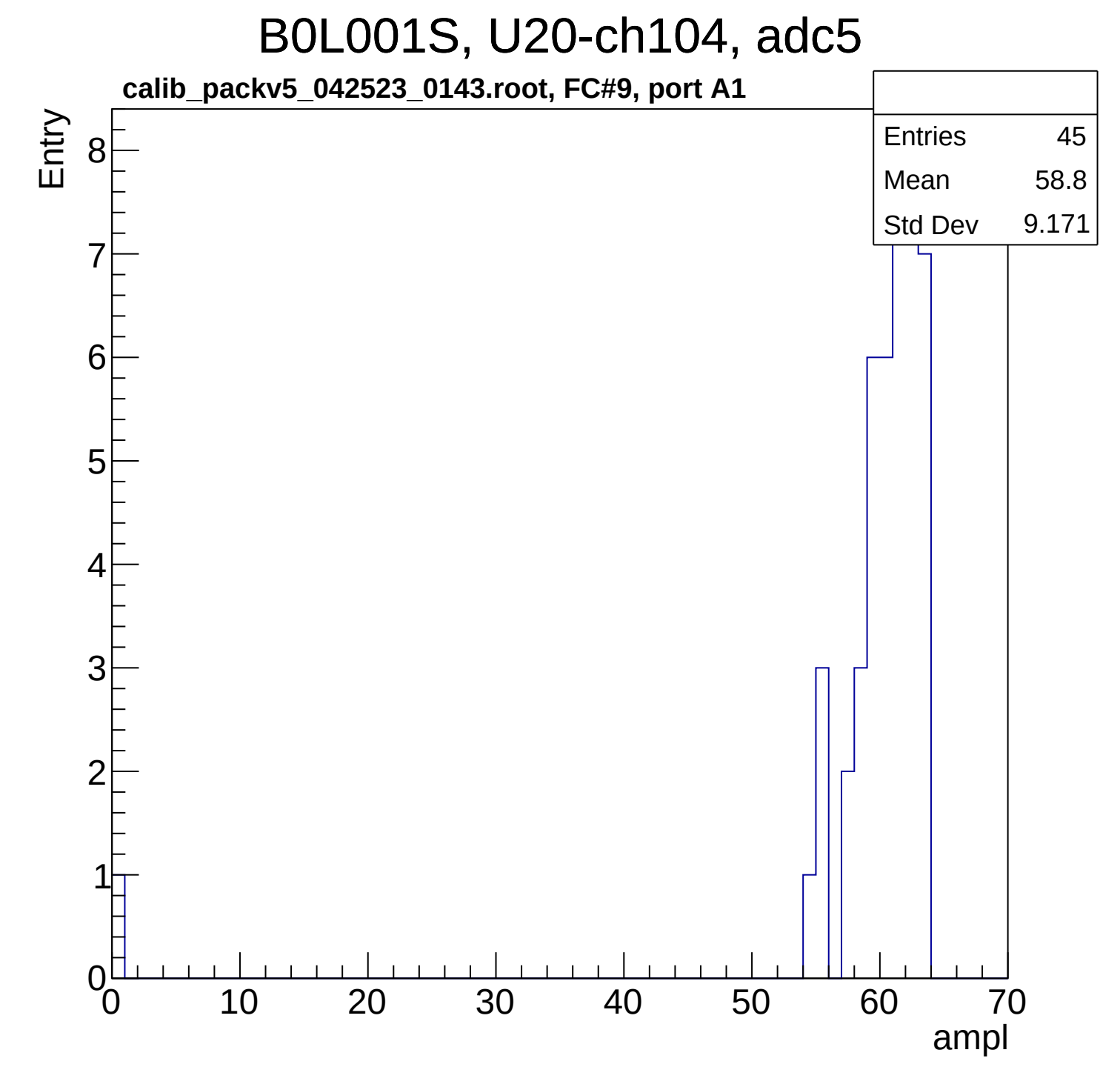
B0L001S, U20-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.8
Std Dev 9.171
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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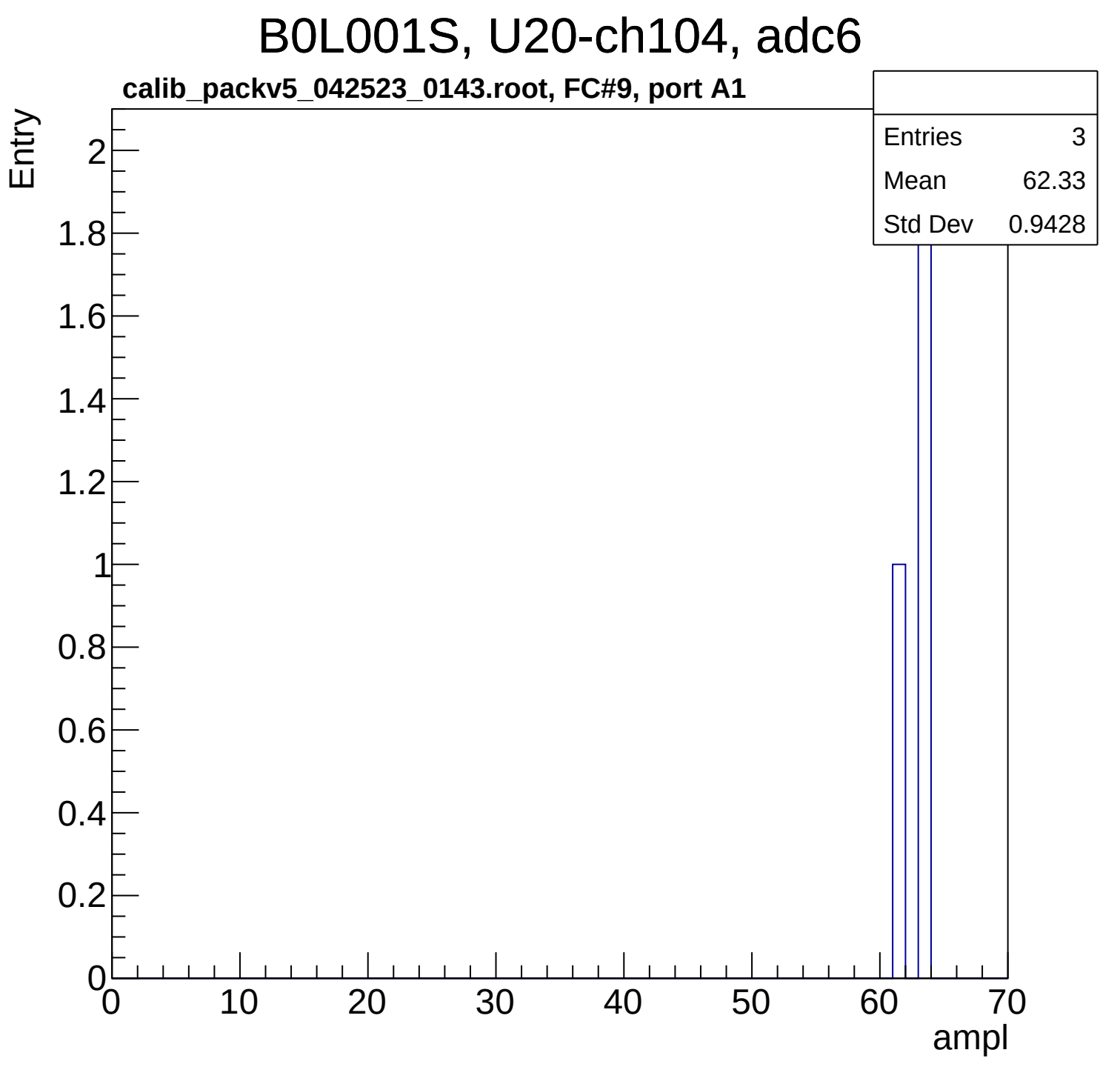
B0L001S, U20-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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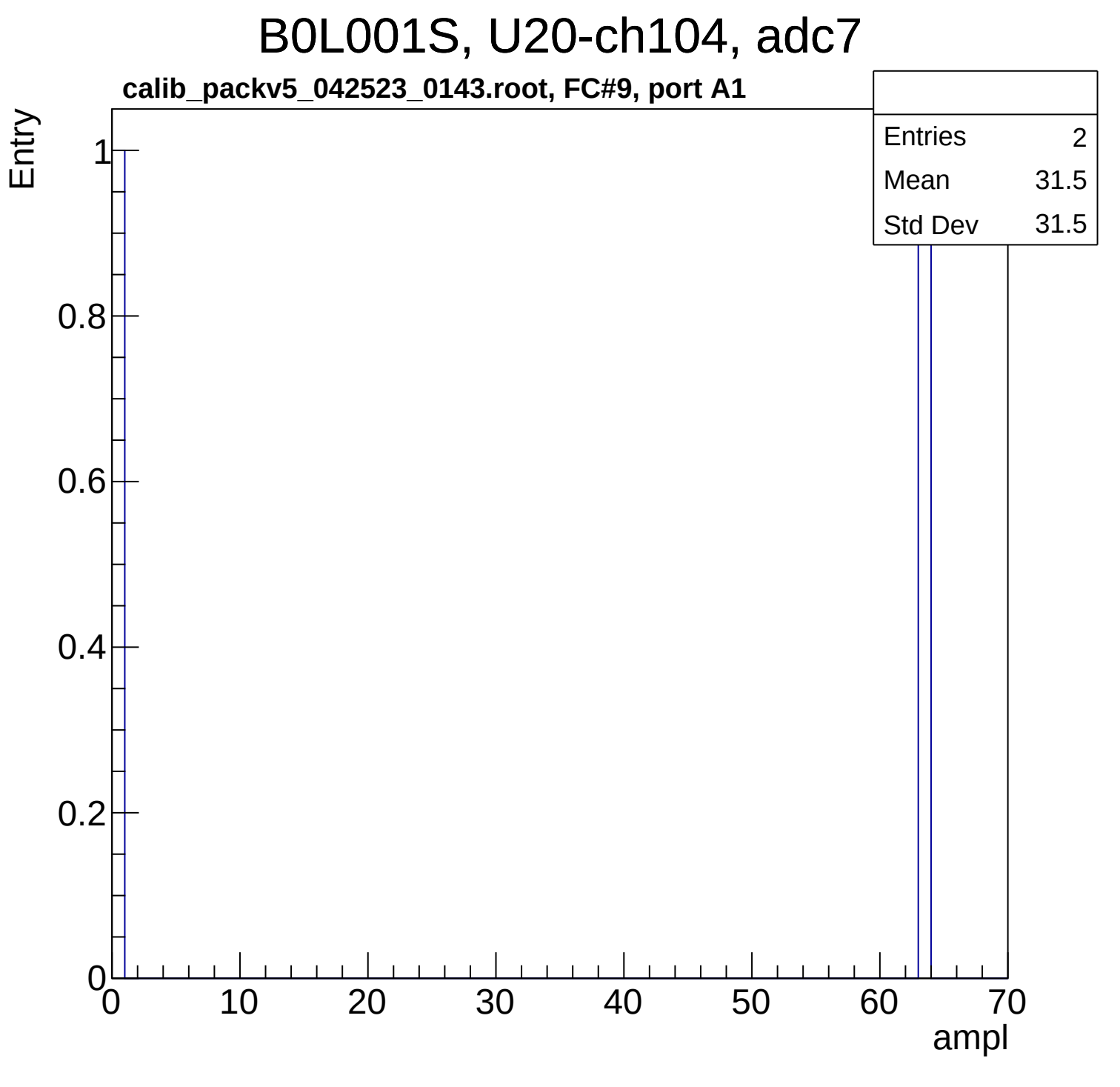
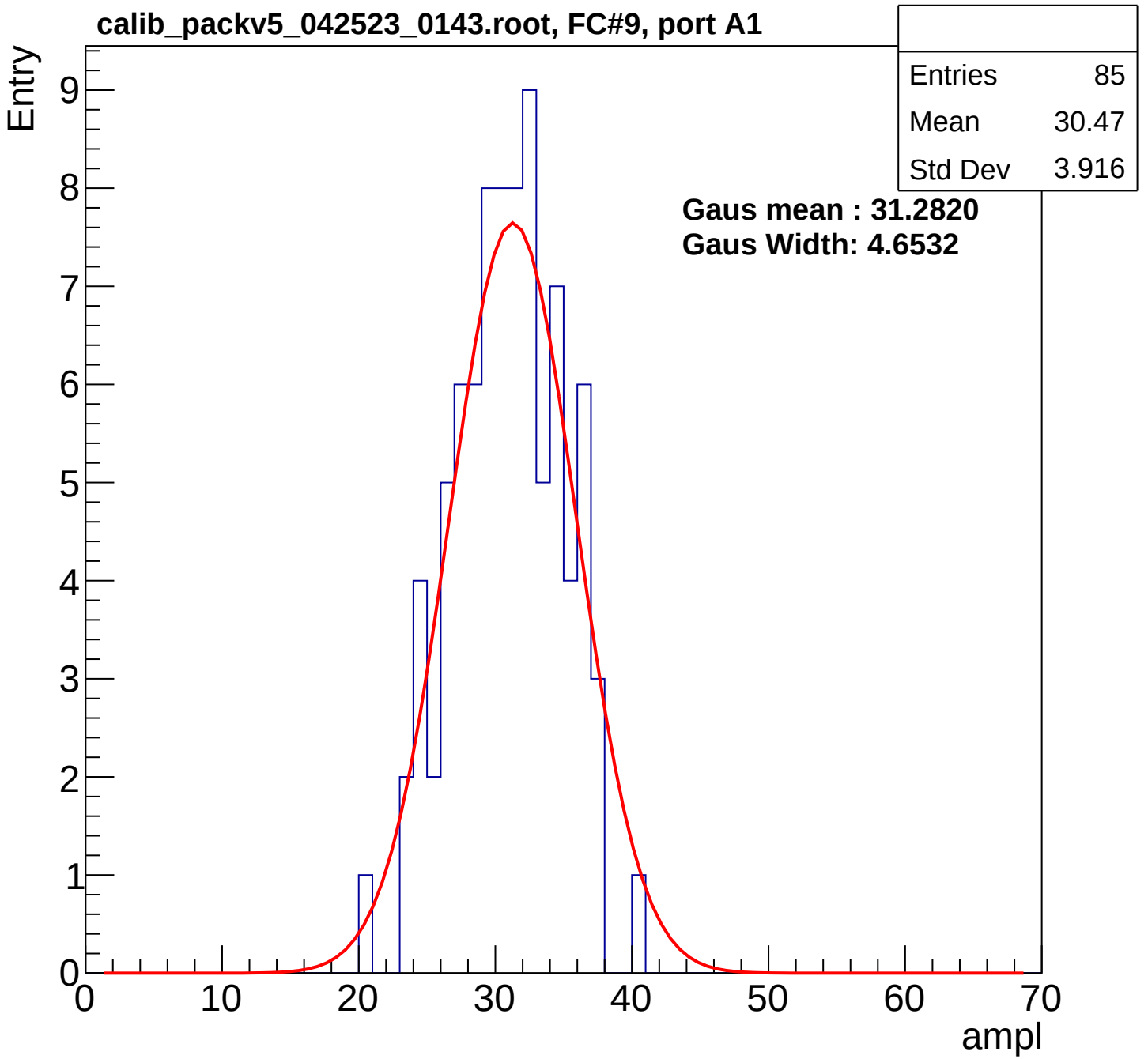

B0L001S, U20-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 30.47
Std Dev 3.916
Gaus mean : 31.2820
Gaus Width: 4.6532
Entry
ampl
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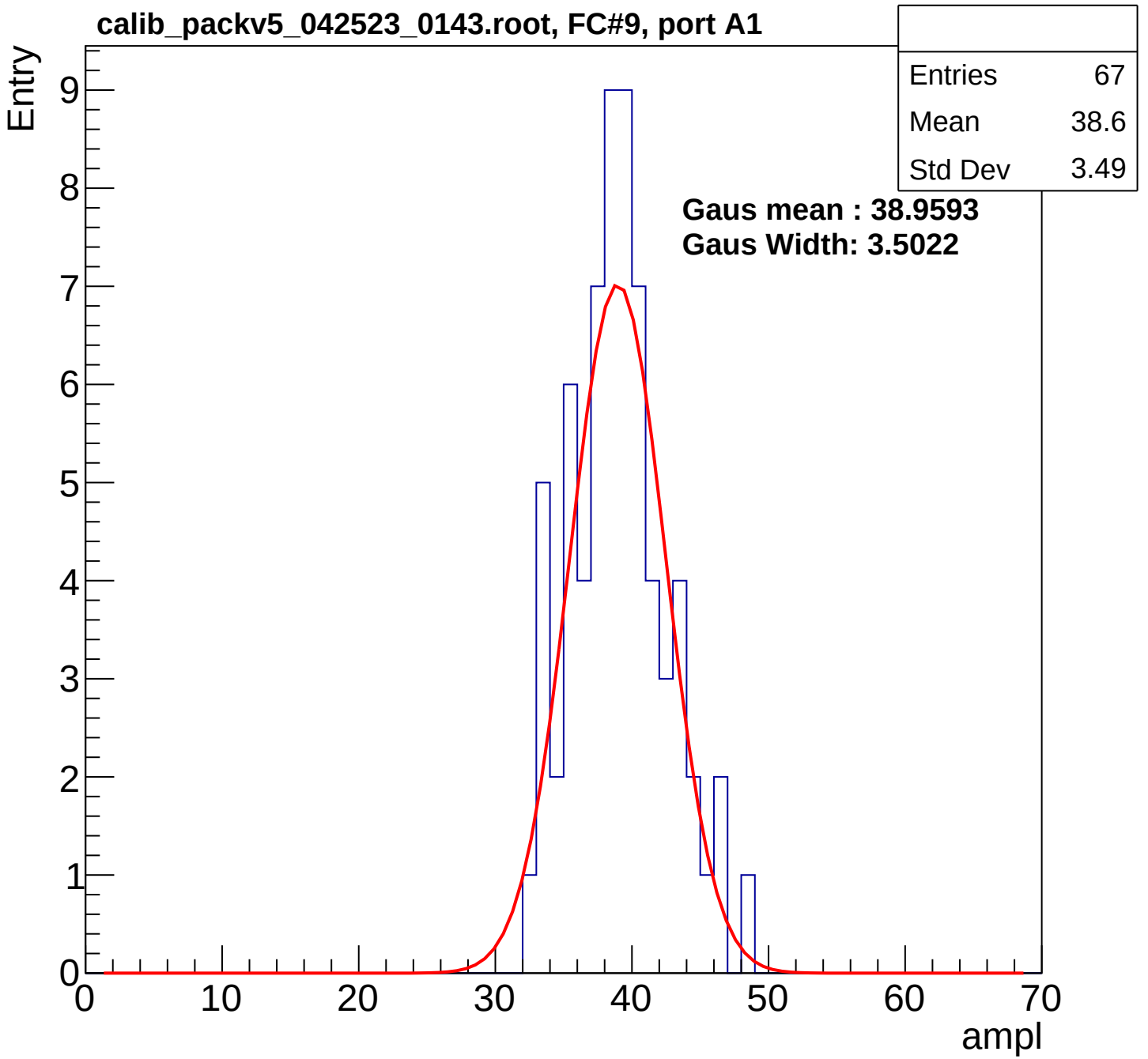
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.6
Std Dev 3.49
Gaus mean : 38.9593
Gaus Width: 3.5022
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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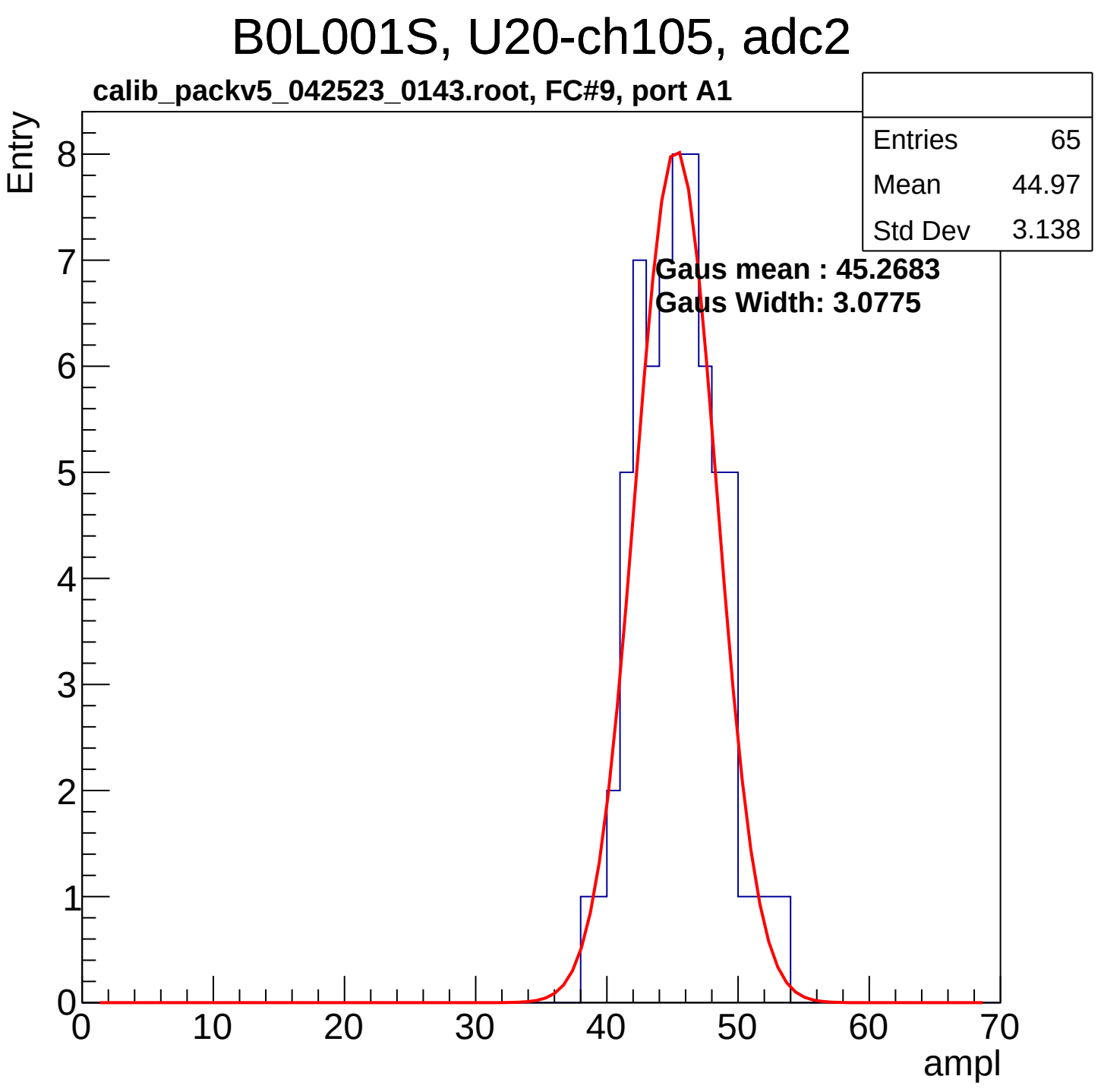
B0L001S, U20-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.97
Std Dev 3.138
Gaus mean : 45.2683
Gaus Width: 3.0775
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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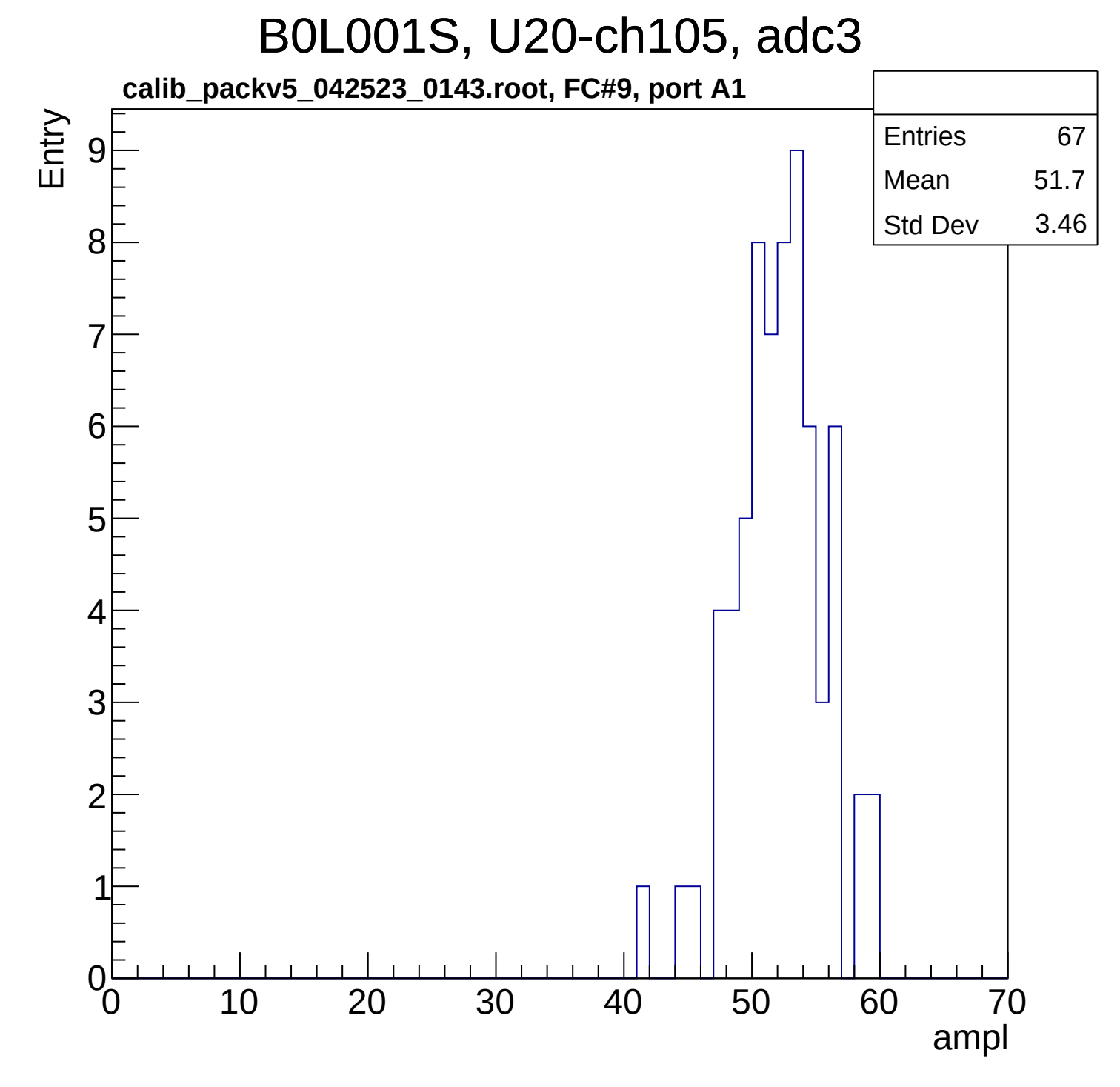
B0L001S, U20-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.7
Std Dev 3.46
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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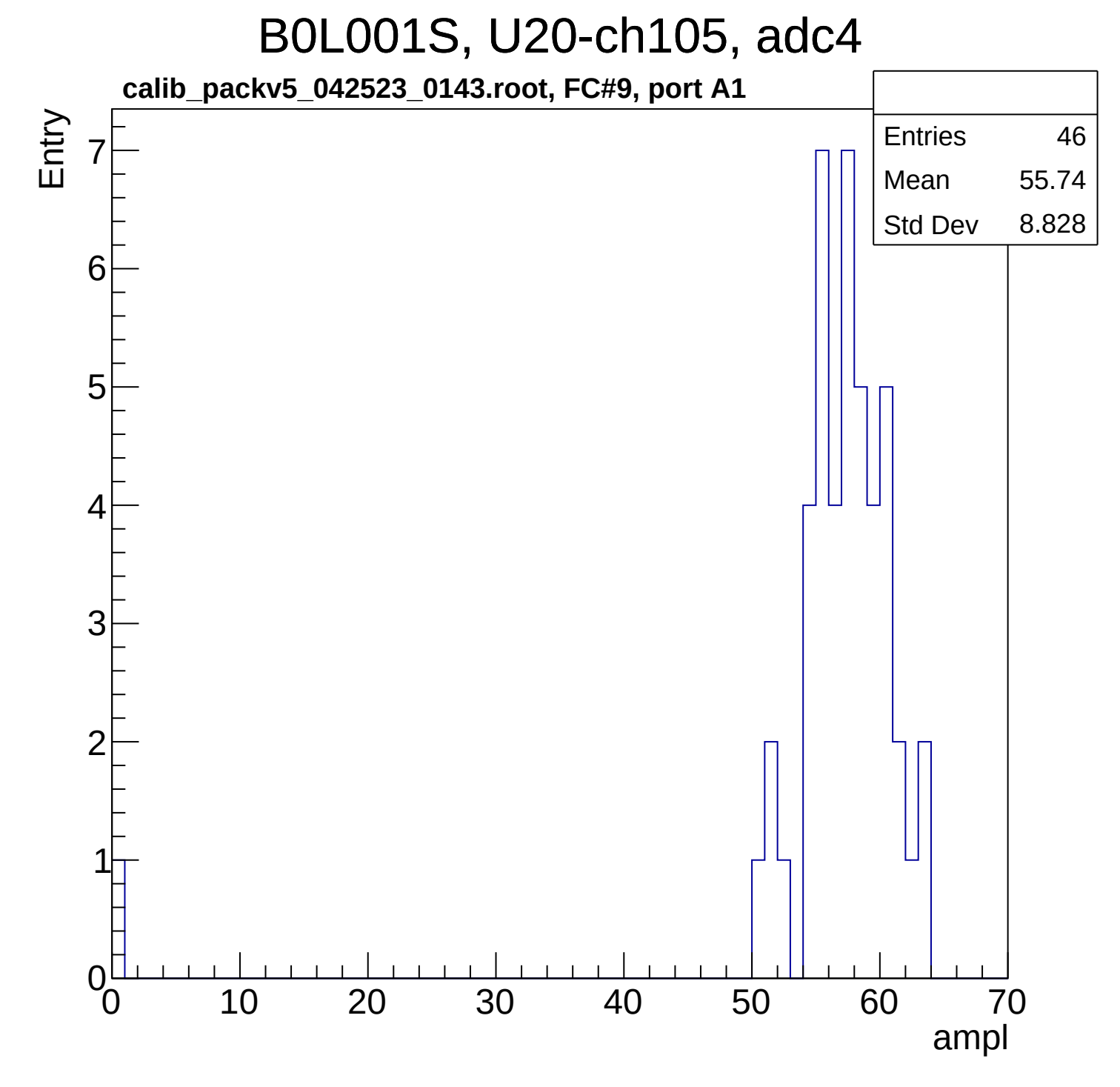
B0L001S, U20-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.74
Std Dev 8.828
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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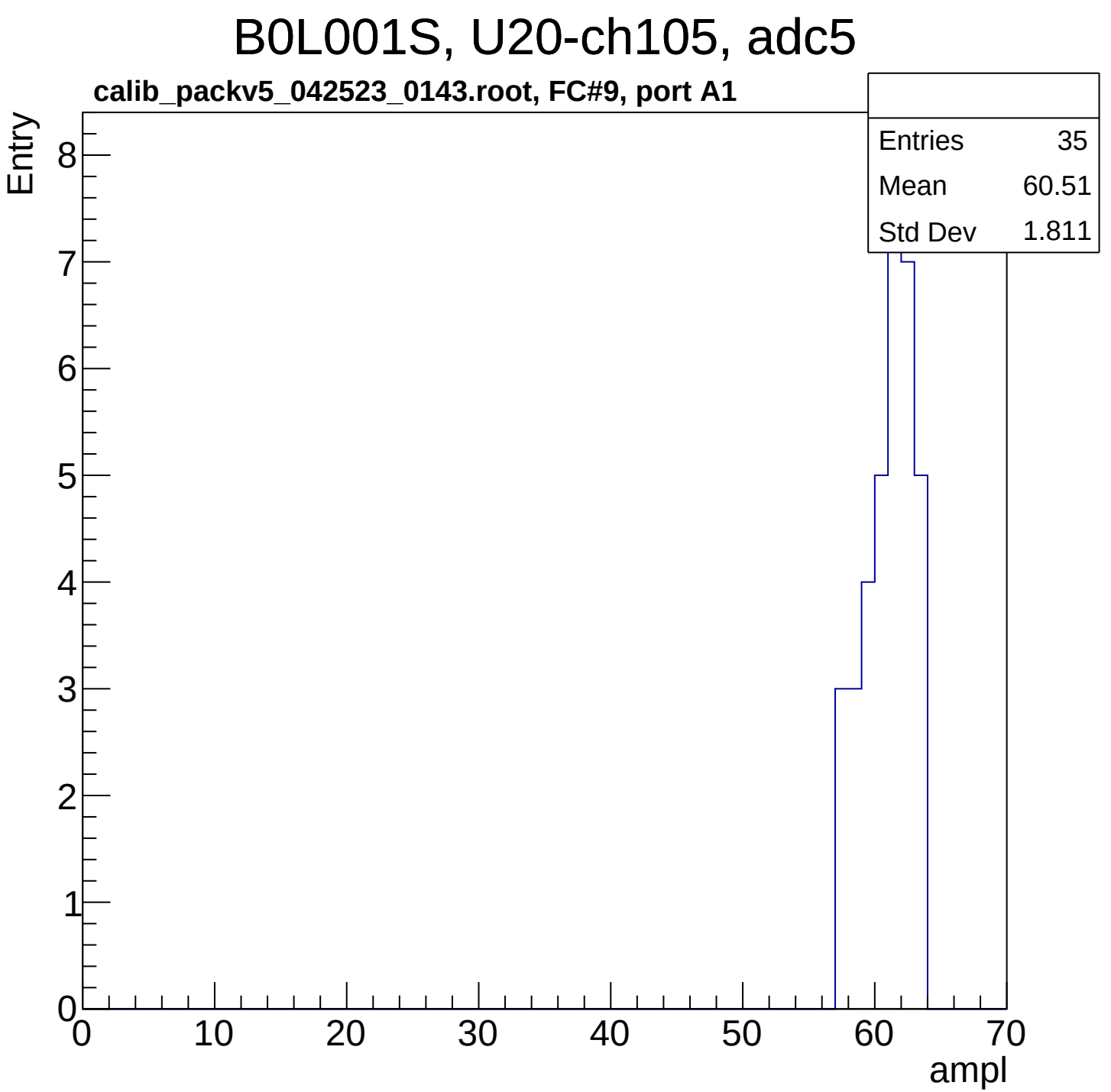
B0L001S, U20-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.51
Std Dev 1.811
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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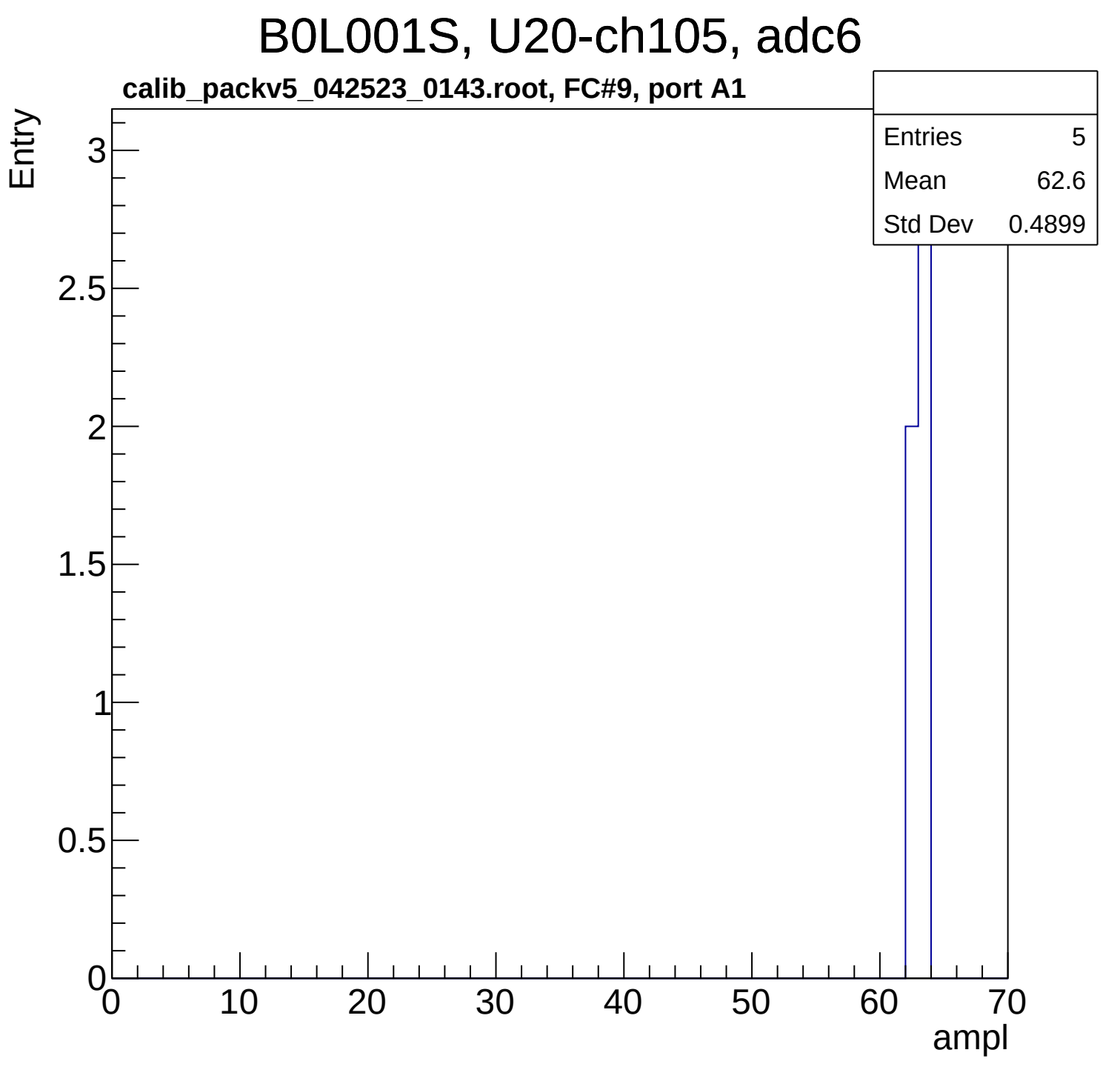
B0L001S, U20-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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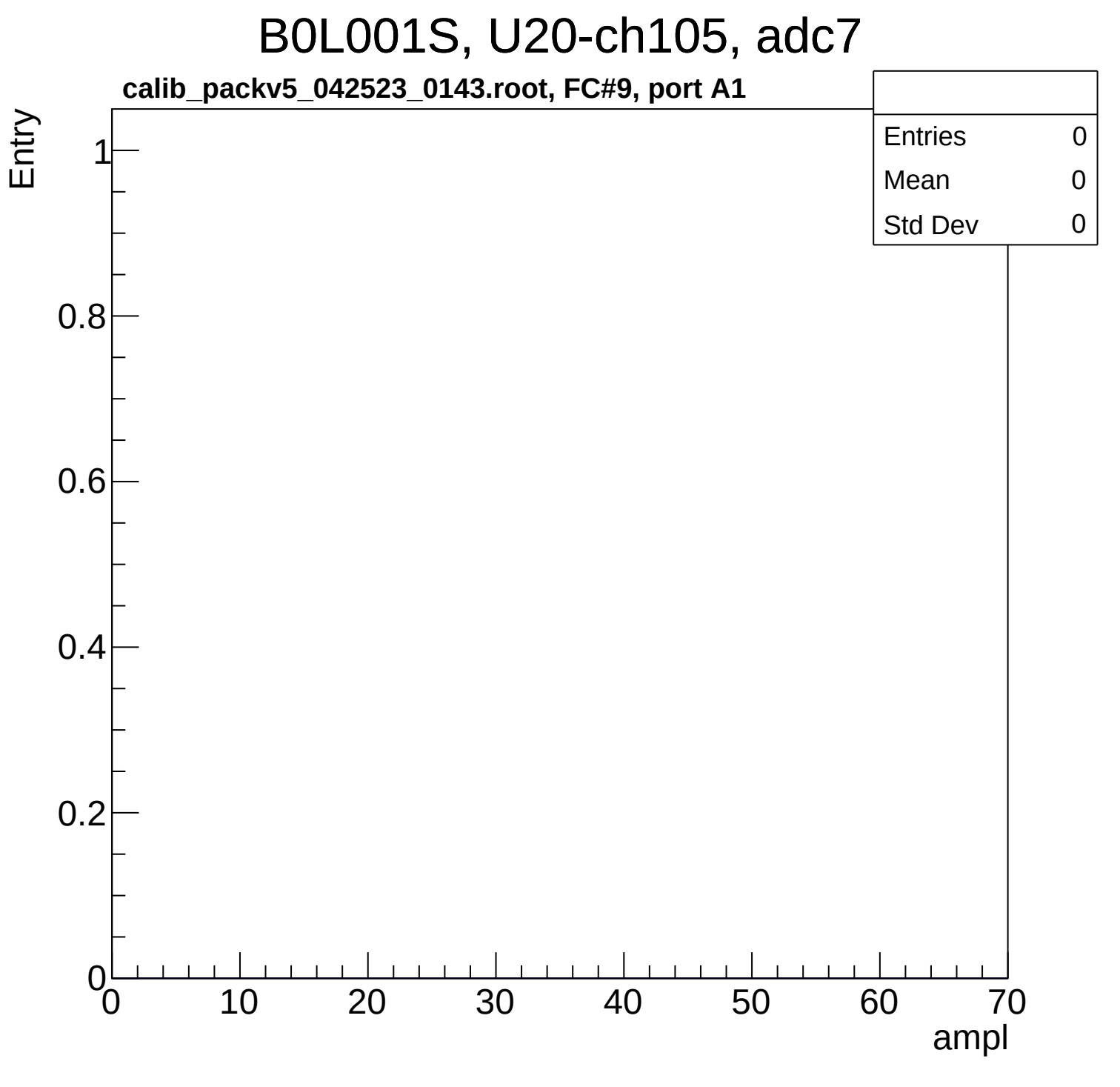
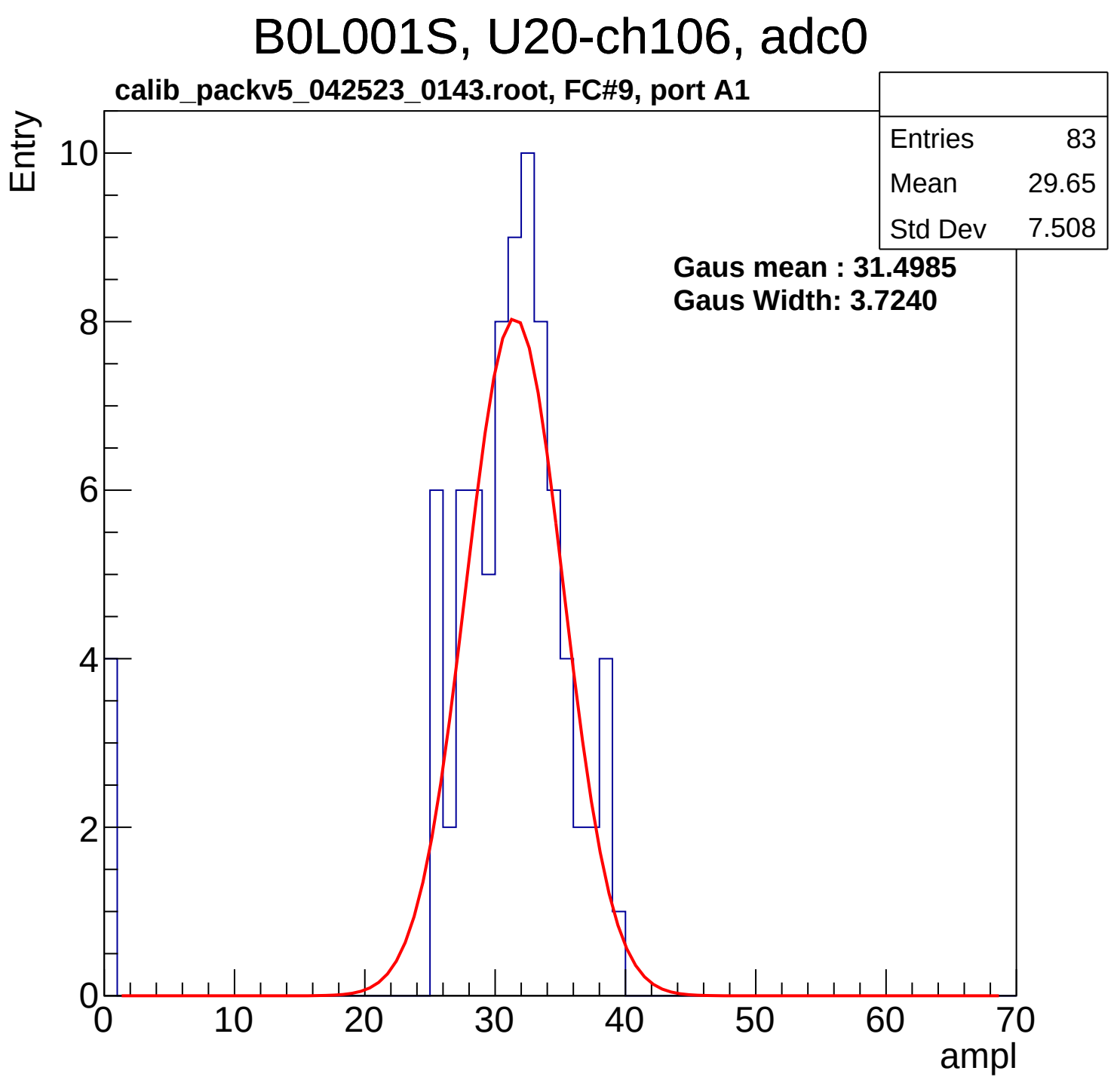
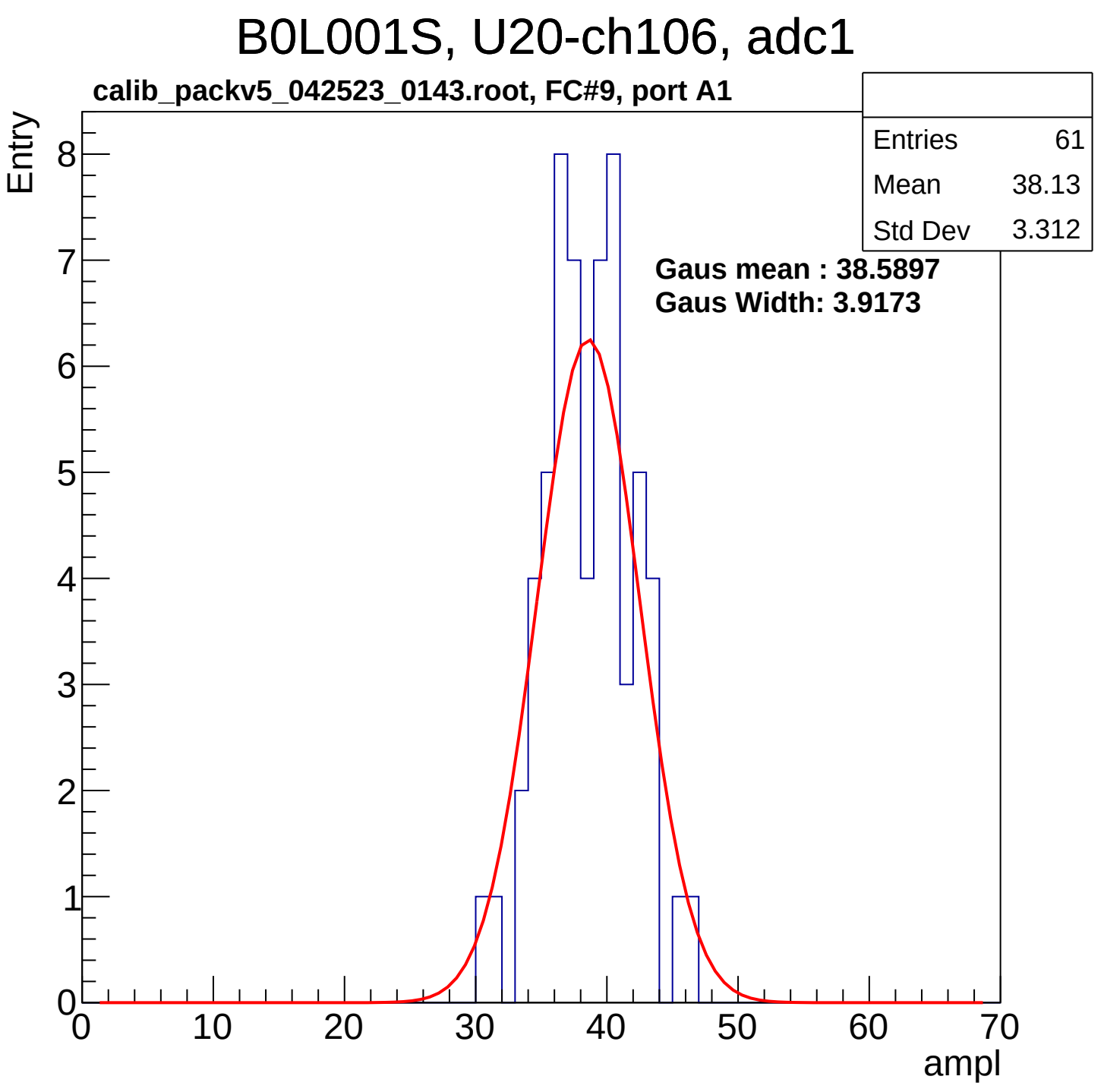
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U20-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 29.65
Std Dev 7.508
Gaus mean : 31.4985
Gaus Width: 3.7240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 38.13
Std Dev 3.312
Gaus mean : 38.5897
Gaus Width: 3.9173
Entry
ampl
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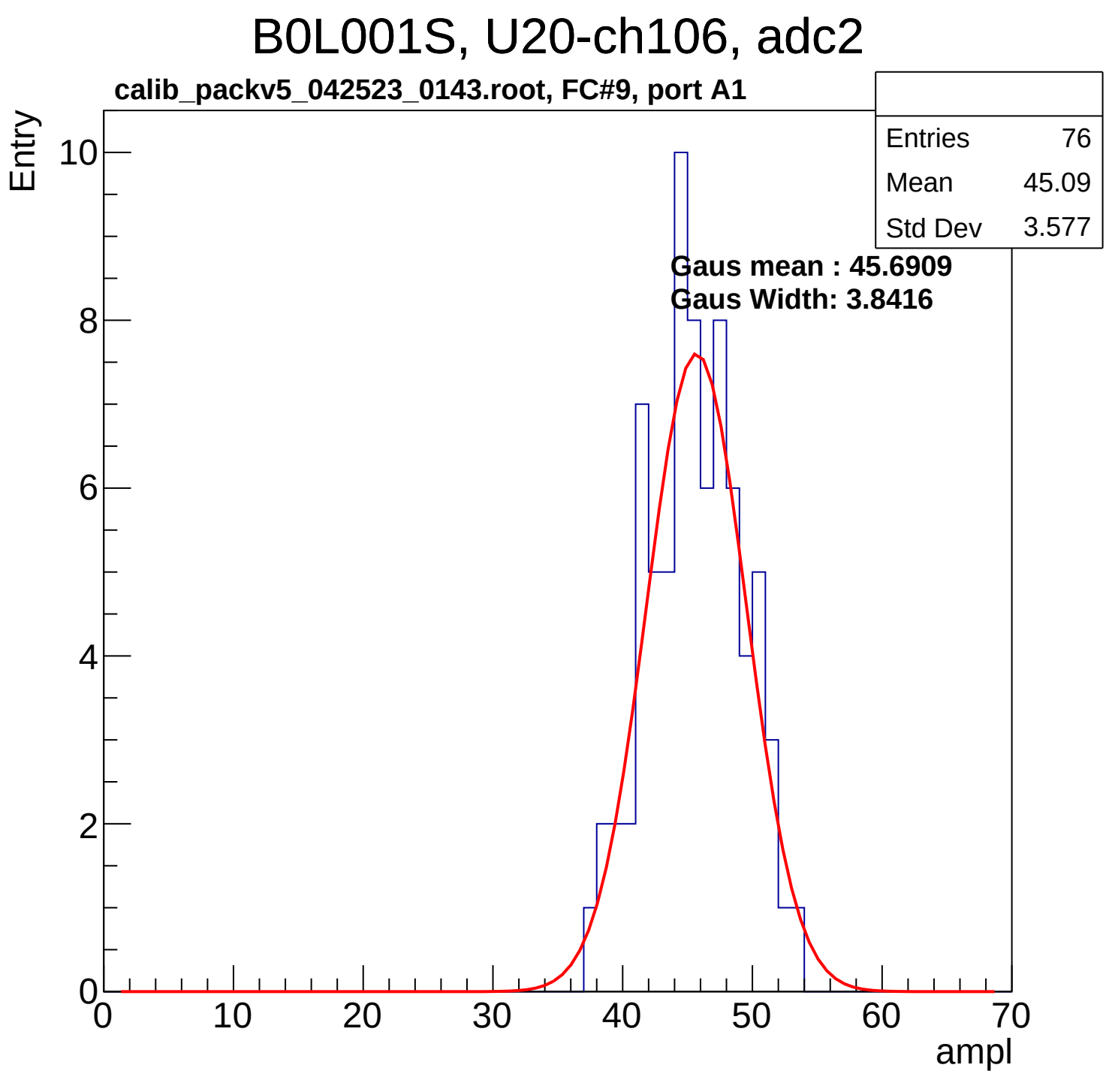
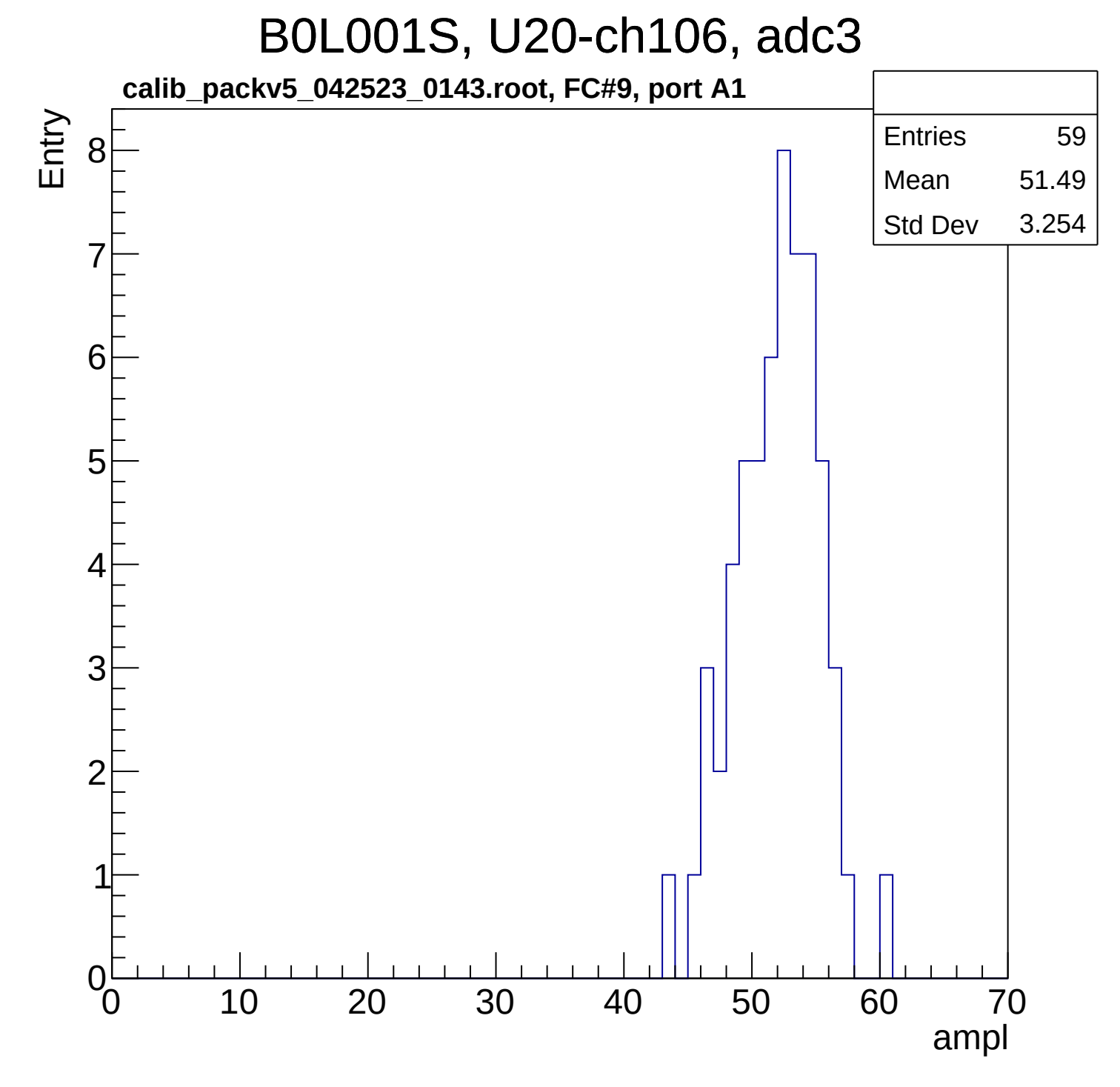
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 45.09
Std Dev 3.577
Gaus mean : 45.6909
Gaus Width: 3.8416
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.49
Std Dev 3.254
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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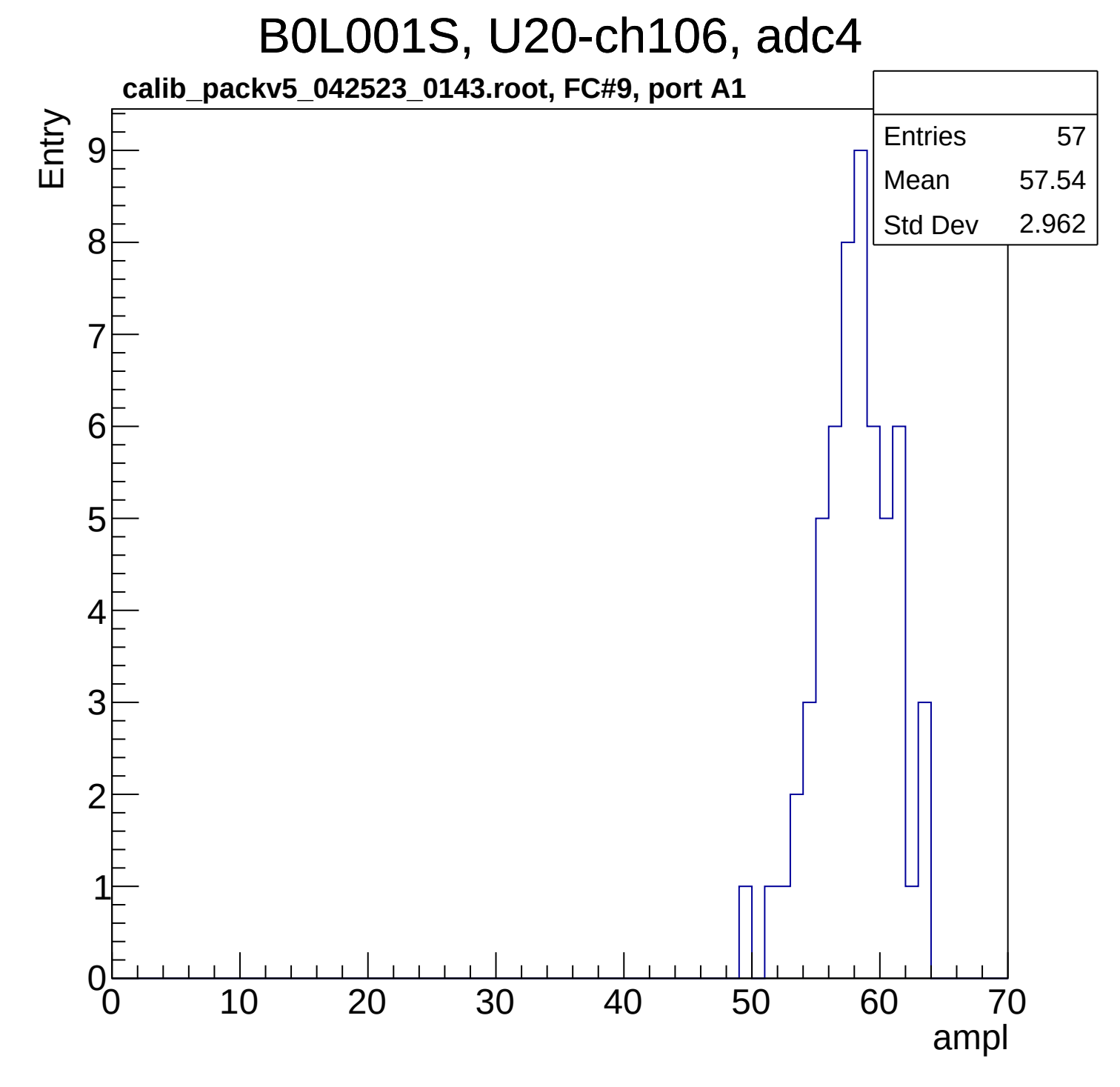
B0L001S, U20-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.54
Std Dev 2.962
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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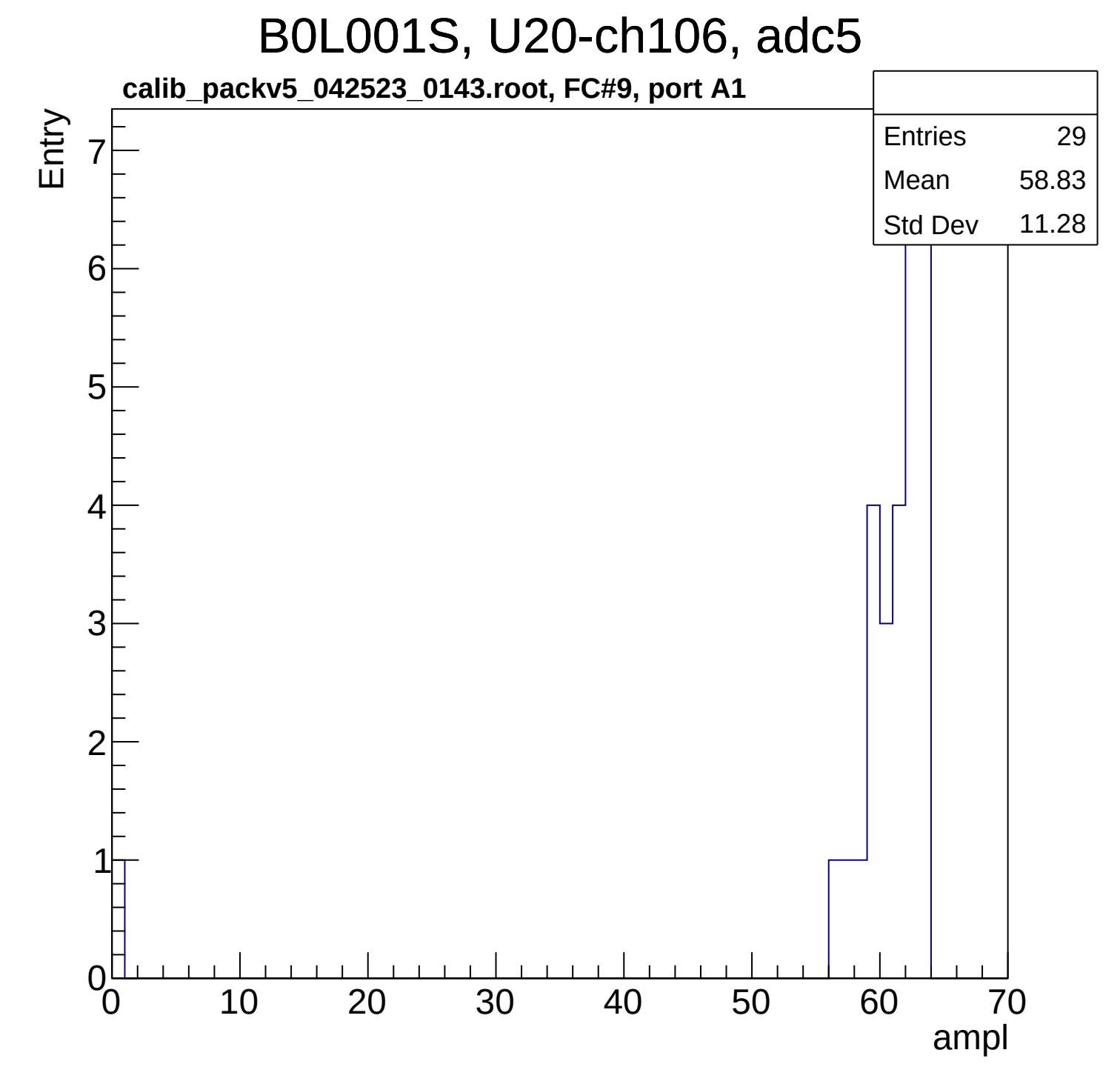
B0L001S, U20-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.83
Std Dev 11.28
Entry
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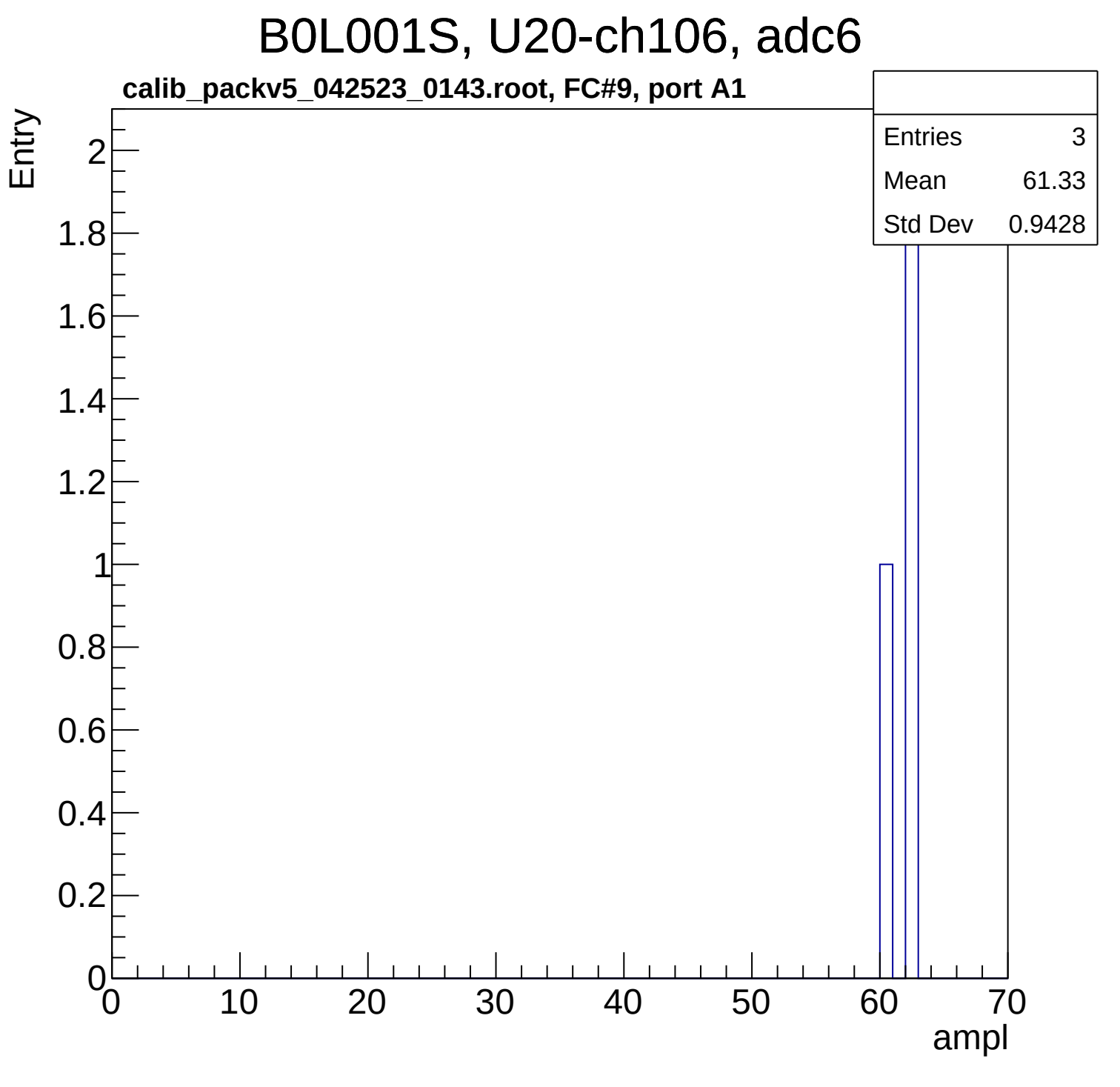
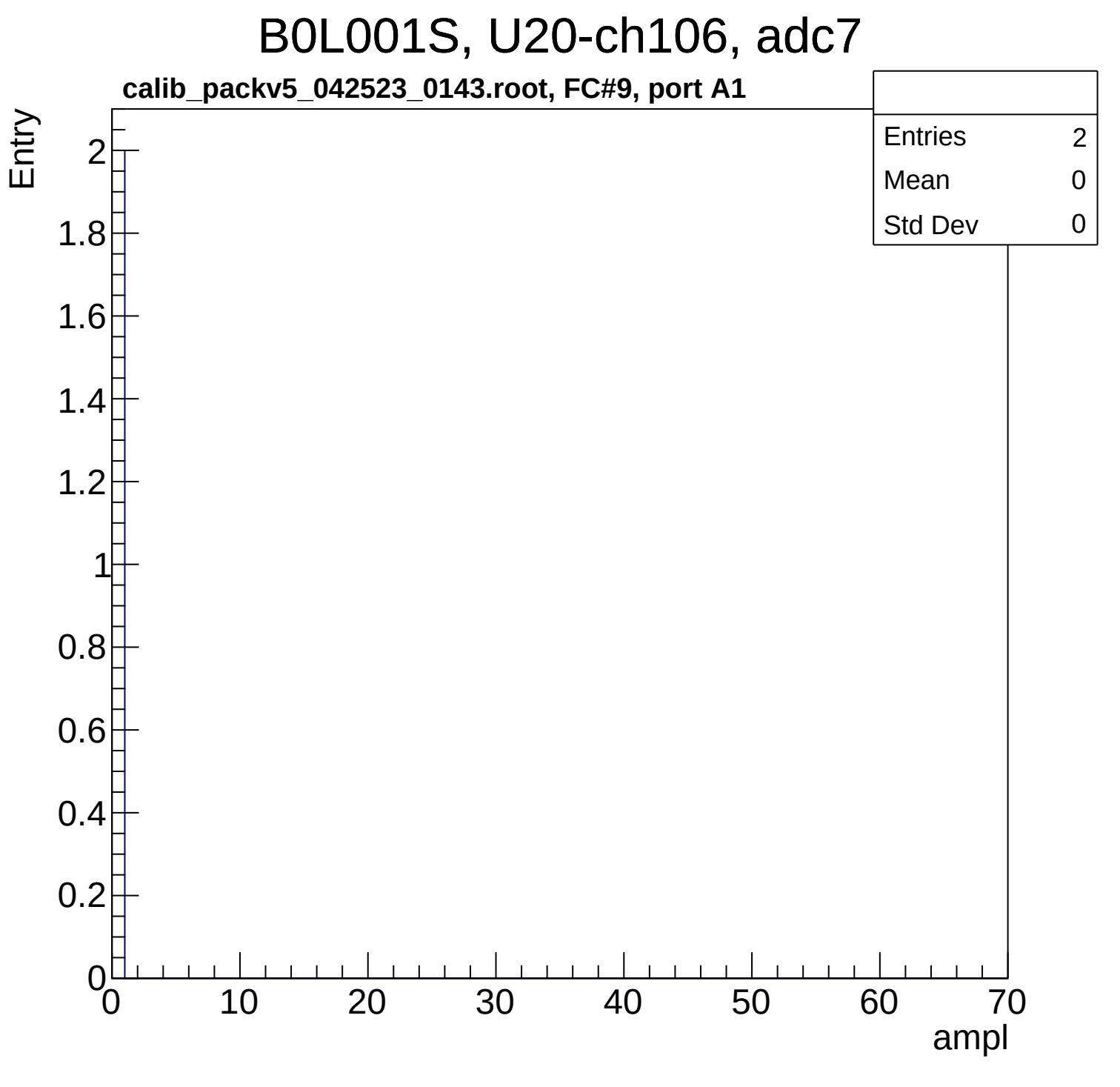
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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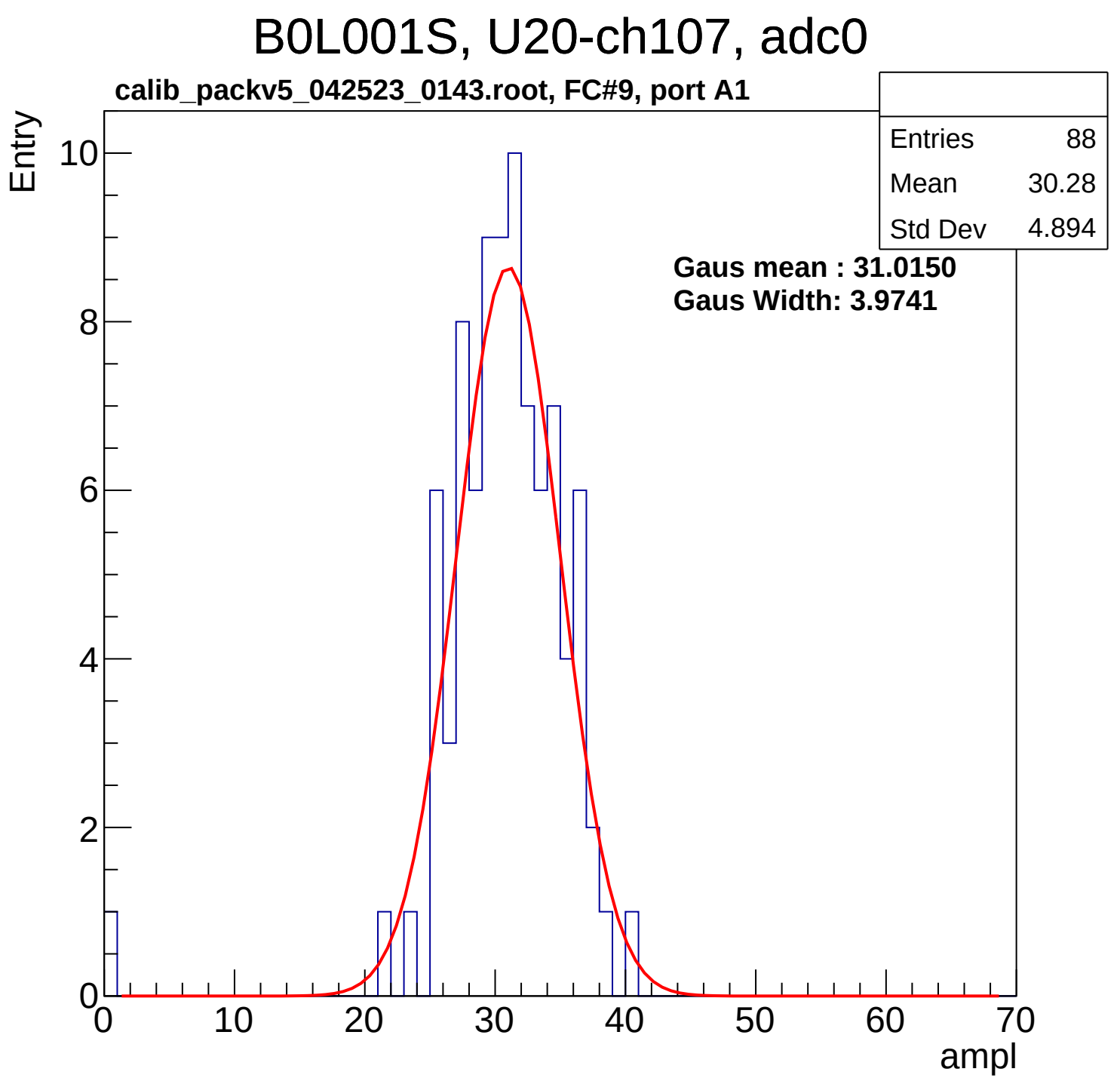
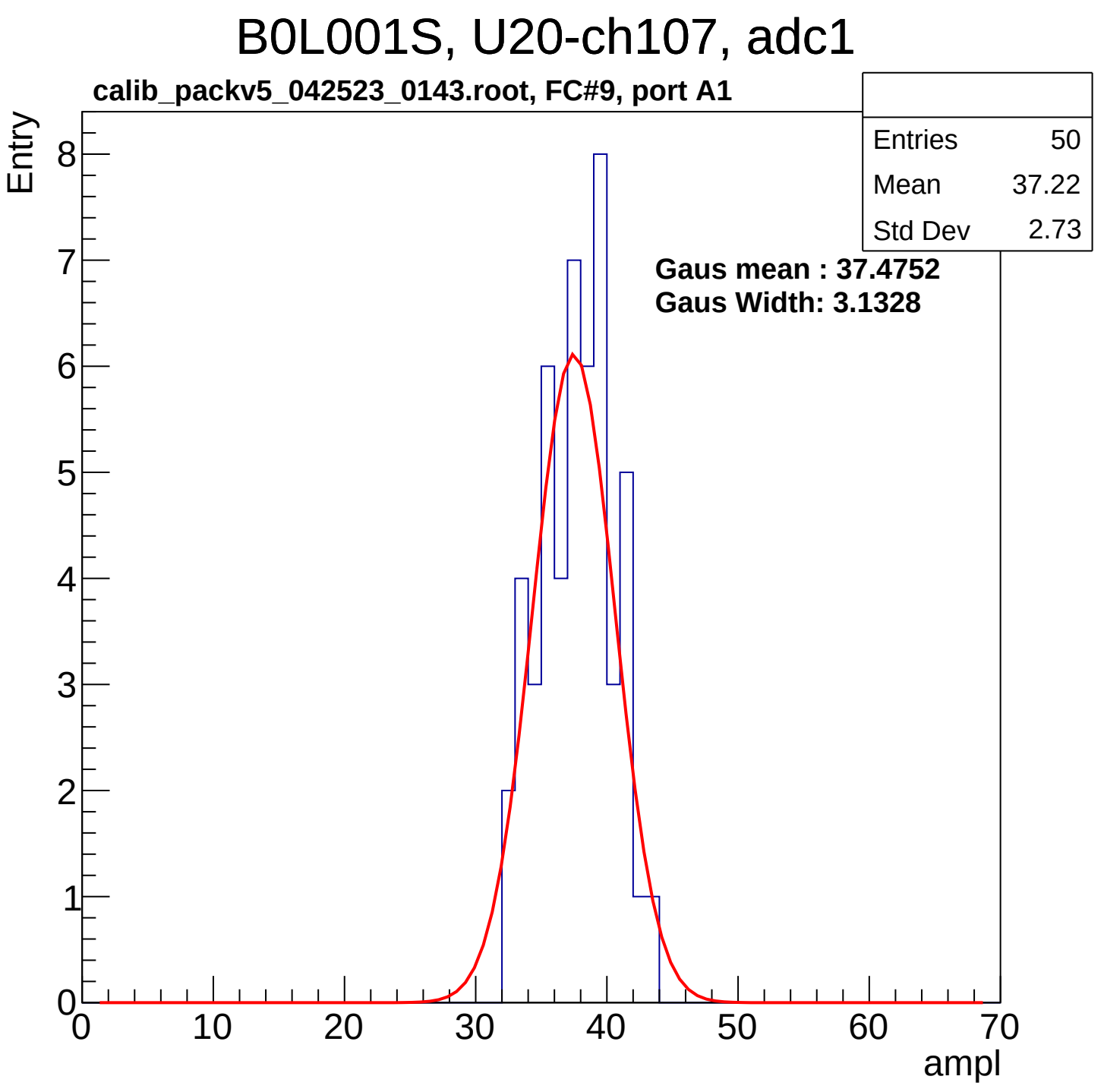
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 30.28
Std Dev 4.894
Gaus mean : 31.0150
Gaus Width: 3.9741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 37.22
Std Dev 2.73
Gaus mean : 37.4752
Gaus Width: 3.1328
Entry
ampl
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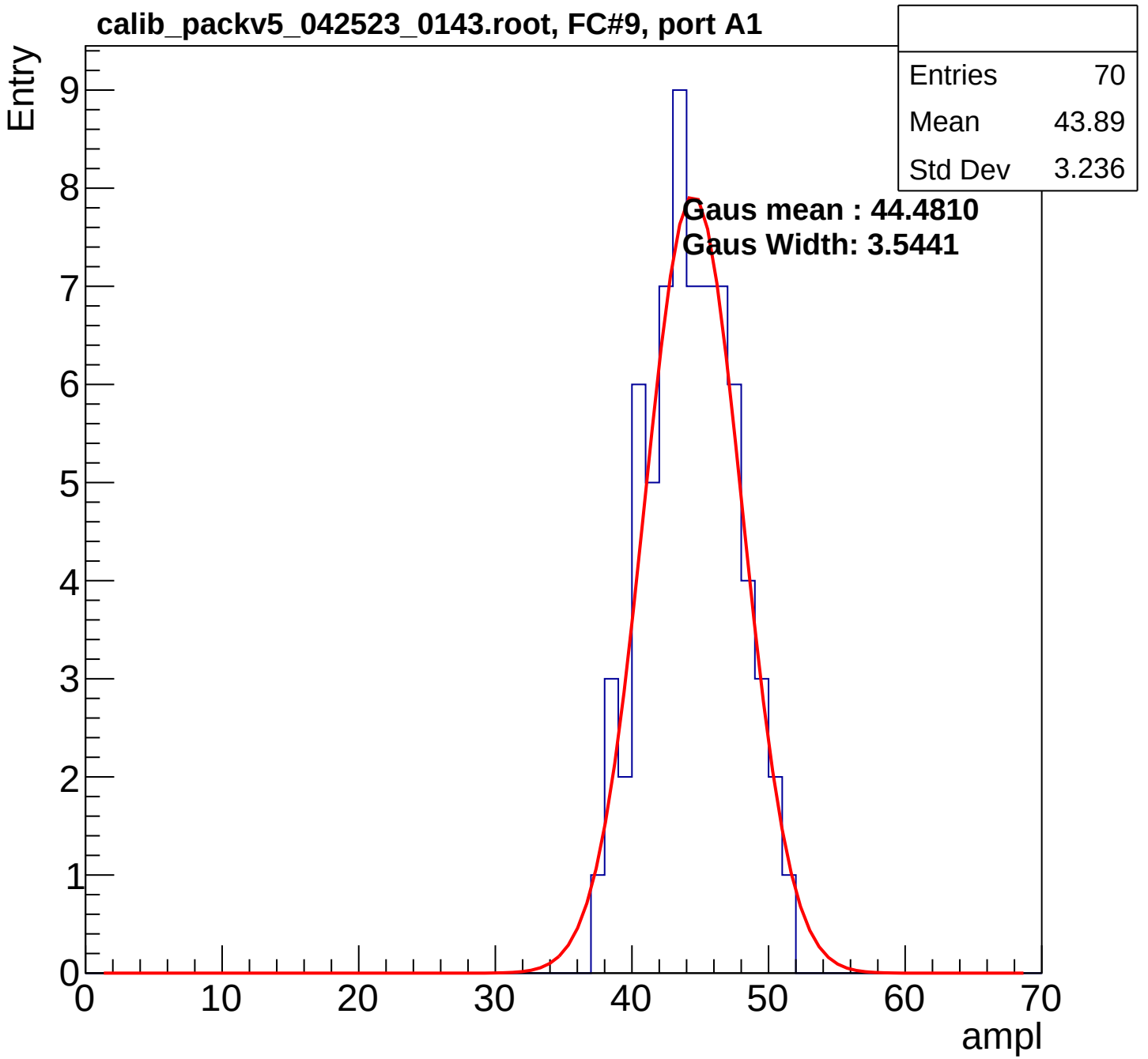
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.89
Std Dev 3.236
Gaus mean : 44.4810
Gaus Width: 3.5441
Entry
ampl
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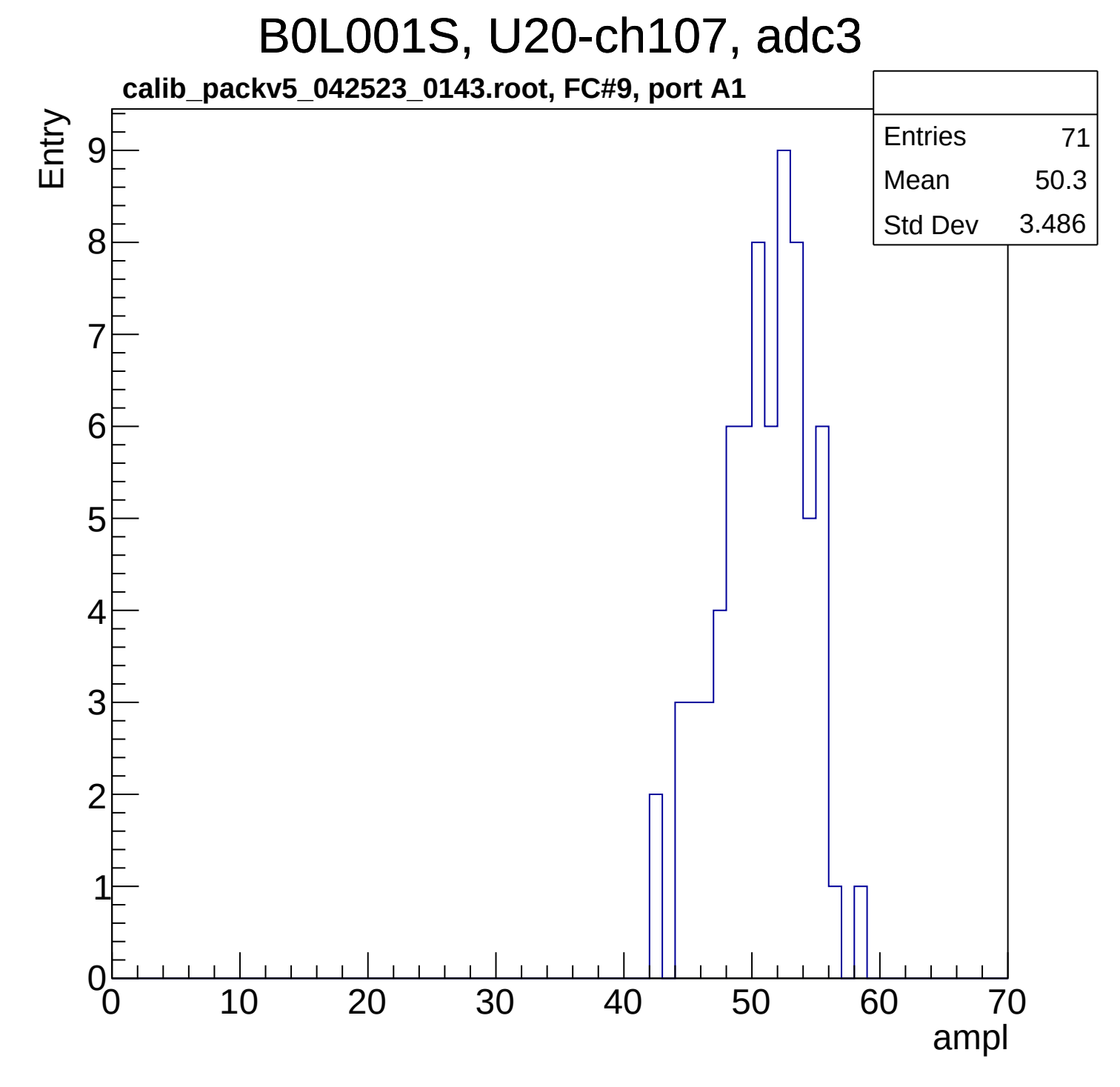
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.3
Std Dev 3.486
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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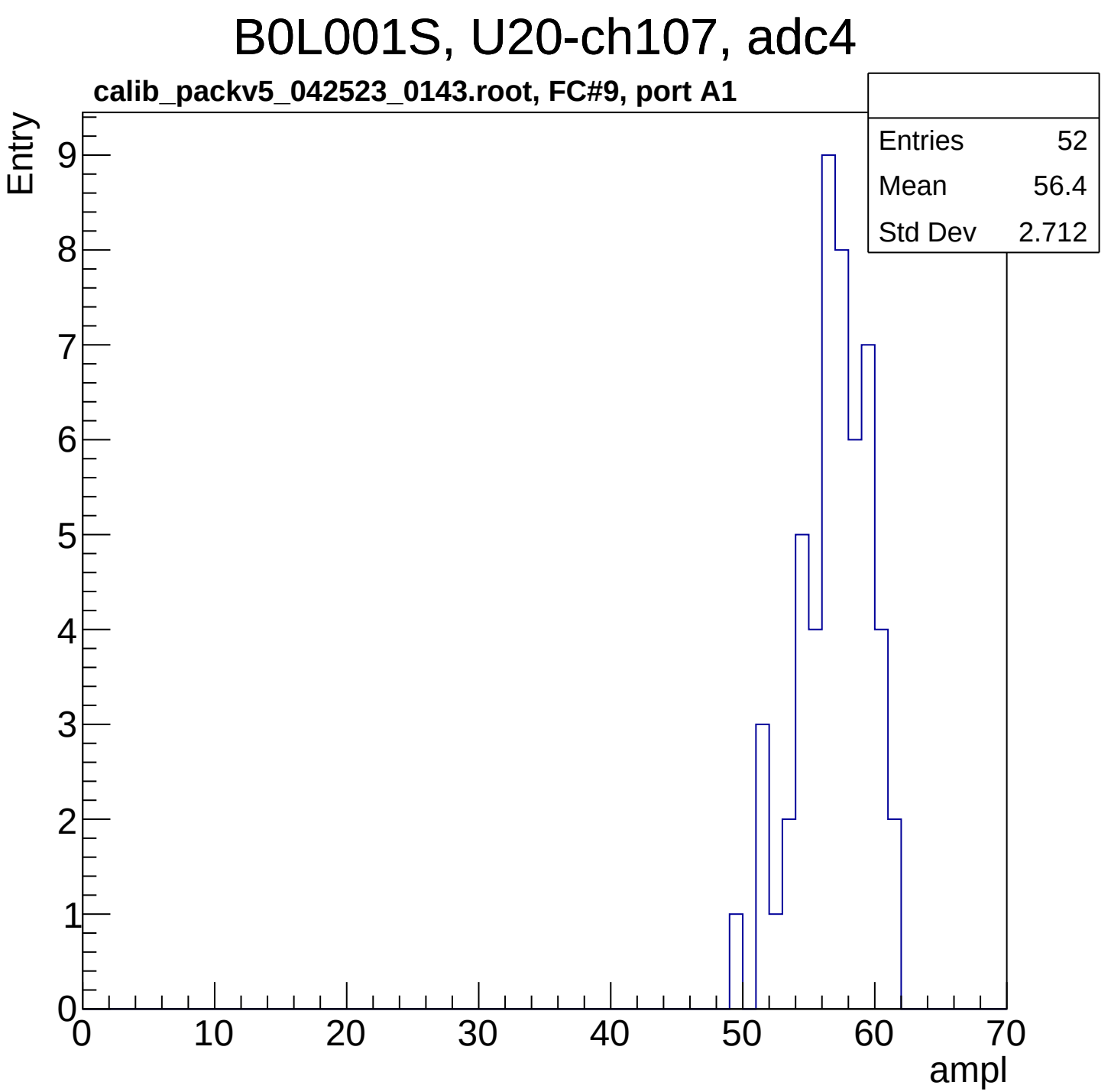
B0L001S, U20-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.4
Std Dev 2.712
Entry
ampl
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2
3
4
5
6
7
8
9
0
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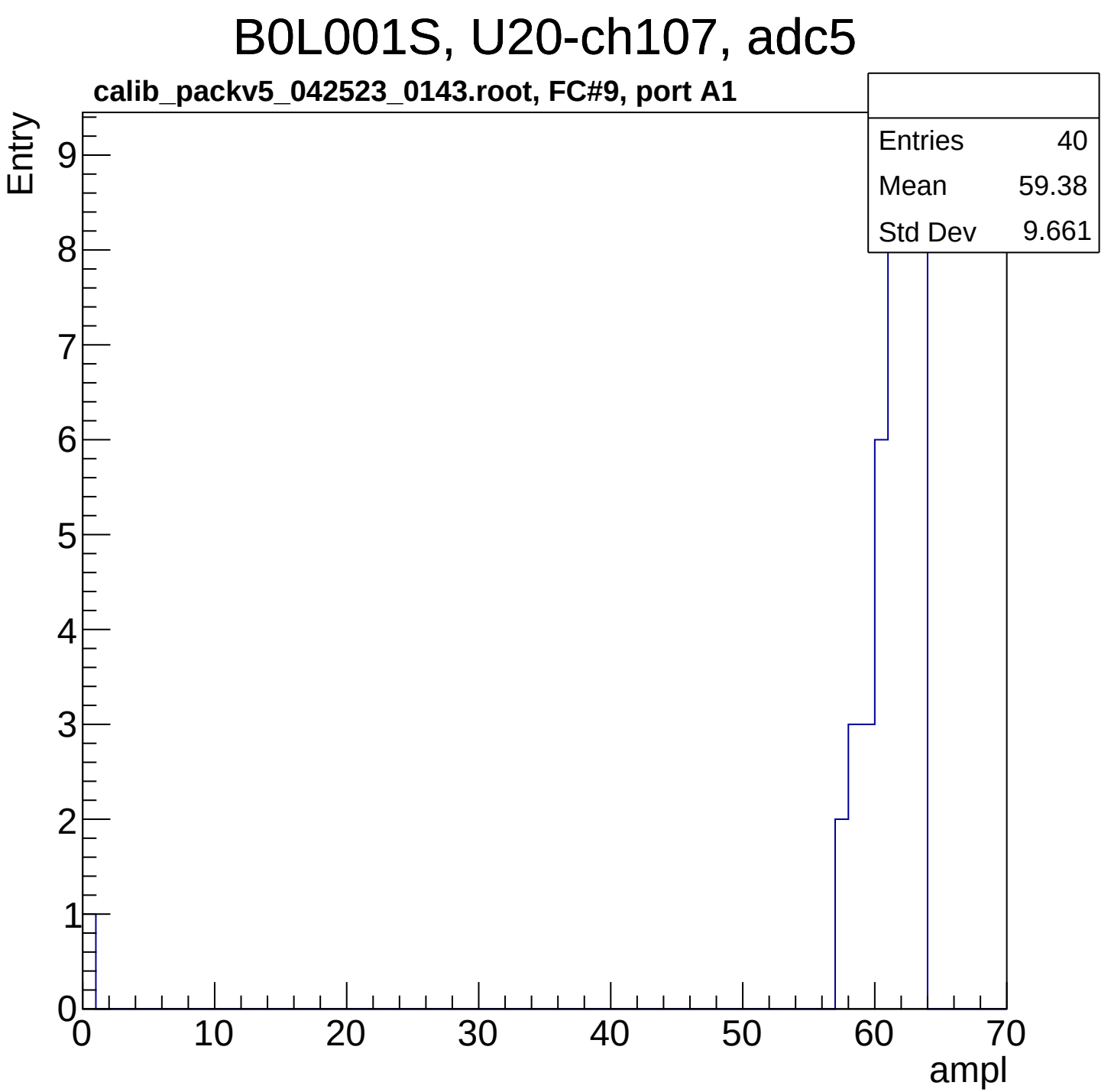
B0L001S, U20-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.38
Std Dev 9.661
Entry
ampl
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2
3
4
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6
7
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0
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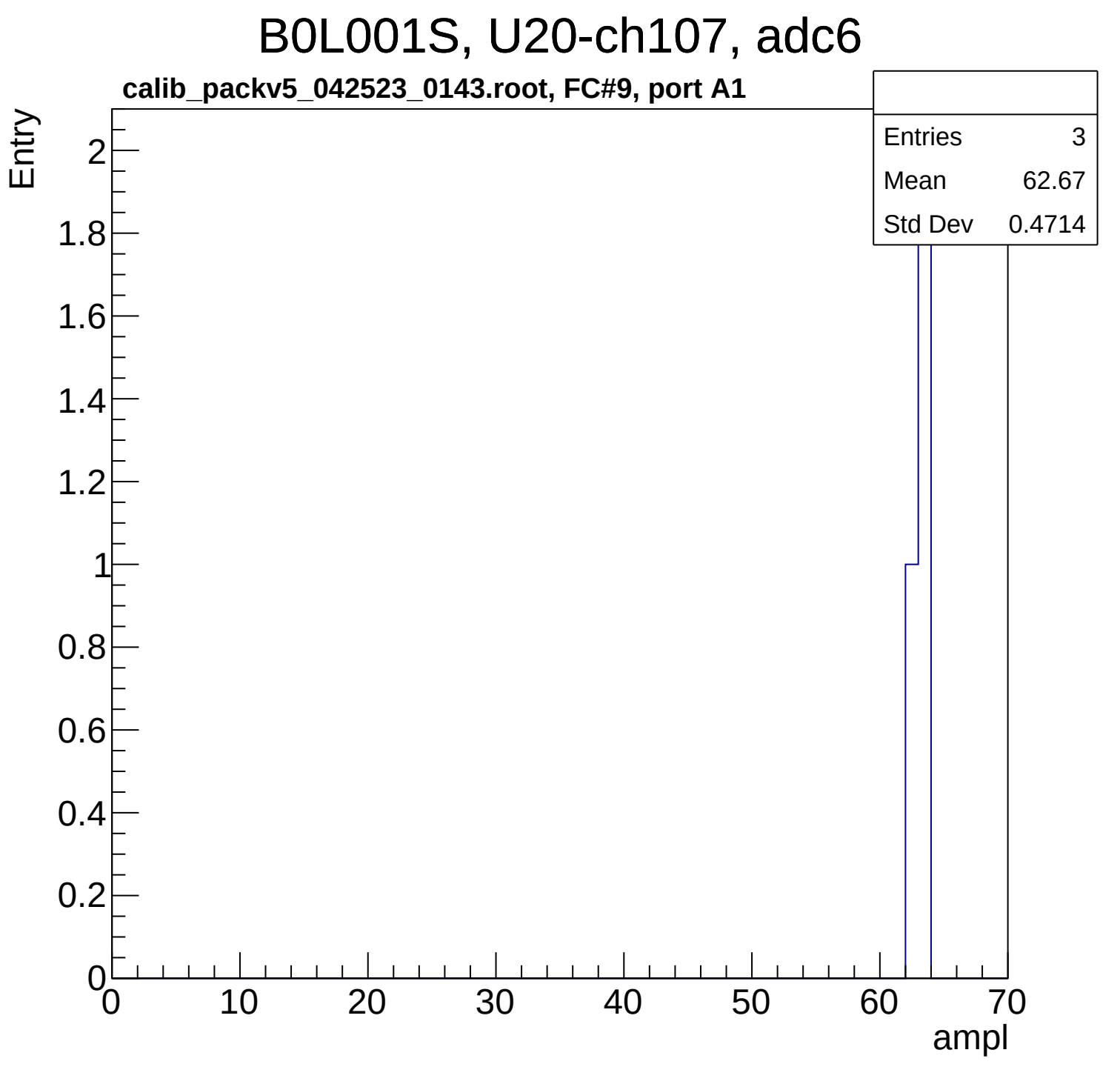
B0L001S, U20-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
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0
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40
50
60
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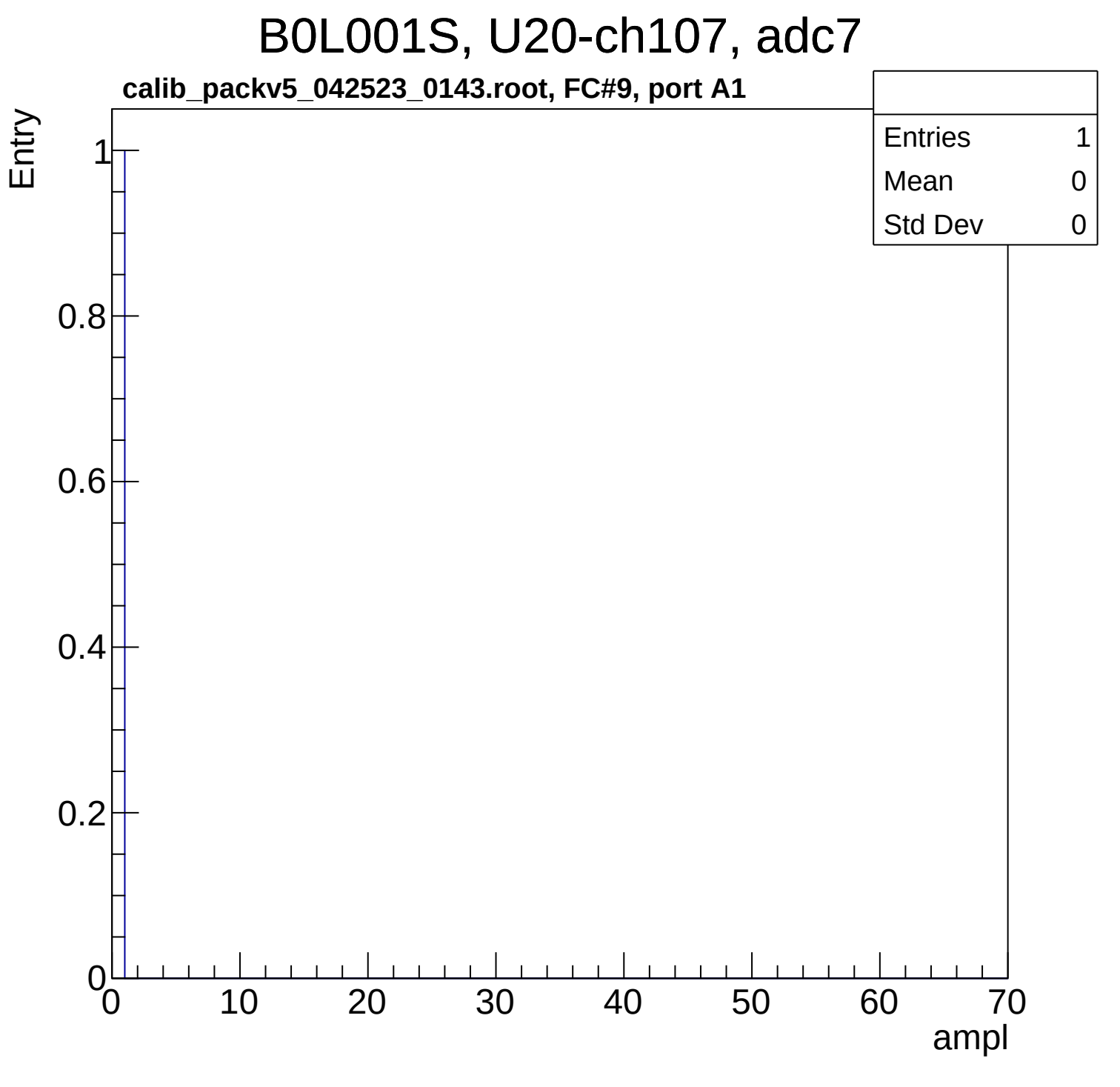
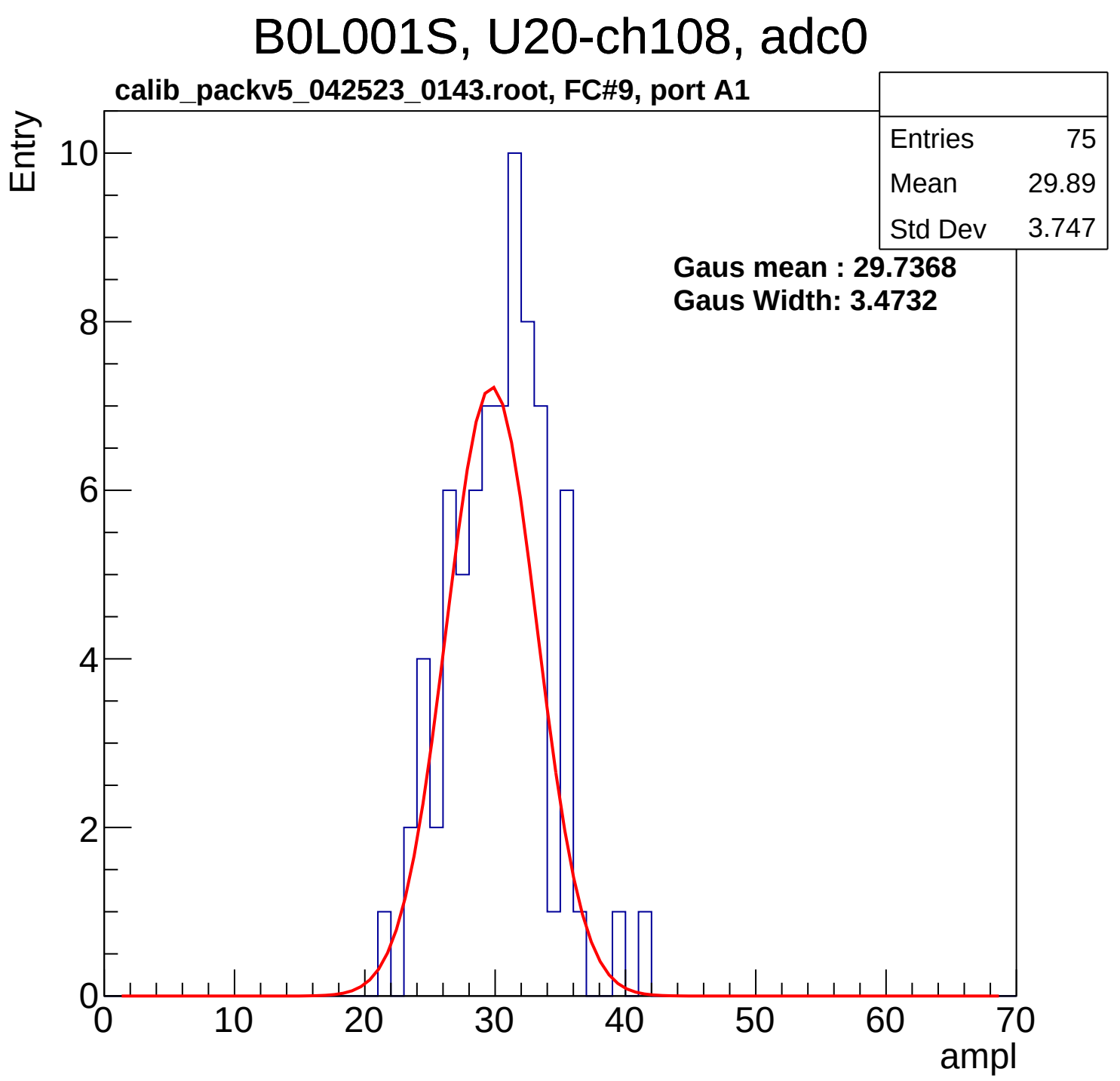
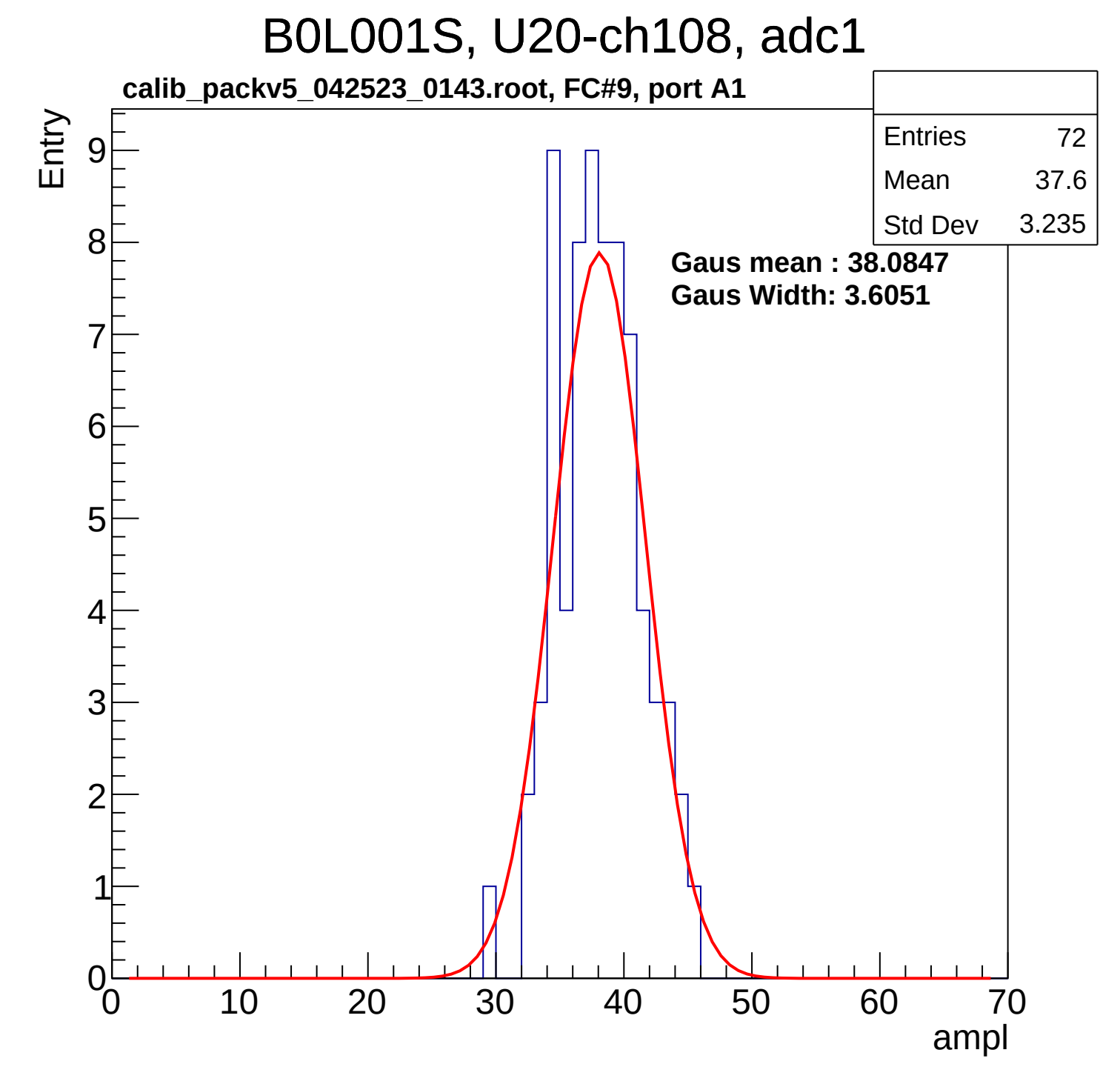

B0L001S, U20-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.89
Std Dev 3.747
Gaus mean : 29.7368
Gaus Width: 3.4732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.6
Std Dev 3.235
Gaus mean : 38.0847
Gaus Width: 3.6051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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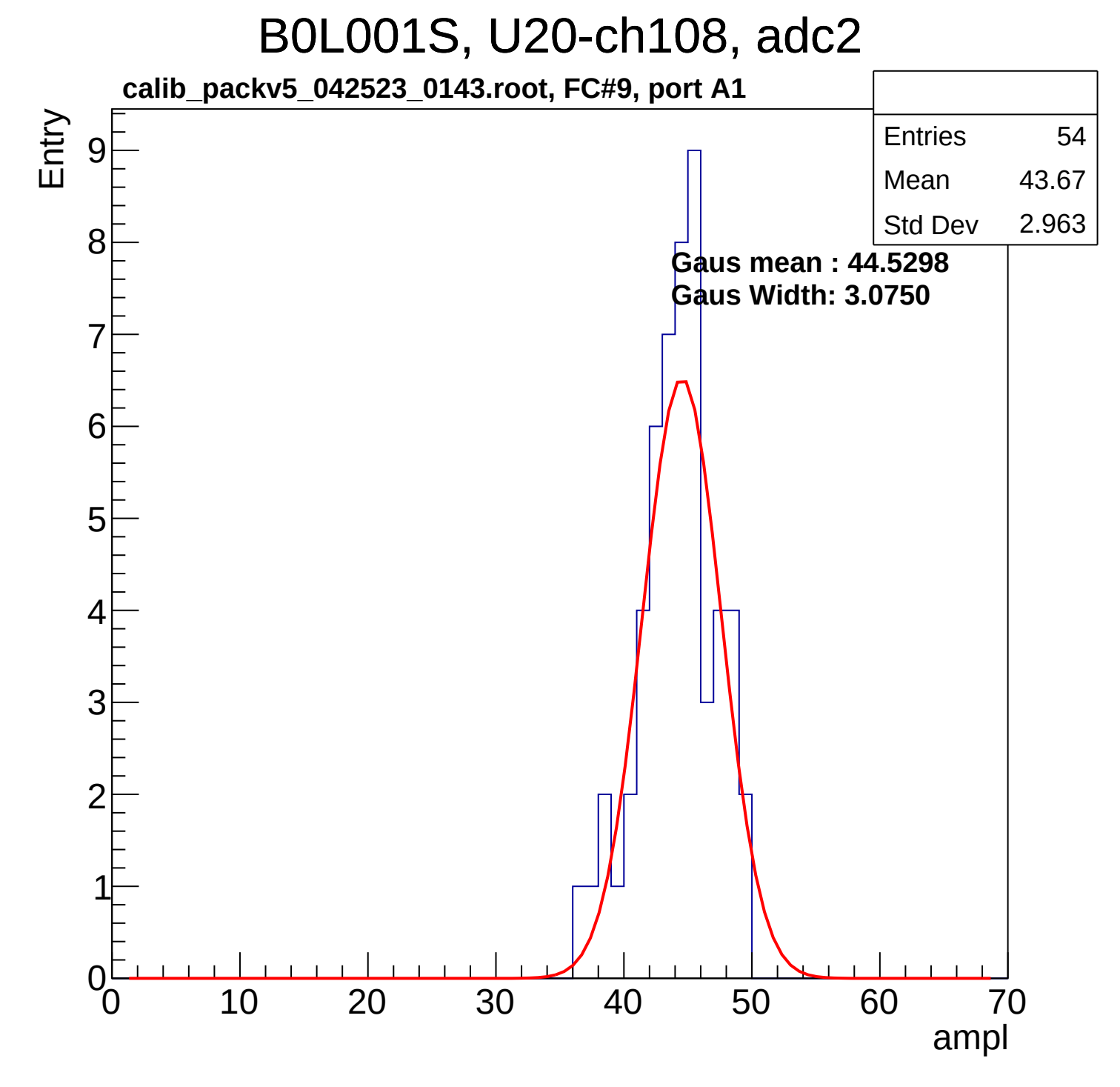
B0L001S, U20-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.67
Std Dev 2.963
Gaus mean : 44.5298
Gaus Width: 3.0750
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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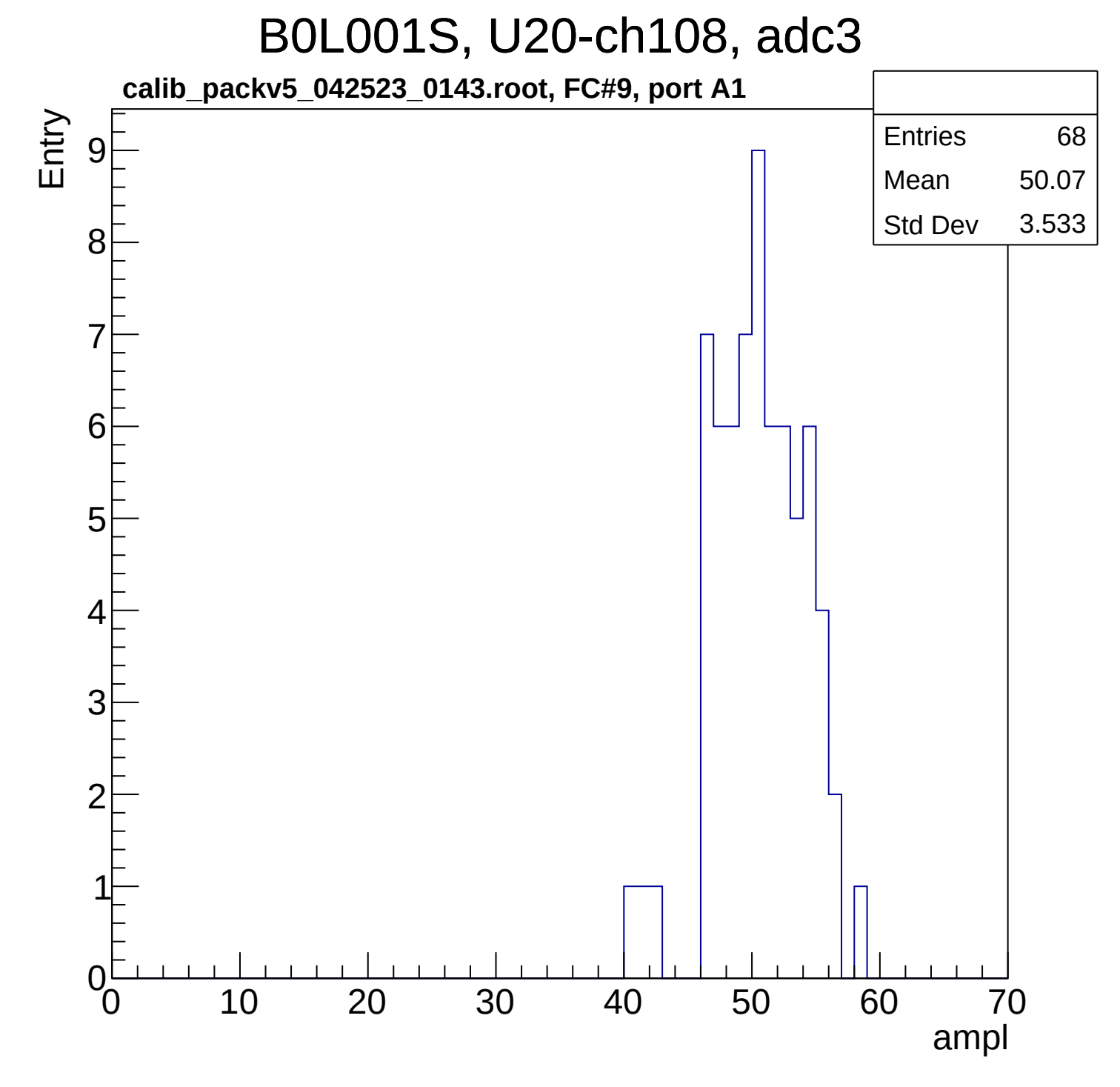
B0L001S, U20-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50.07
Std Dev 3.533
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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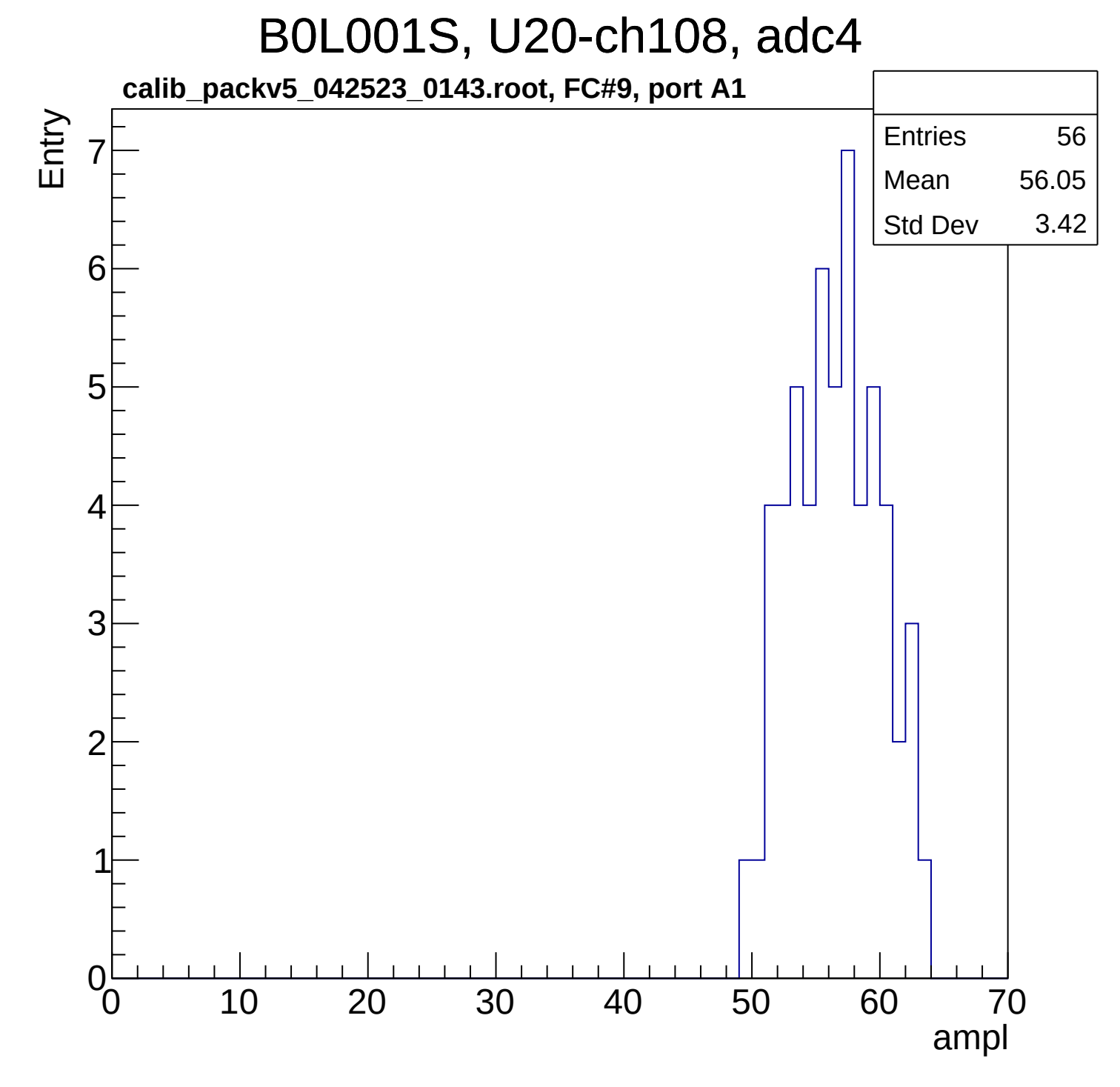
B0L001S, U20-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.05
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
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7
0
10
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60
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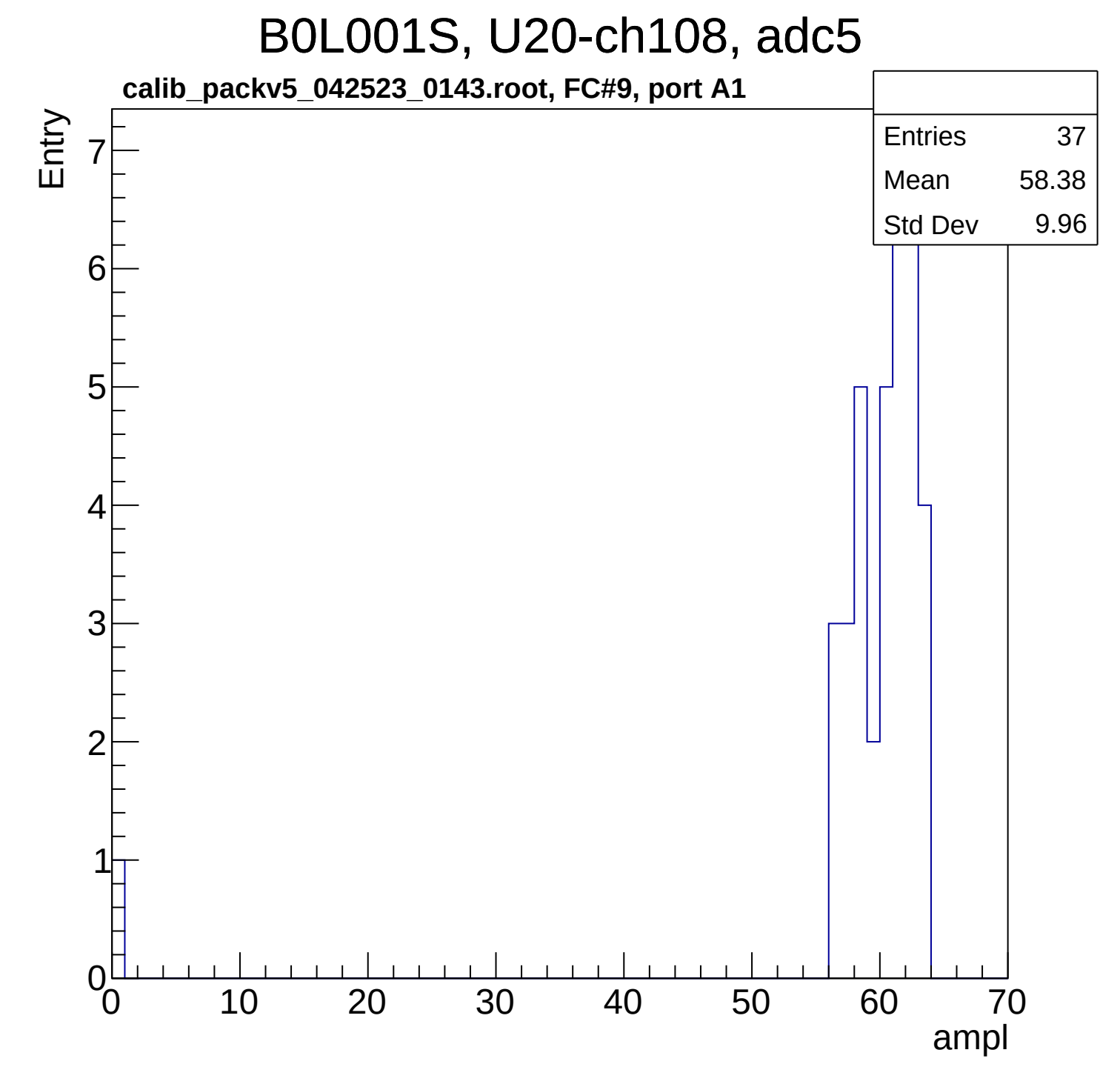
B0L001S, U20-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.38
Std Dev 9.96
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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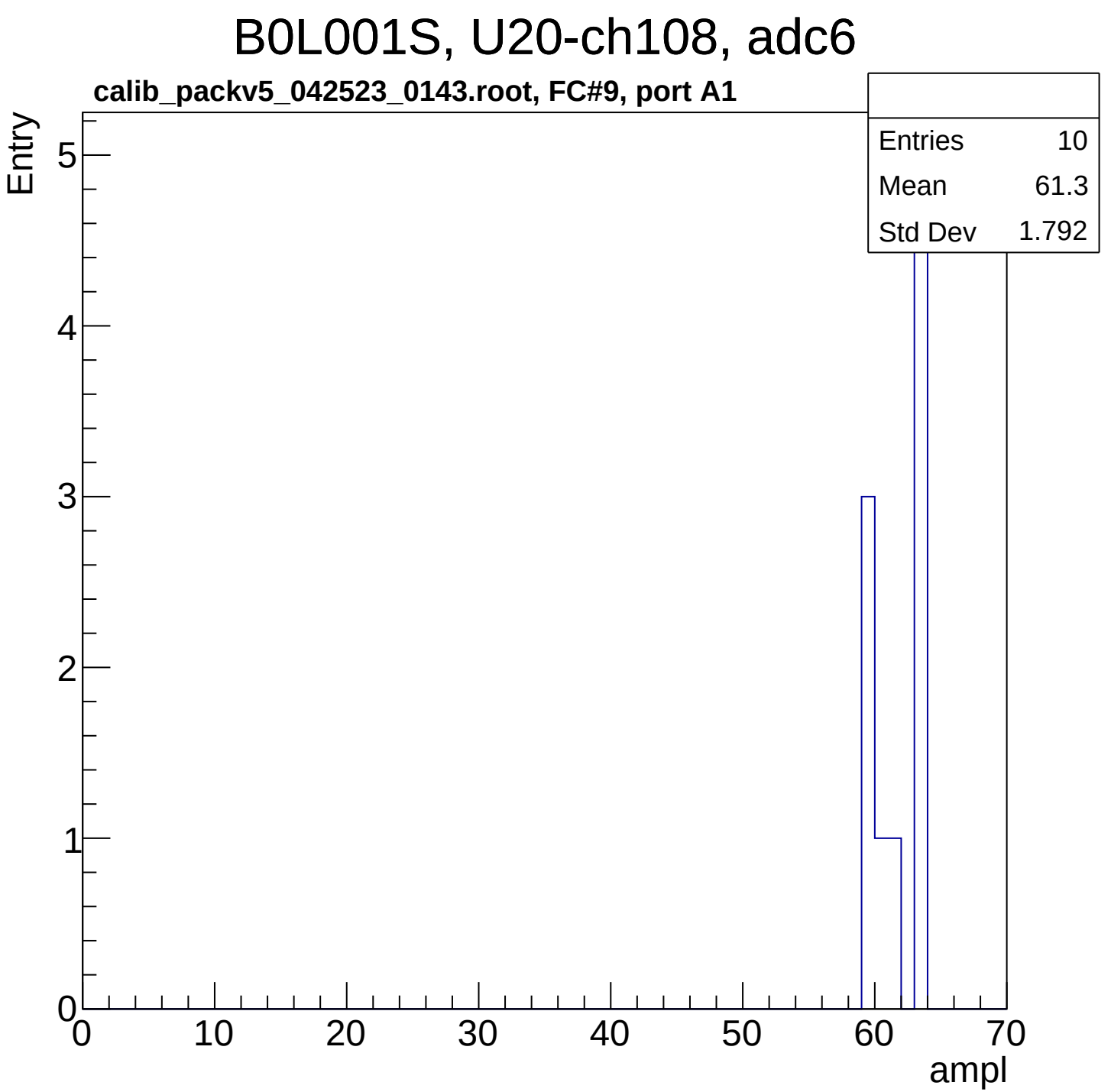
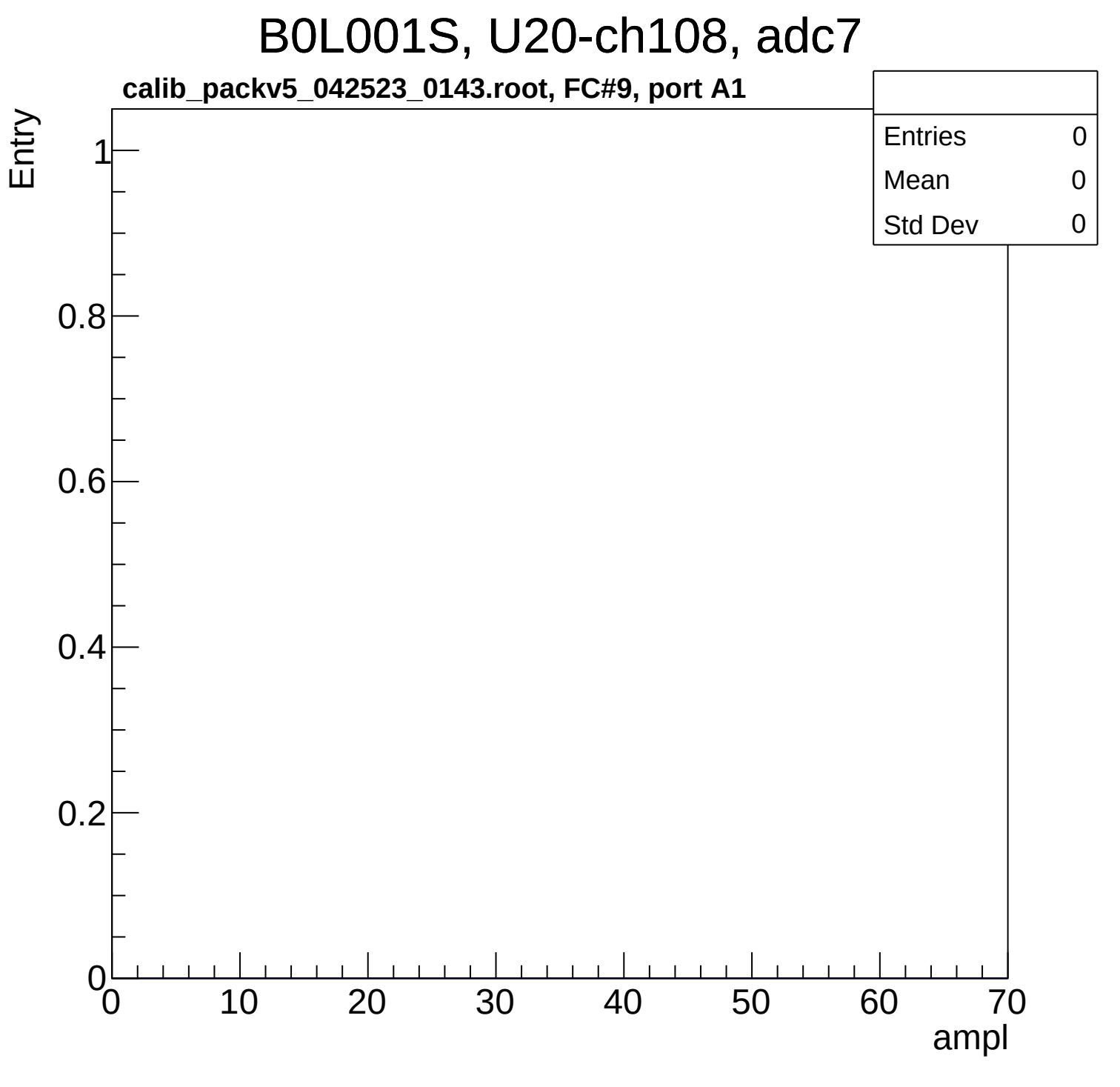

B0L001S, U20-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.3
Std Dev 1.792
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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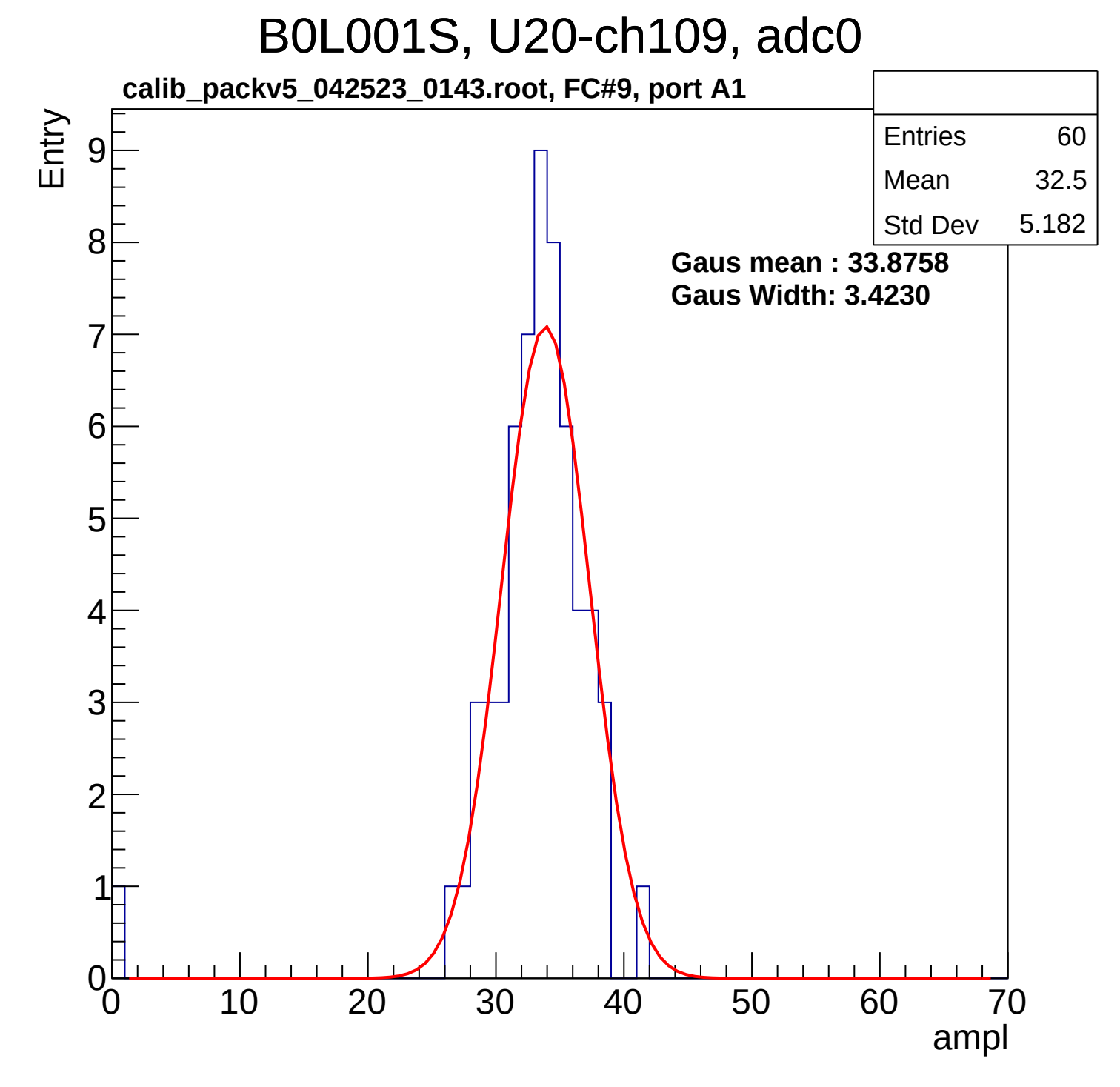
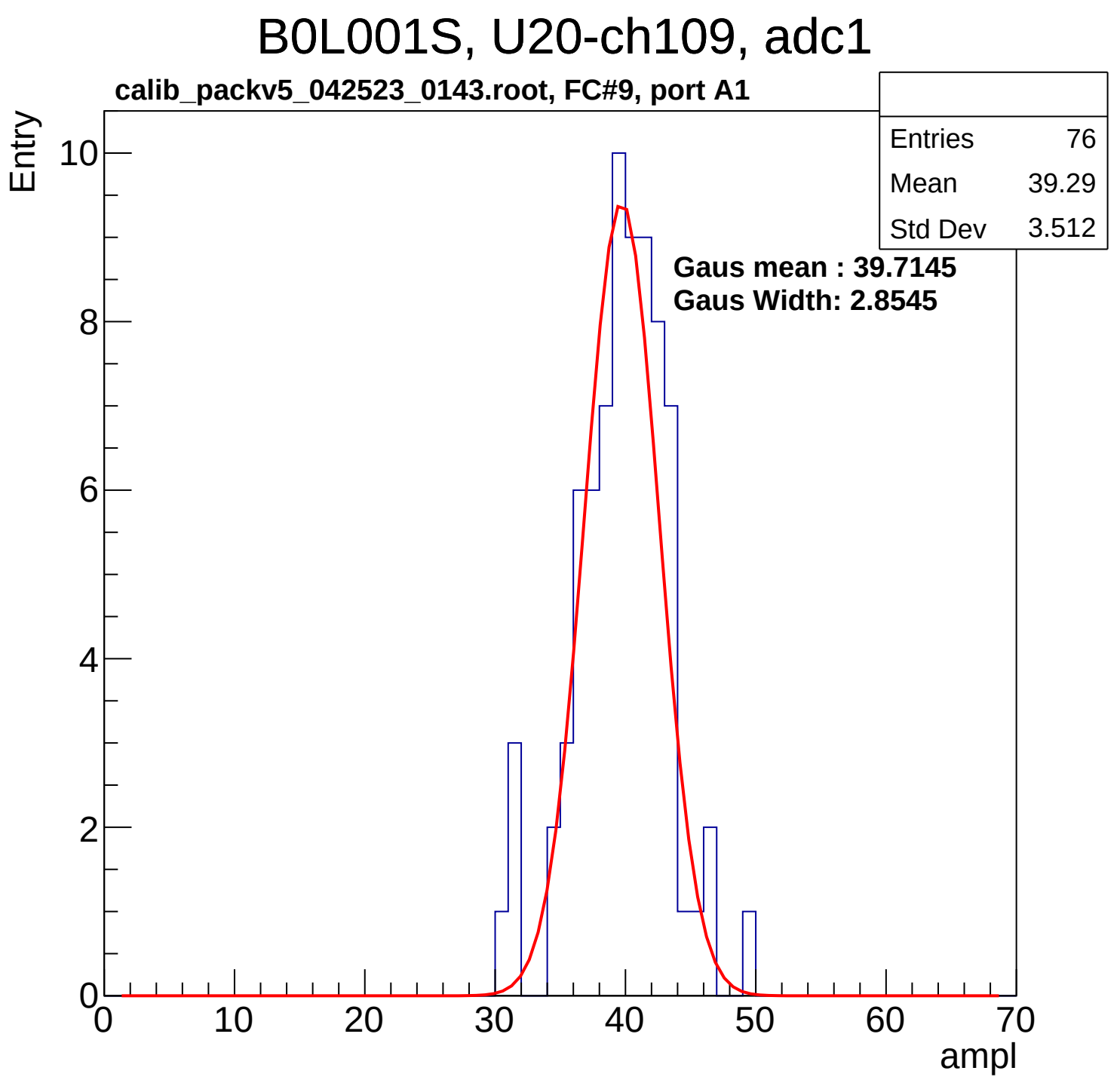
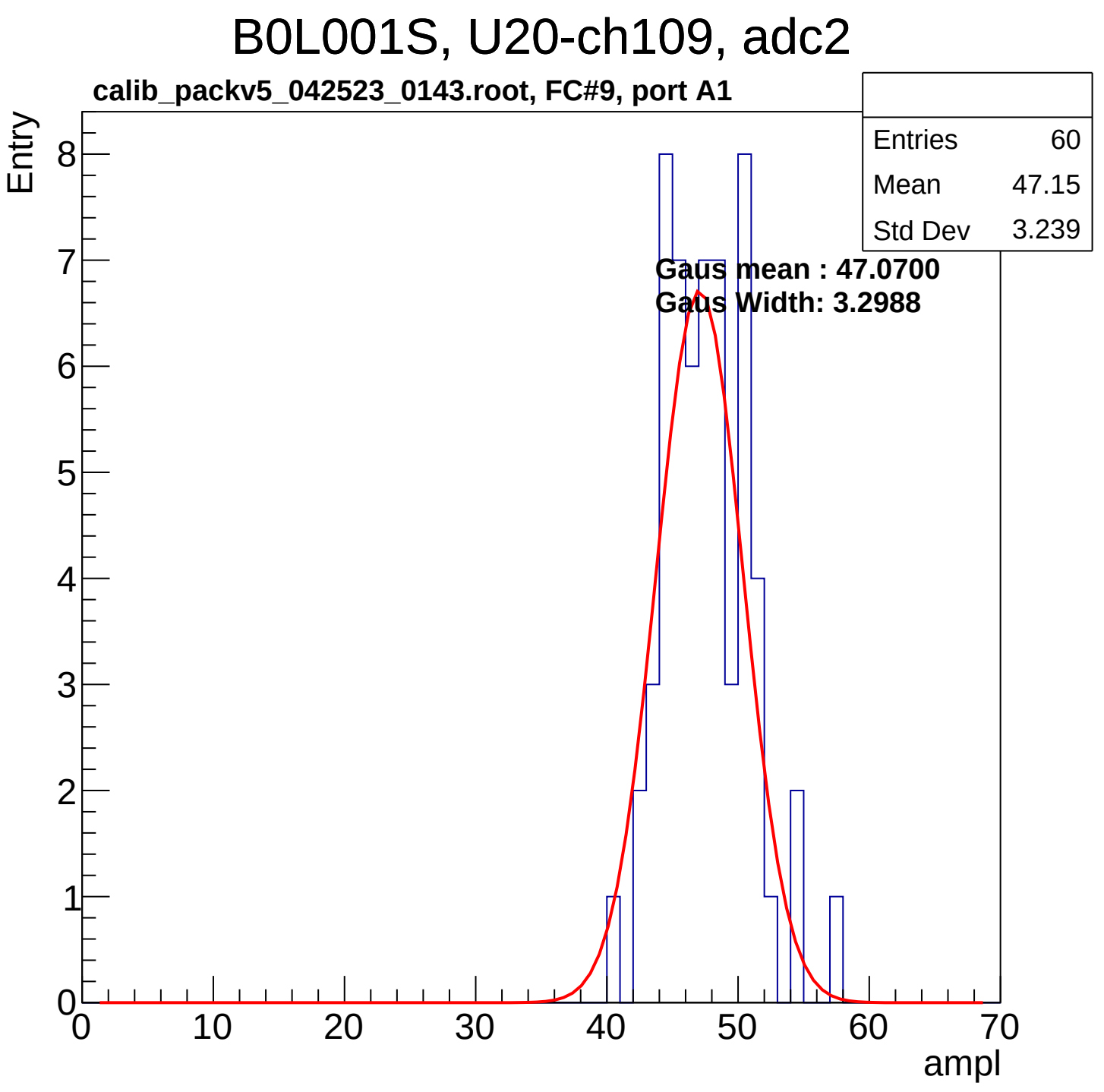
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 32.5
Std Dev 5.182
Gaus mean : 33.8758
Gaus Width: 3.4230
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 39.29
Std Dev 3.512
Gaus mean : 39.7145
Gaus Width: 2.8545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 47.15
Std Dev 3.239
Gaus mean : 47.0700
Gaus Width: 3.2988
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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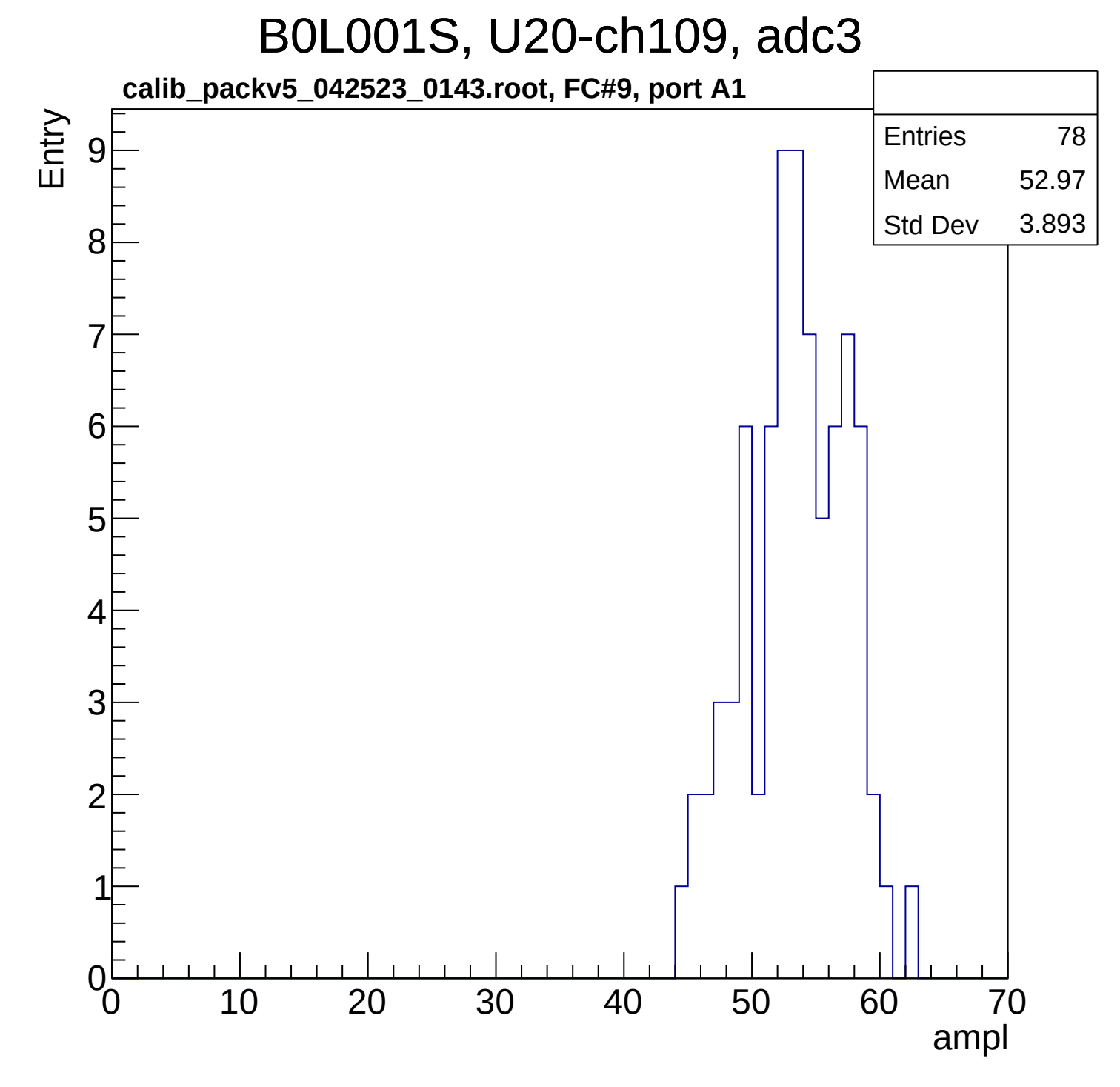
B0L001S, U20-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 52.97
Std Dev 3.893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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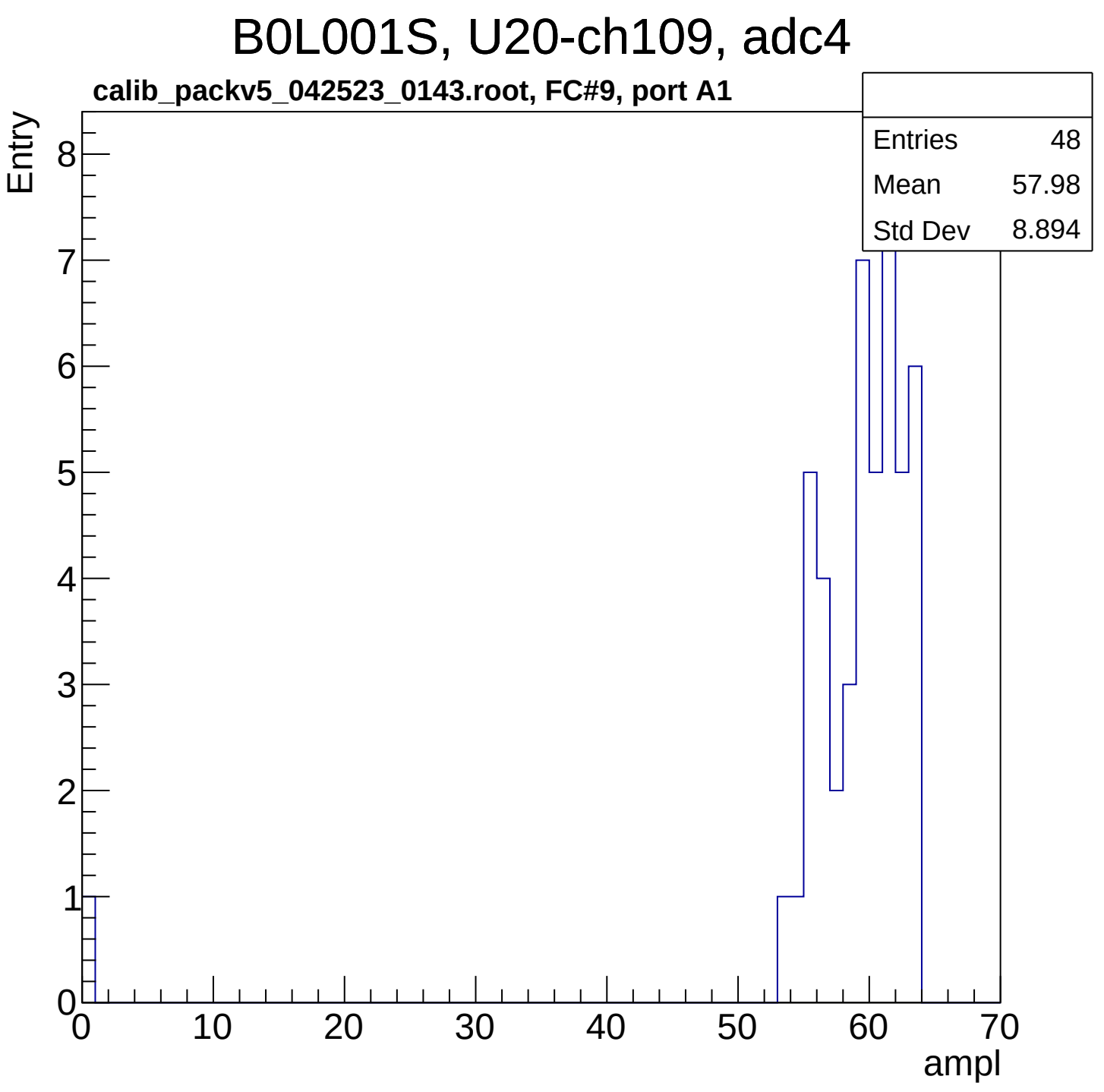
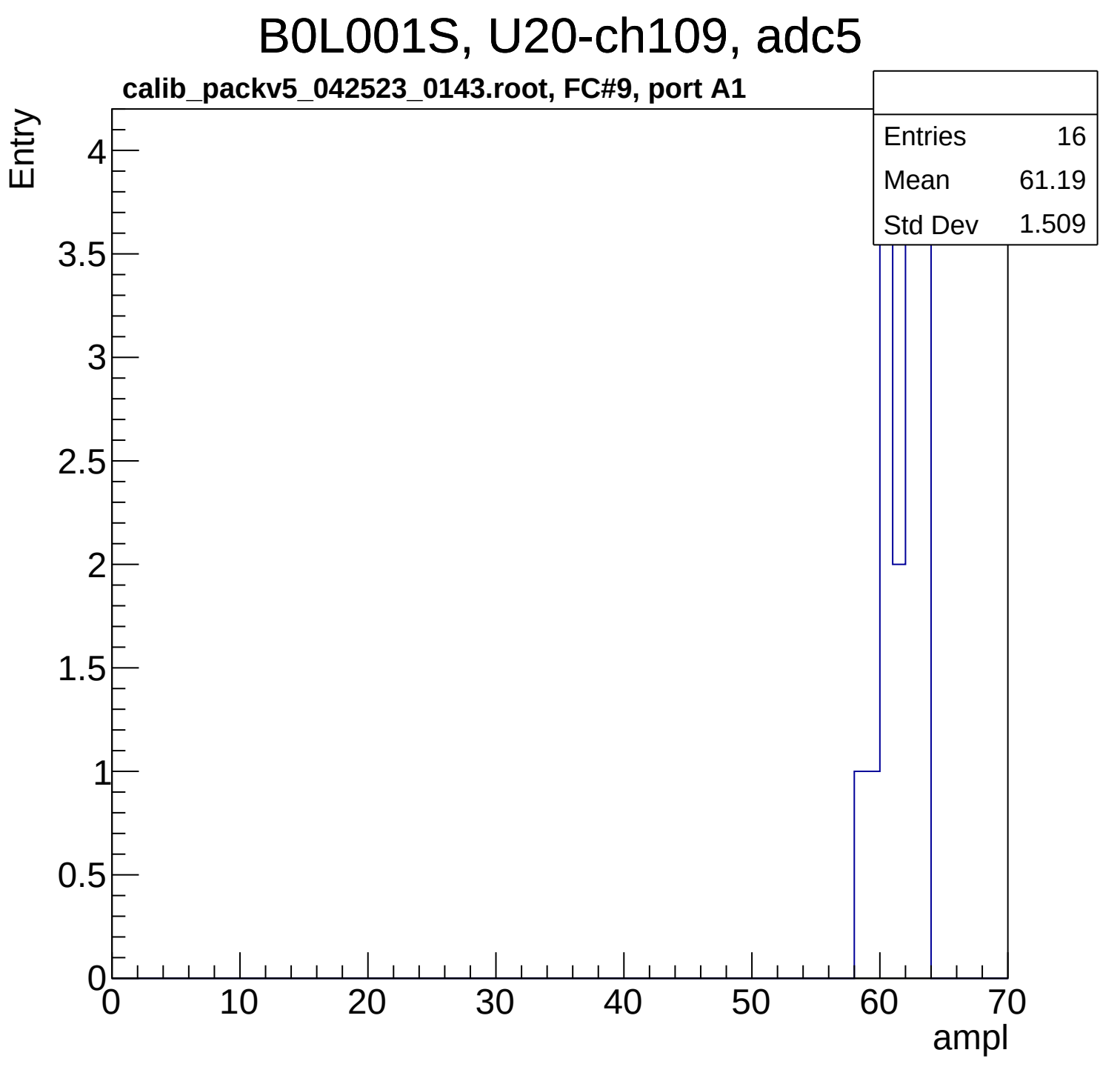
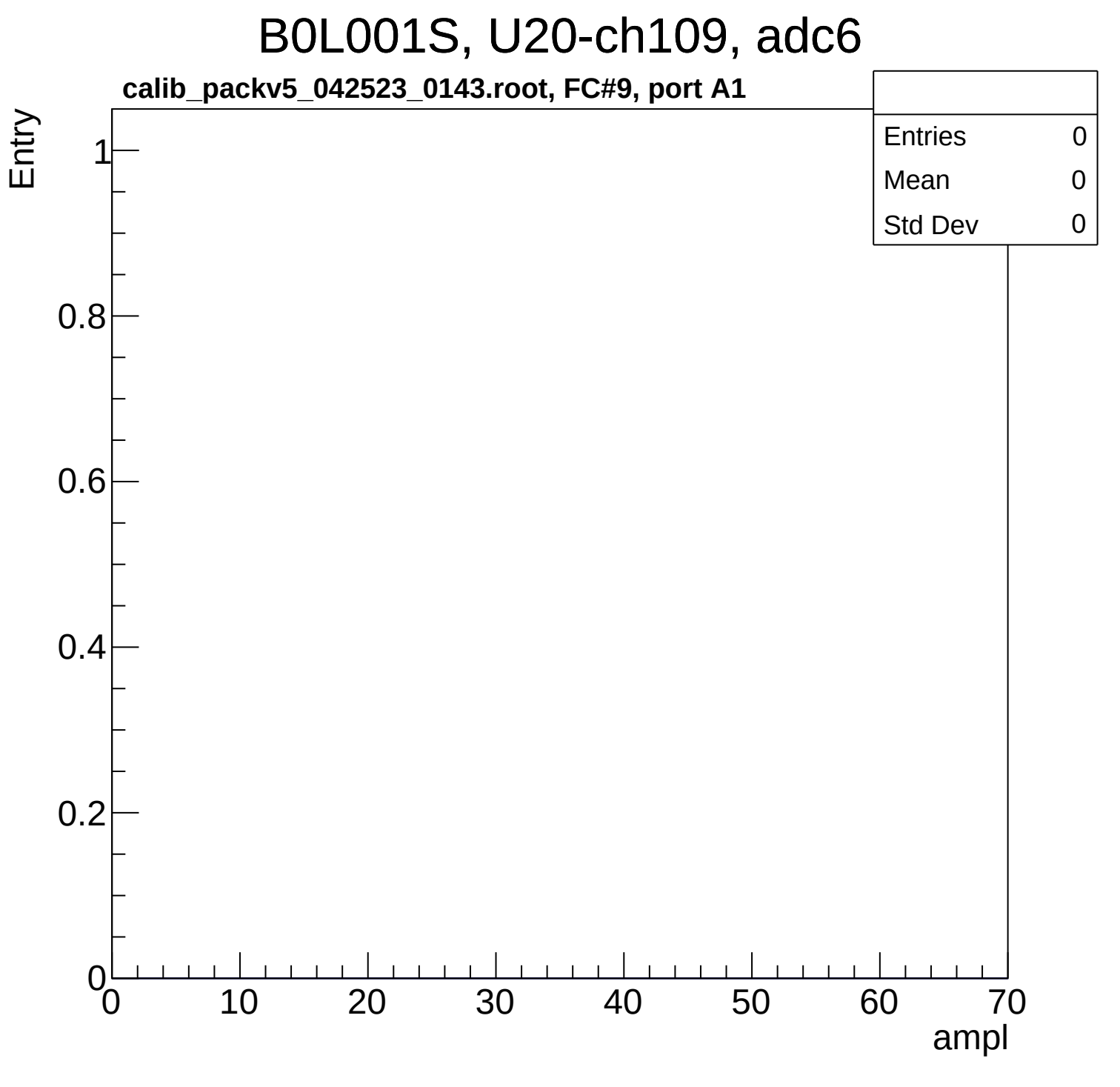
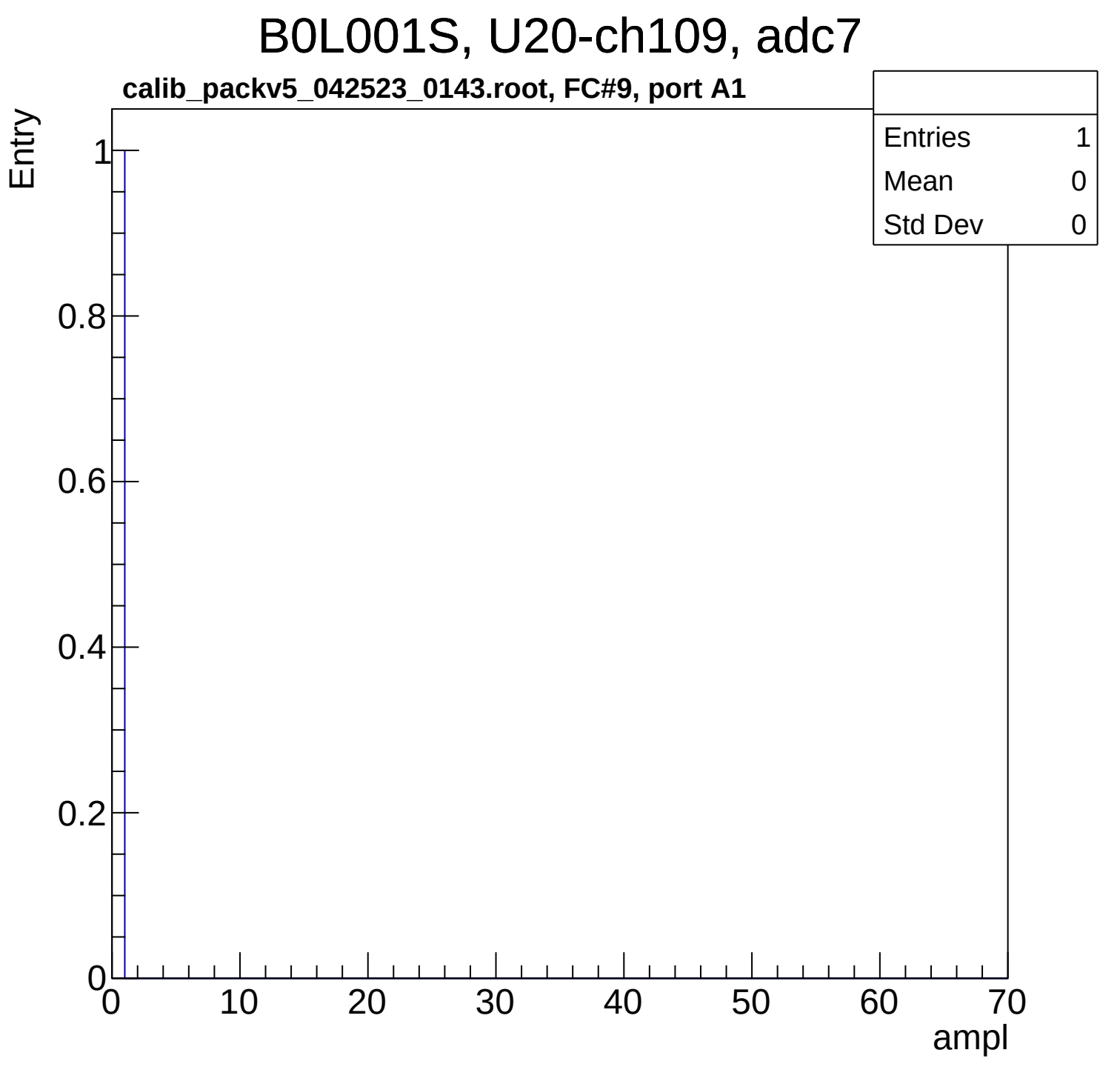
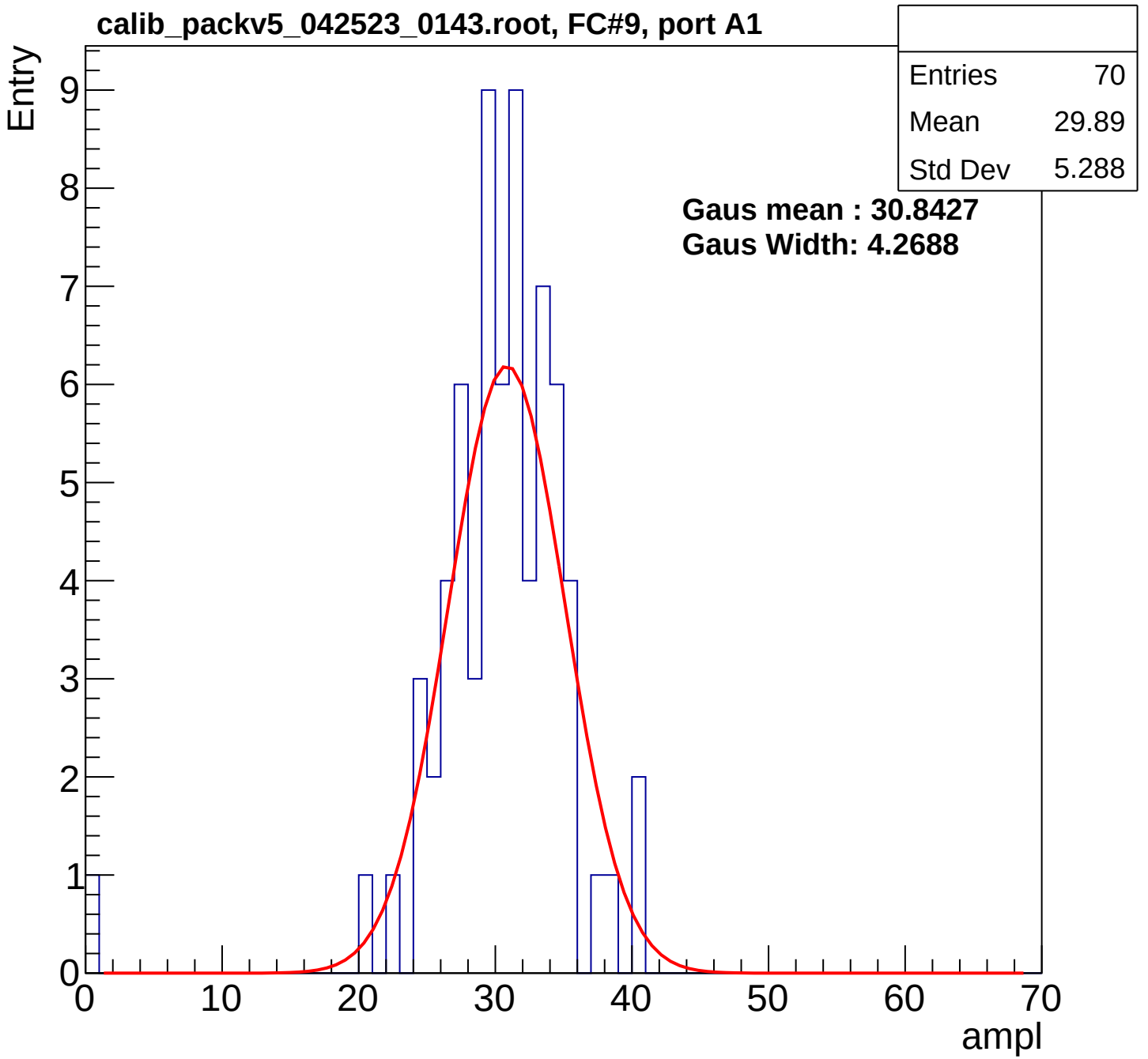
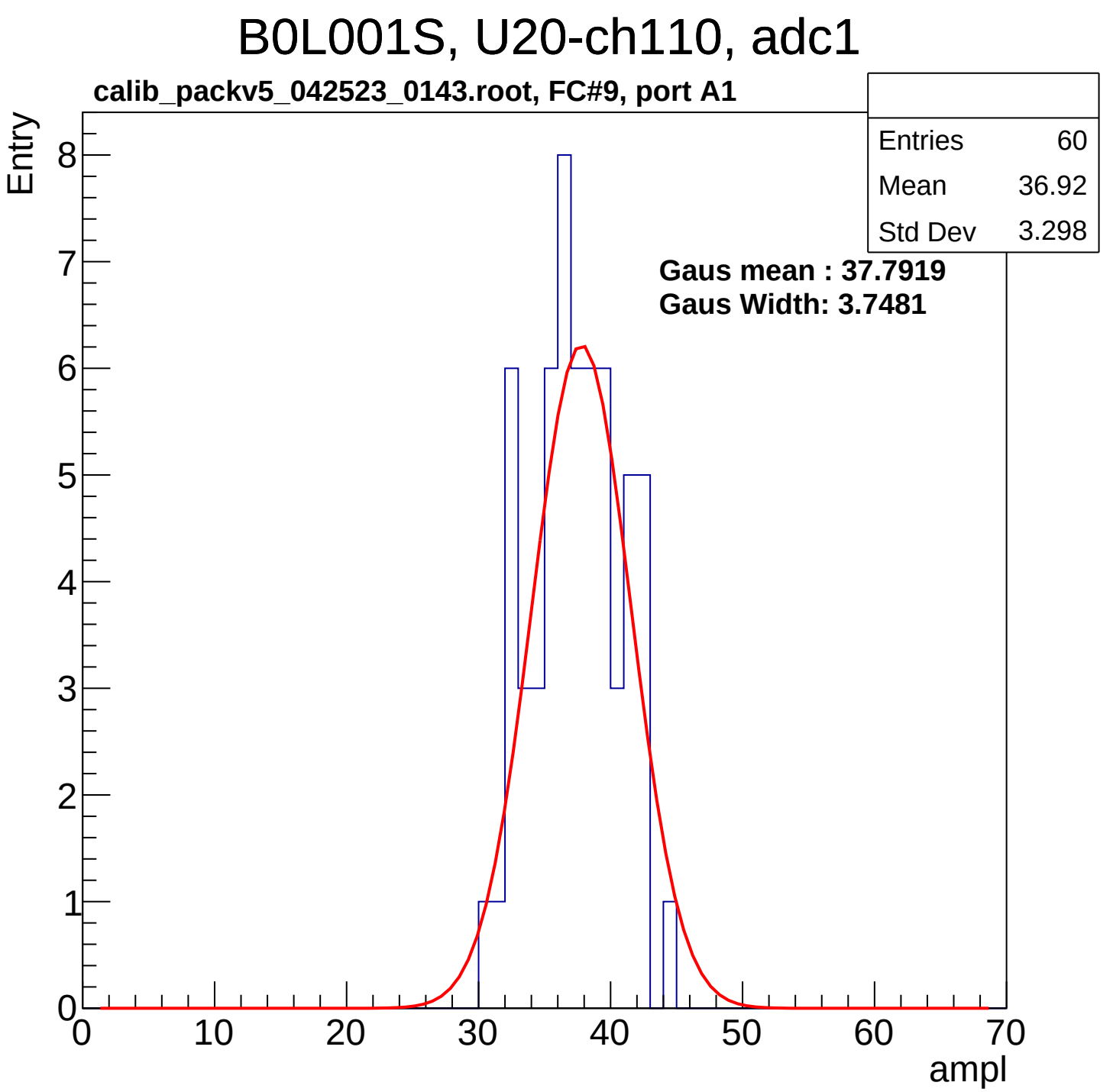

B0L001S, U20-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.98
Std Dev 8.894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

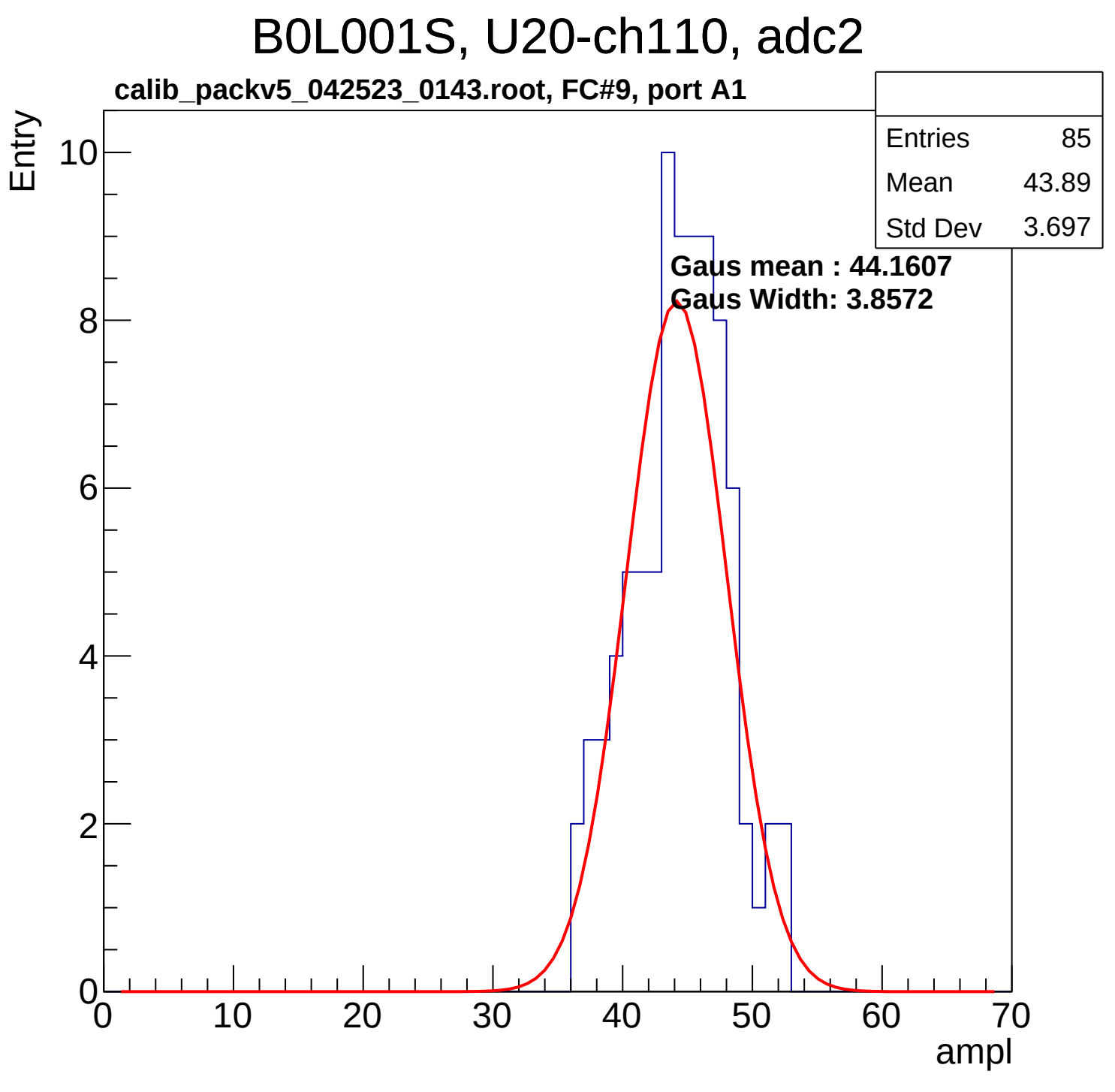

B0L001S, U20-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 61.19
Std Dev 1.509
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

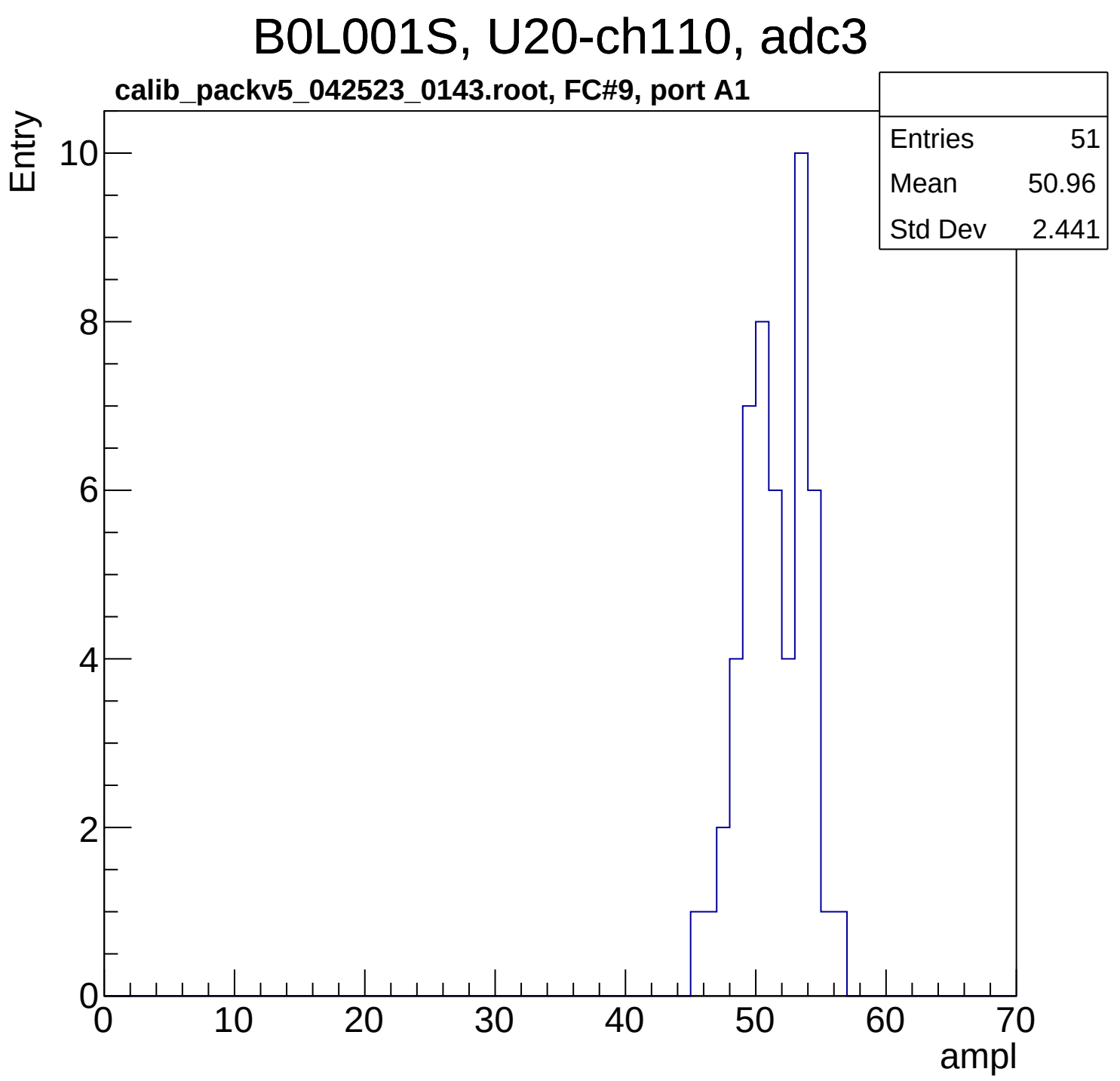

B0L001S, U20-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.89
Std Dev 5.288
Gaus mean : 30.8427
Gaus Width: 4.2688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.92
Std Dev 3.298
Gaus mean : 37.7919
Gaus Width: 3.7481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 43.89
Std Dev 3.697
Gaus mean : 44.1607
Gaus Width: 3.8572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 50.96
Std Dev 2.441
Entry
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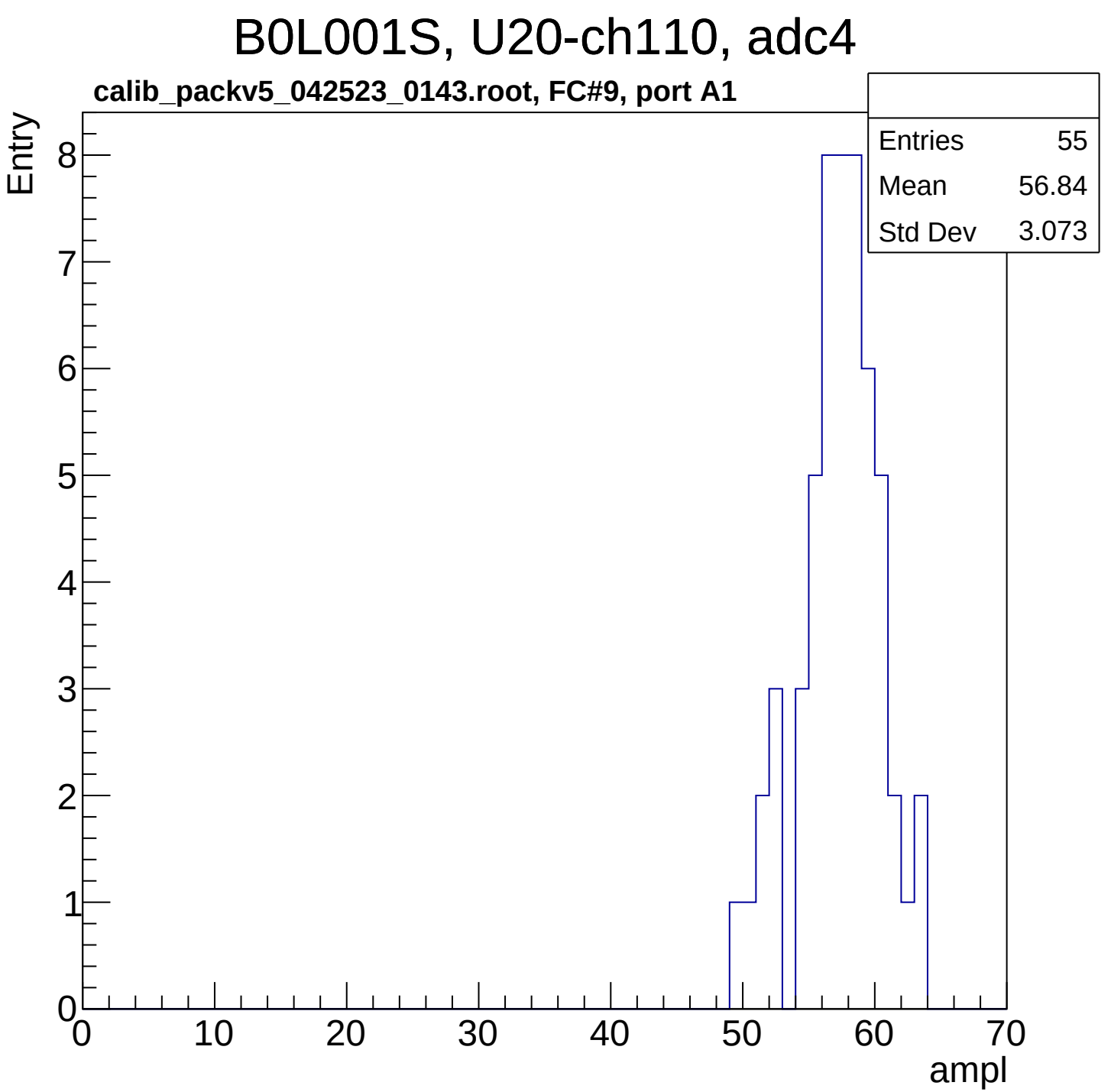
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.84
Std Dev 3.073
Entry
ampl
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2
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4
5
6
7
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0
10
20
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40
50
60
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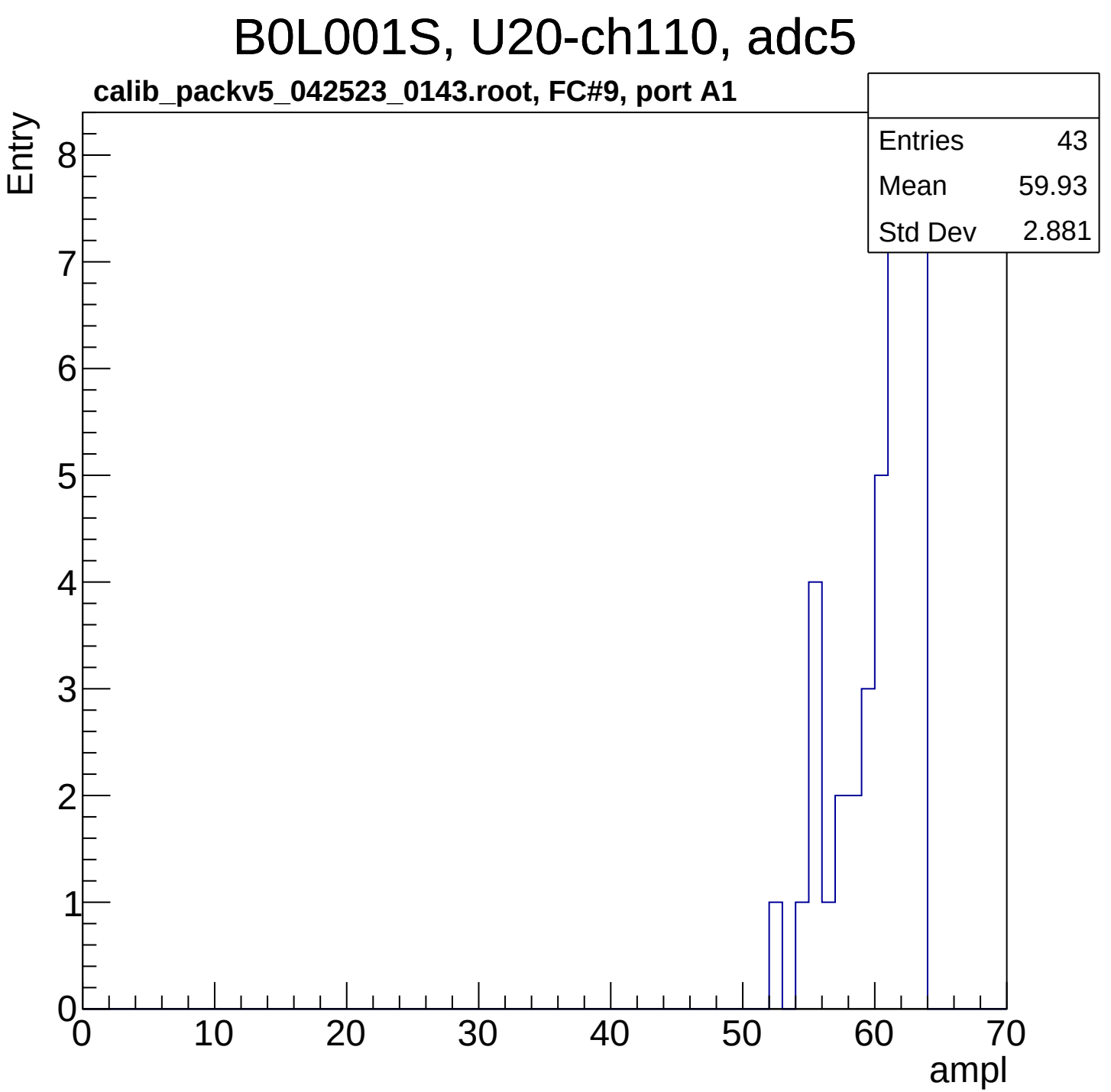
B0L001S, U20-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.93
Std Dev 2.881
Entry
ampl
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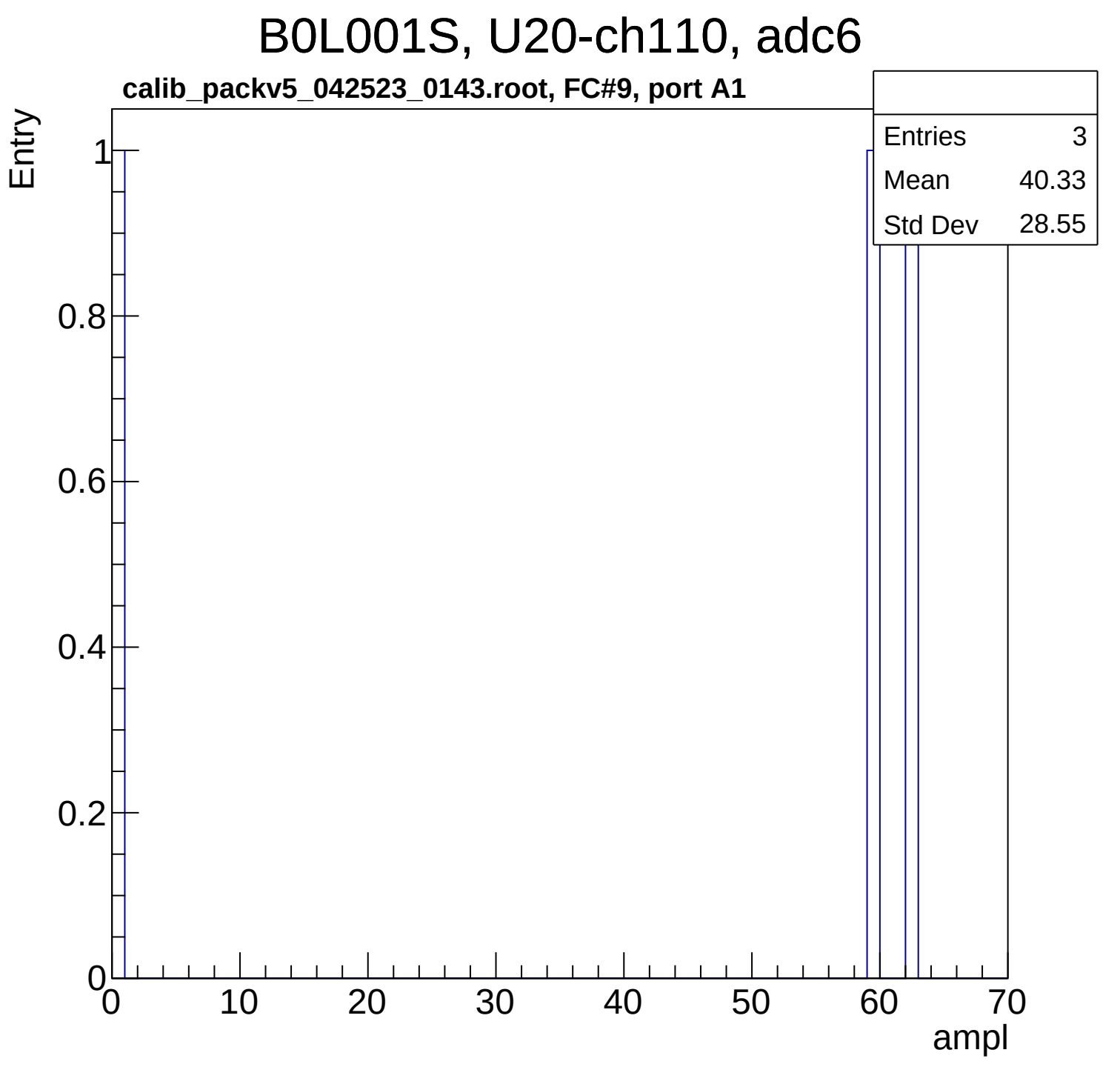
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 40.33
Std Dev 28.55
Entry
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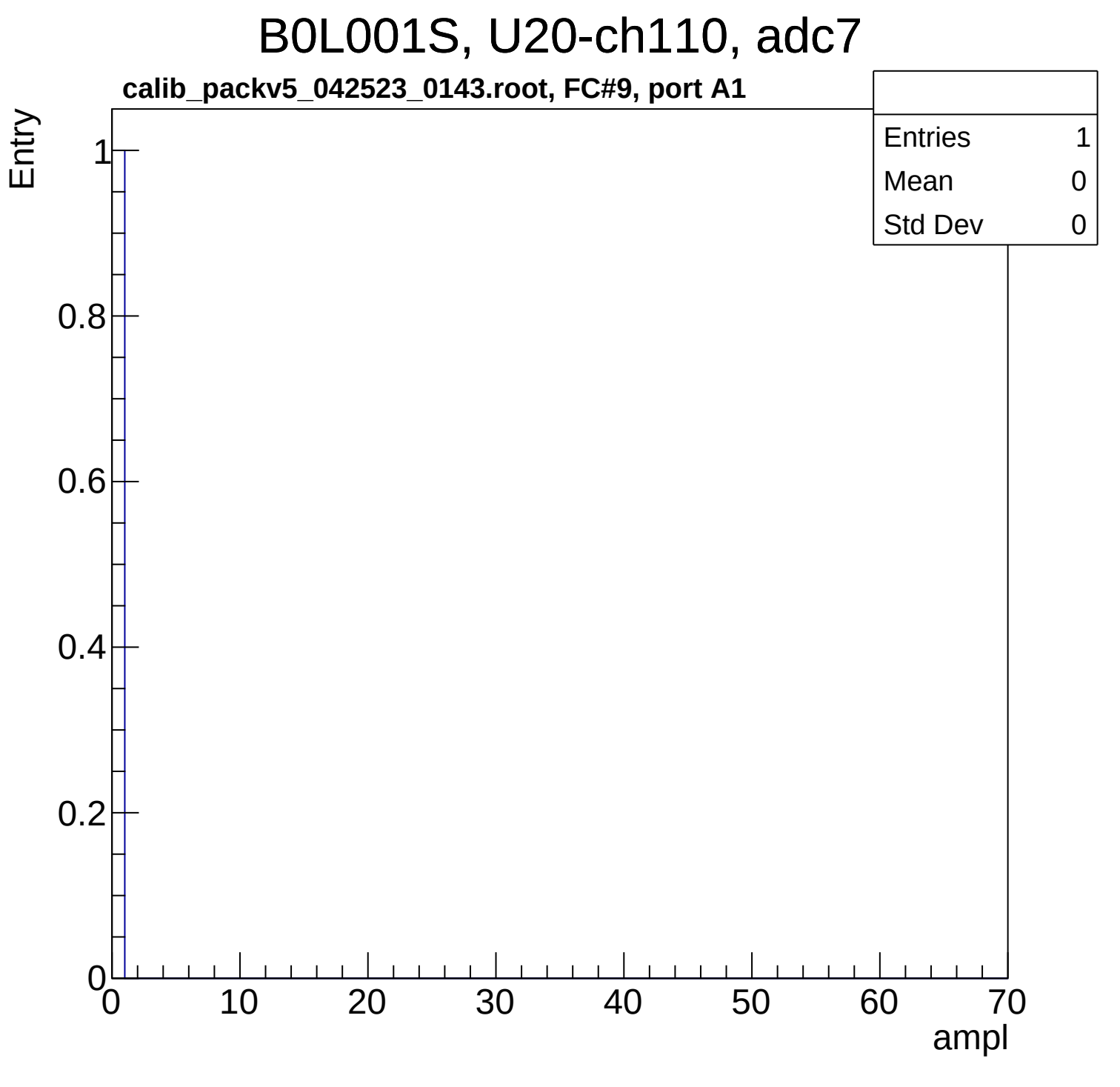
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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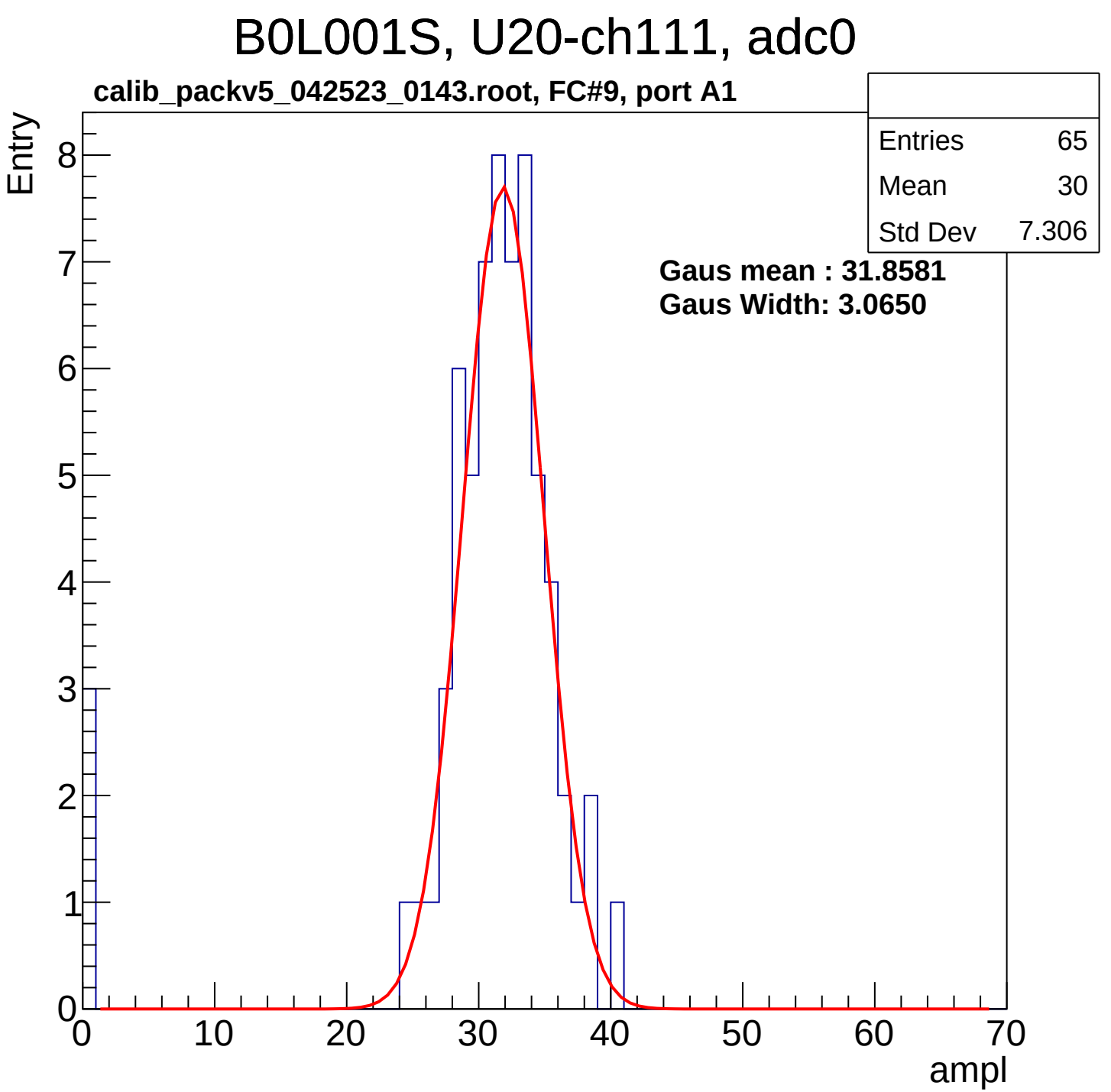
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30
Std Dev 7.306
Gaus mean : 31.8581
Gaus Width: 3.0650
Entry
ampl
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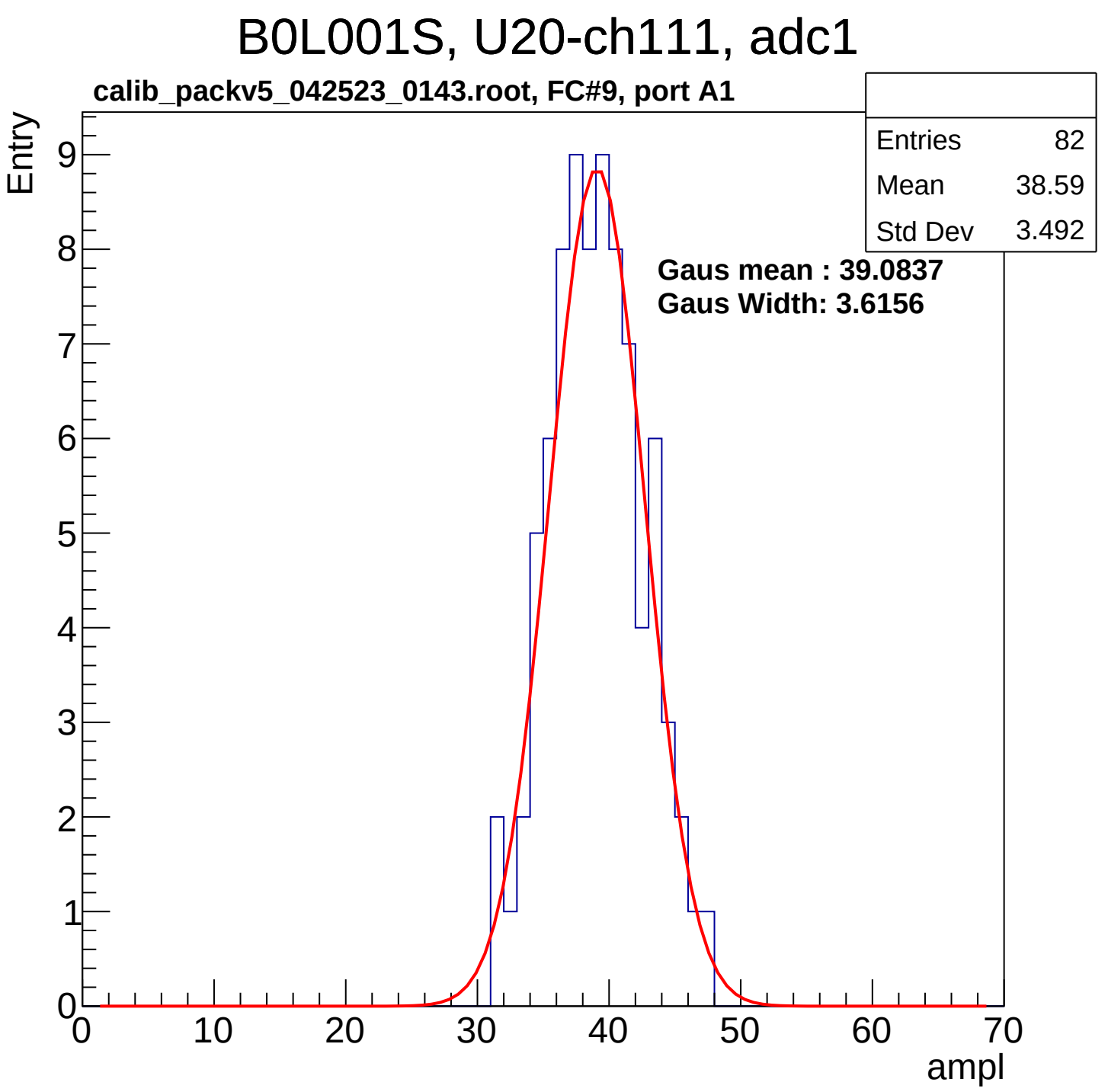
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 38.59
Std Dev 3.492
Gaus mean : 39.0837
Gaus Width: 3.6156
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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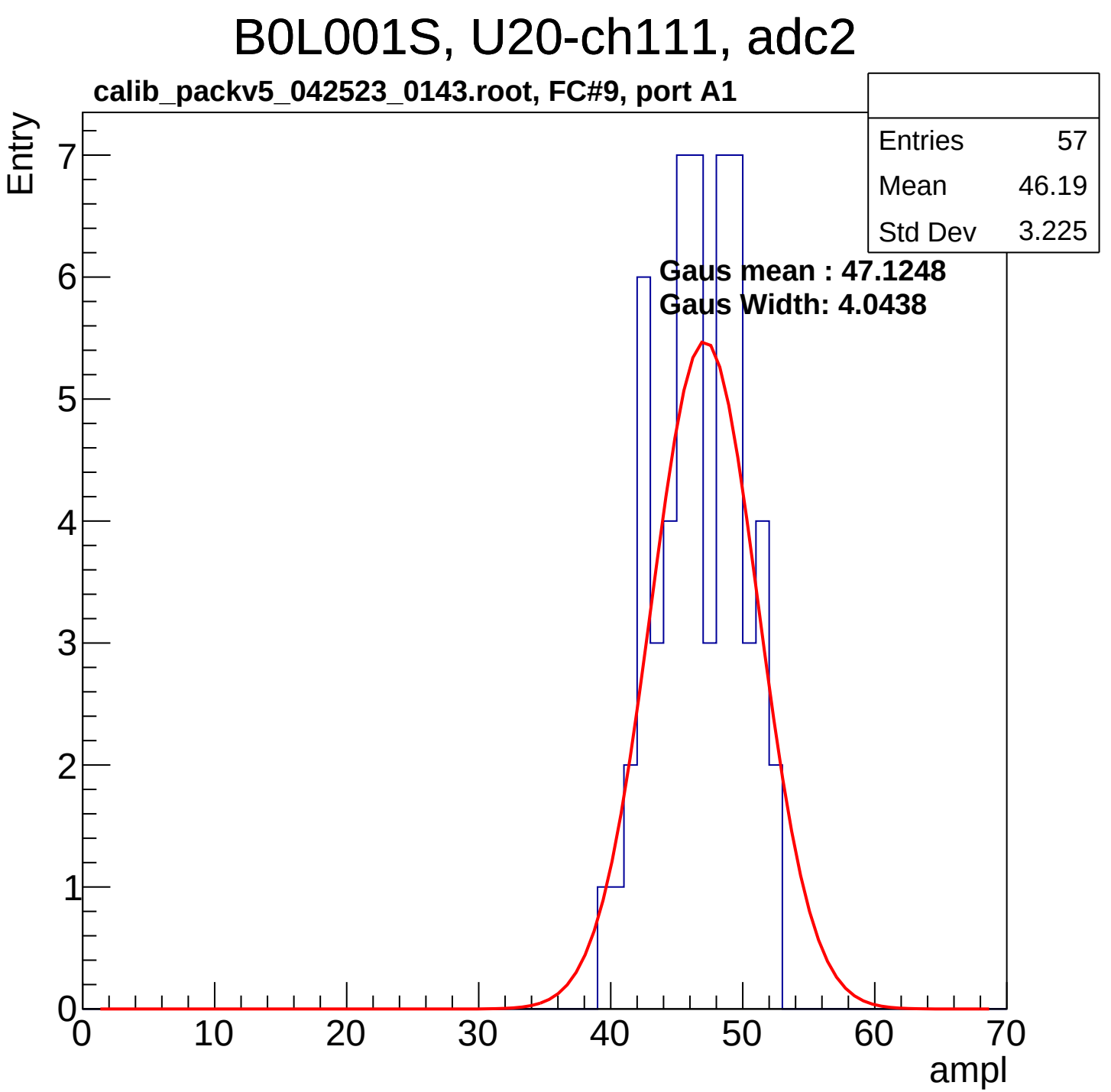
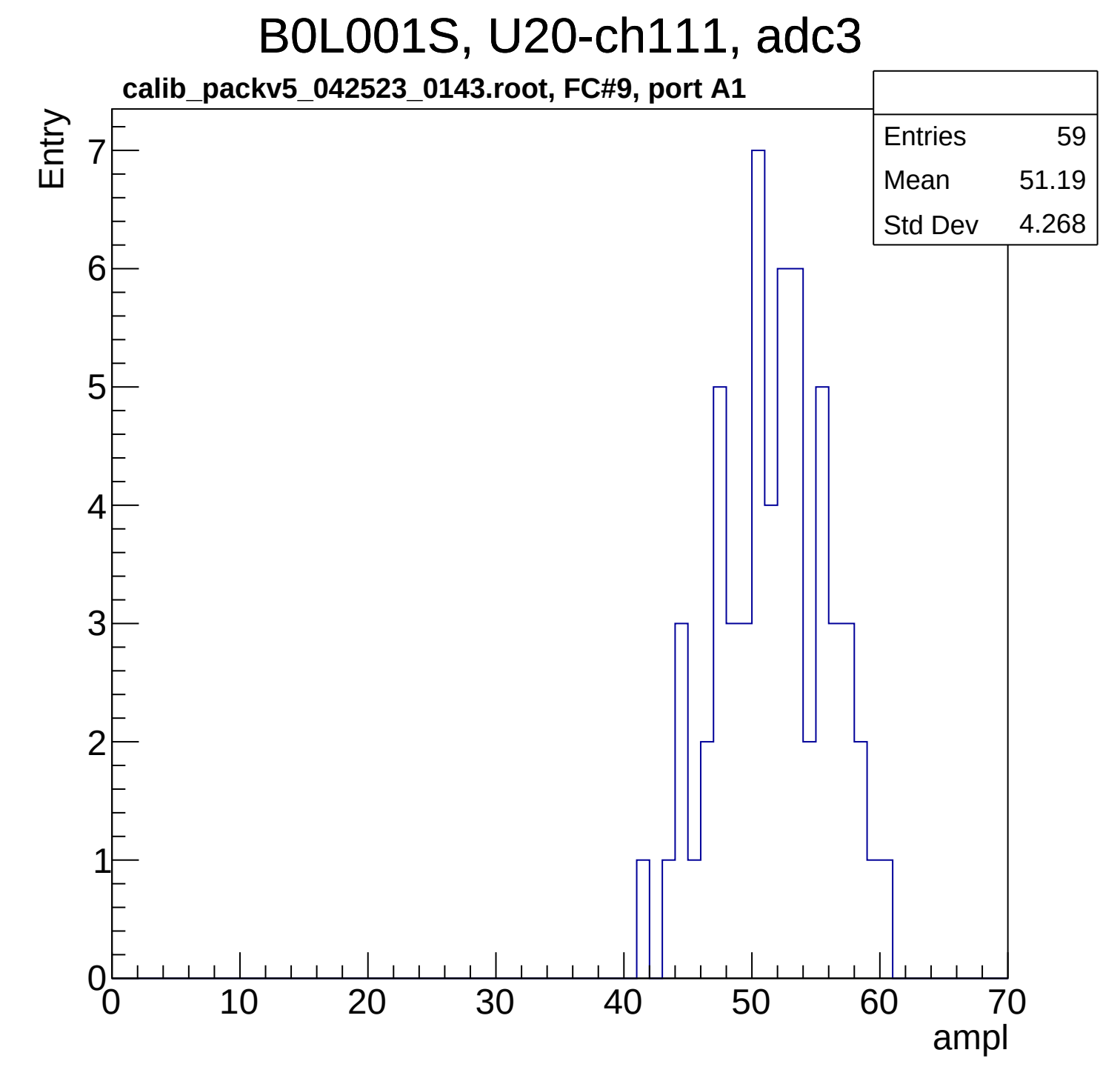

B0L001S, U20-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 46.19
Std Dev 3.225
Gaus mean : 47.1248
Gaus Width: 4.0438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.19
Std Dev 4.268
Entry
ampl
0
1
2
3
4
5
6
7
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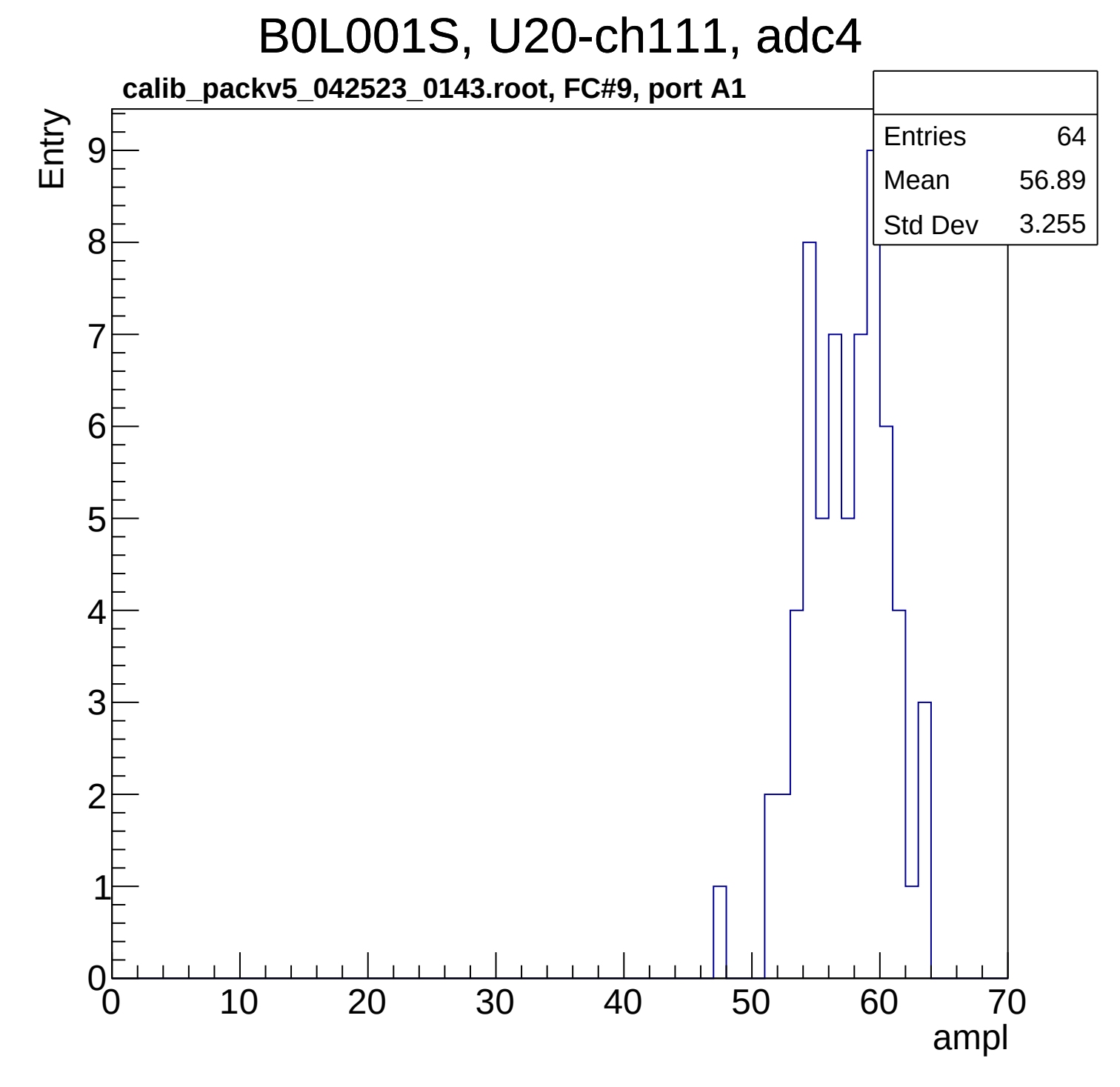
B0L001S, U20-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.89
Std Dev 3.255
Entry
ampl
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3
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0
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60
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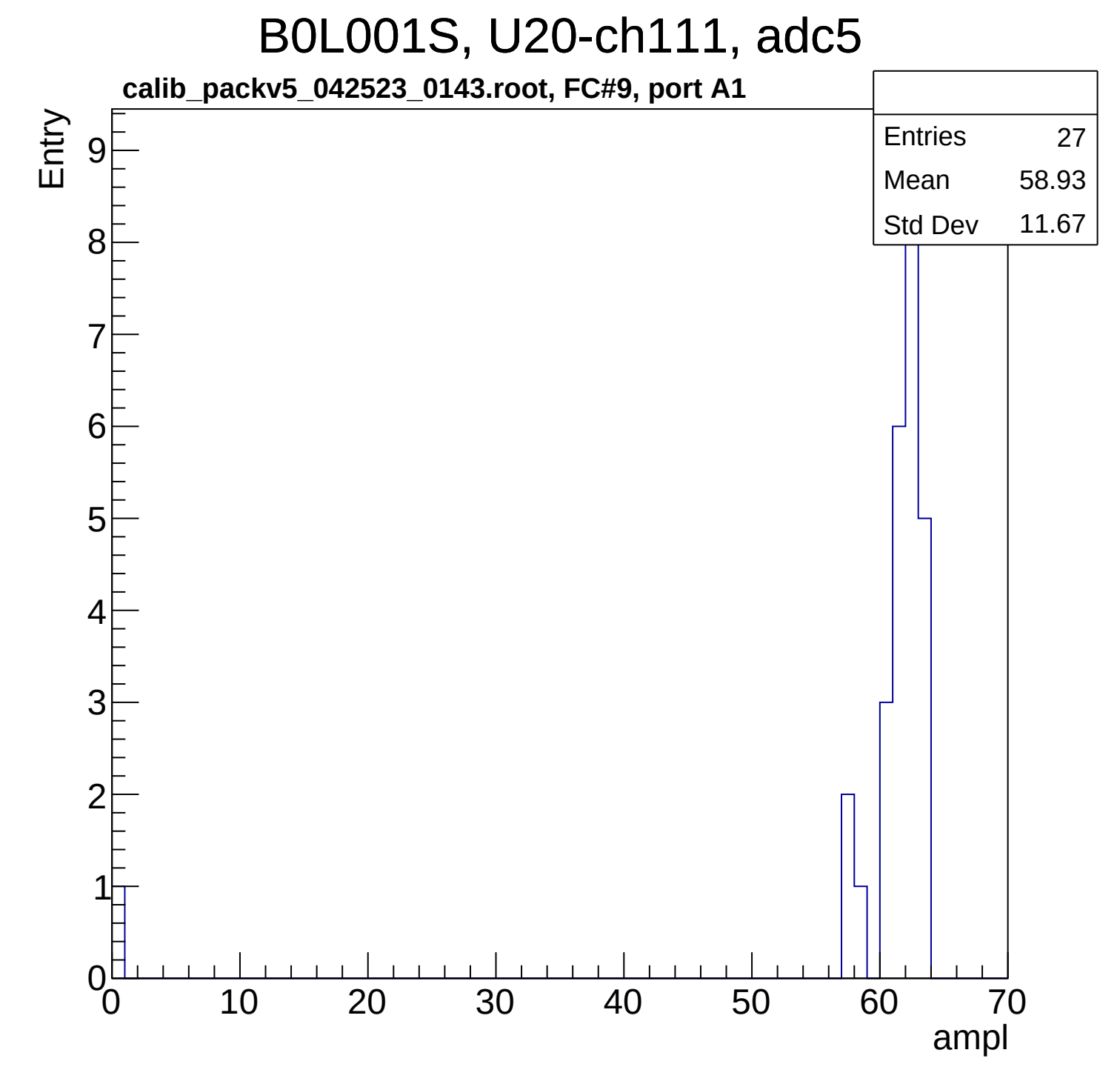
B0L001S, U20-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.93
Std Dev 11.67
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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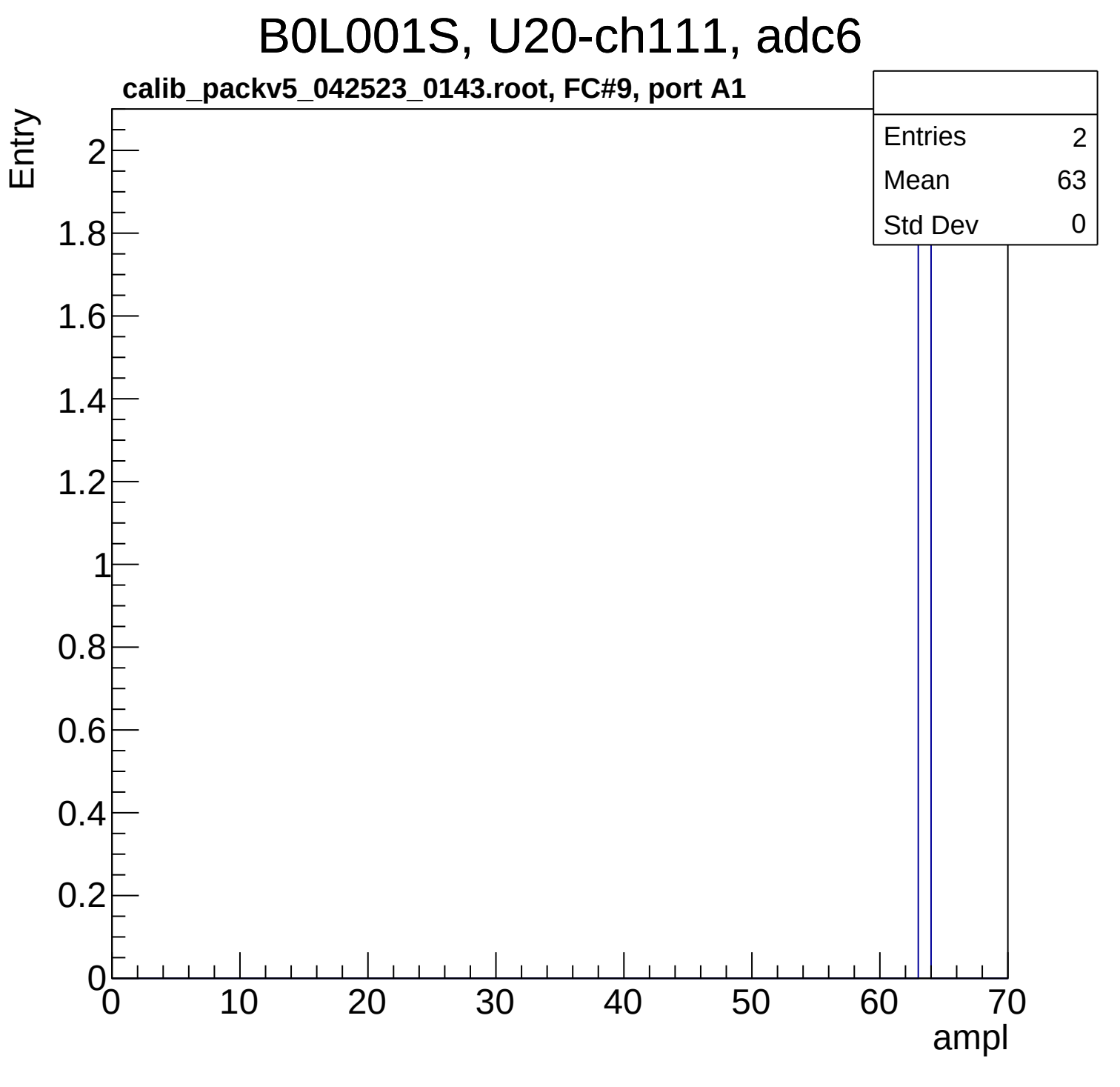
B0L001S, U20-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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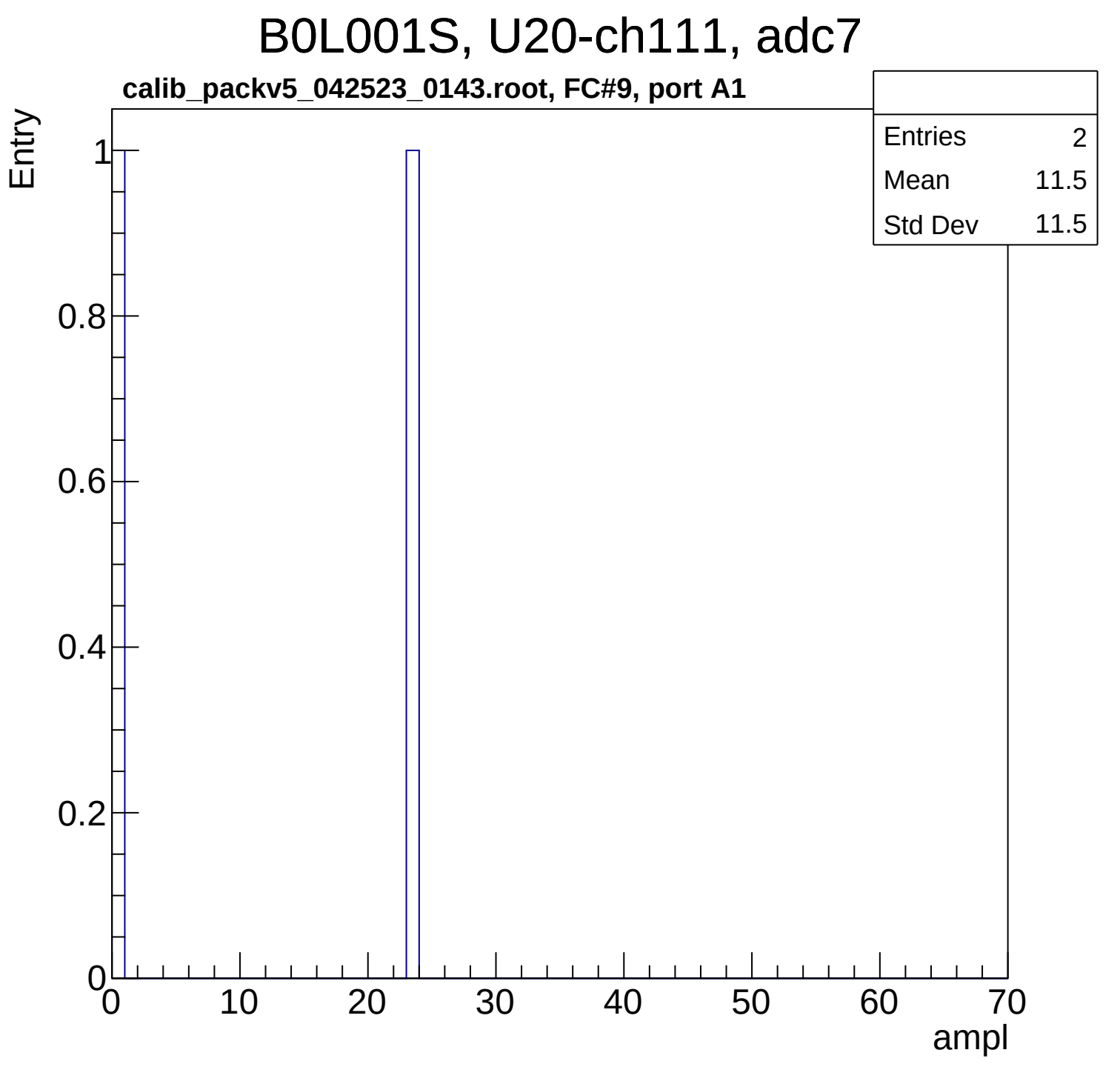
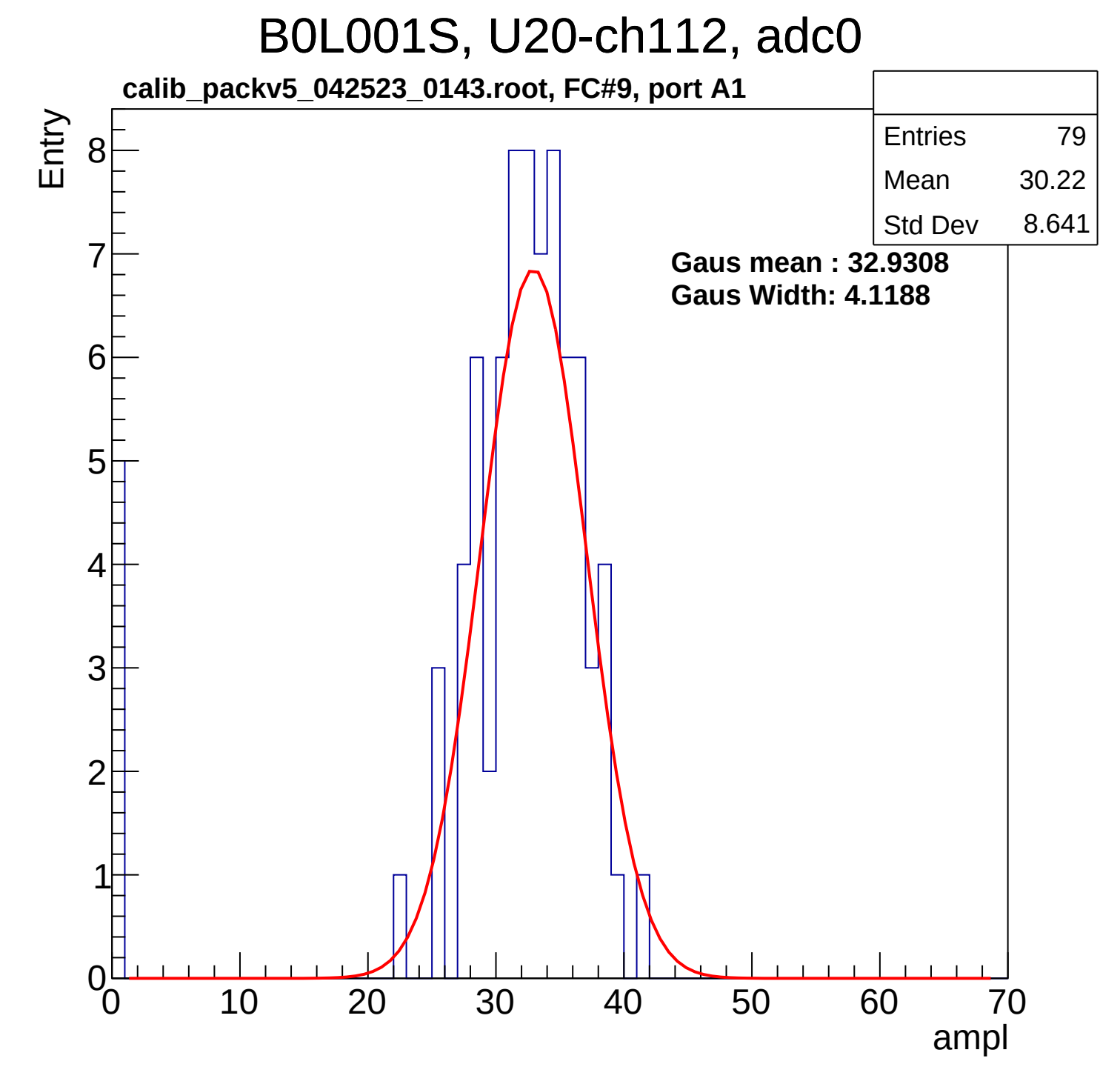
40
50
60
70


B0L001S, U20-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.22
Std Dev 8.641
Gaus mean : 32.9308
Gaus Width: 4.1188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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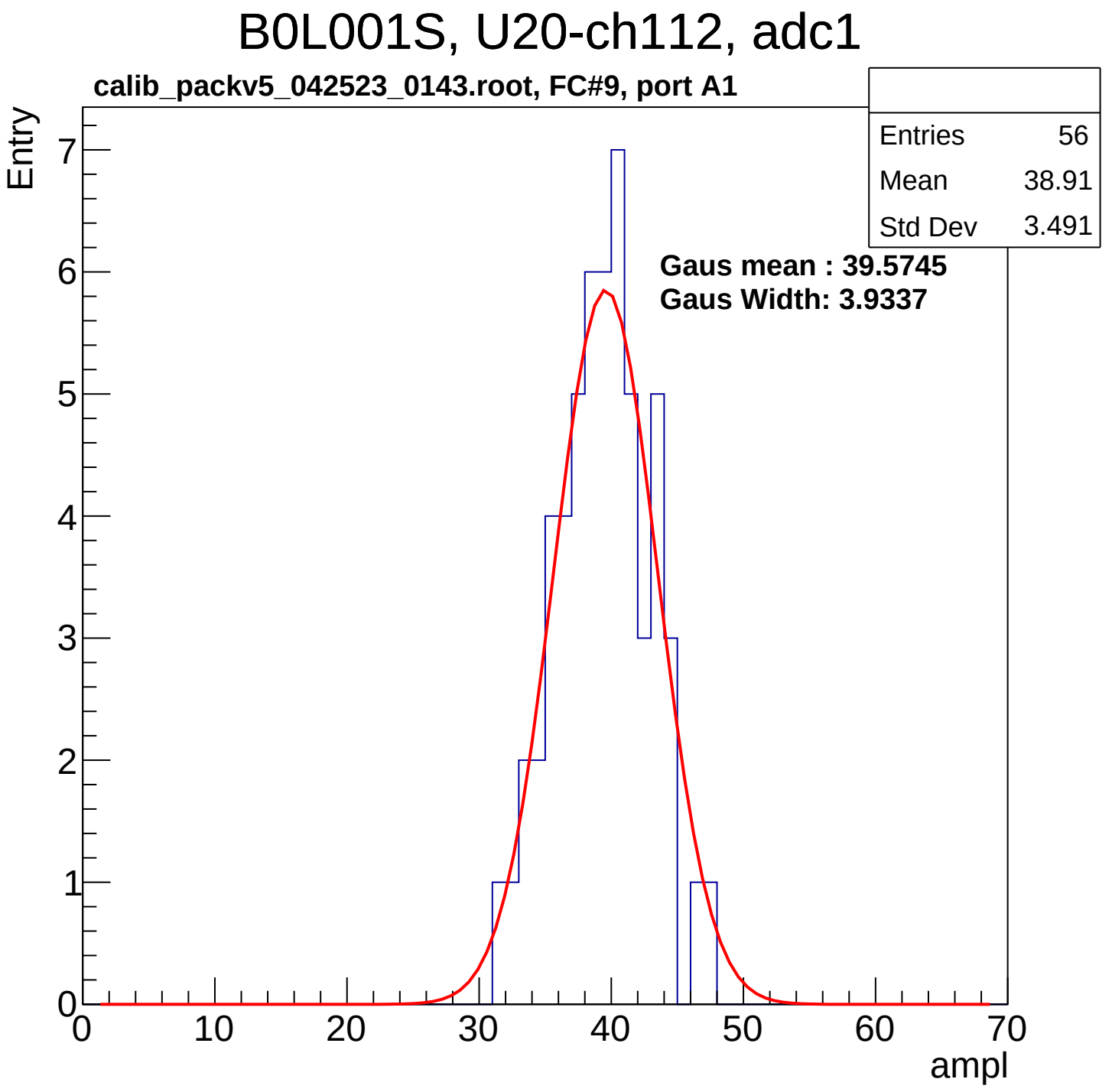
B0L001S, U20-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 38.91
Std Dev 3.491
Gaus mean : 39.5745
Gaus Width: 3.9337
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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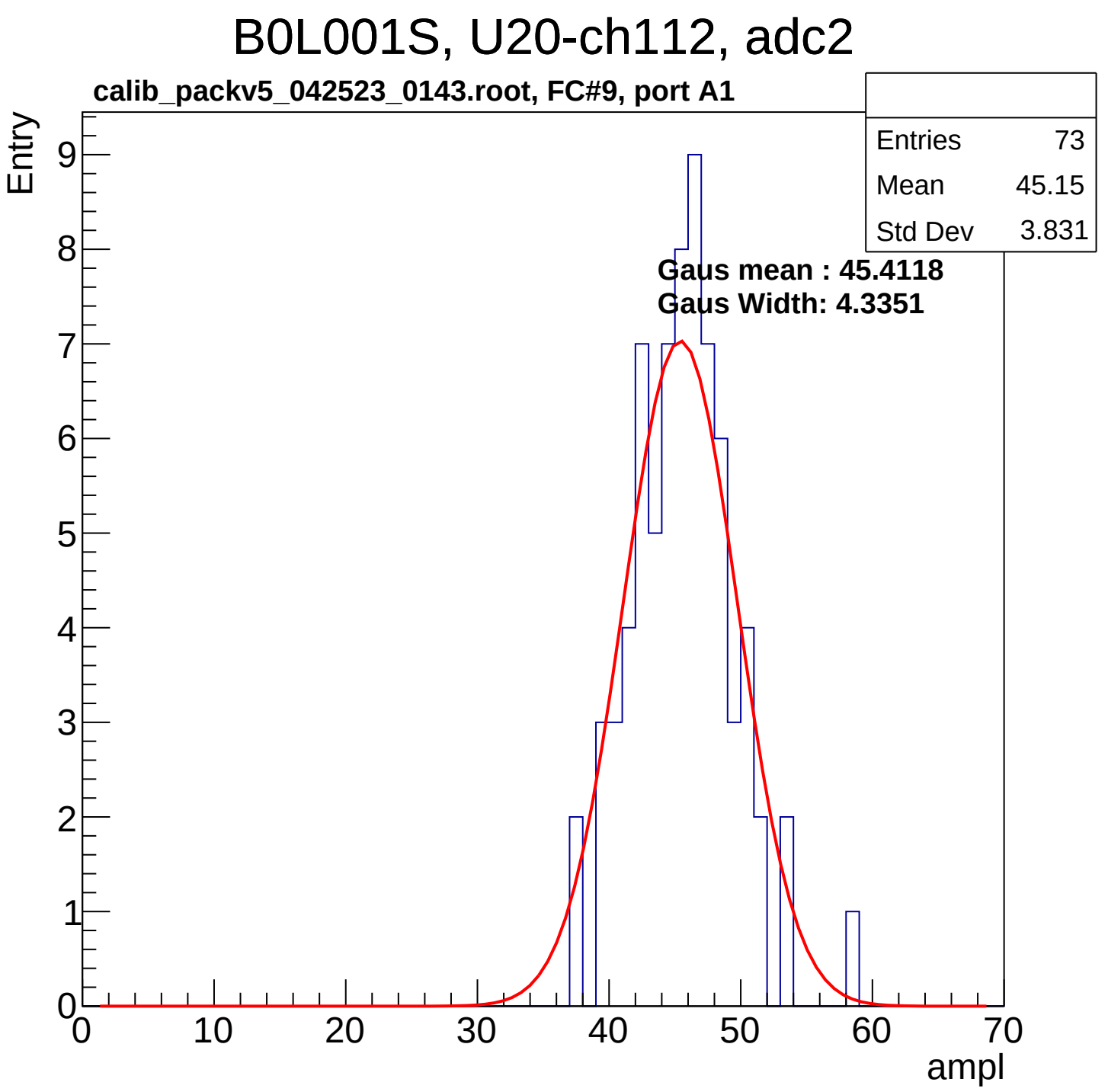
B0L001S, U20-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.15
Std Dev 3.831
Gaus mean : 45.4118
Gaus Width: 4.3351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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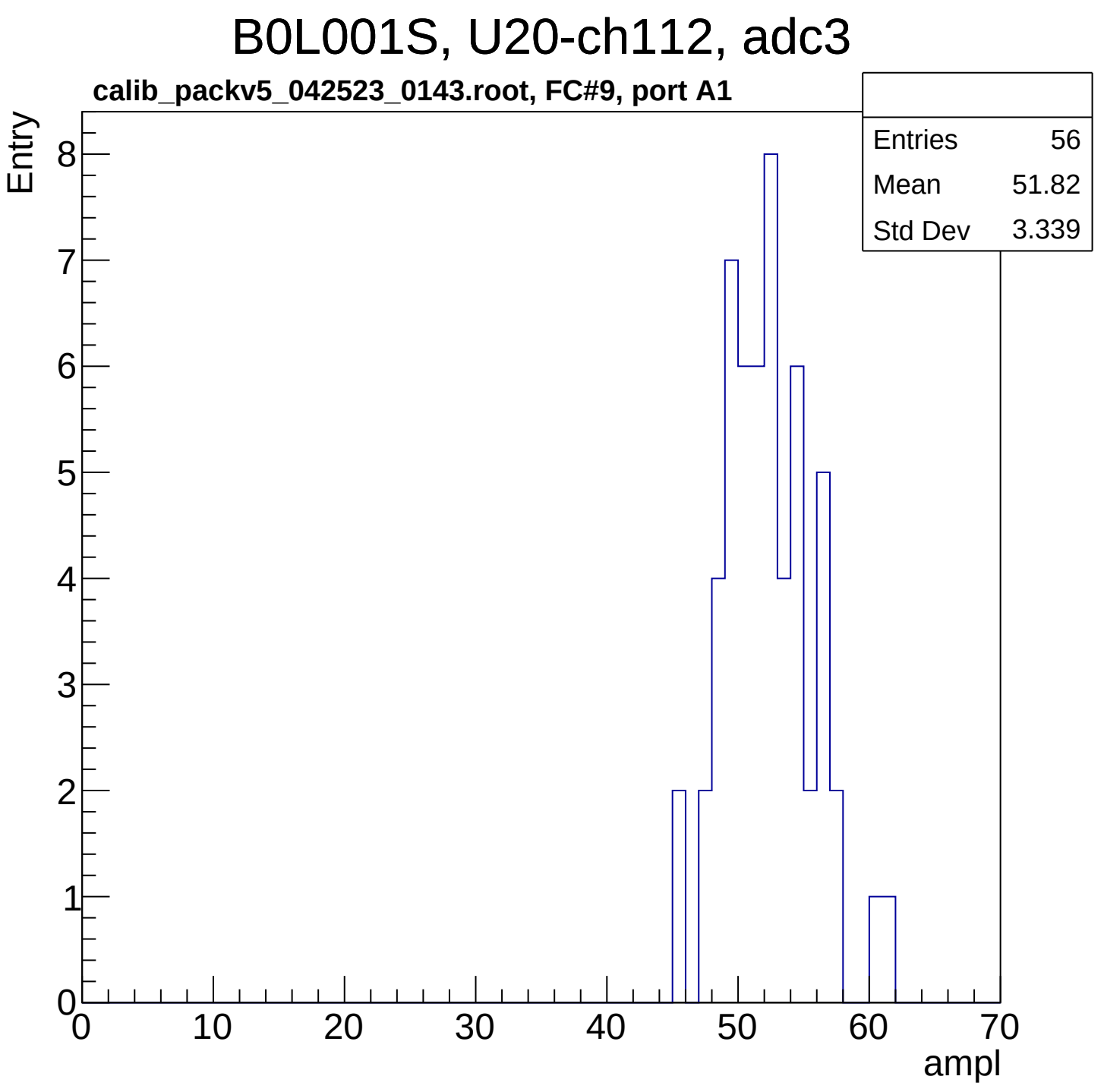
B0L001S, U20-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 51.82
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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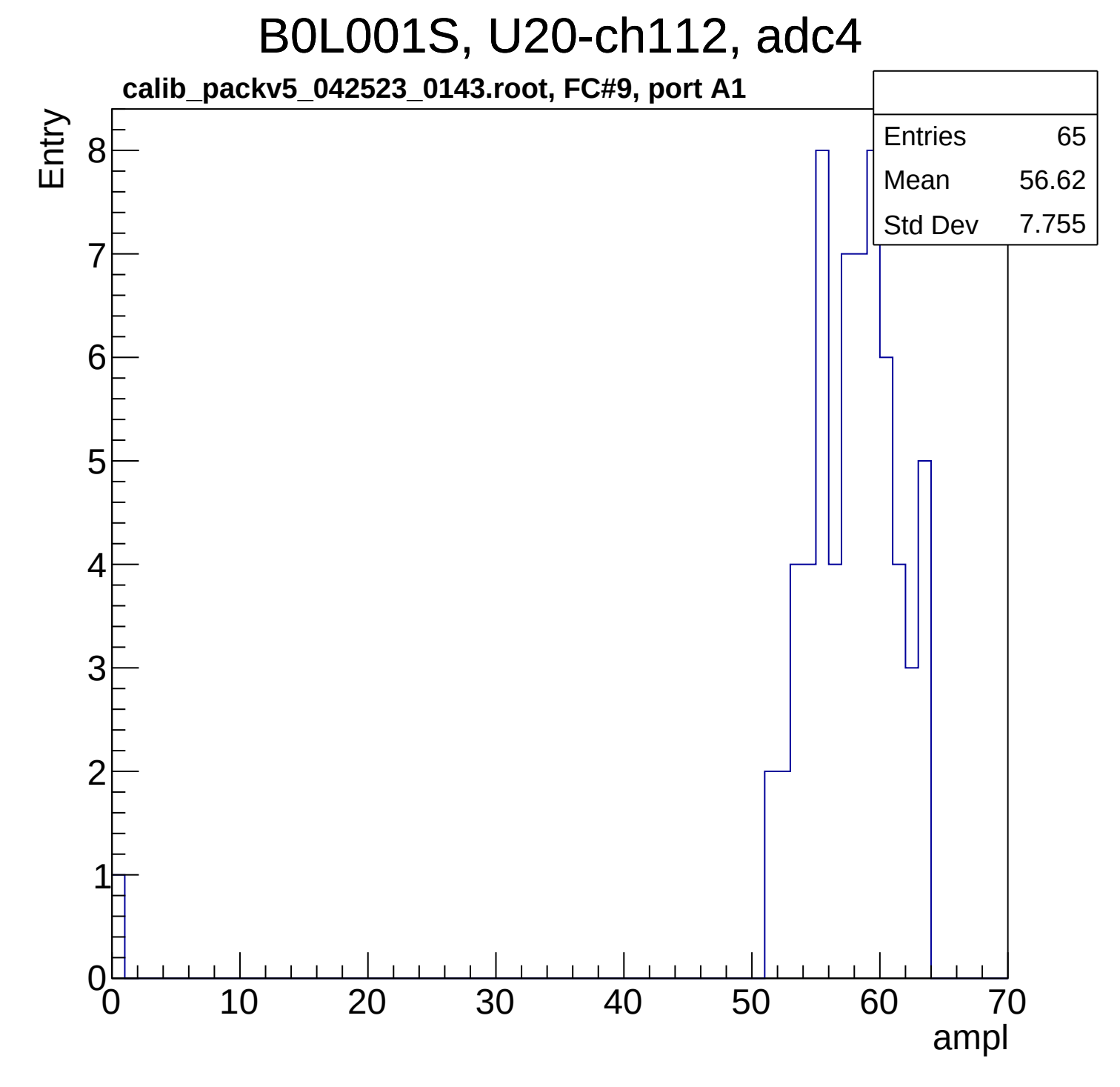
B0L001S, U20-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 56.62
Std Dev 7.755
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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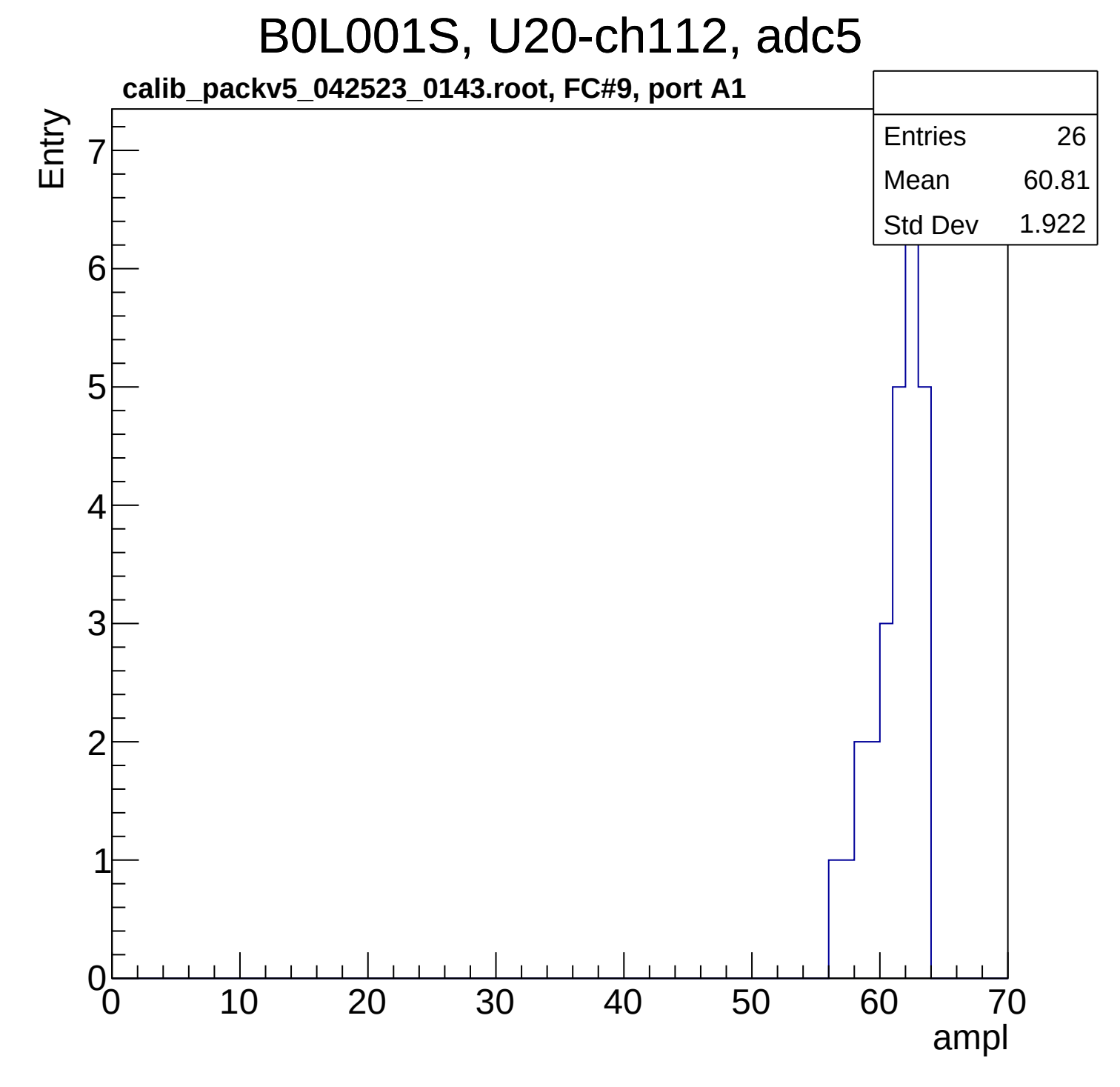
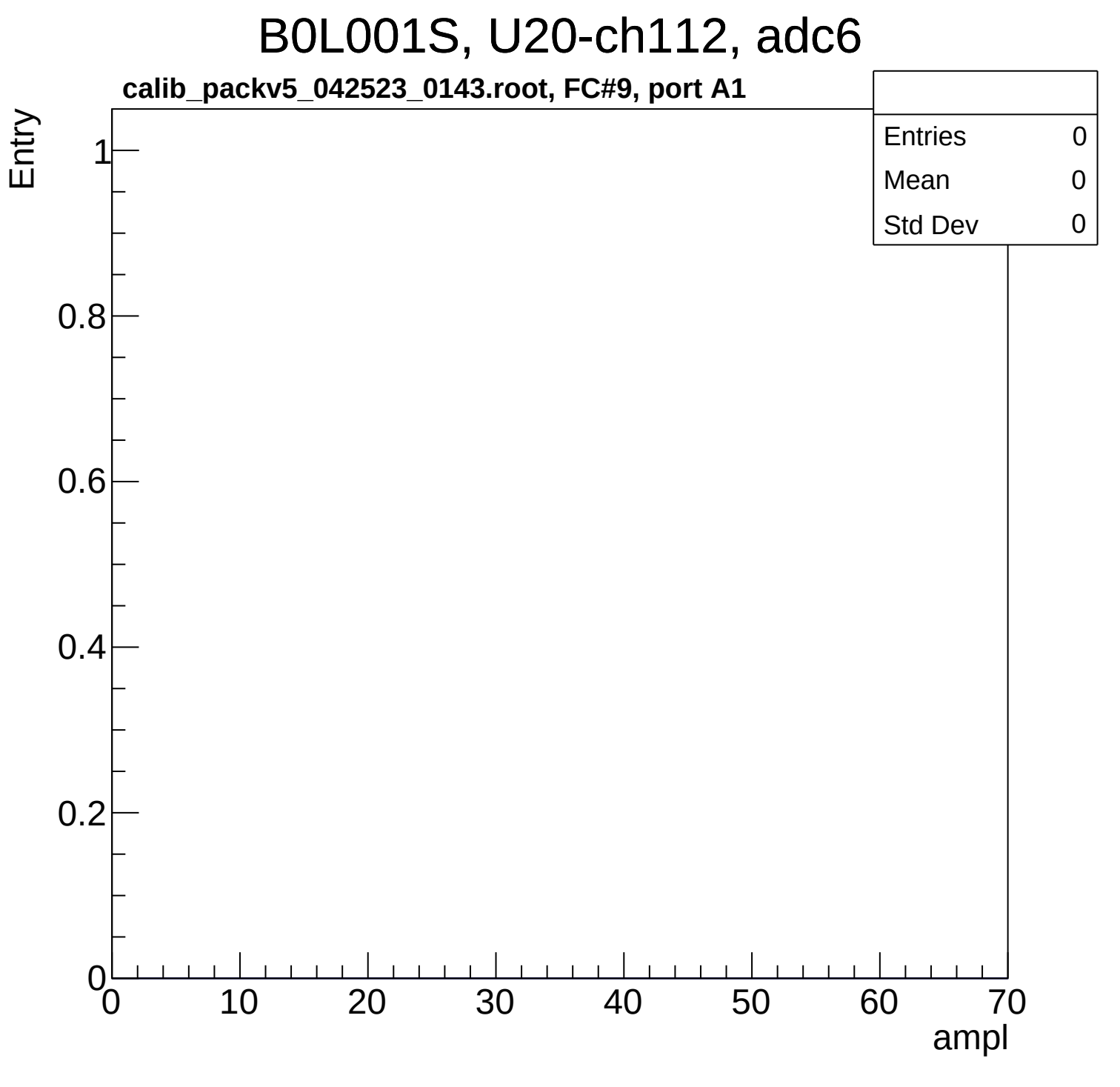
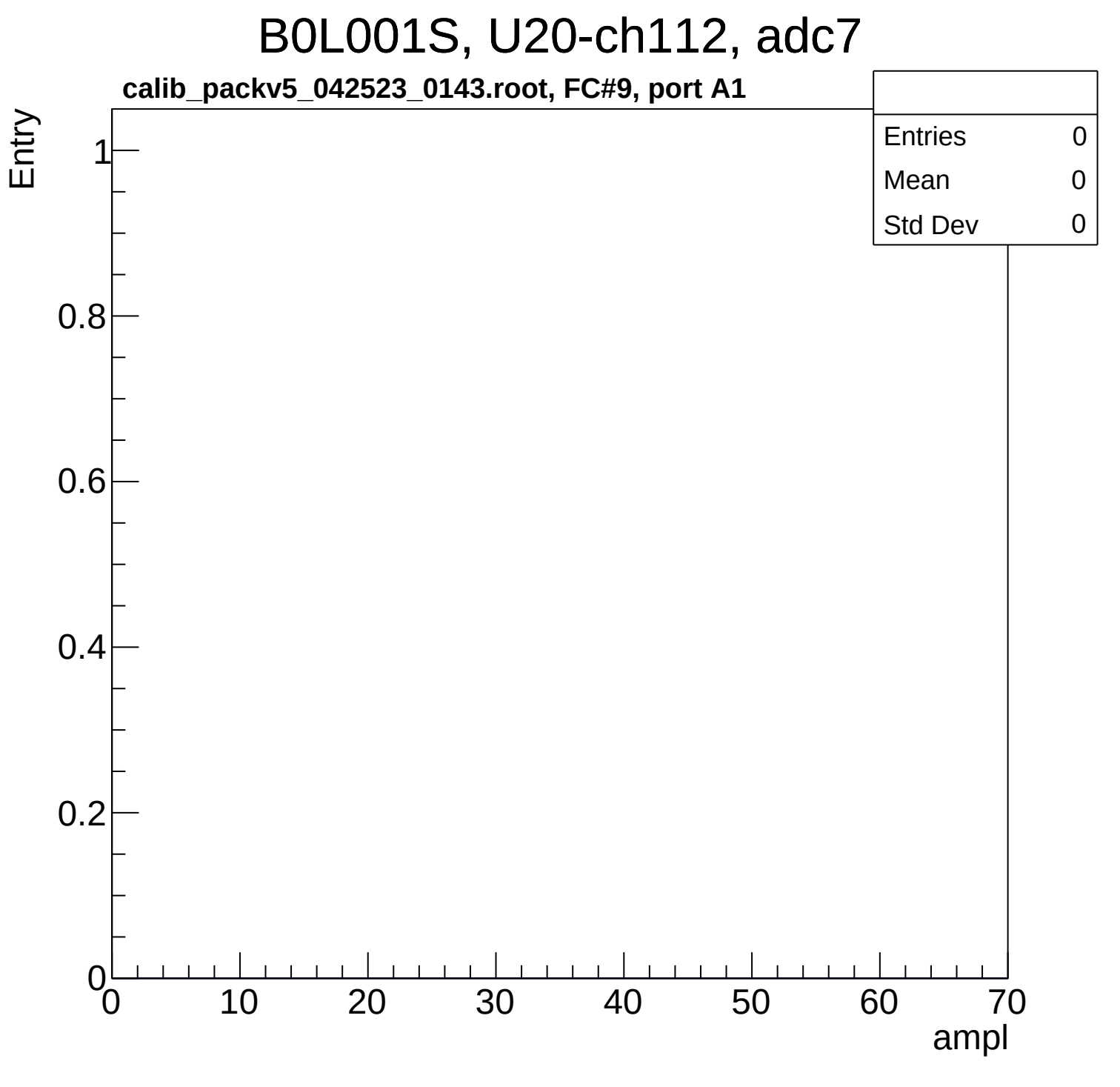
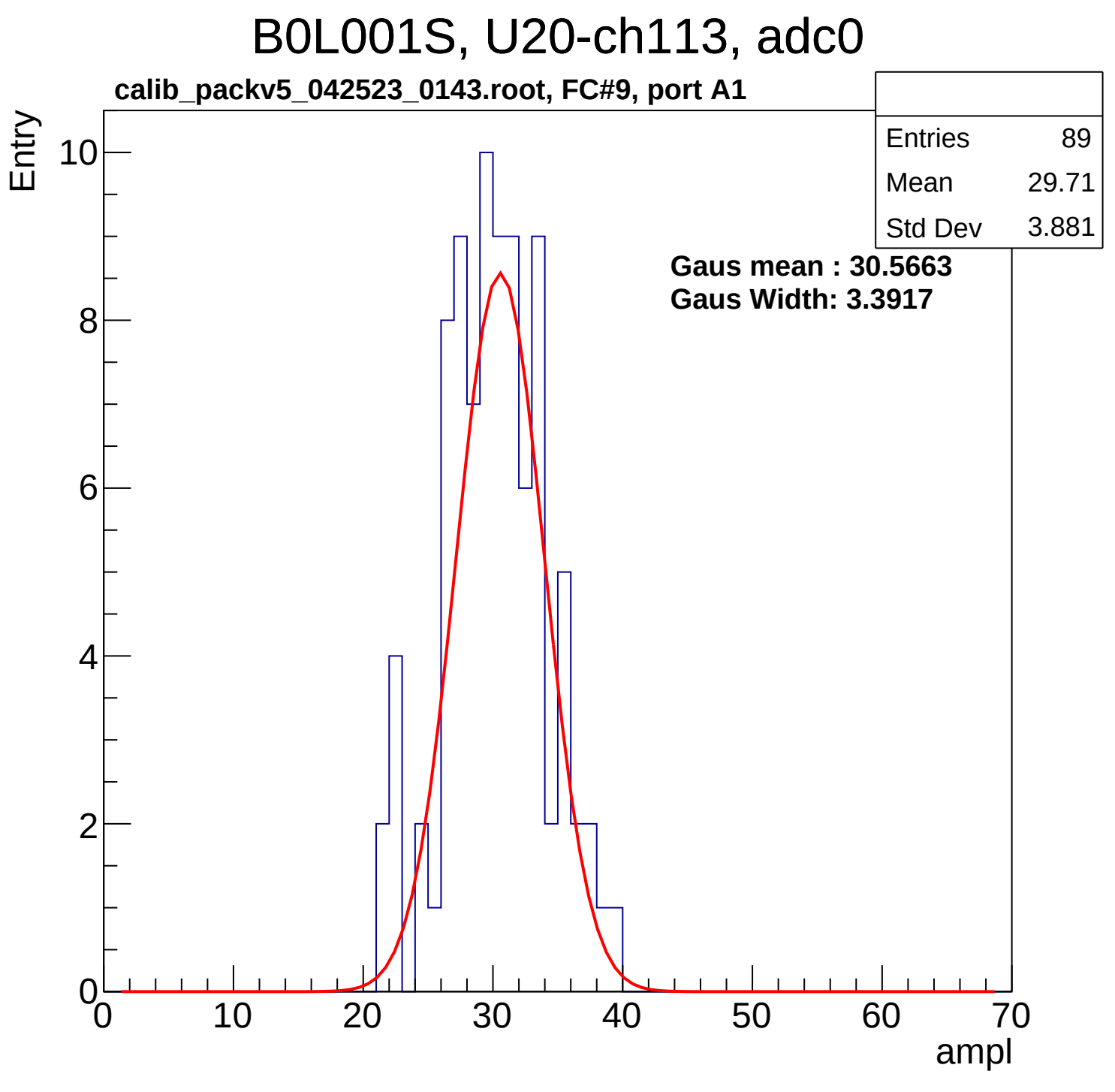
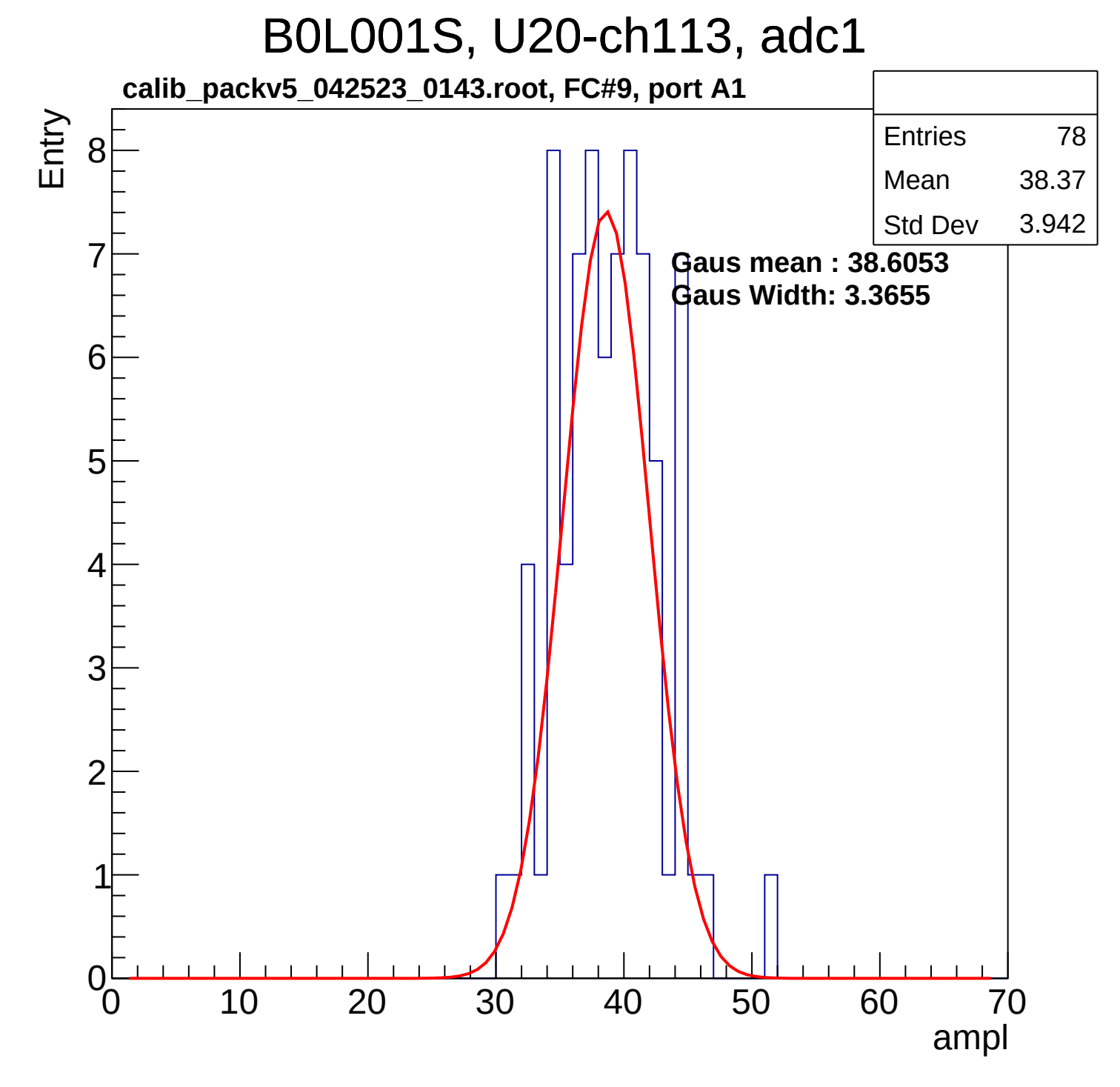

B0L001S, U20-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.81
Std Dev 1.922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 29.71
Std Dev 3.881
Gaus mean : 30.5663
Gaus Width: 3.3917
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.37
Std Dev 3.942
Gaus mean : 38.6053
Gaus Width: 3.3655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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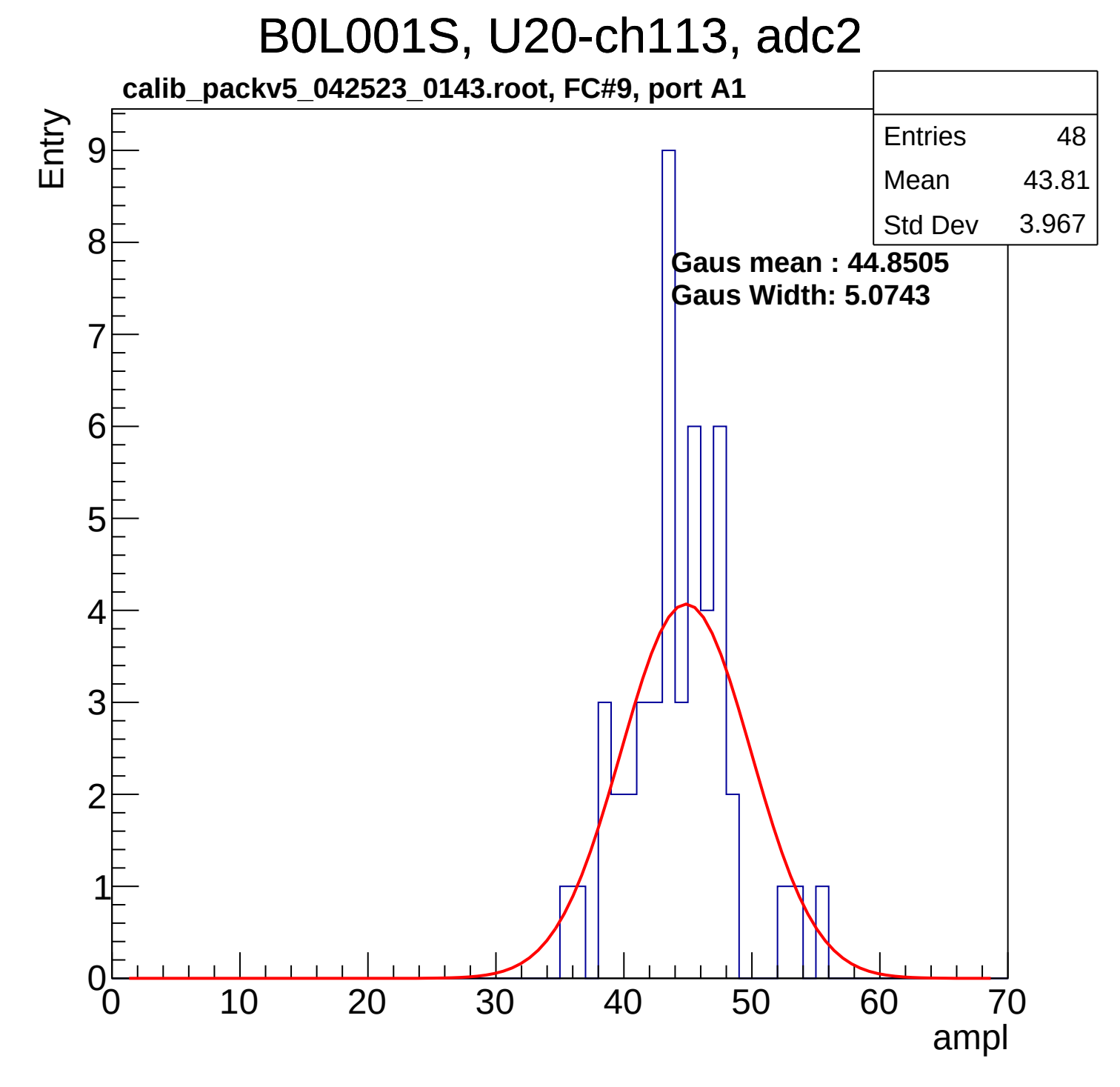
B0L001S, U20-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 43.81
Std Dev 3.967
Gaus mean : 44.8505
Gaus Width: 5.0743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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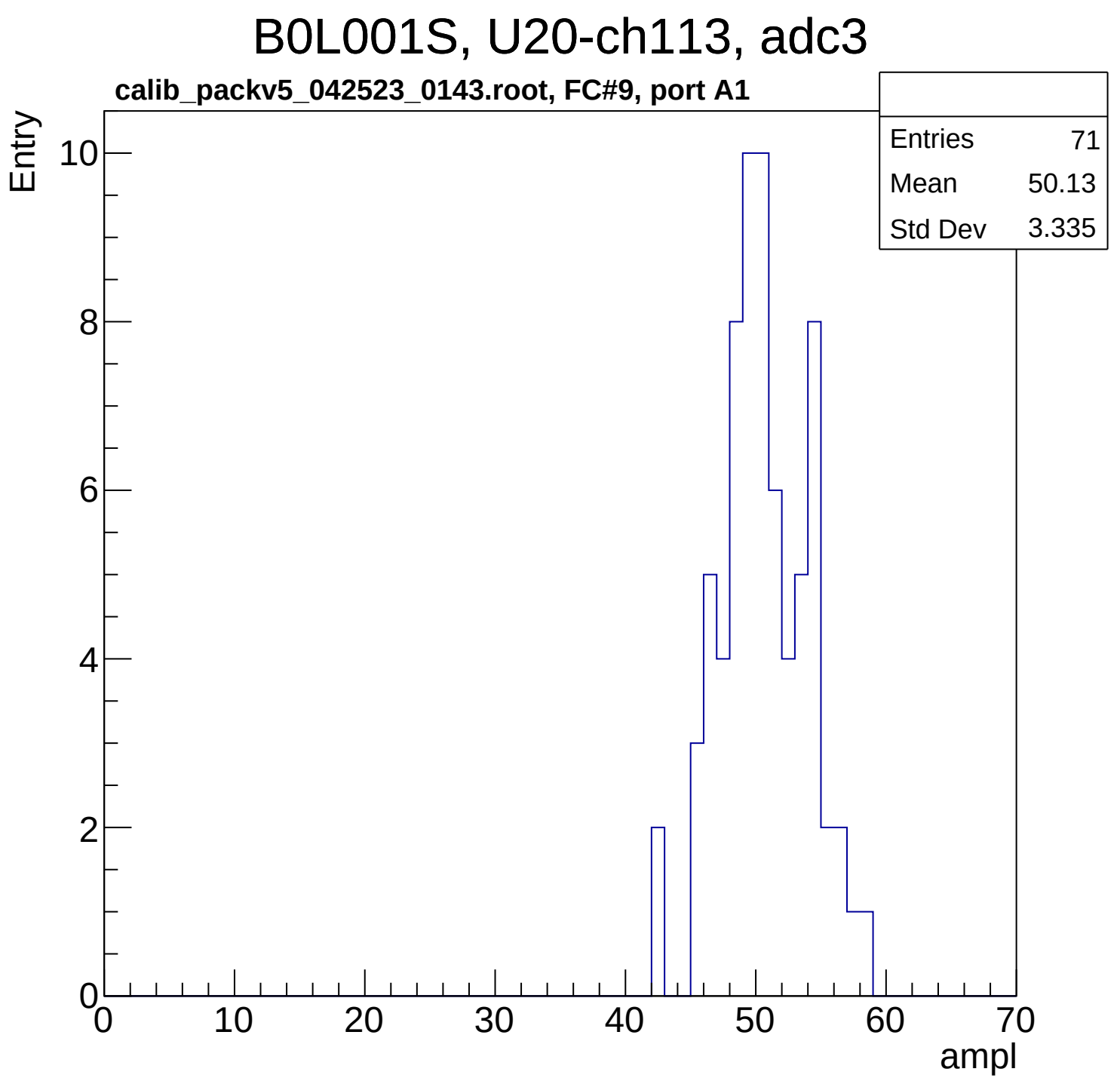
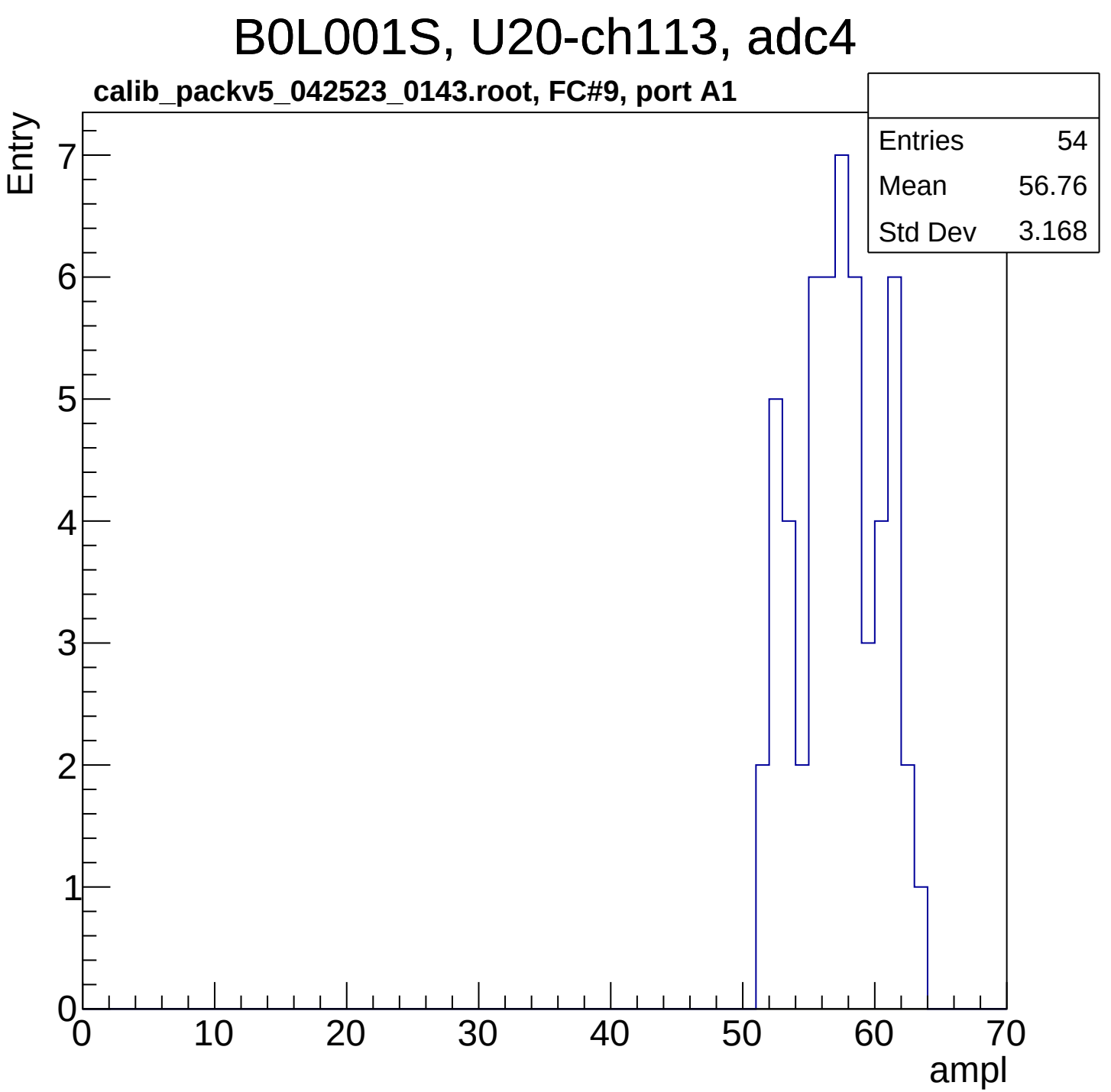
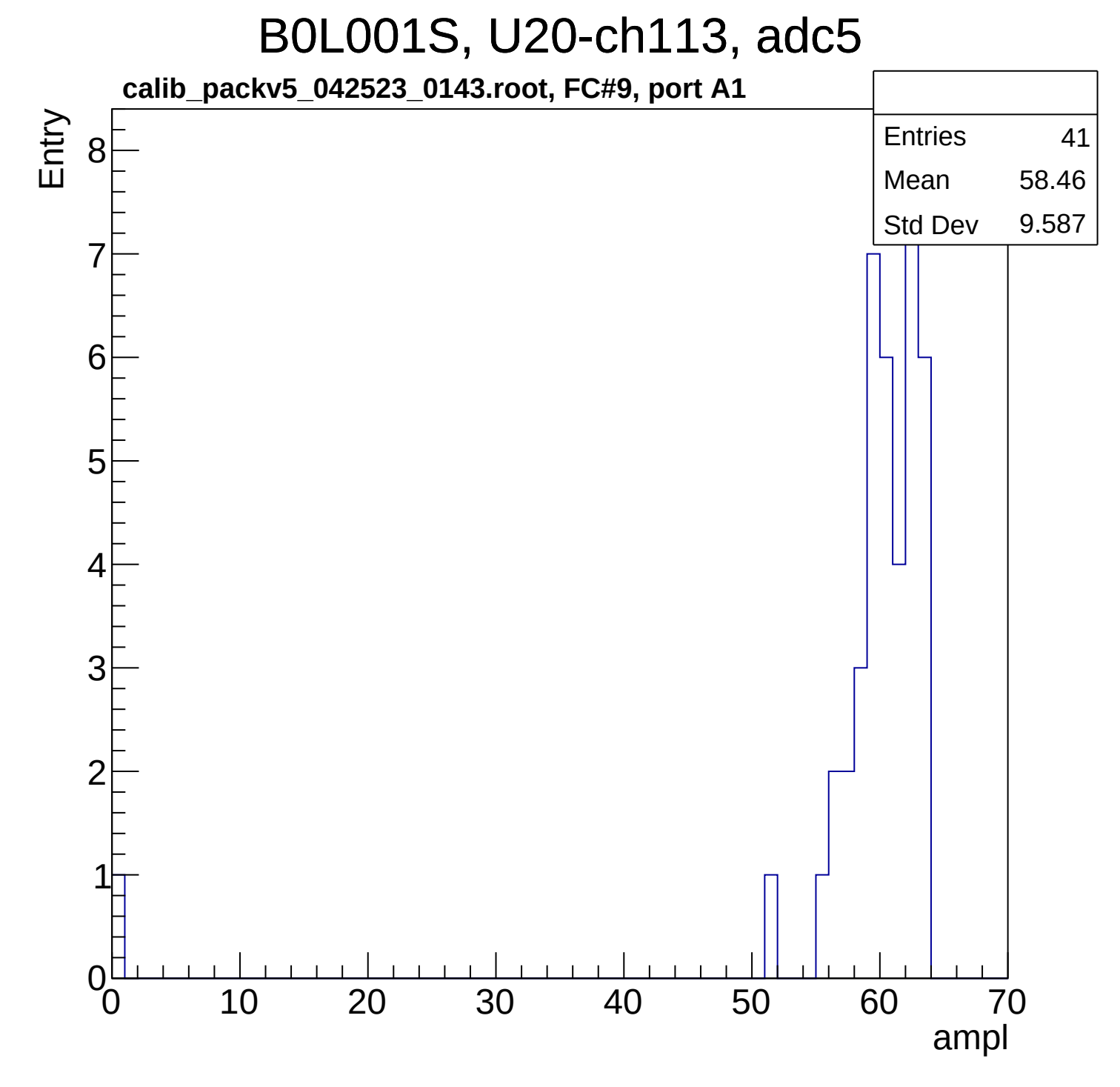

B0L001S, U20-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.13
Std Dev 3.335
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.76
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.46
Std Dev 9.587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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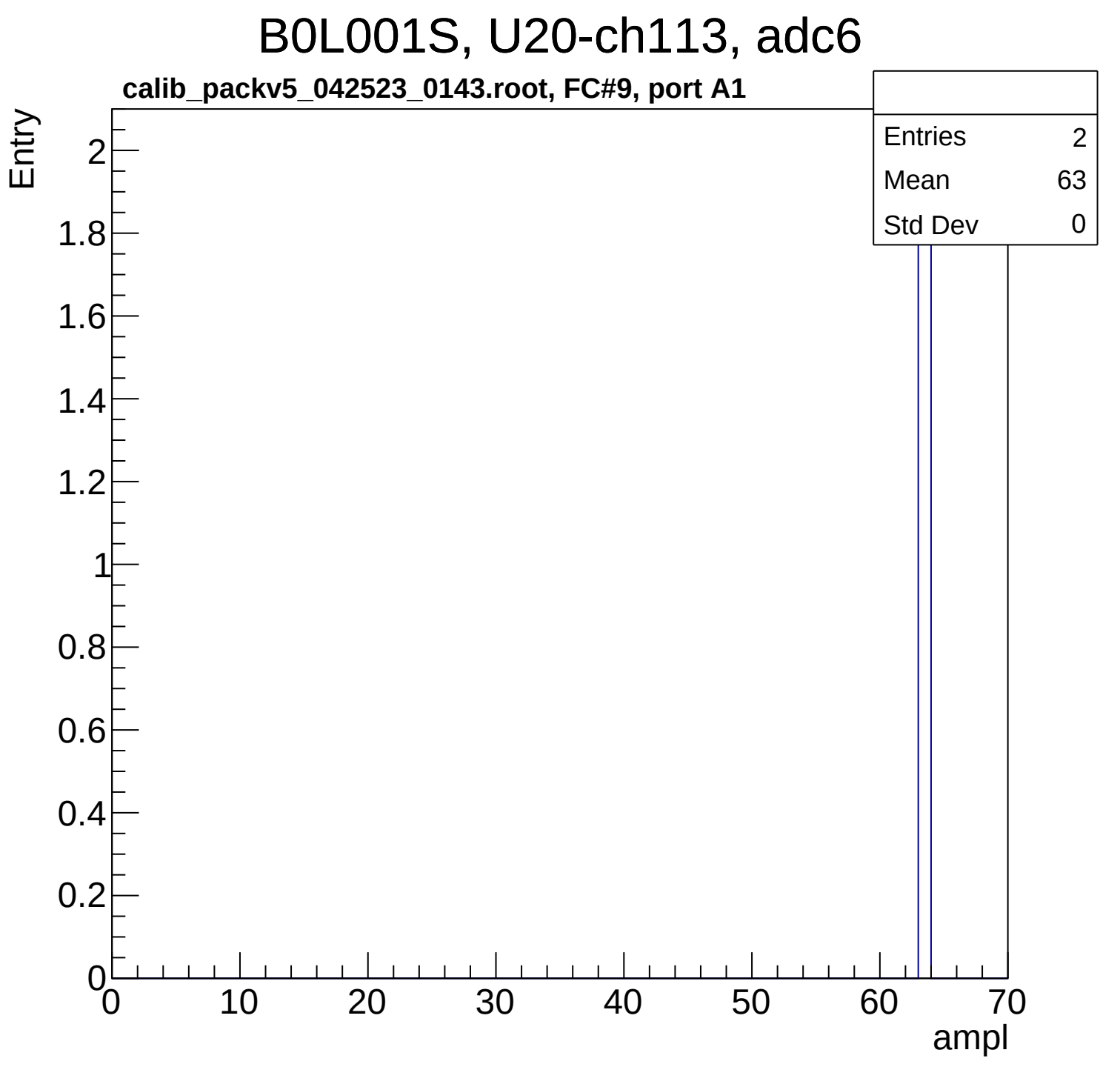
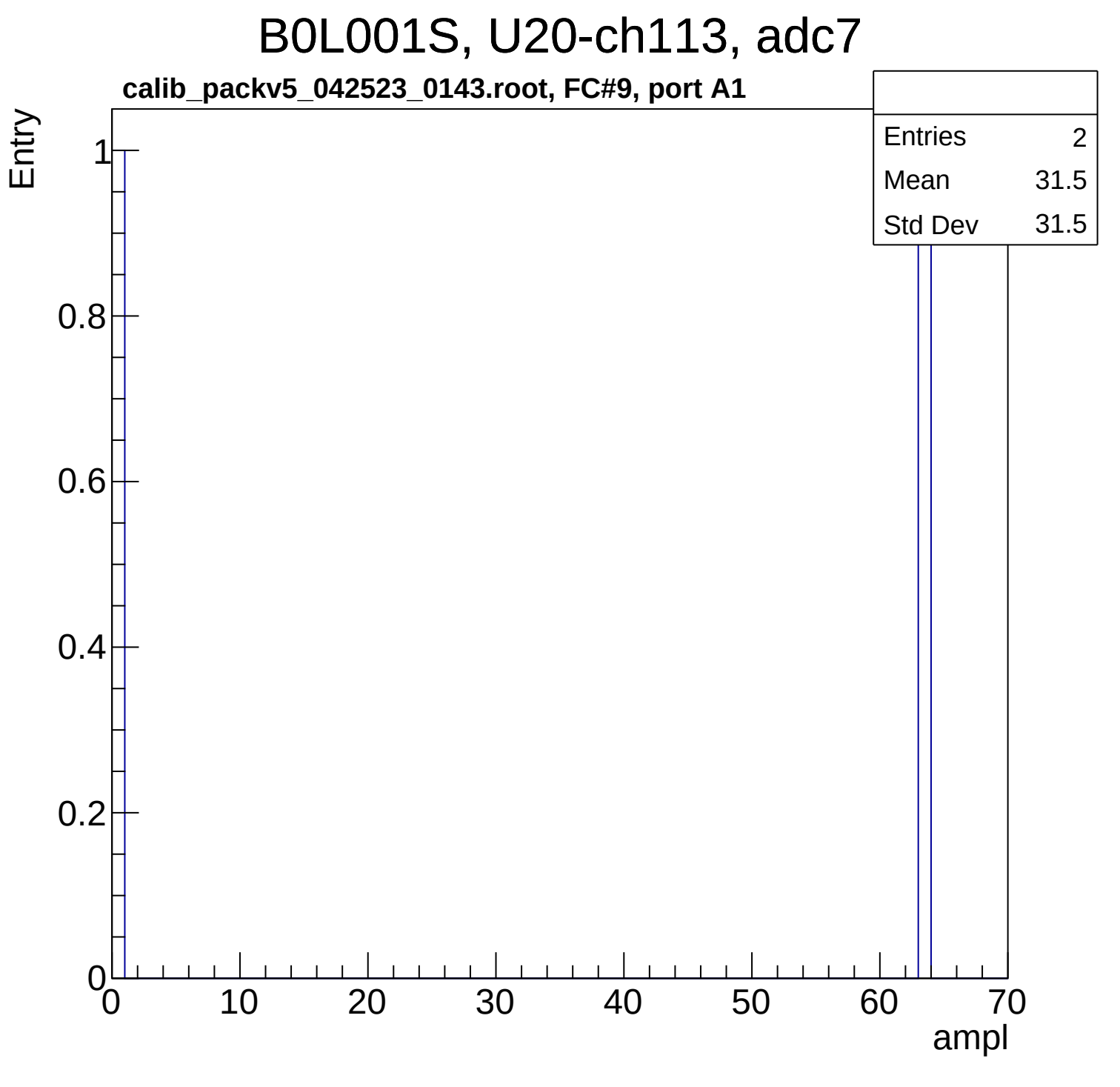
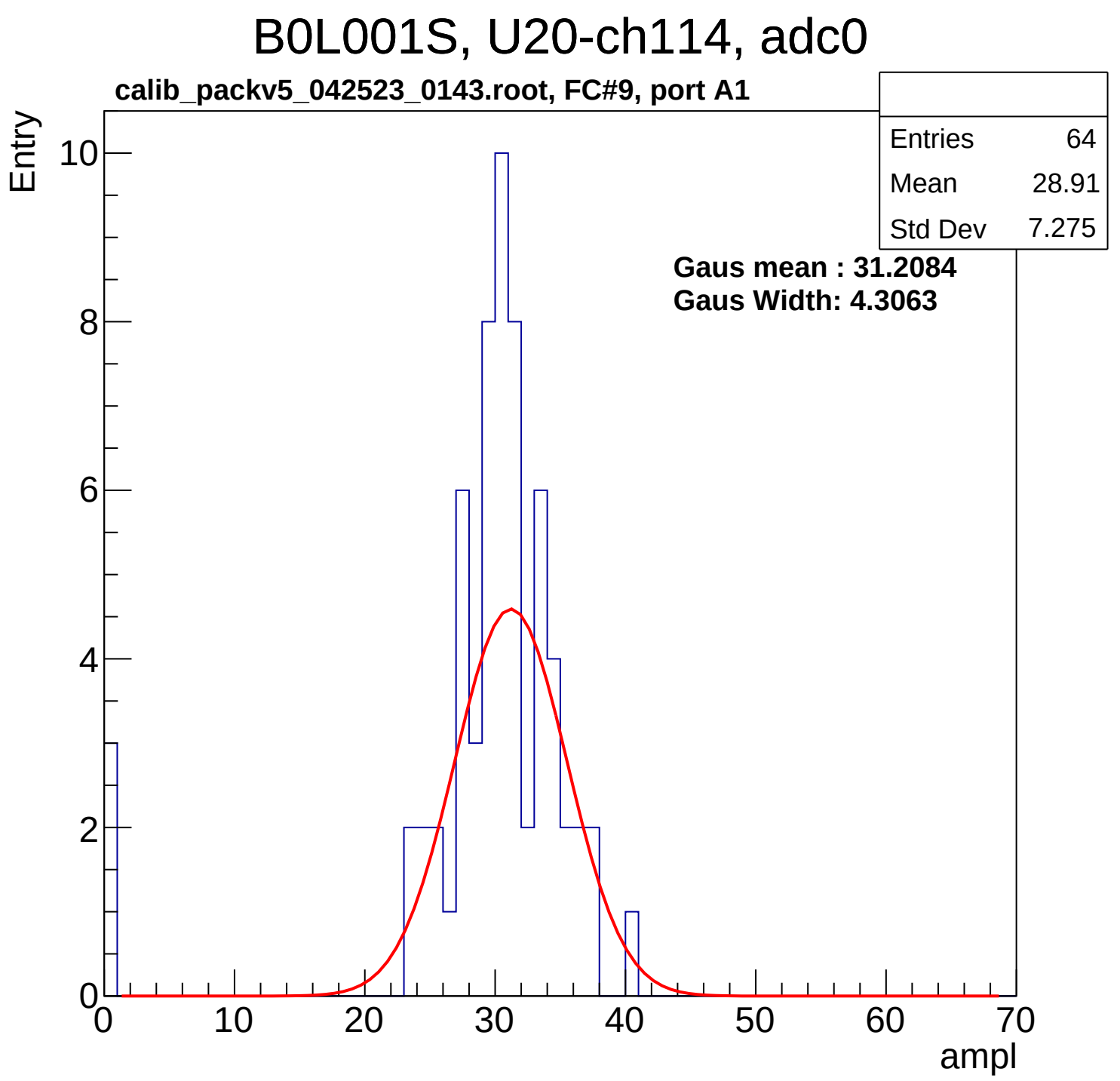
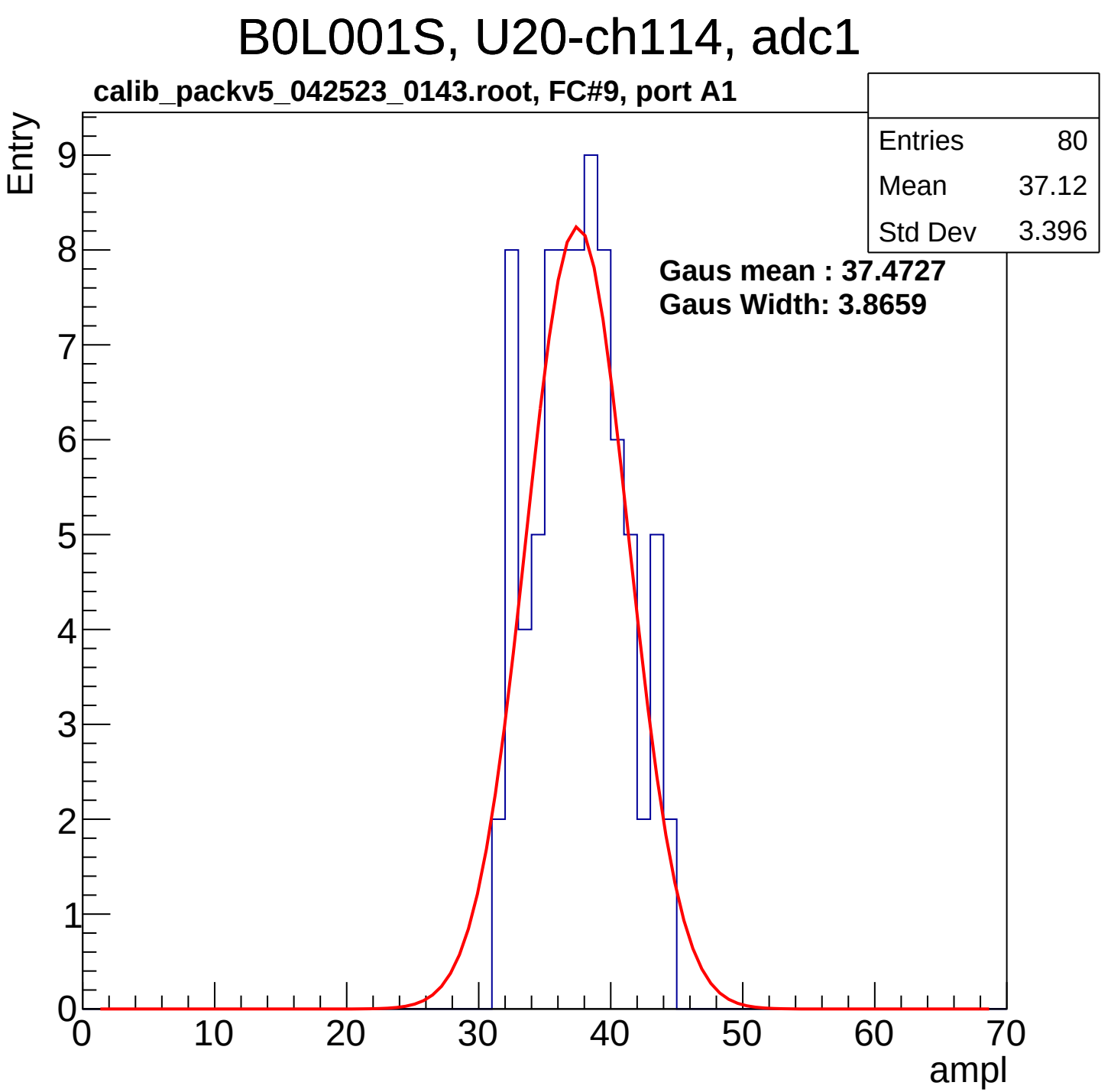

B0L001S, U20-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U20-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 28.91
Std Dev 7.275
Gaus mean : 31.2084
Gaus Width: 4.3063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.12
Std Dev 3.396
Gaus mean : 37.4727
Gaus Width: 3.8659
Entry
ampl
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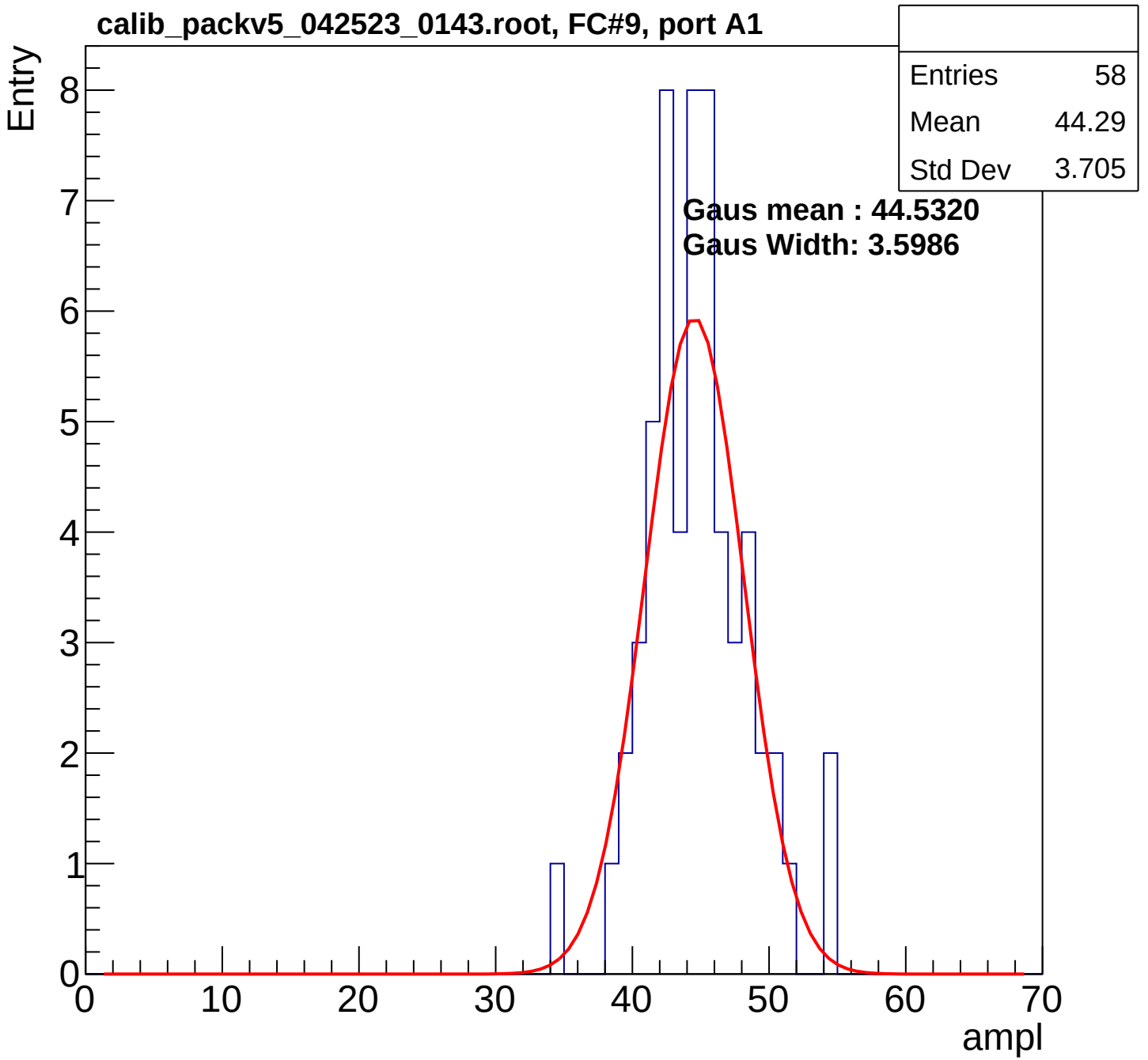
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.29
Std Dev 3.705
Gaus mean : 44.5320
Gaus Width: 3.5986
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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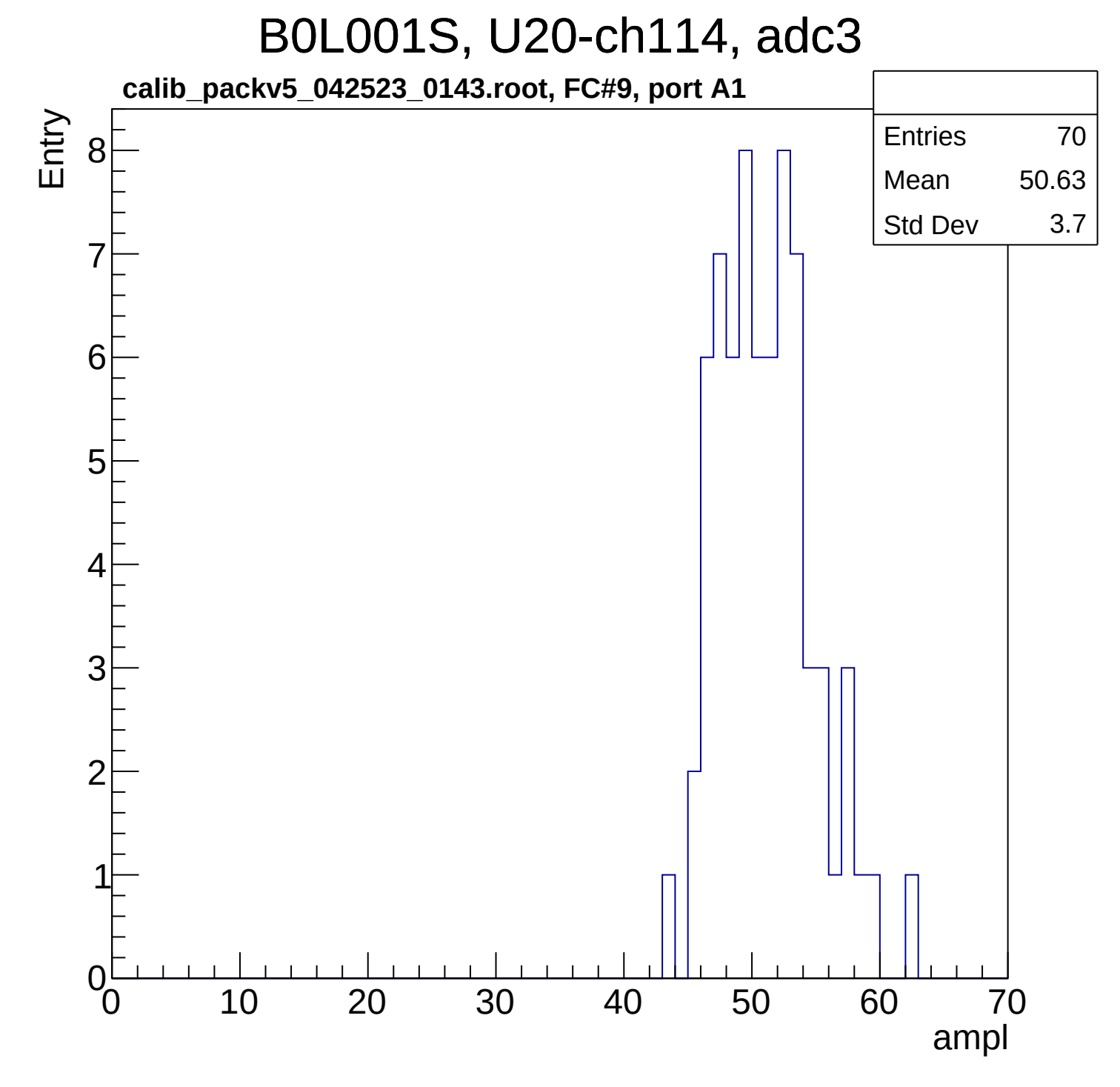
B0L001S, U20-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.63
Std Dev 3.7
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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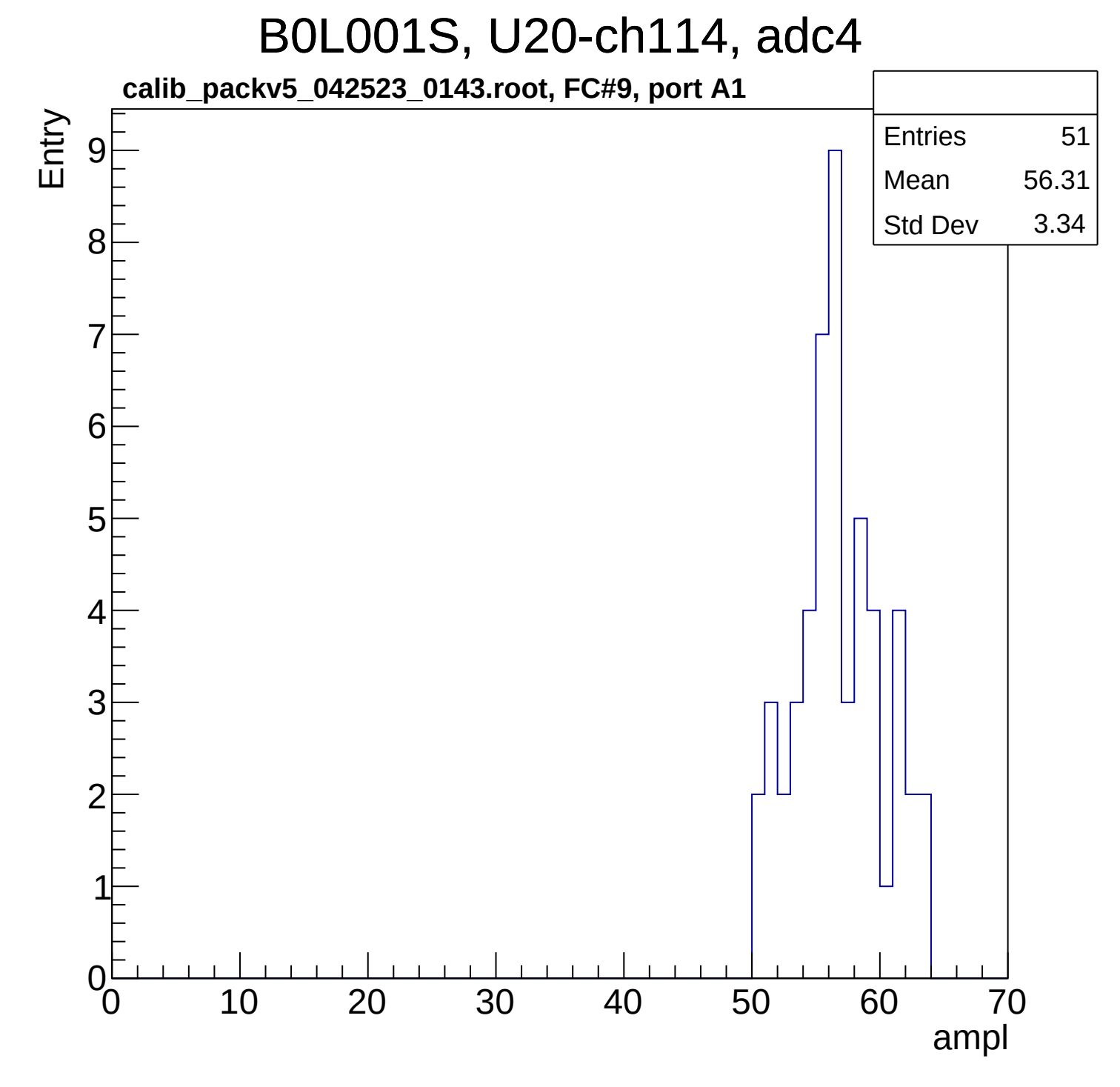
B0L001S, U20-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.31
Std Dev 3.34
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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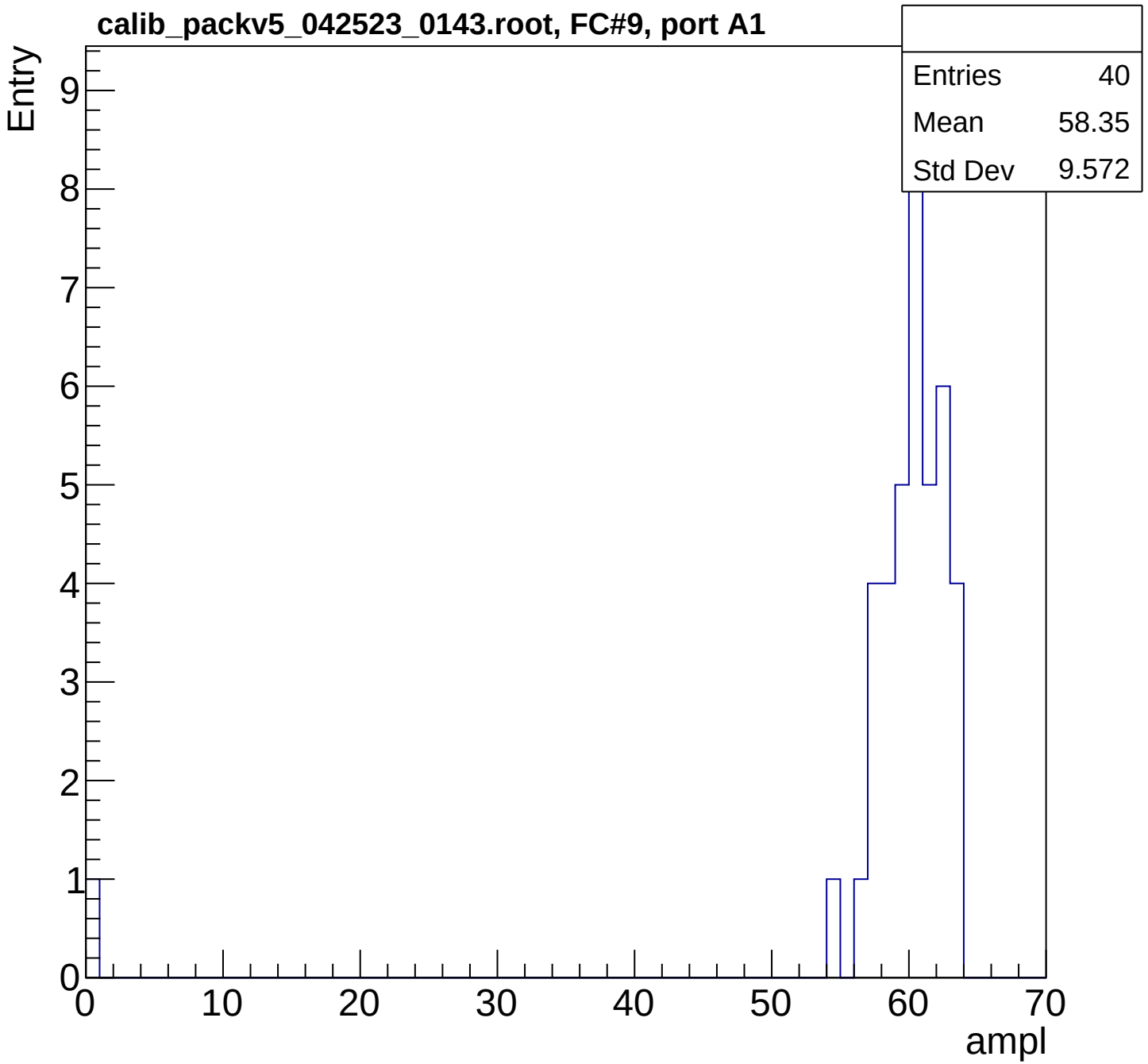
40
50
60
70


B0L001S, U20-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.35
Std Dev 9.572
Entry
ampl
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2
3
4
5
6
7
8
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0
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50
60
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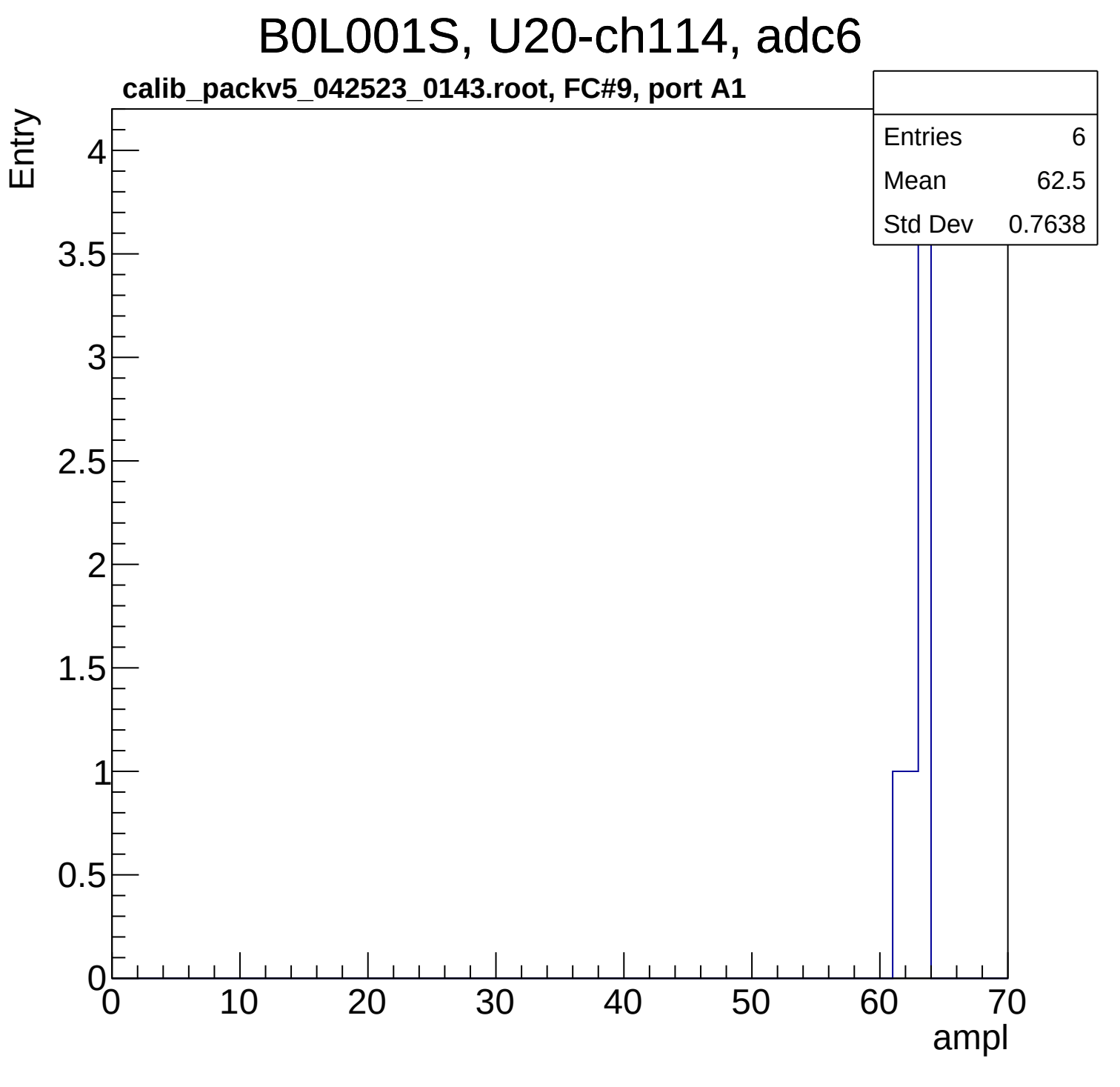
B0L001S, U20-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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0.5
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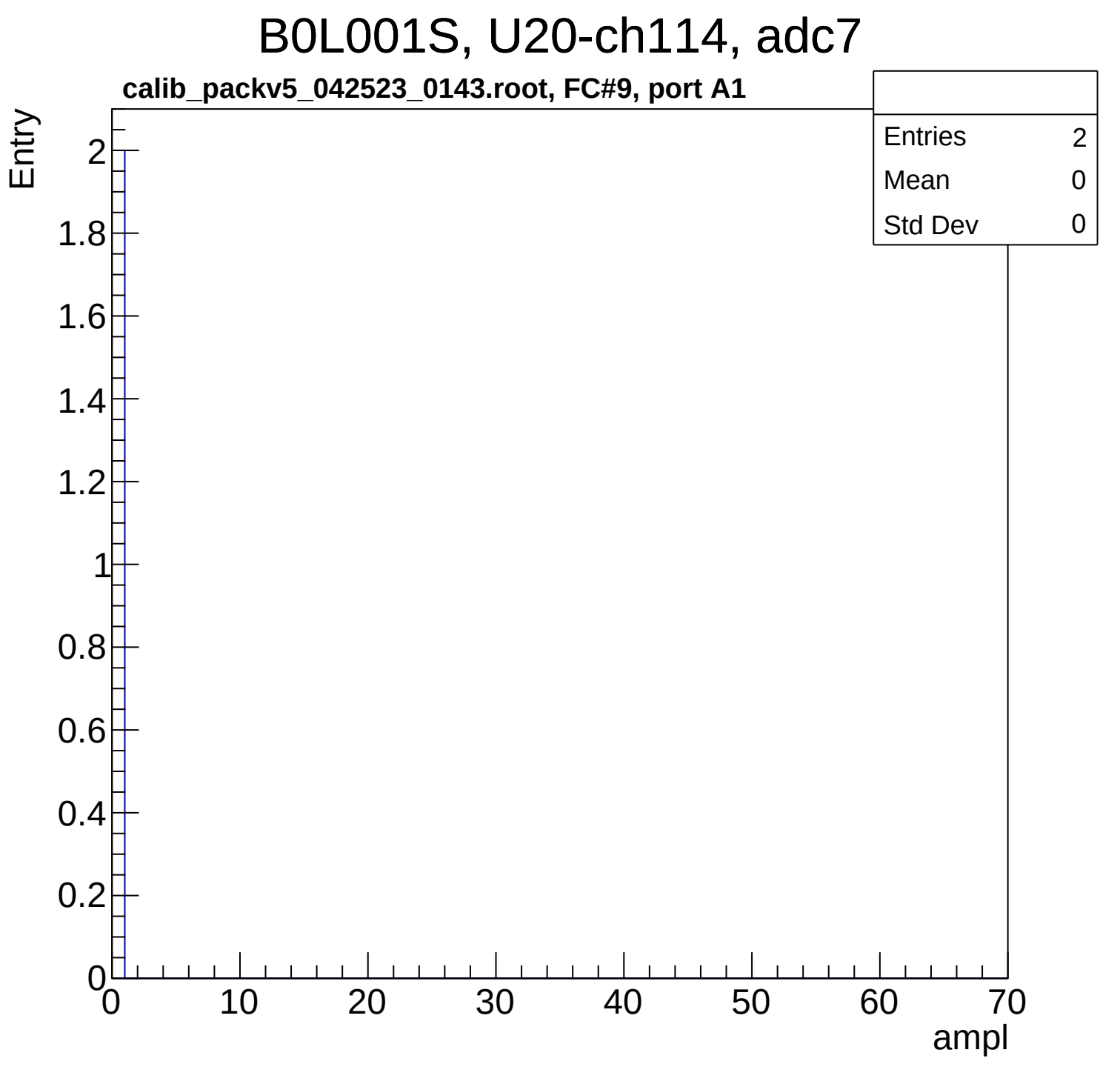
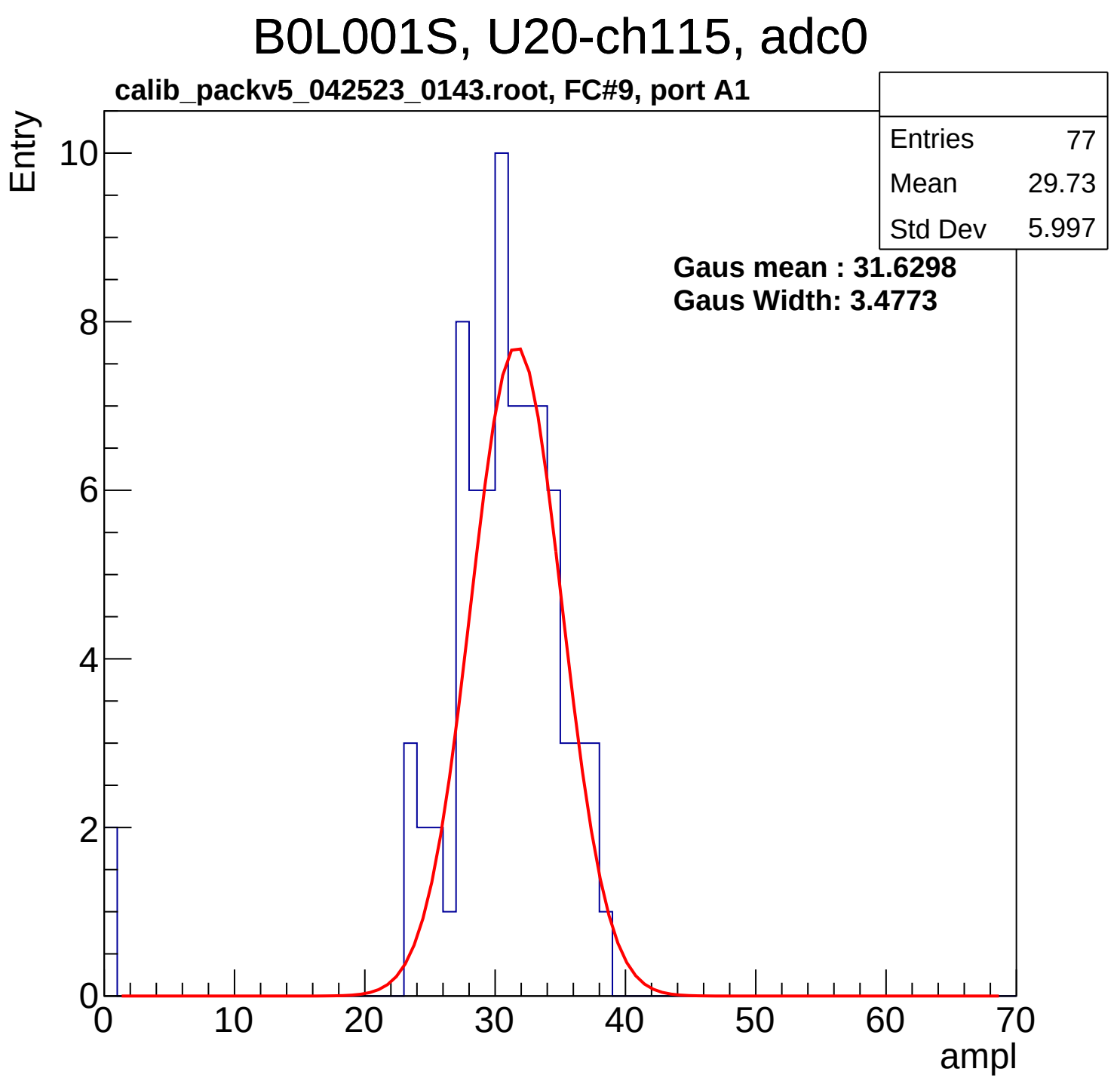
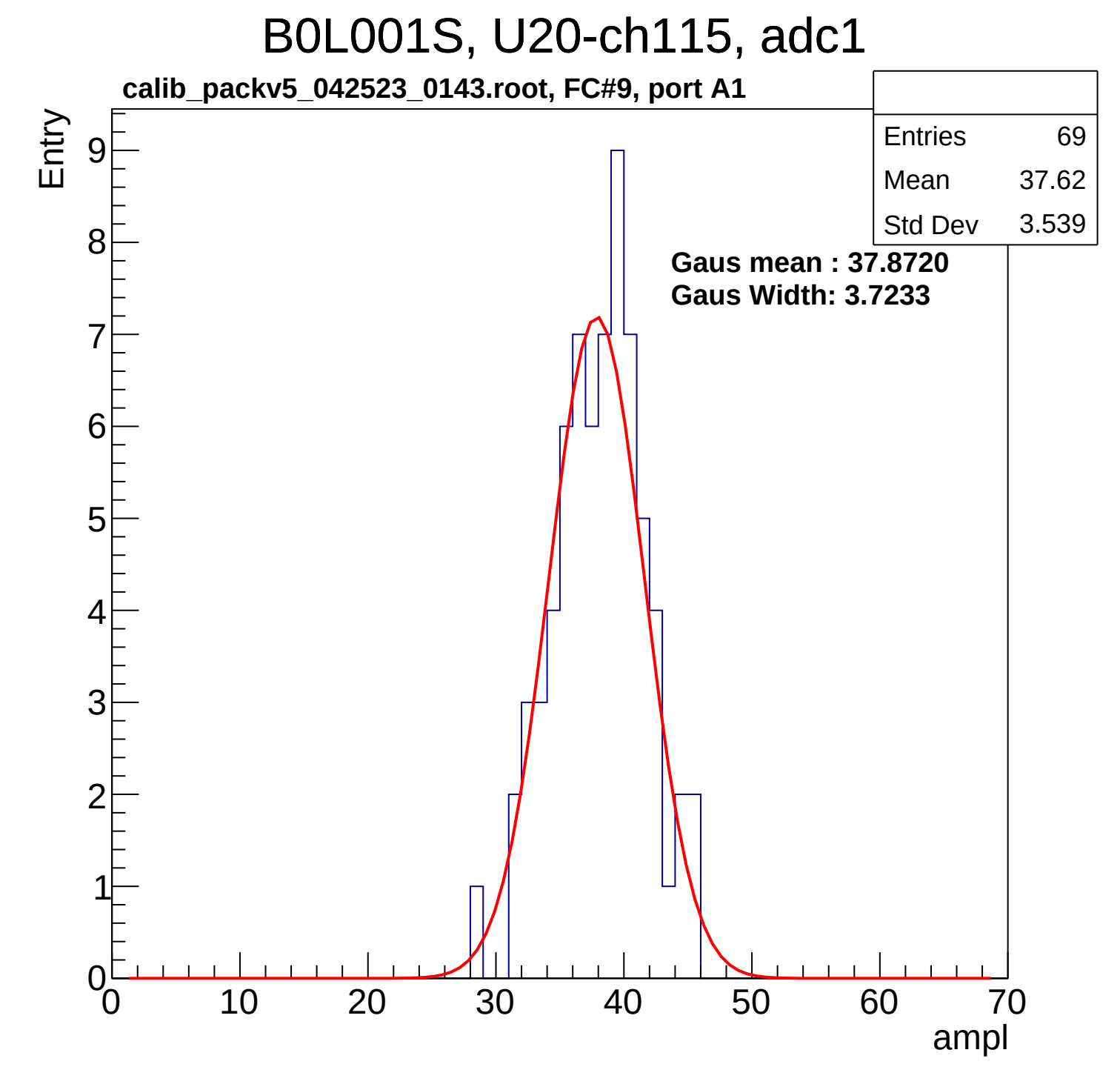
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U20-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.73
Std Dev 5.997
Gaus mean : 31.6298
Gaus Width: 3.4773
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.62
Std Dev 3.539
Gaus mean : 37.8720
Gaus Width: 3.7233
Entry
ampl
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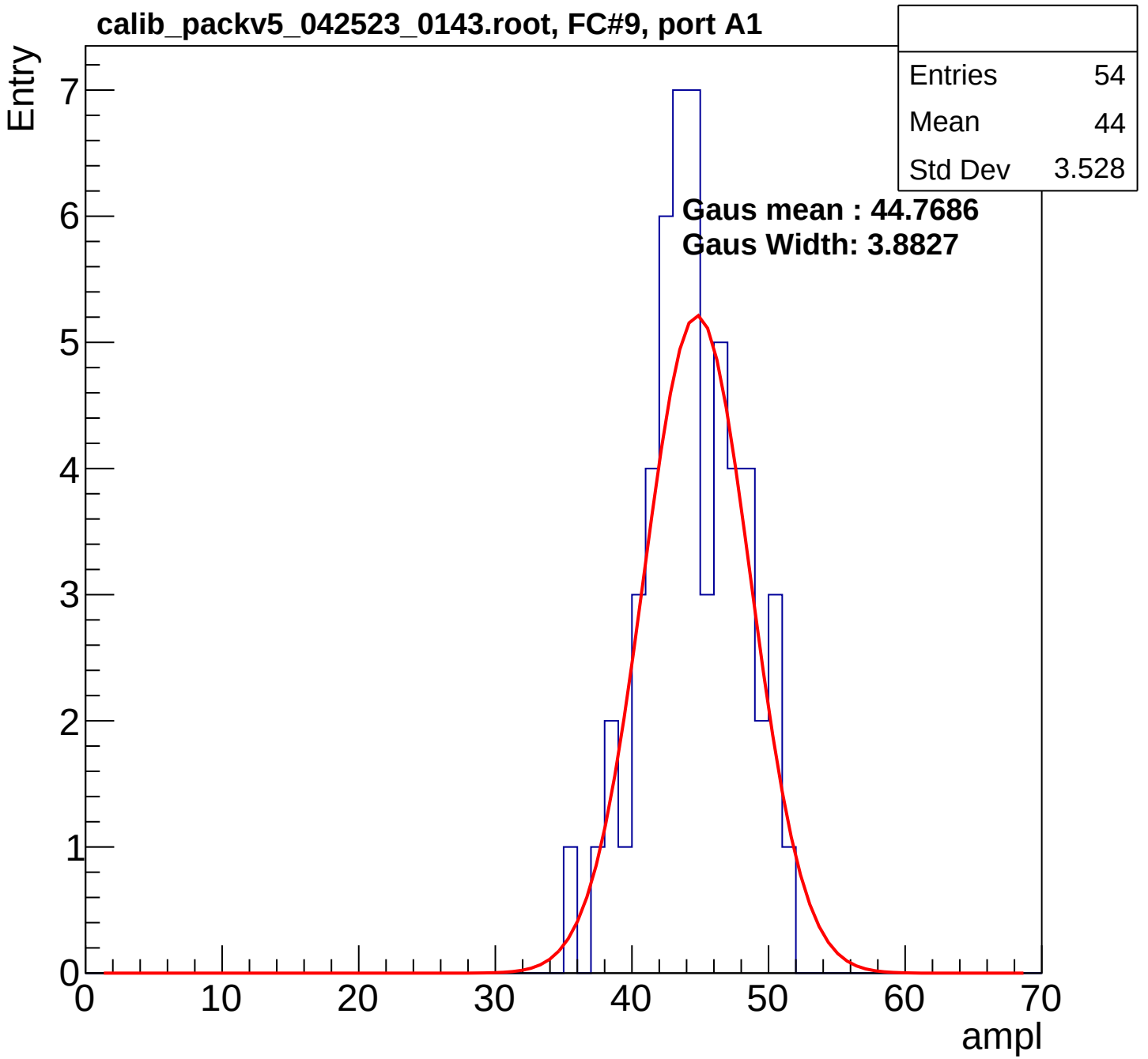
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44
Std Dev 3.528
Gaus mean : 44.7686
Gaus Width: 3.8827
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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60
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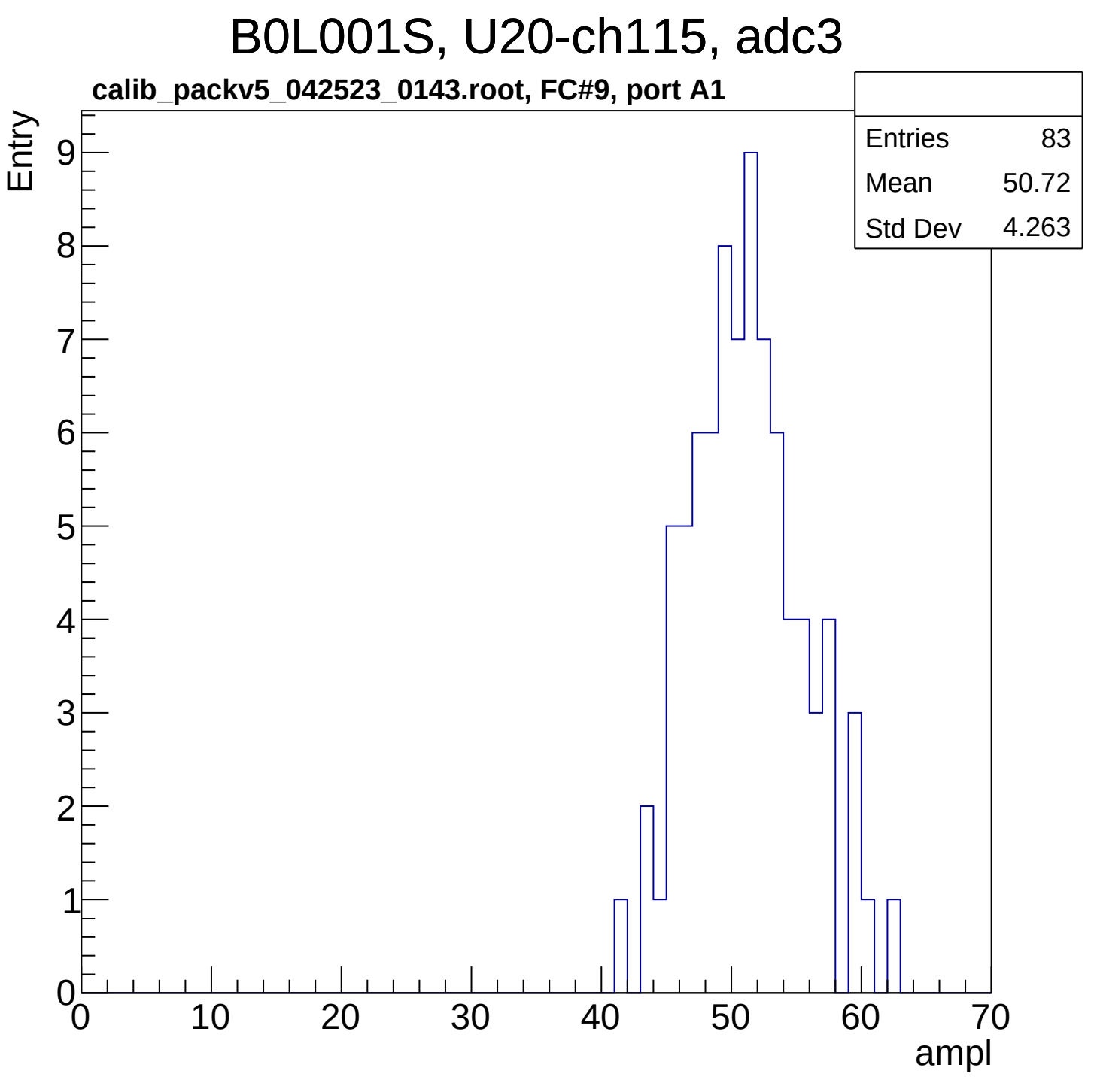
B0L001S, U20-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 50.72
Std Dev 4.263
Entry
ampl
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5
6
7
8
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0
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40
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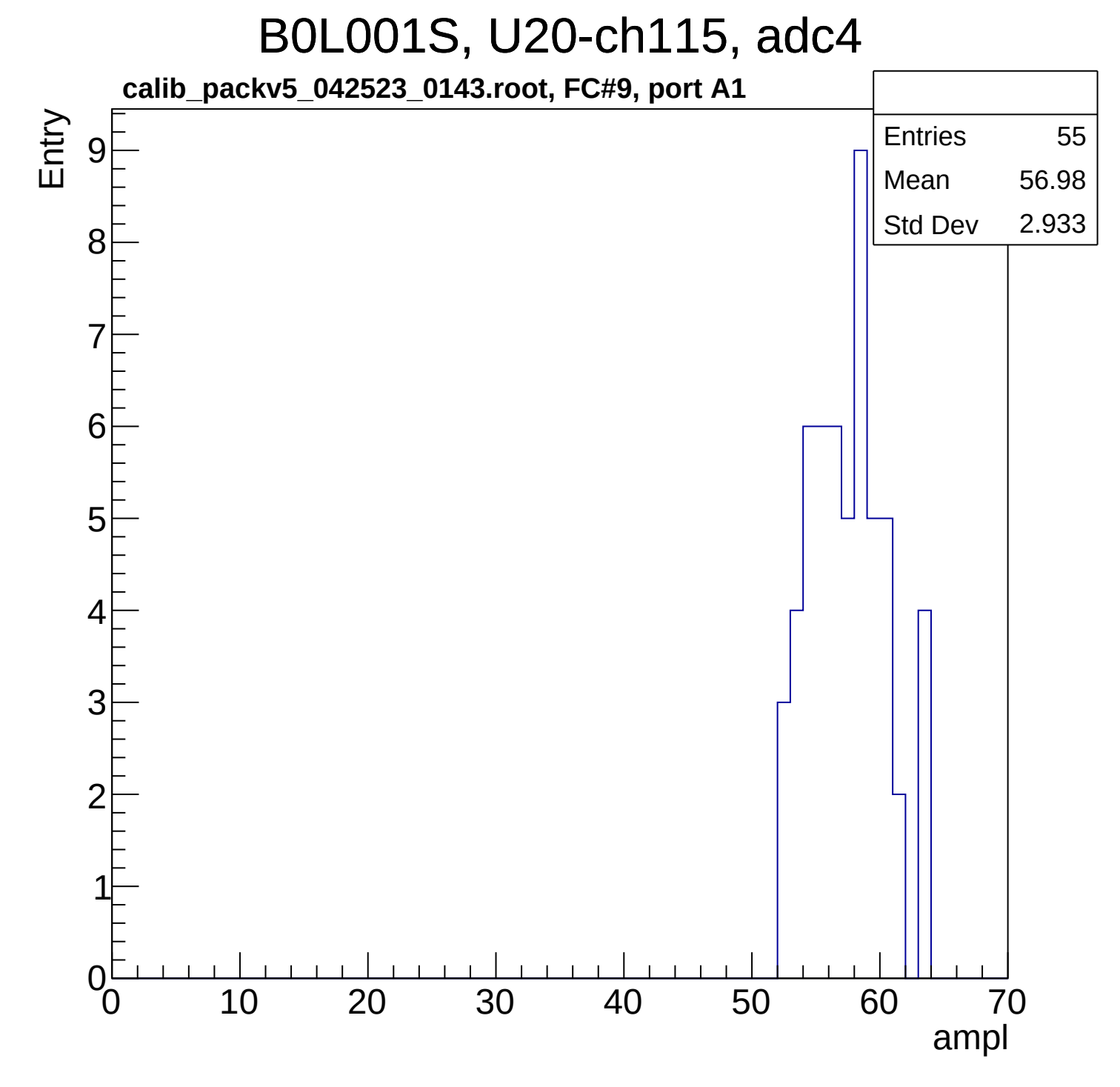
B0L001S, U20-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.98
Std Dev 2.933
Entry
ampl
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10
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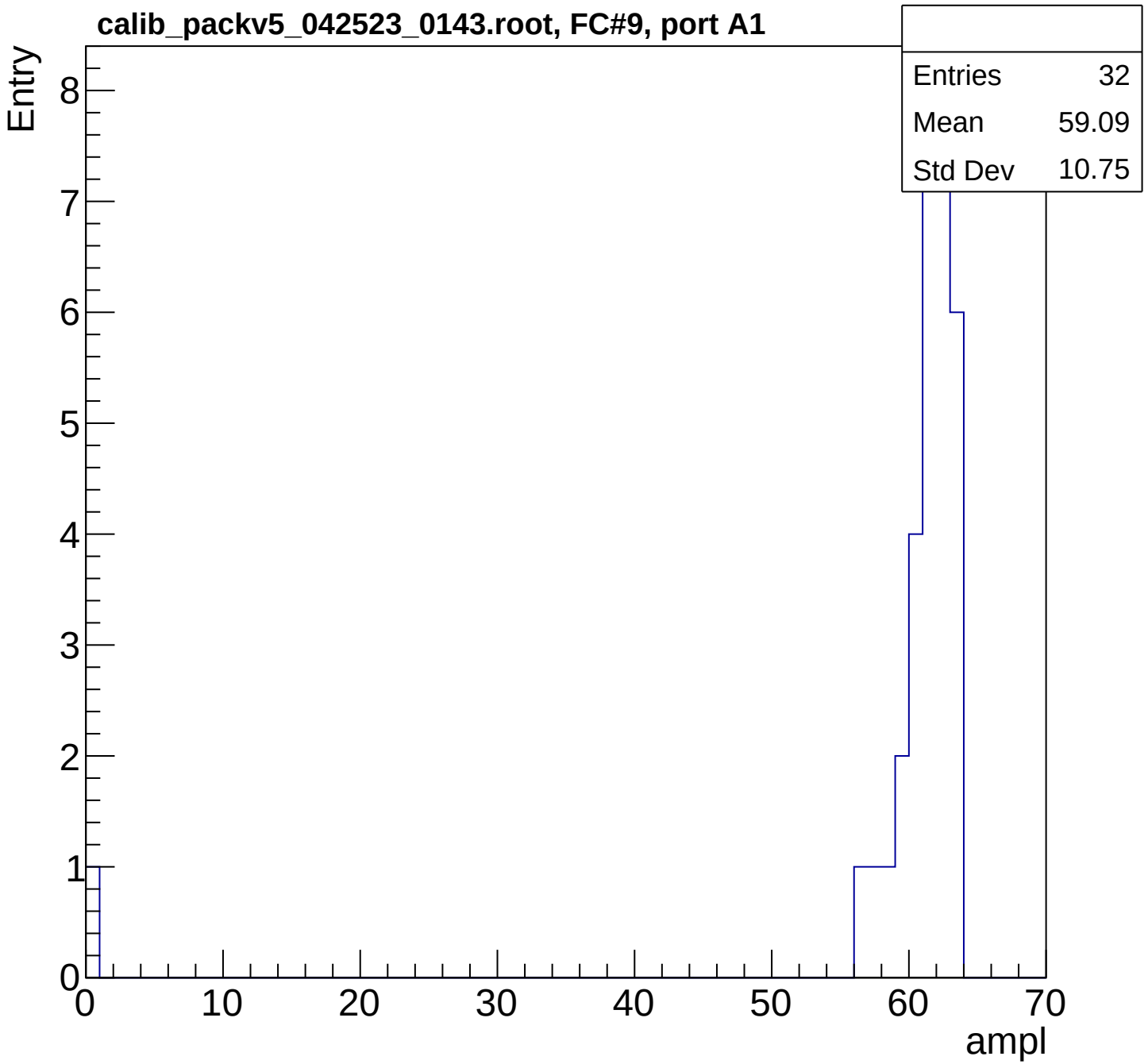
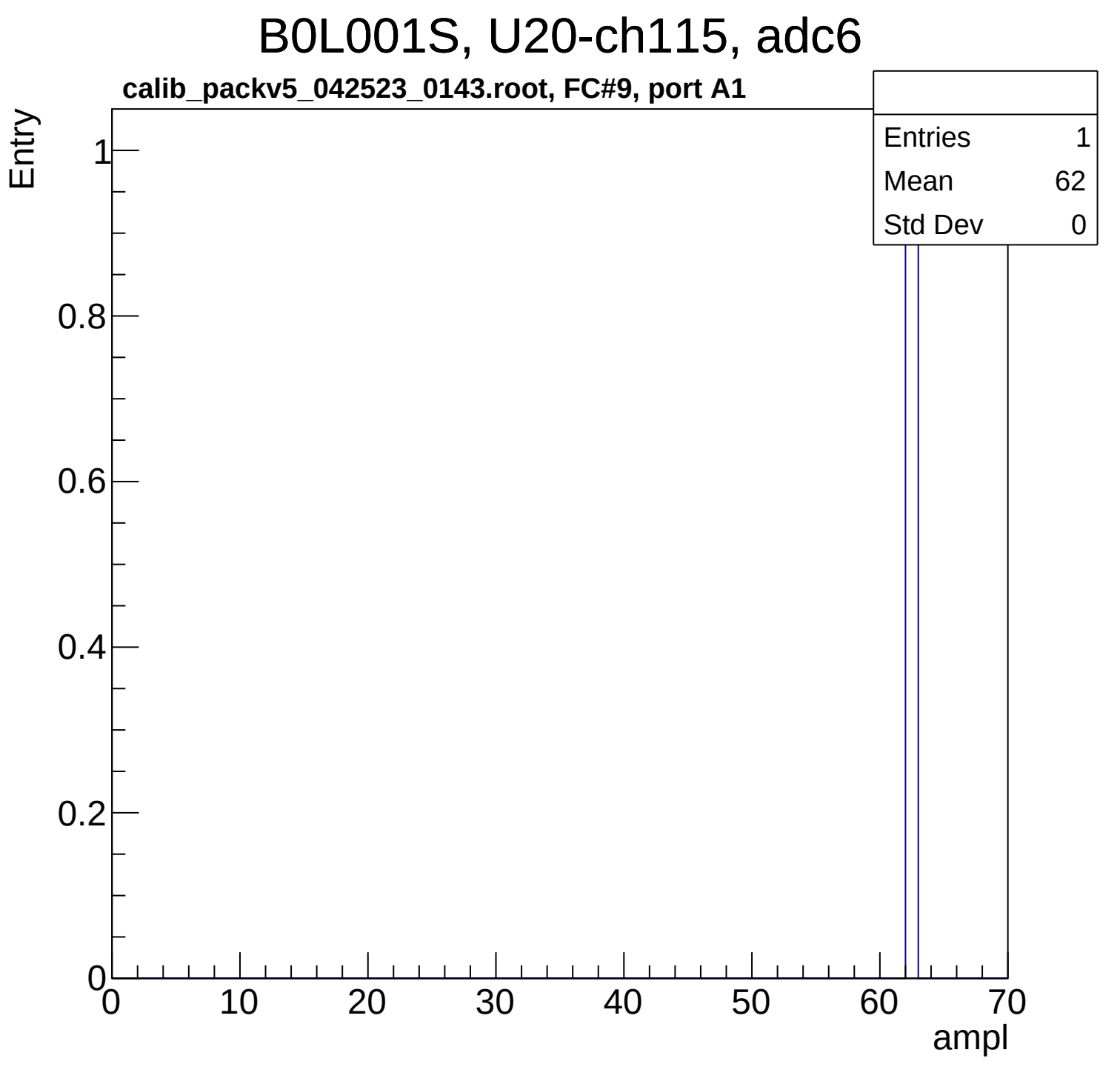
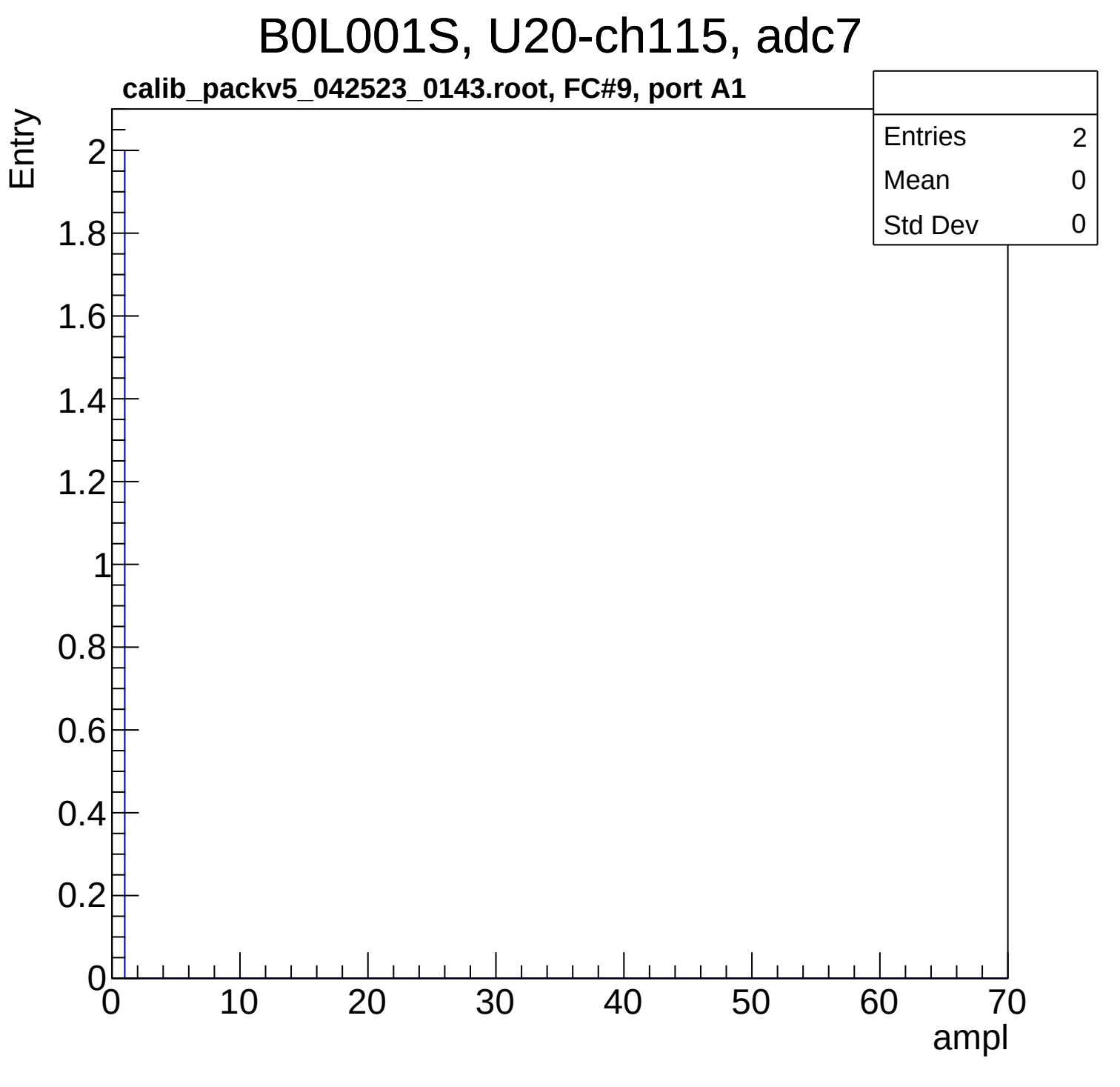
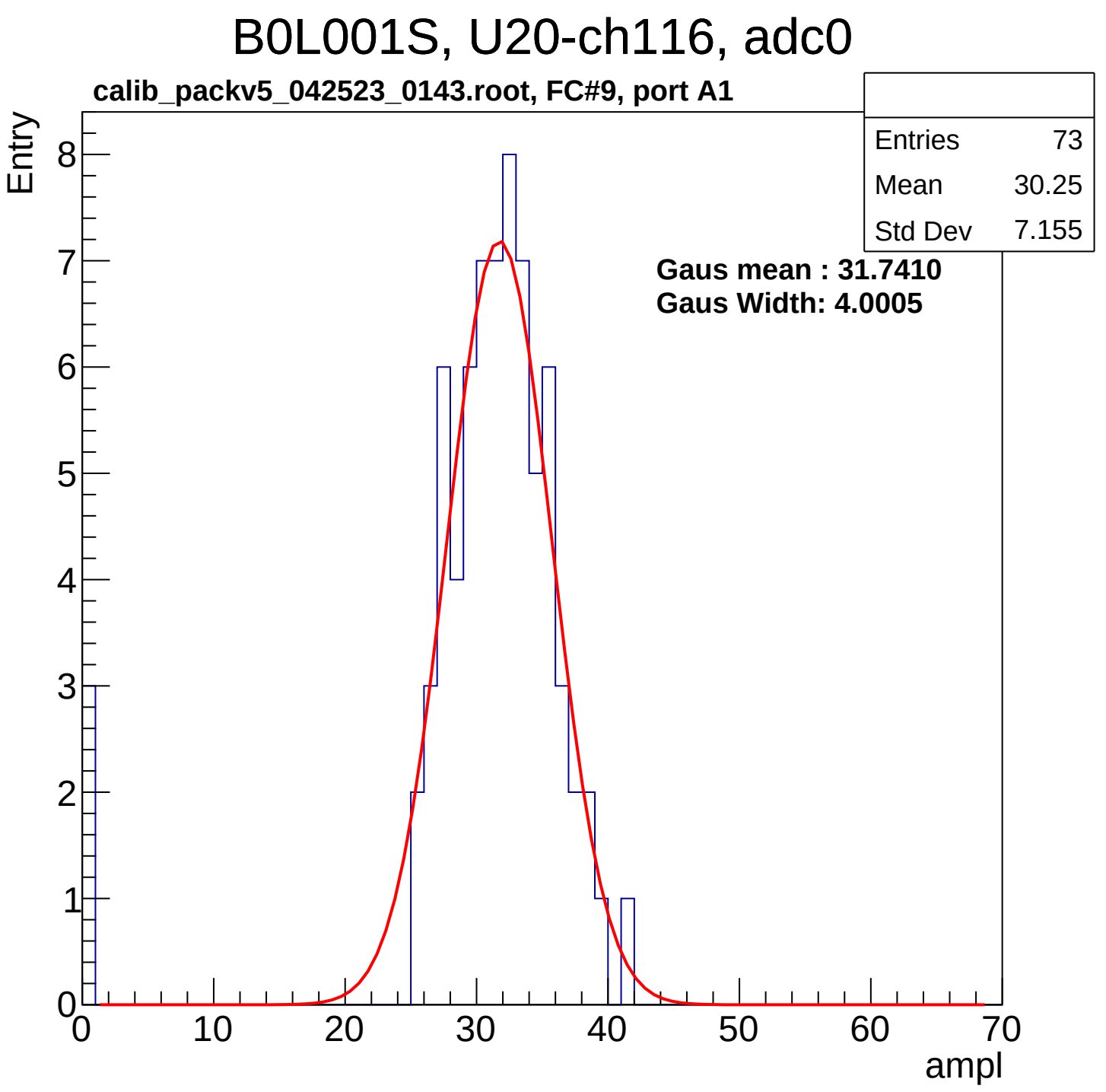
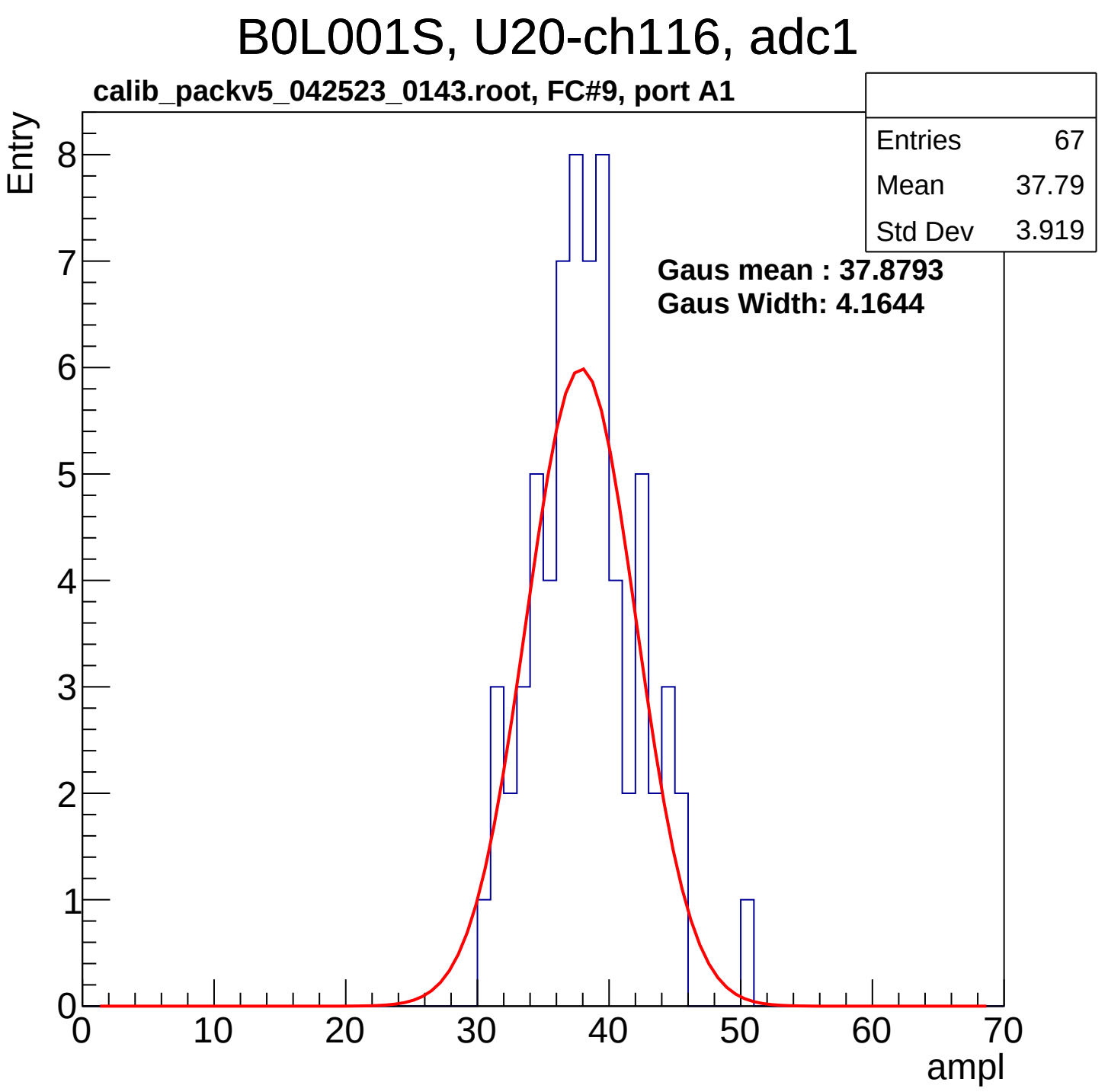

B0L001S, U20-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 59.09
Std Dev 10.75
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.25
Std Dev 7.155
Gaus mean : 31.7410
Gaus Width: 4.0005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.79
Std Dev 3.919
Gaus mean : 37.8793
Gaus Width: 4.1644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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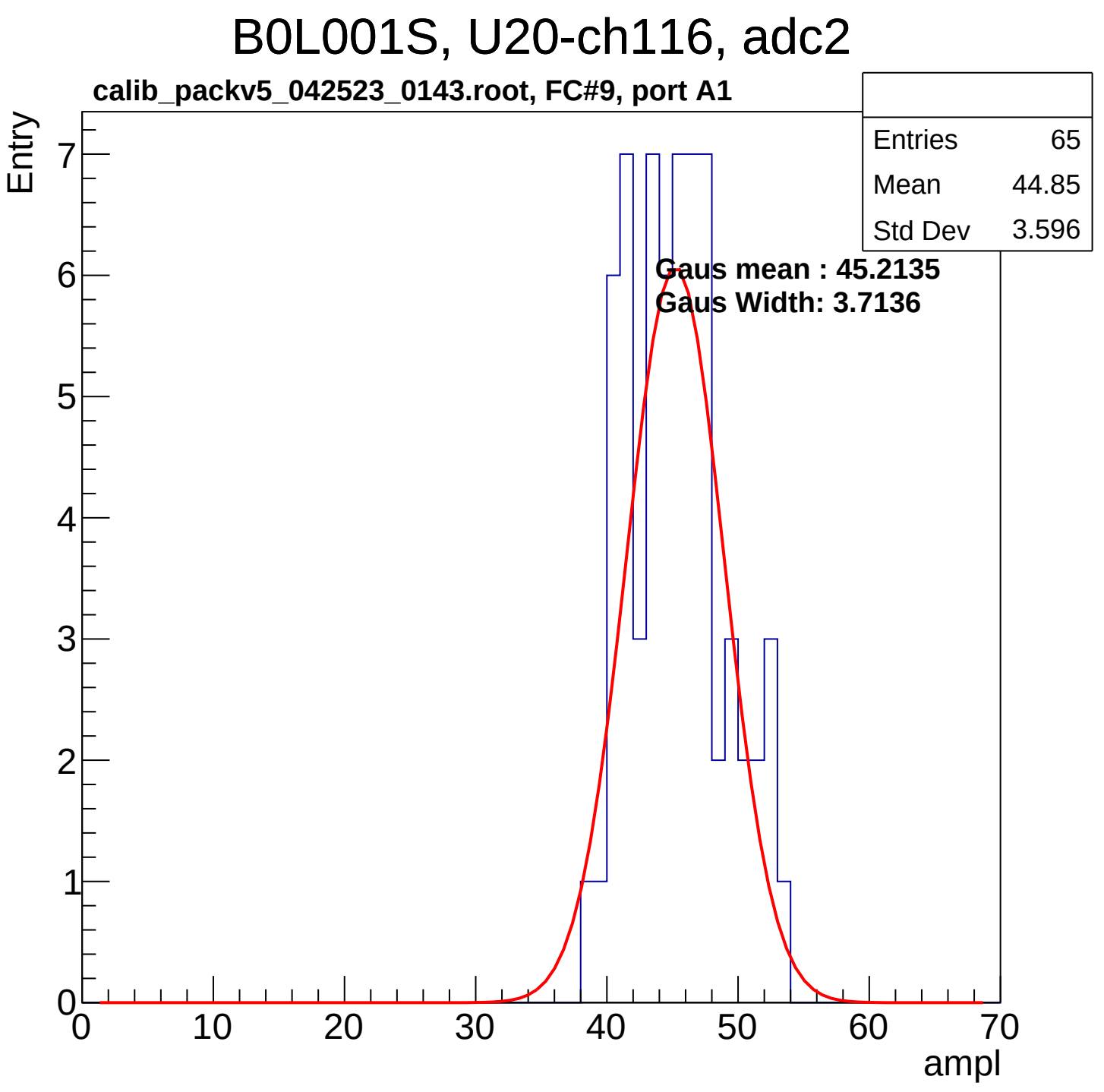
B0L001S, U20-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.85
Std Dev 3.596
Gaus mean : 45.2135
Gaus Width: 3.7136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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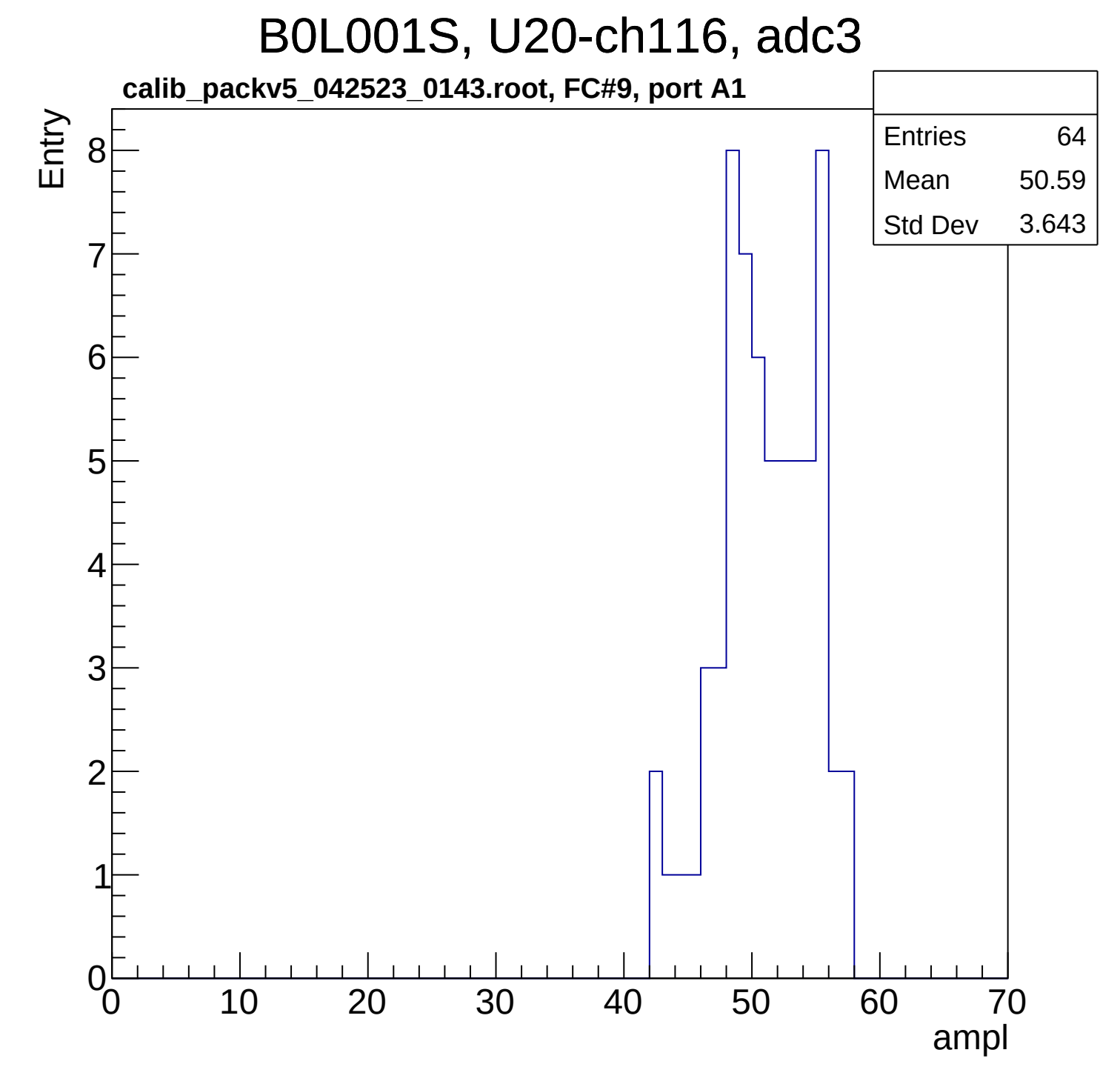
B0L001S, U20-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.59
Std Dev 3.643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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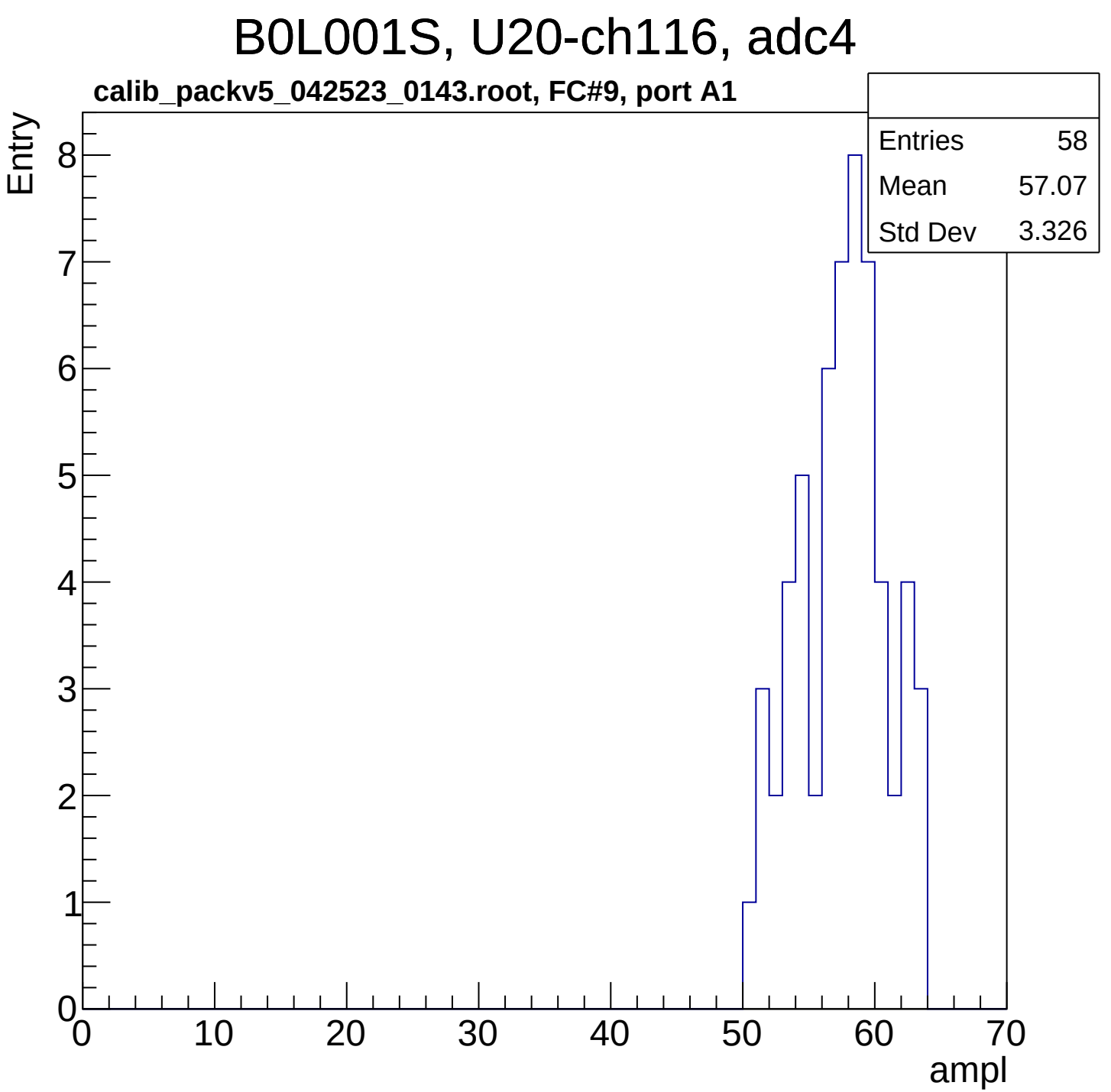
B0L001S, U20-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.07
Std Dev 3.326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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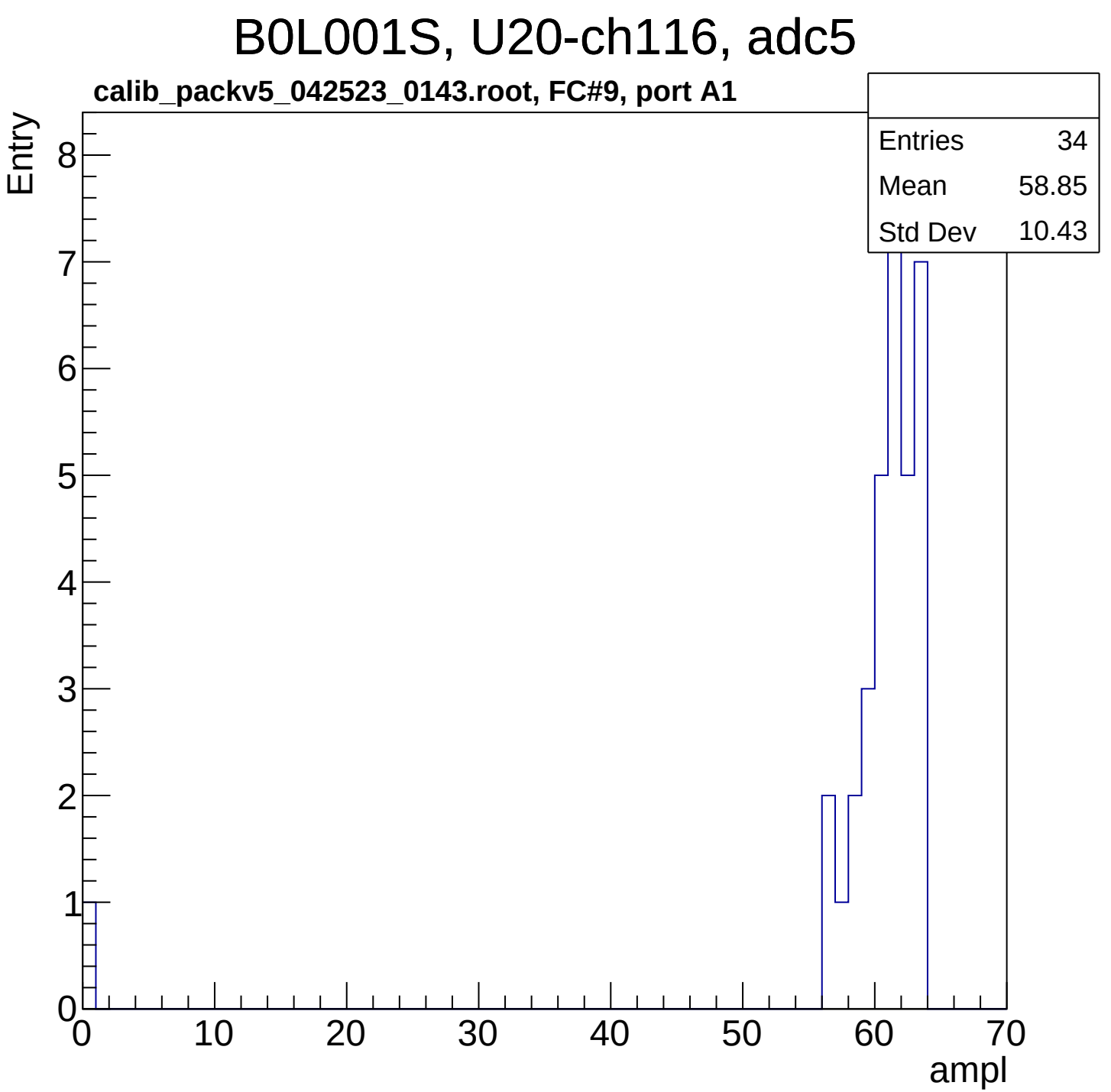
B0L001S, U20-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.85
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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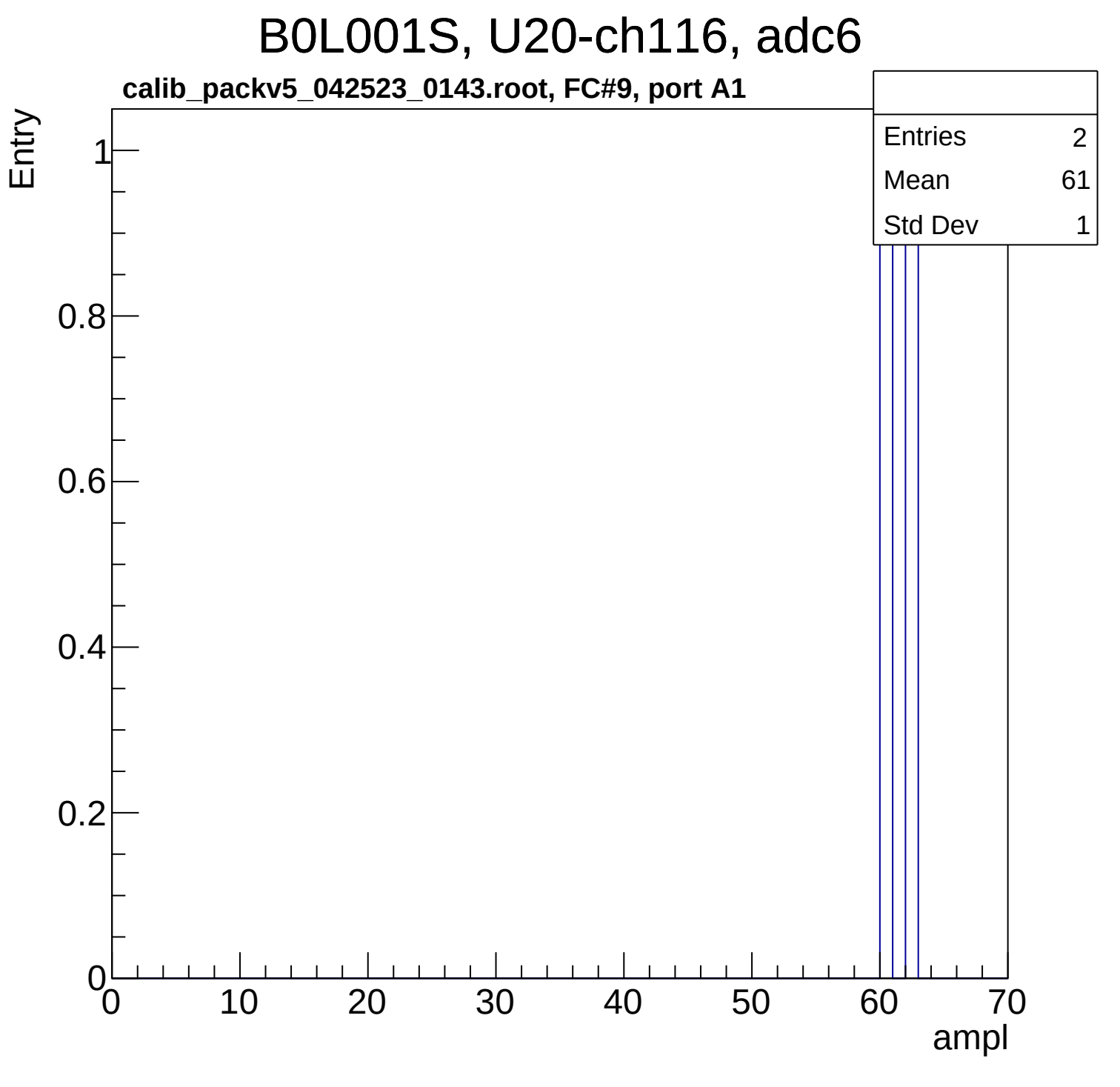
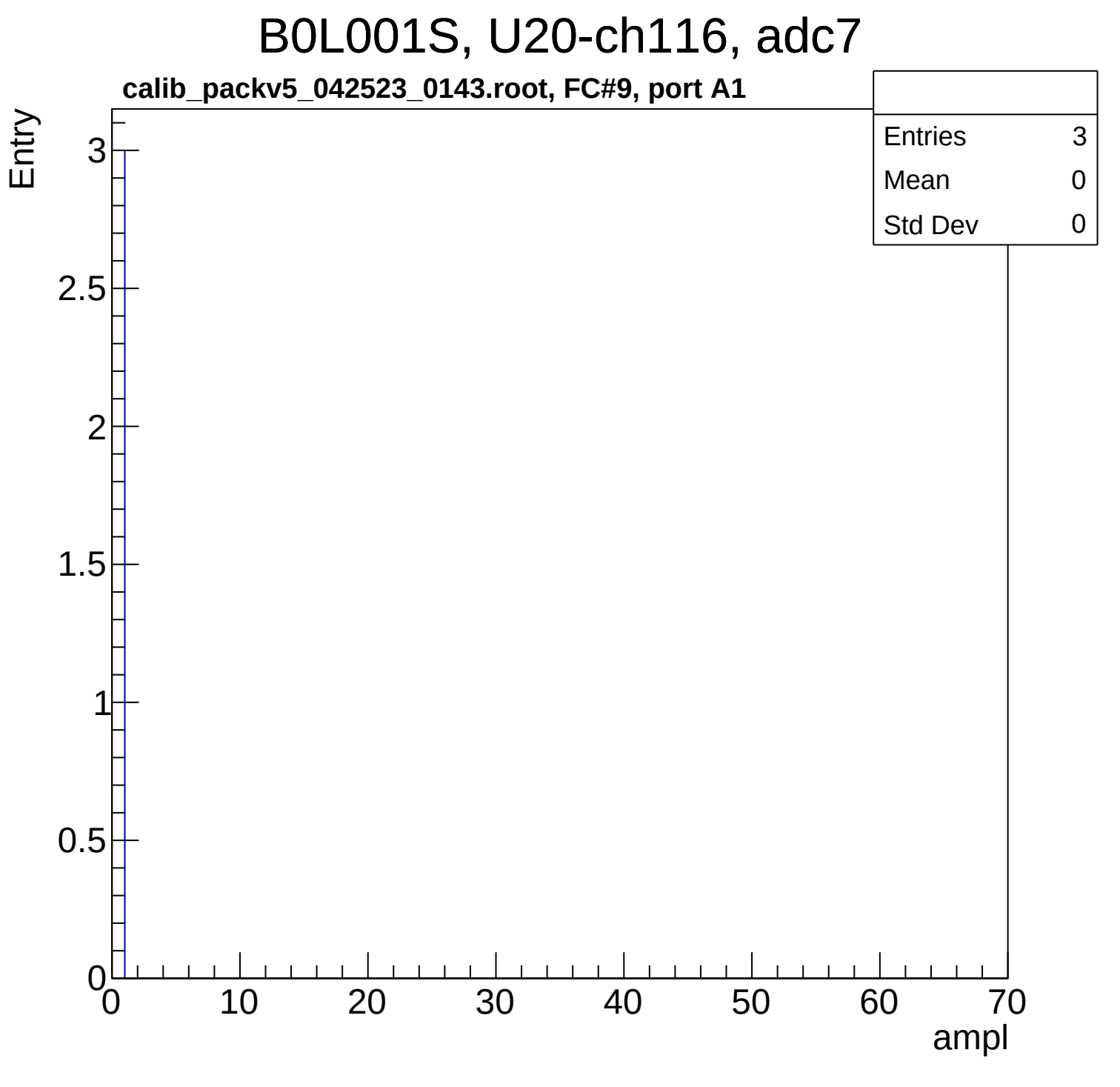
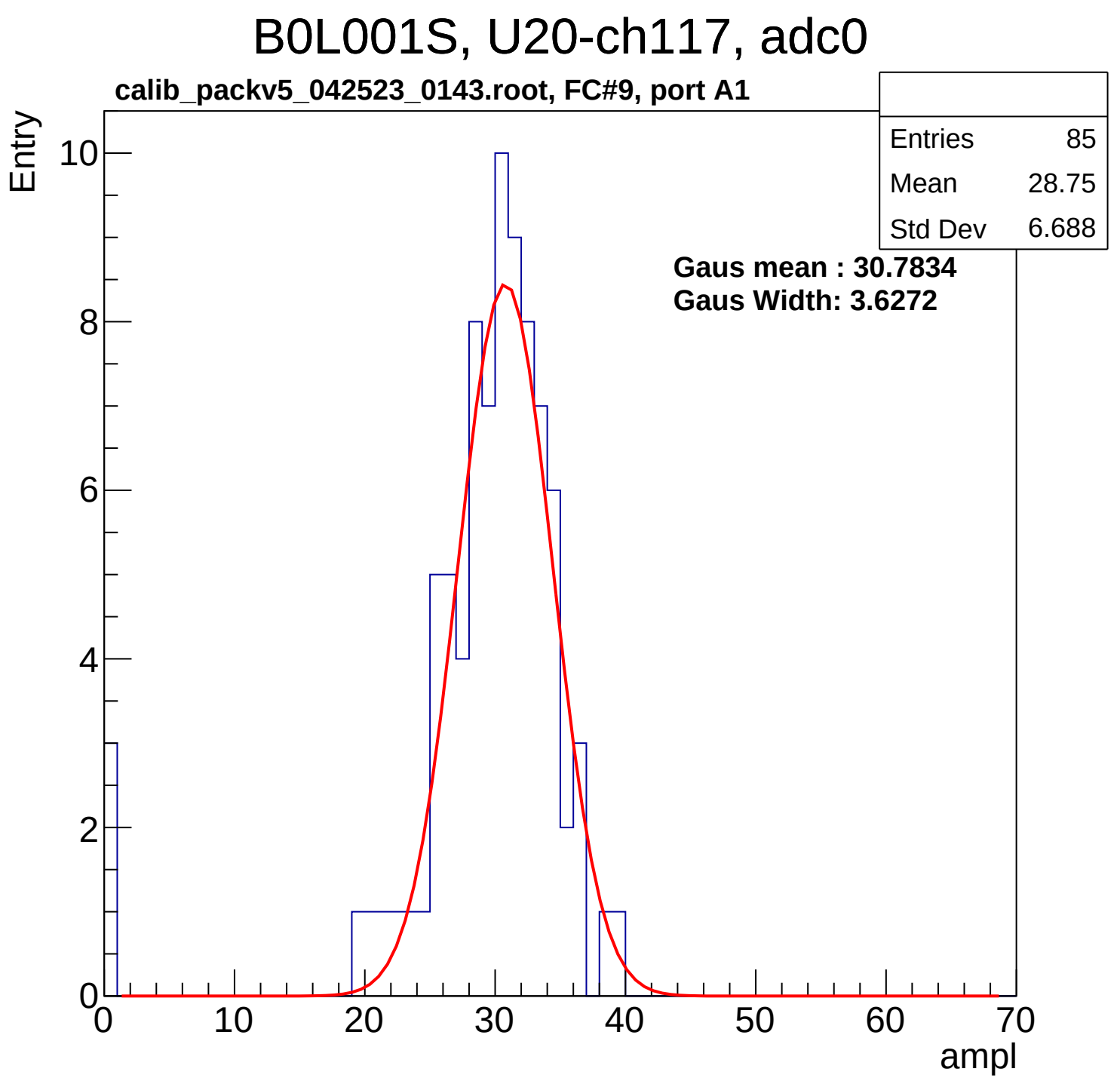
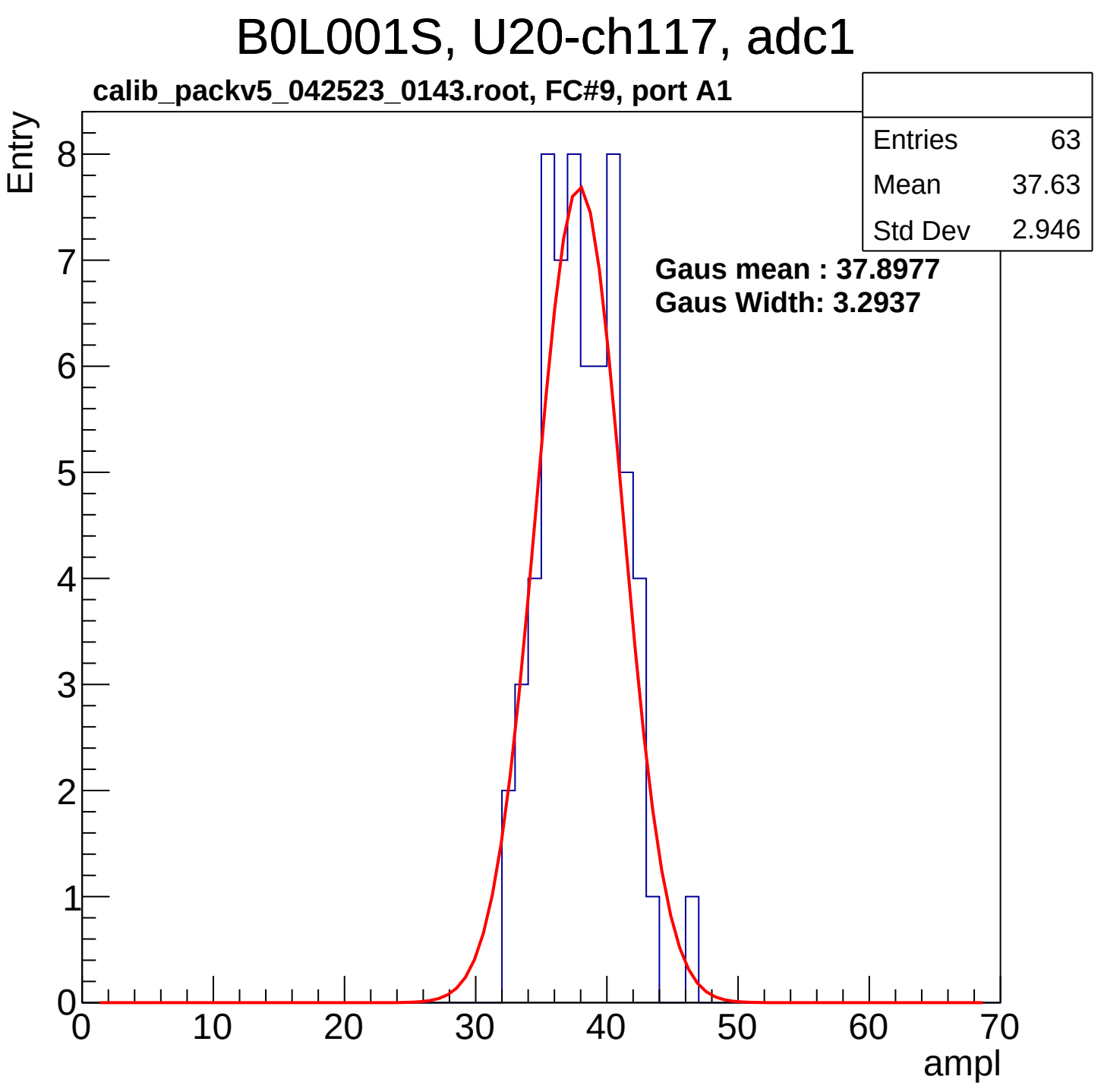
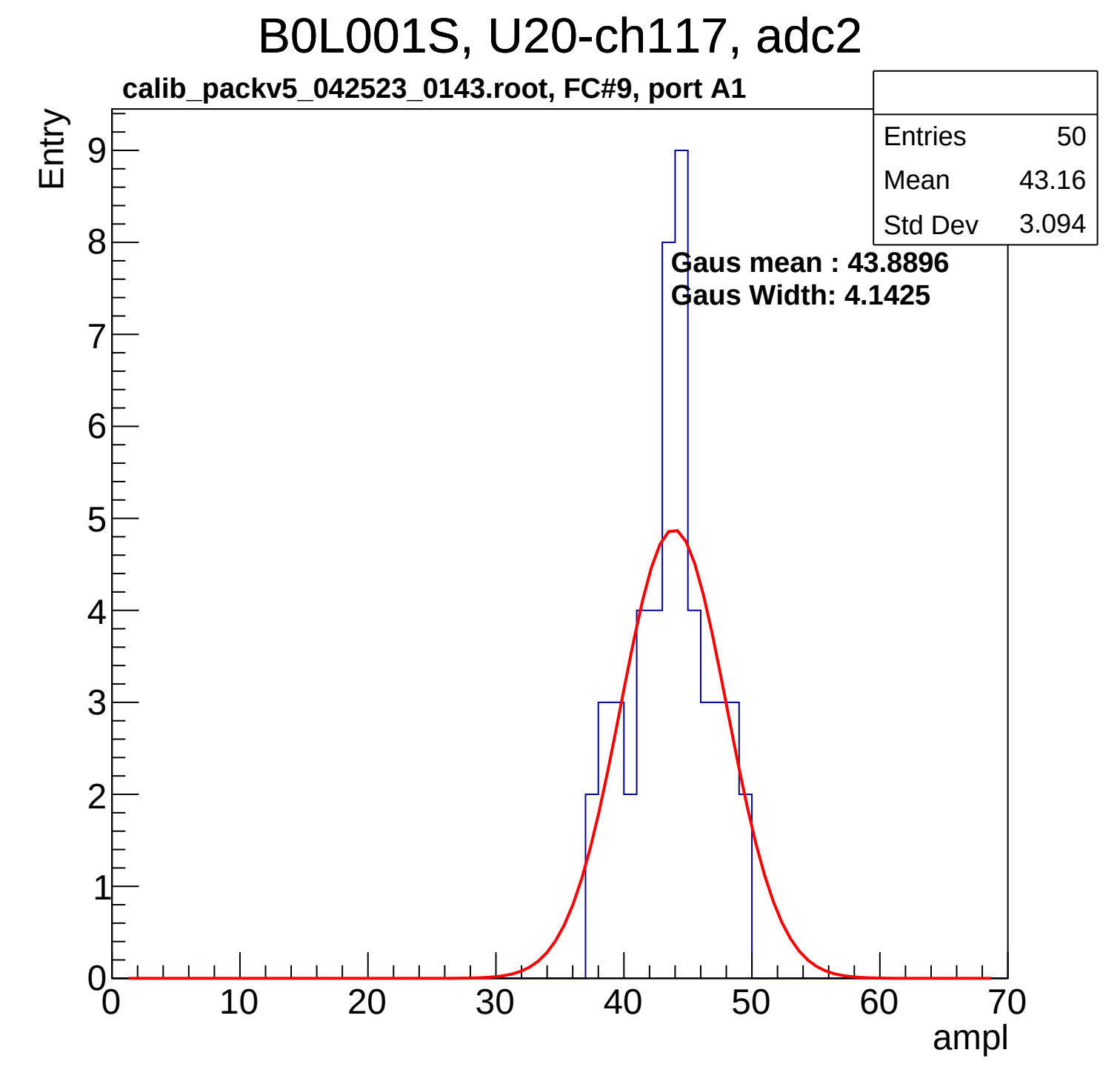

B0L001S, U20-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 28.75
Std Dev 6.688
Gaus mean : 30.7834
Gaus Width: 3.6272
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.63
Std Dev 2.946
Gaus mean : 37.8977
Gaus Width: 3.2937
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 43.16
Std Dev 3.094
Gaus mean : 43.8896
Gaus Width: 4.1425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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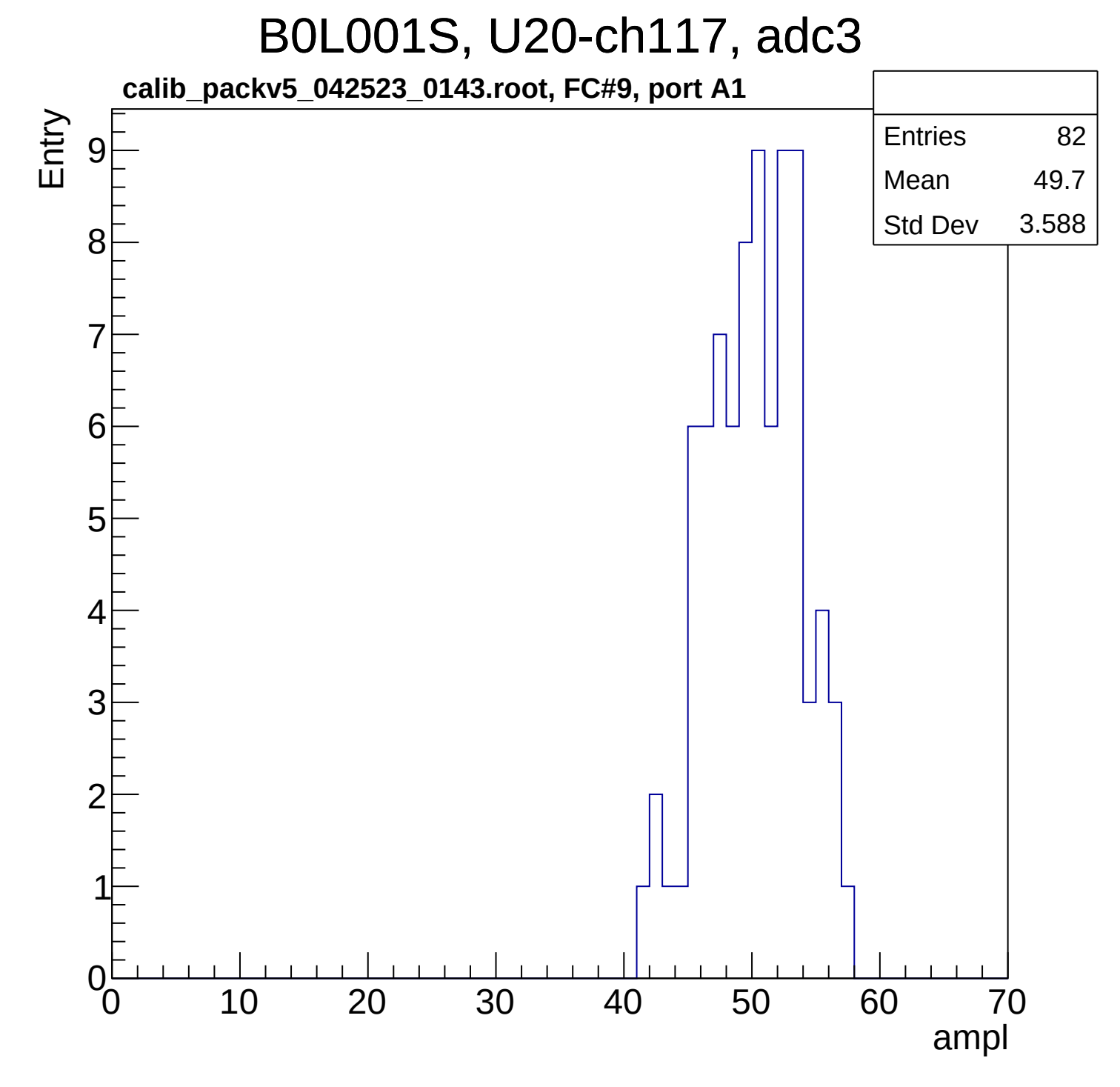
B0L001S, U20-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 49.7
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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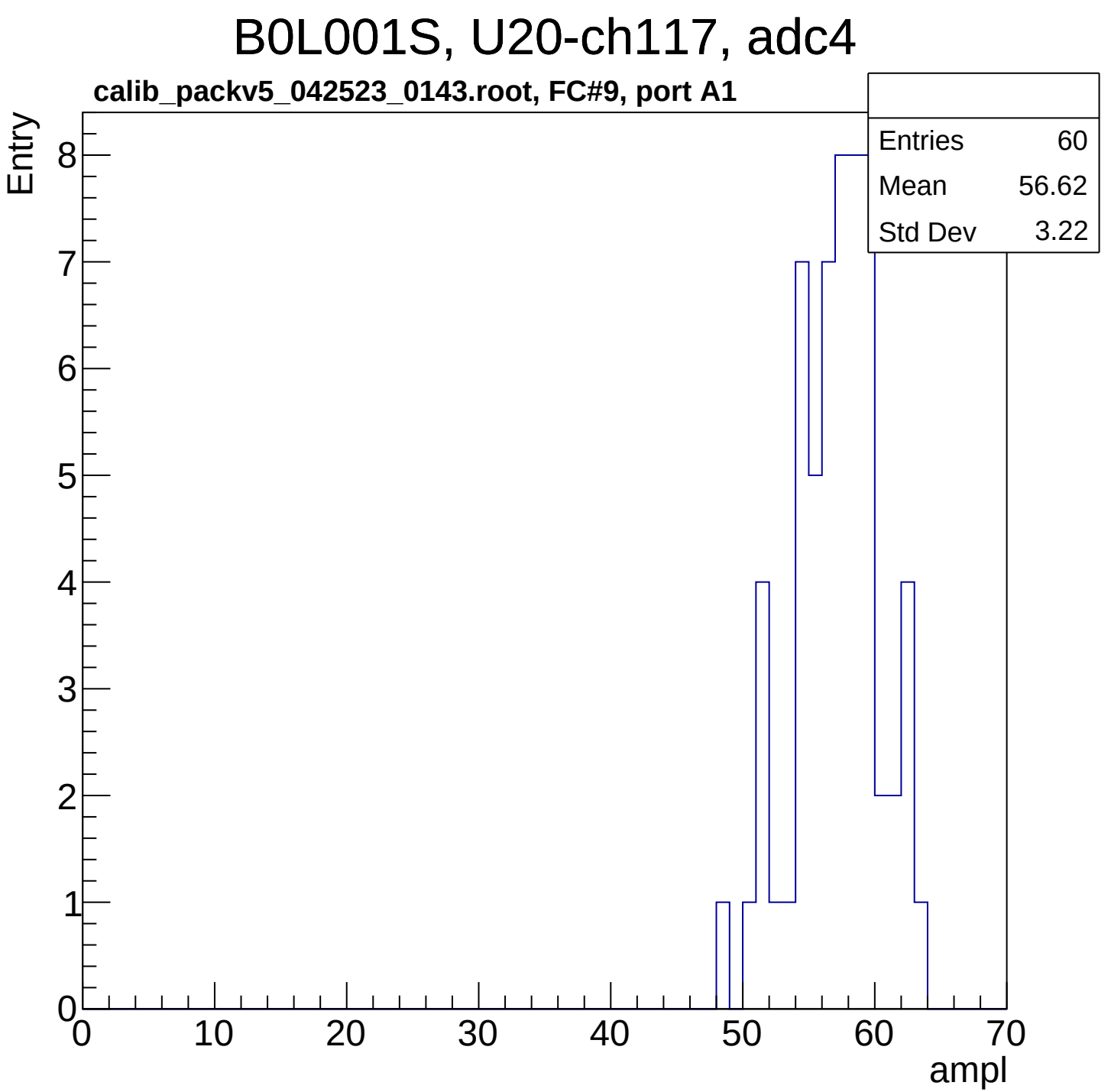
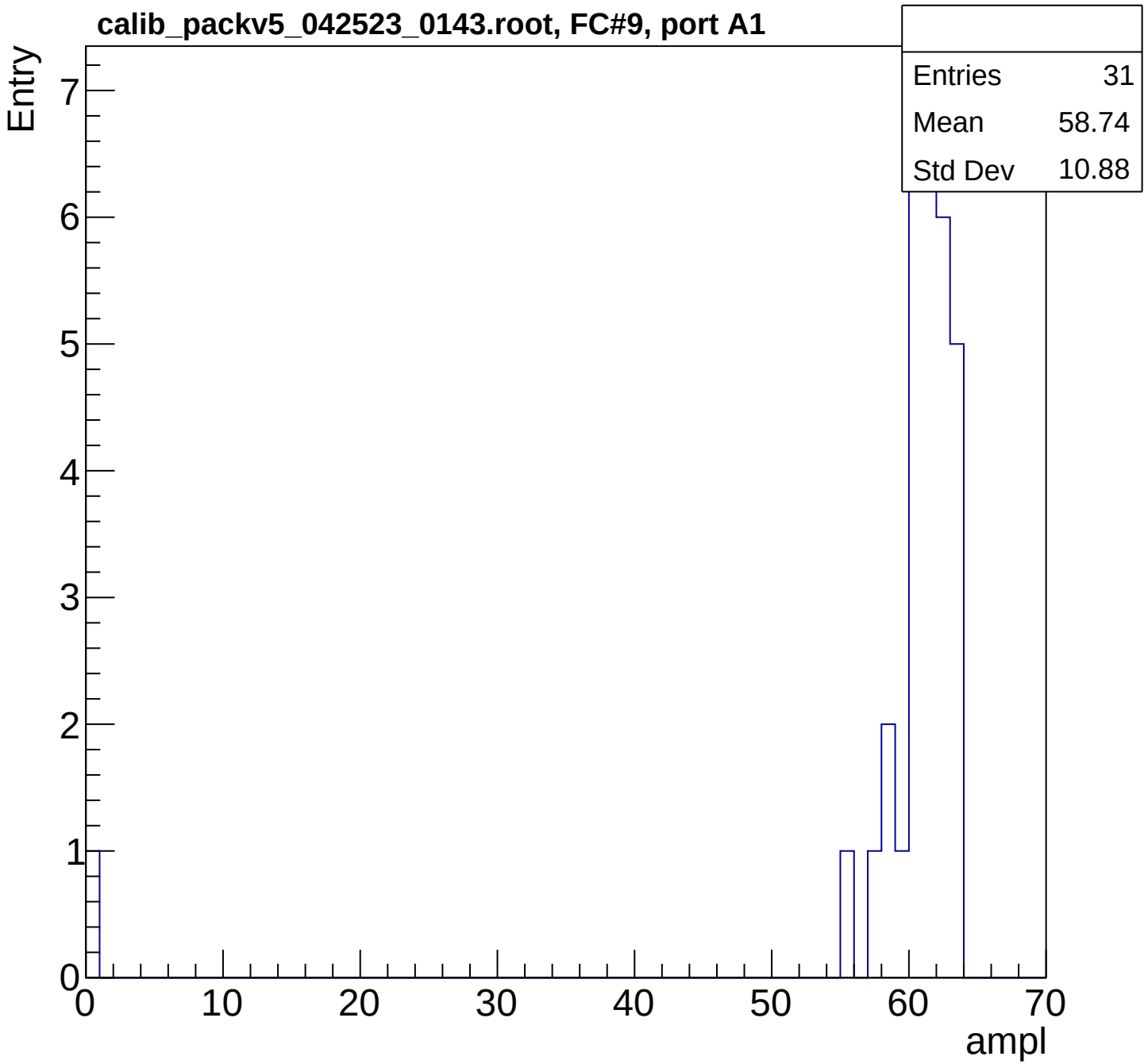
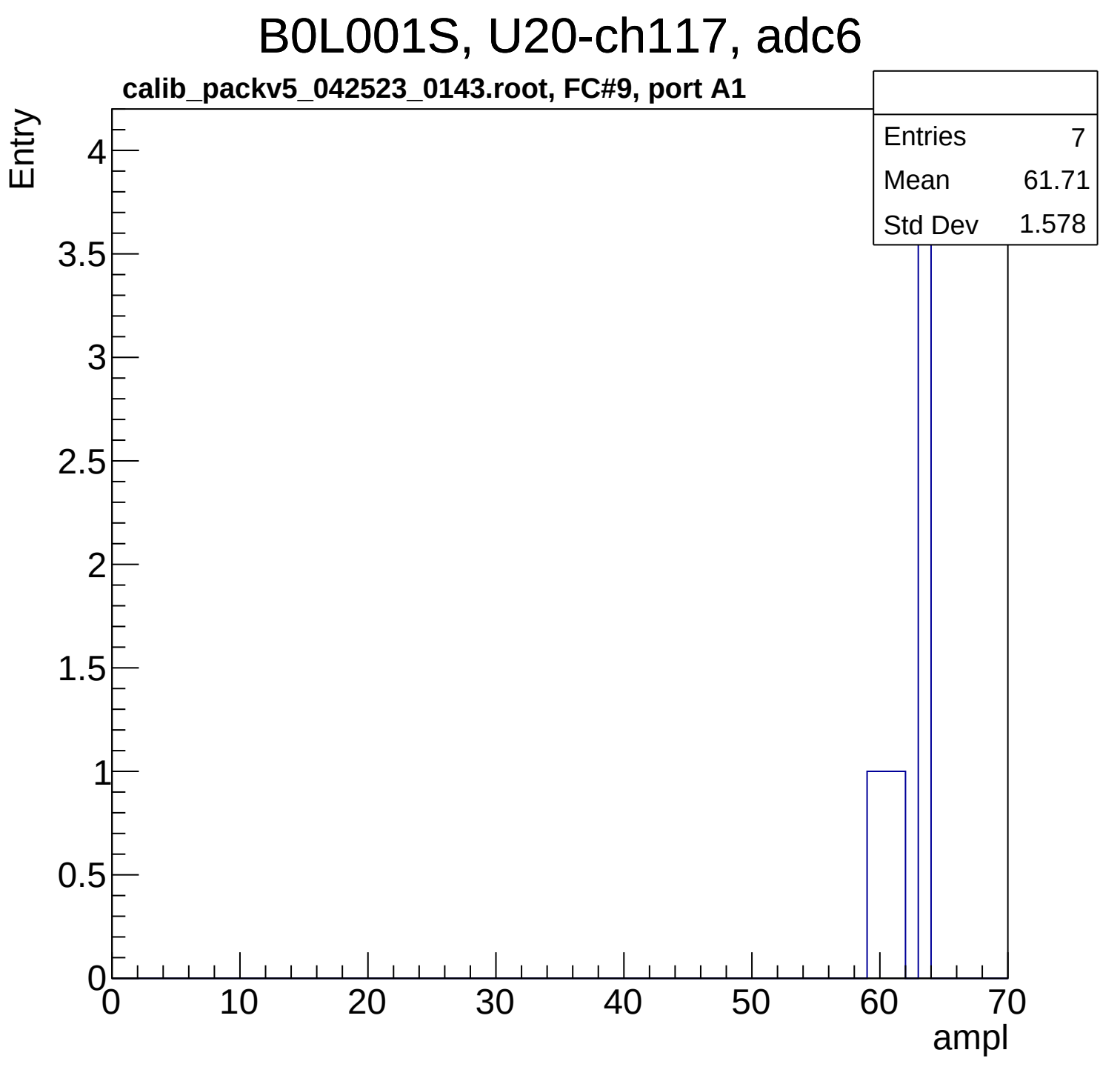

B0L001S, U20-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.62
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.74
Std Dev 10.88
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.71
Std Dev 1.578
Entry
ampl
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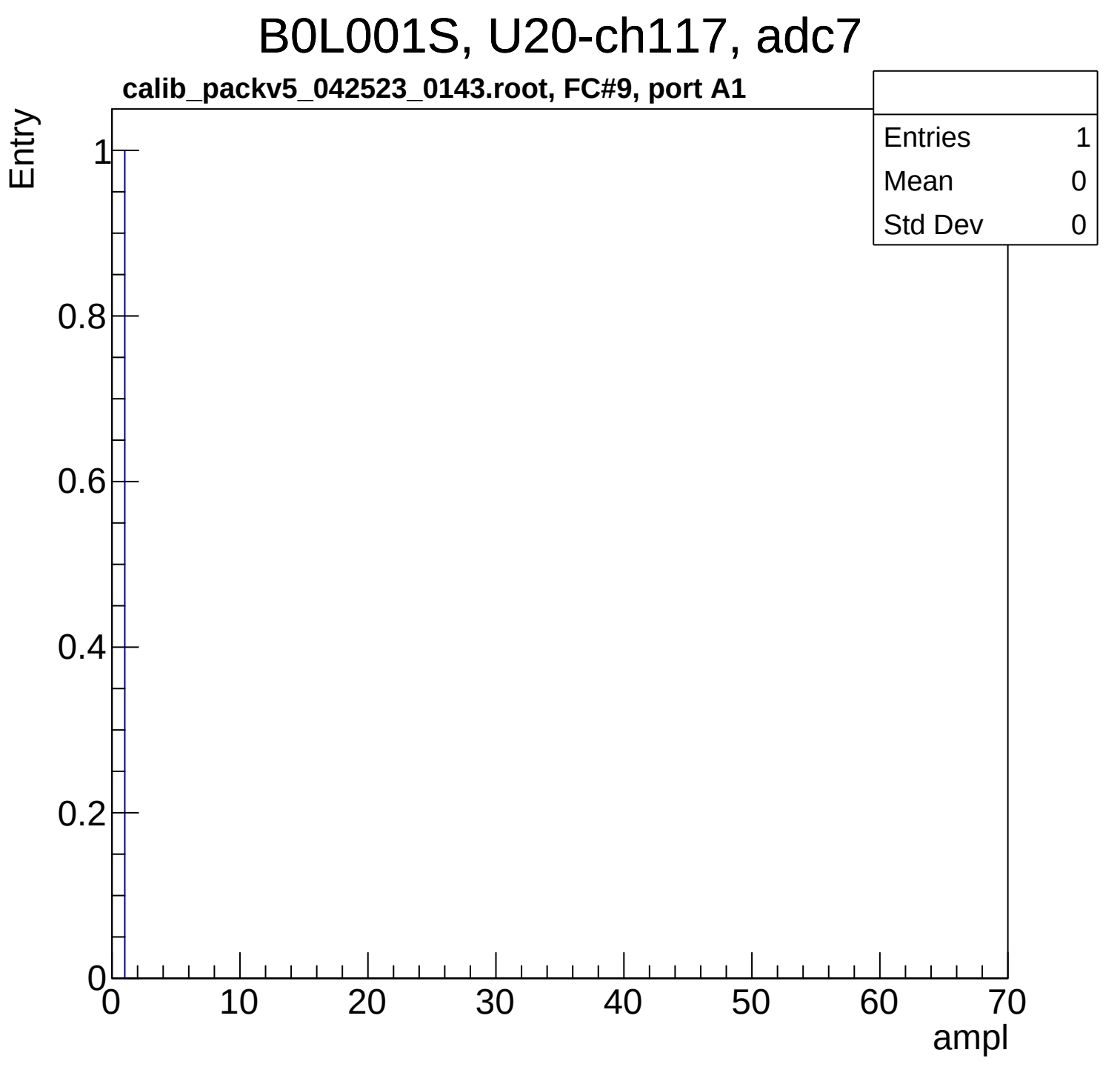
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U20-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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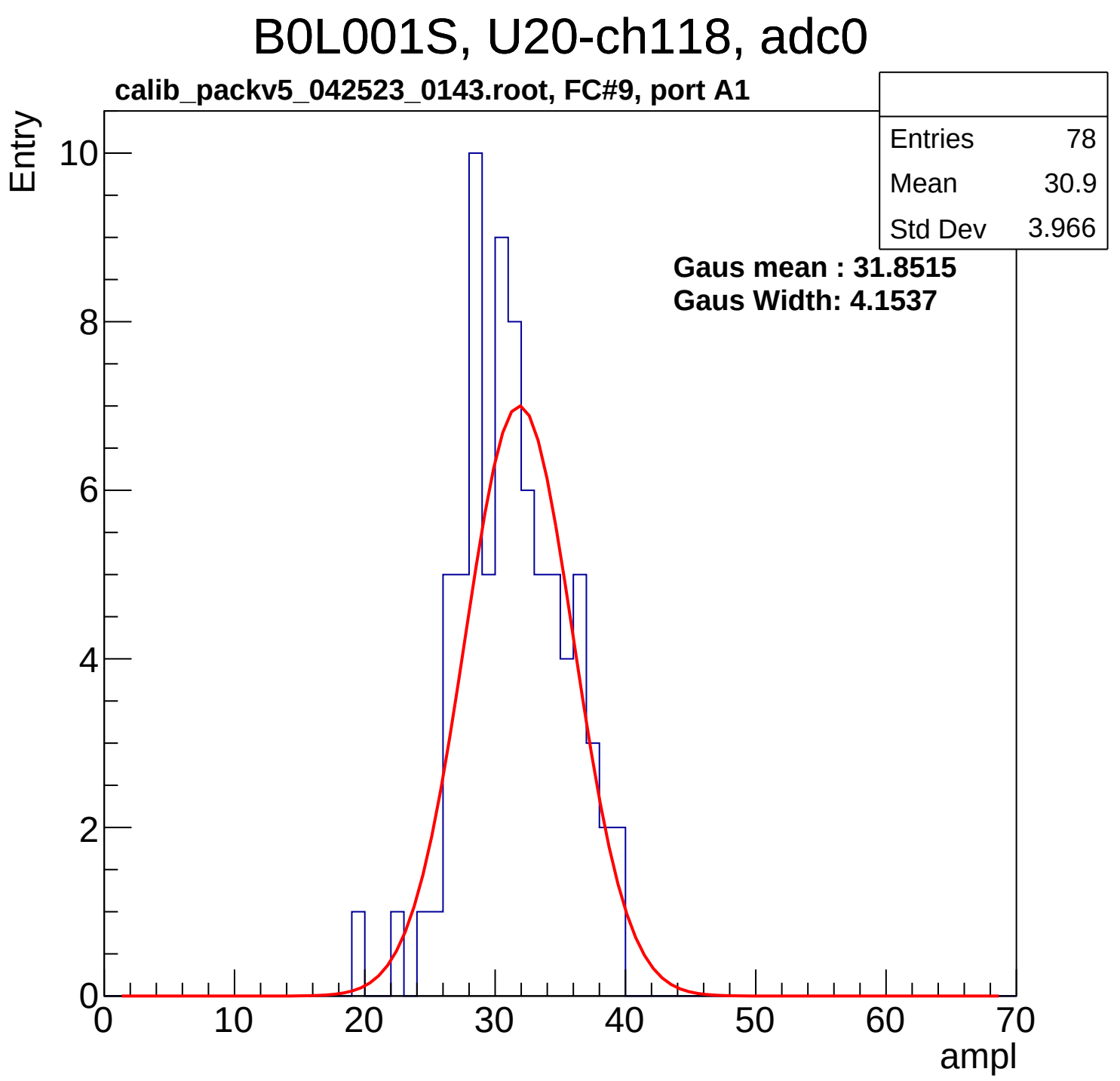
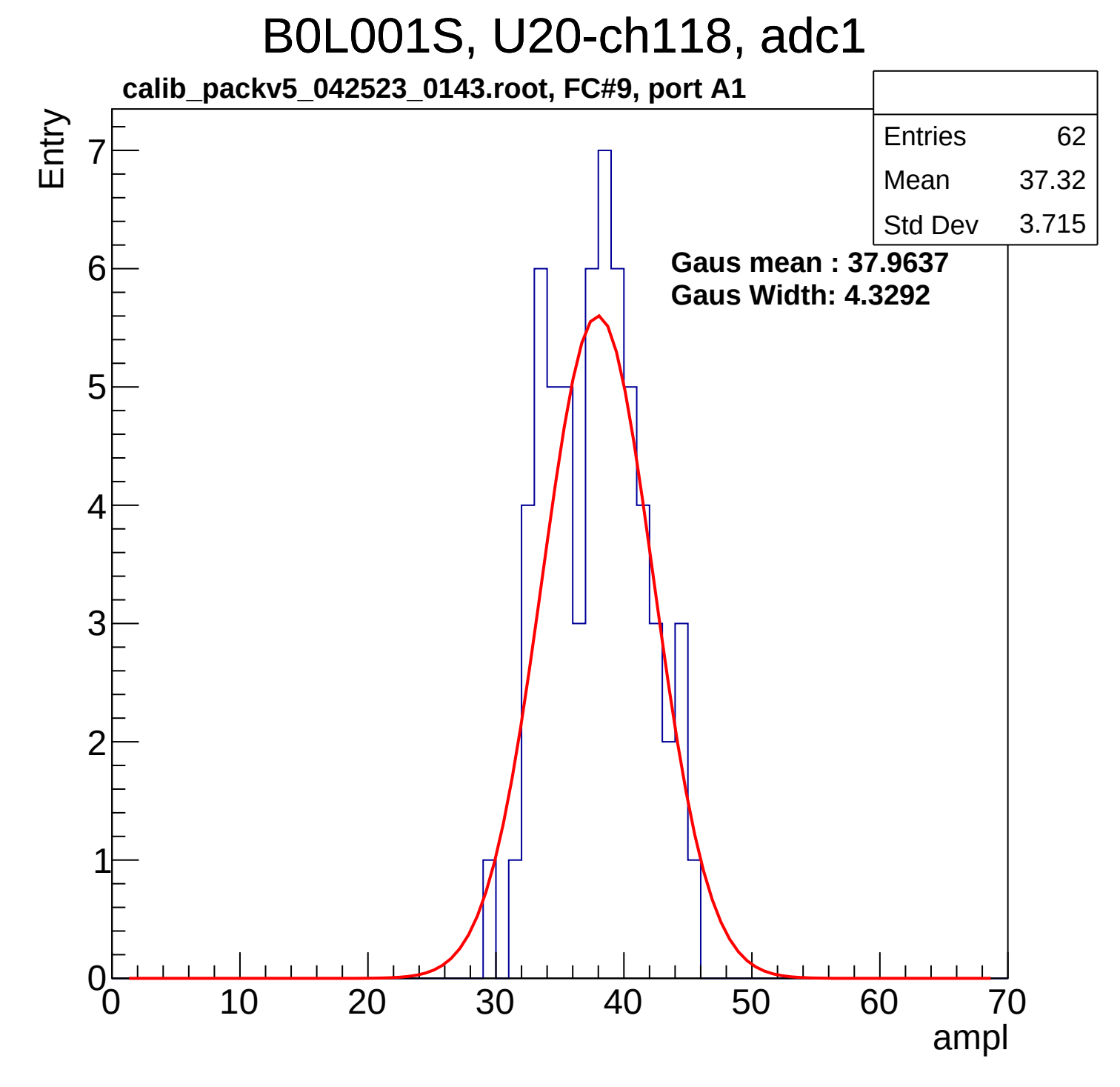
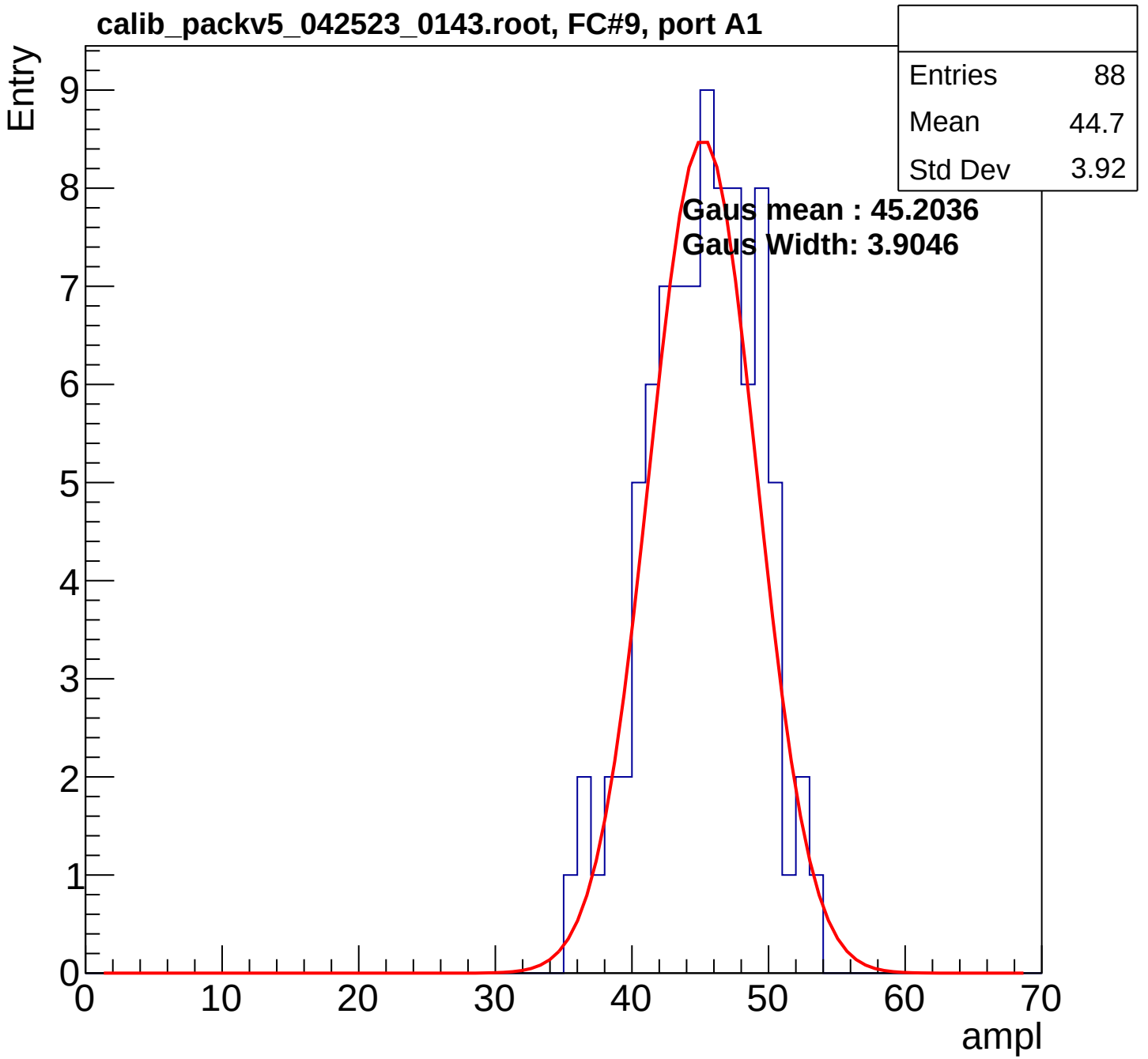
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.9
Std Dev 3.966
Gaus mean : 31.8515
Gaus Width: 4.1537
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.32
Std Dev 3.715
Gaus mean : 37.9637
Gaus Width: 4.3292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 44.7
Std Dev 3.92
Gaus mean : 45.2036
Gaus Width: 3.9046
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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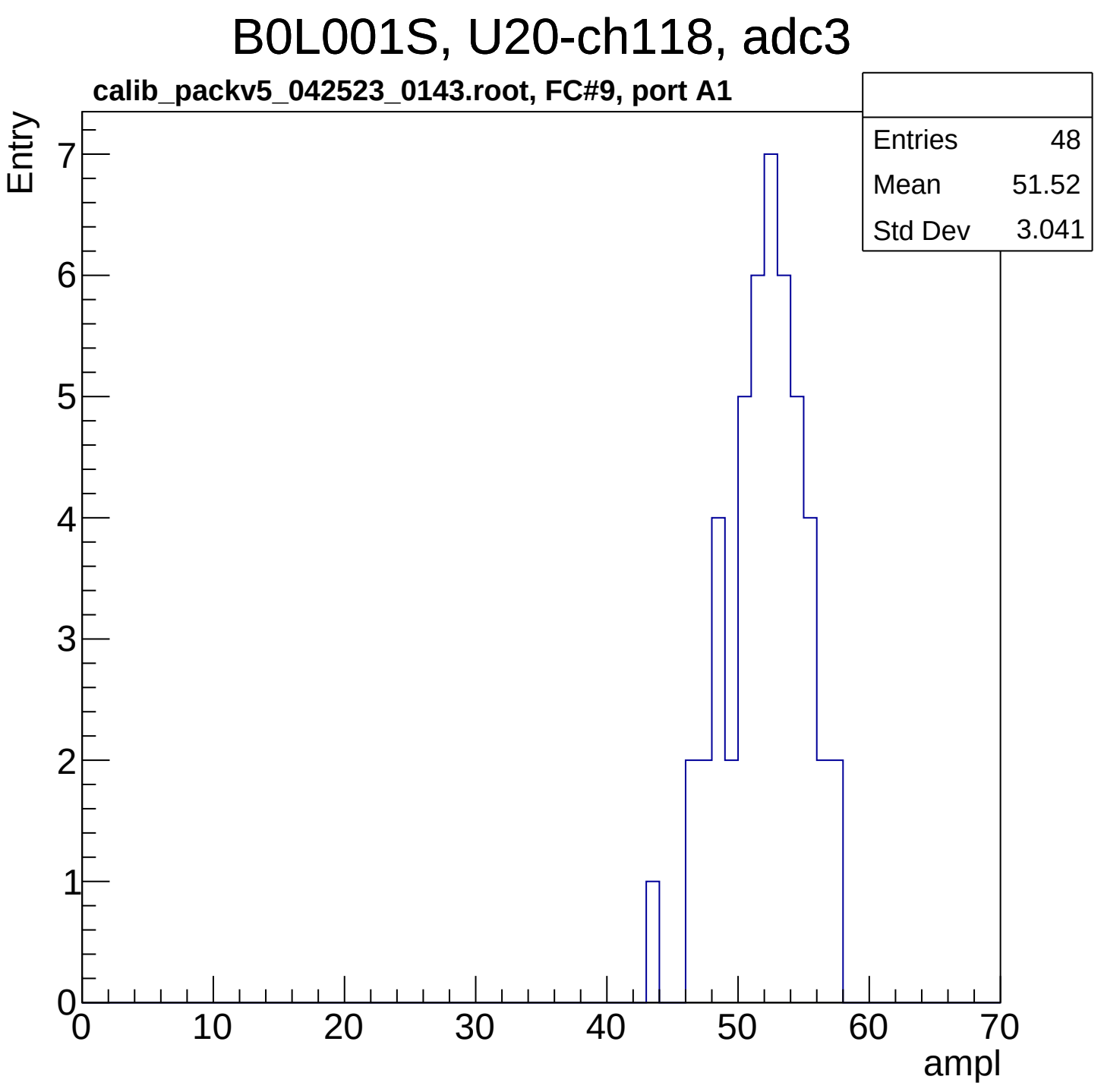
B0L001S, U20-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 51.52
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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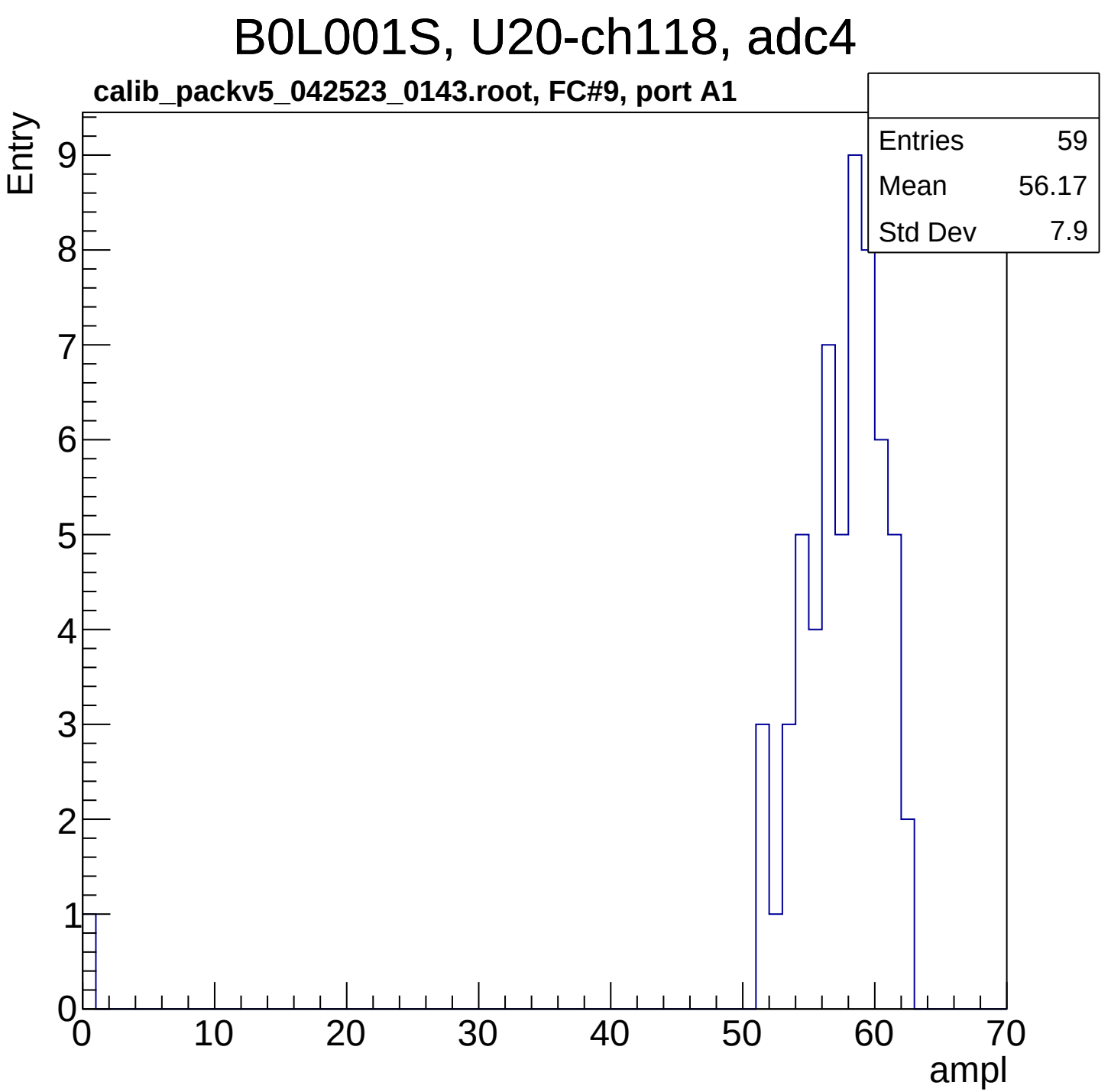
B0L001S, U20-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.17
Std Dev 7.9
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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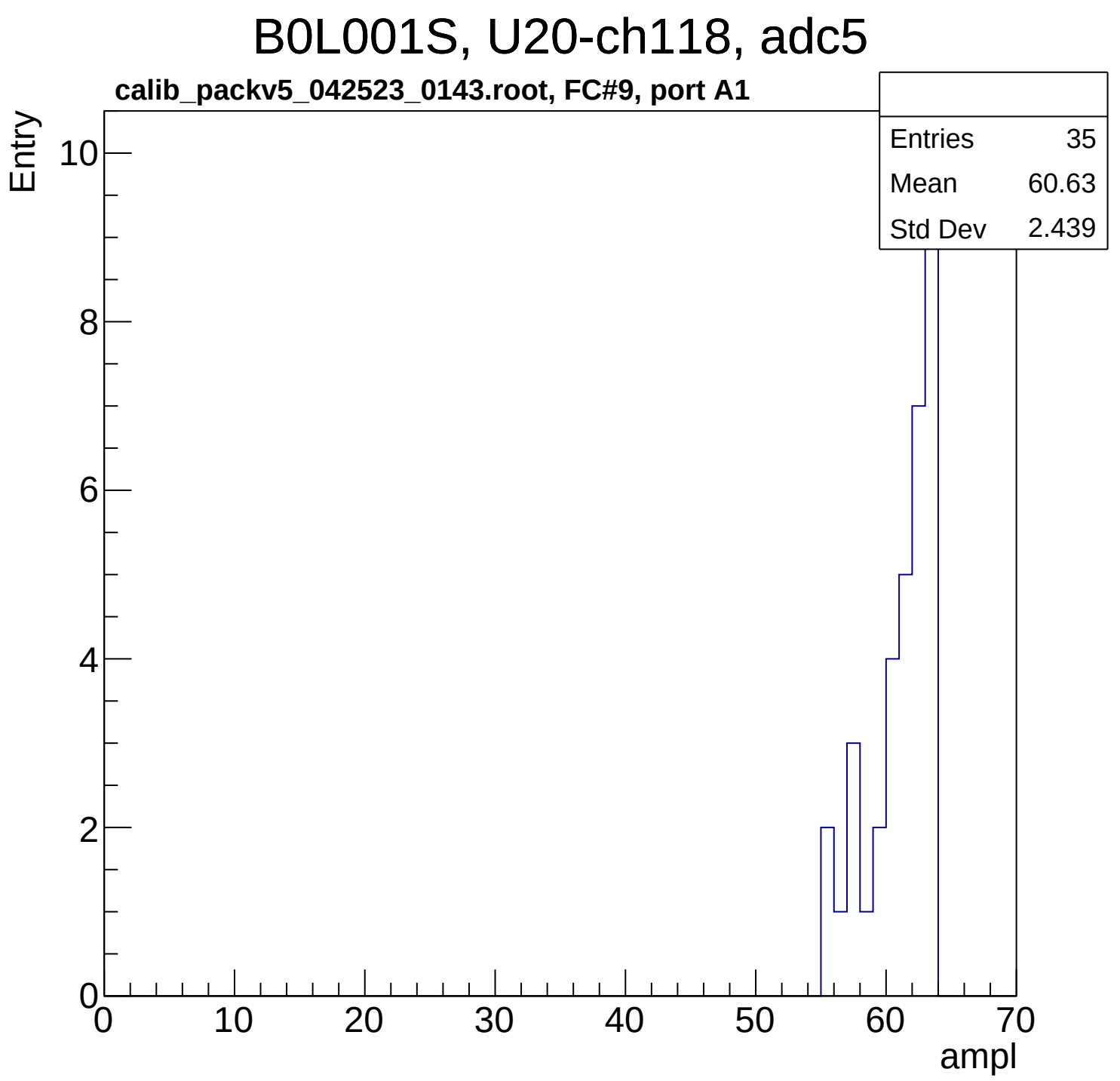
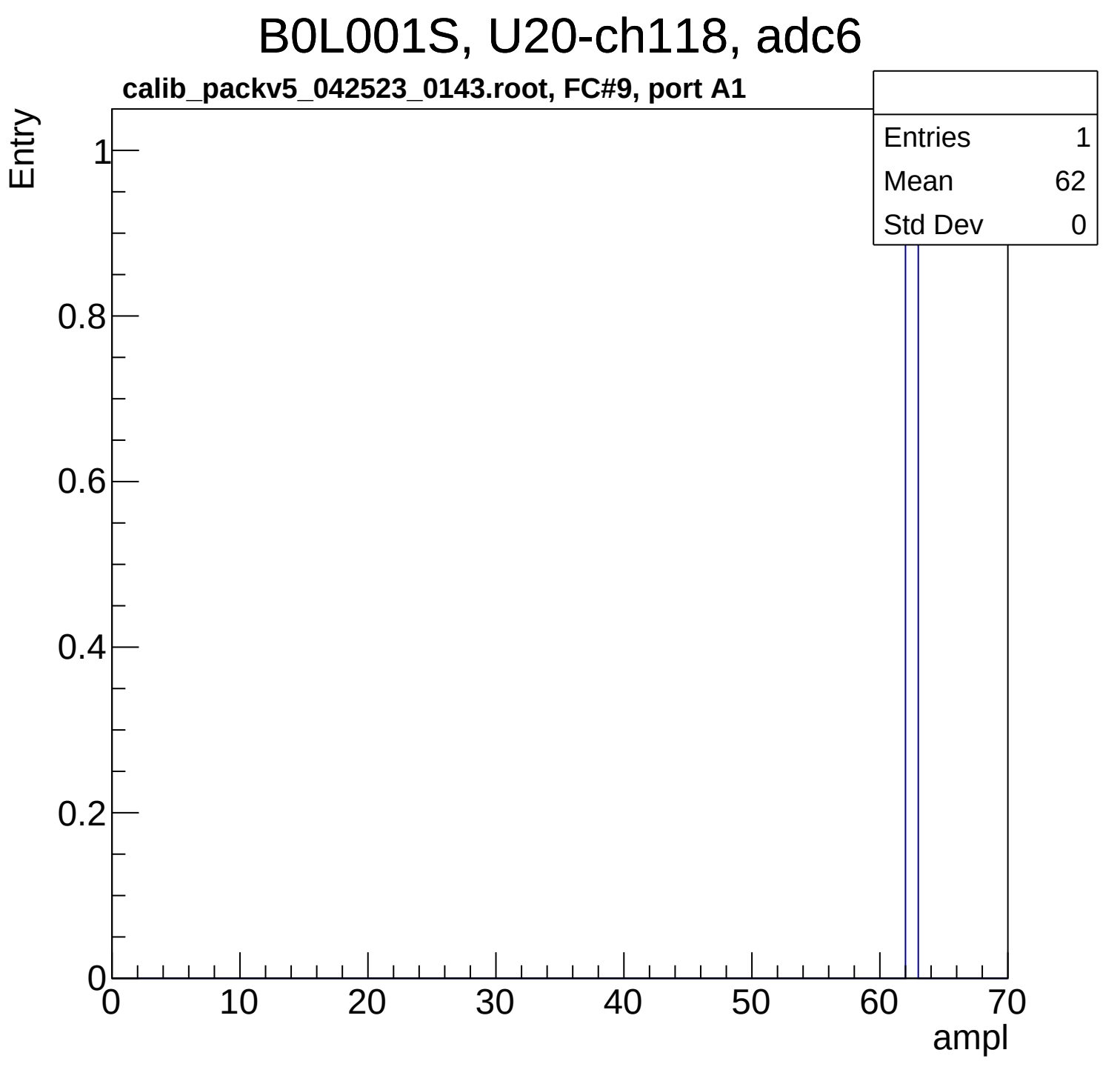
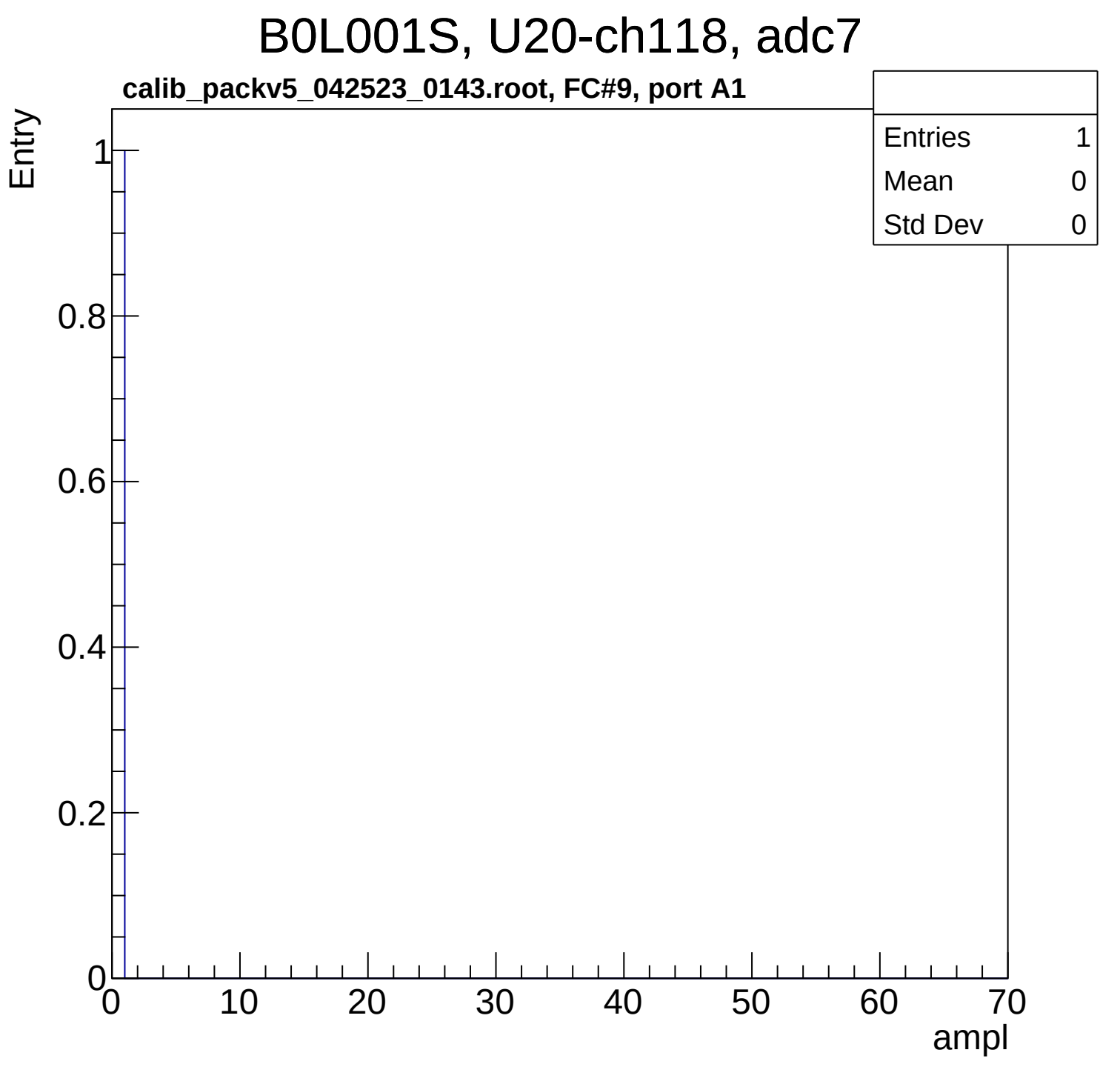
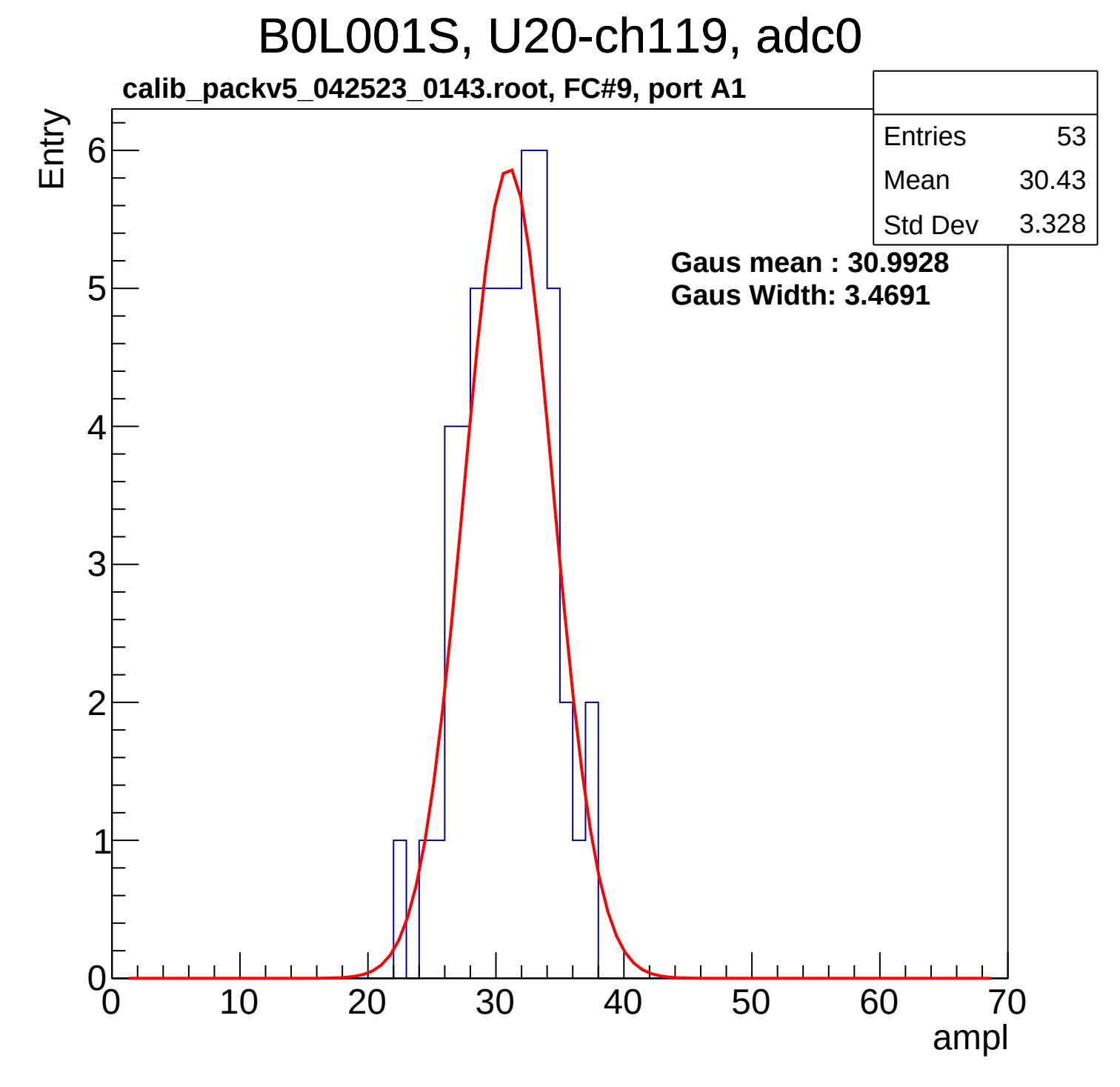
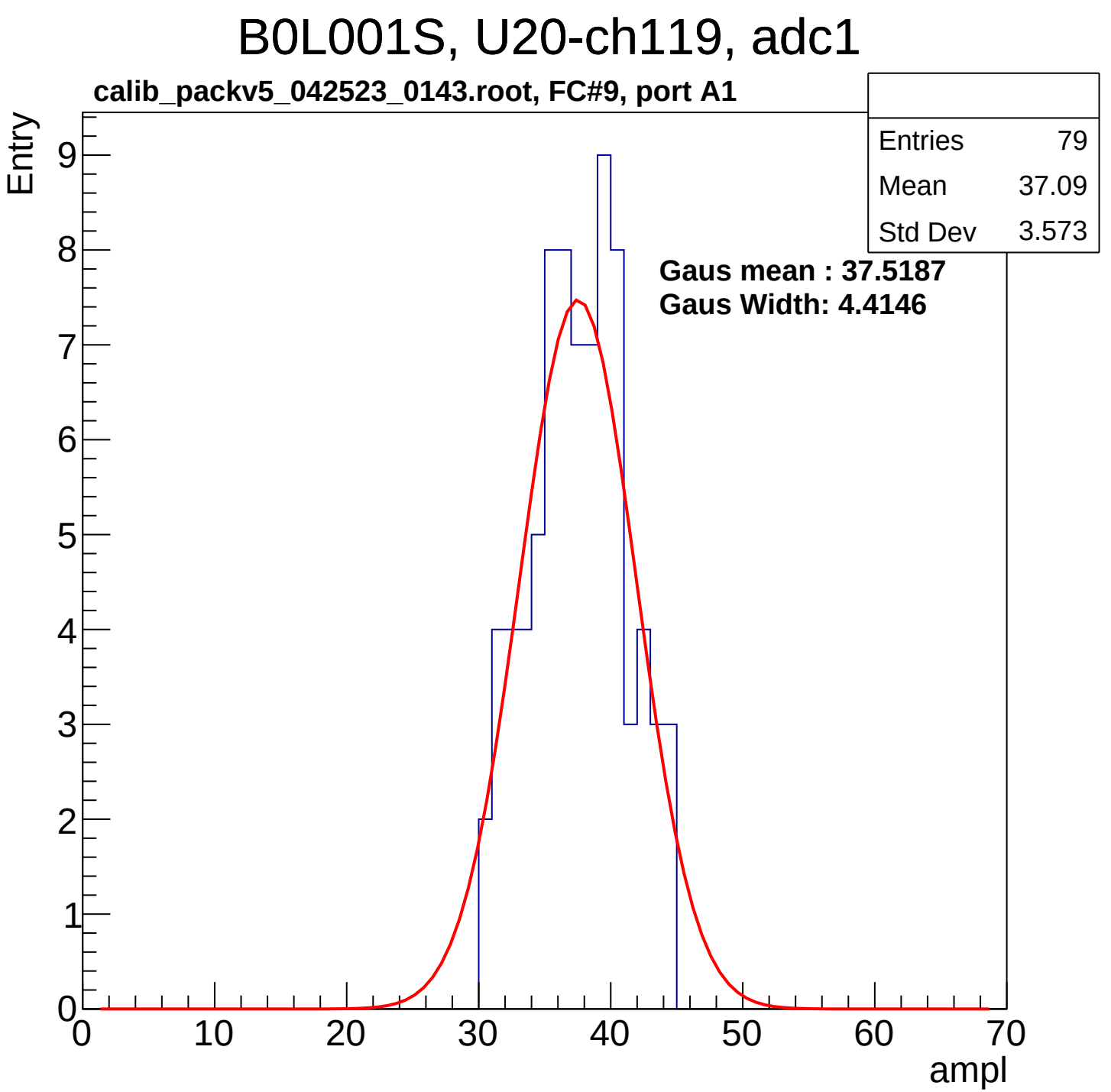

B0L001S, U20-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.63
Std Dev 2.439
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 30.43
Std Dev 3.328
Gaus mean : 30.9928
Gaus Width: 3.4691
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U20-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.09
Std Dev 3.573
Gaus mean : 37.5187
Gaus Width: 4.4146
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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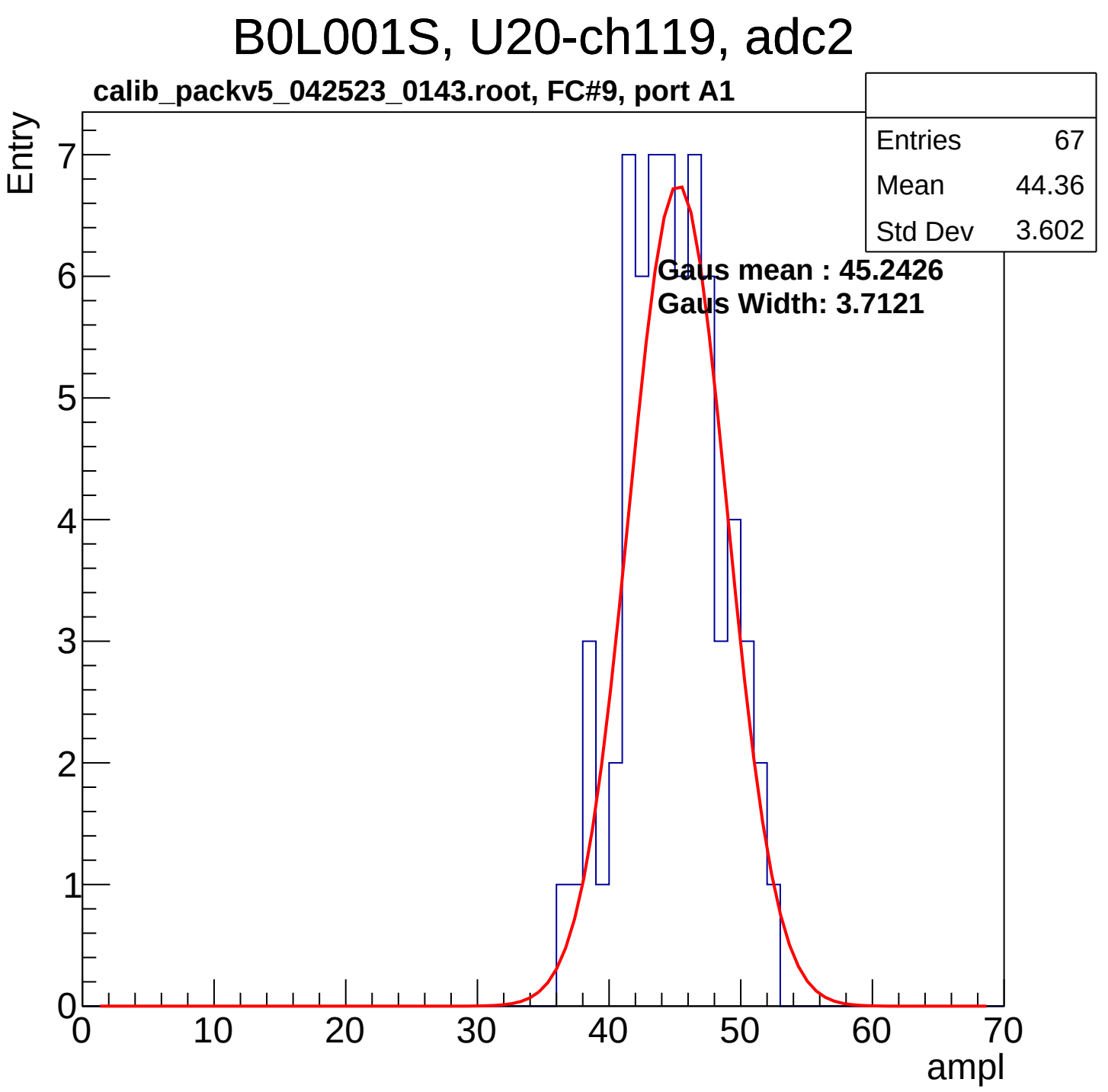
B0L001S, U20-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.36
Std Dev 3.602
Gaus mean : 45.2426
Gaus Width: 3.7121
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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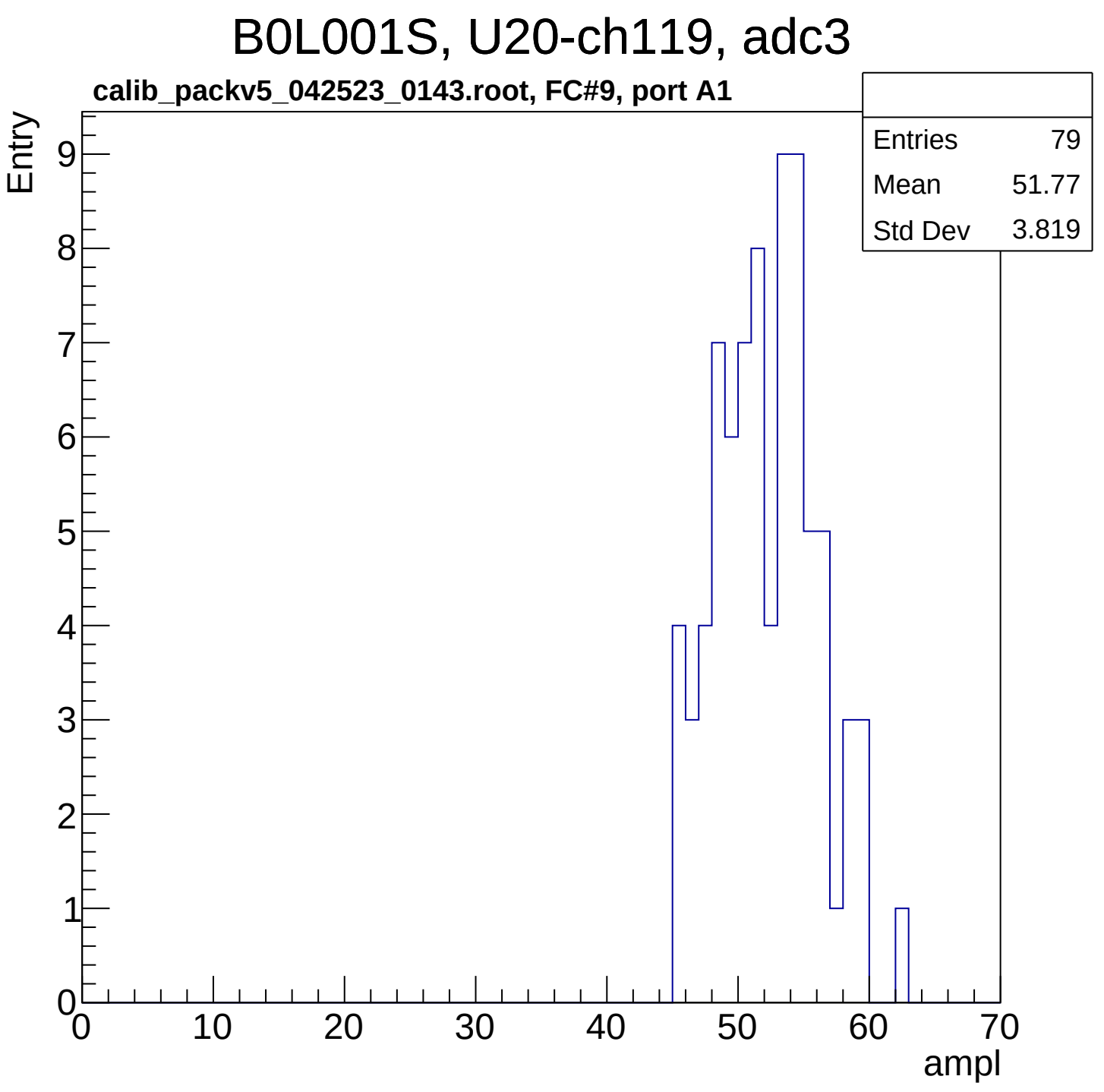
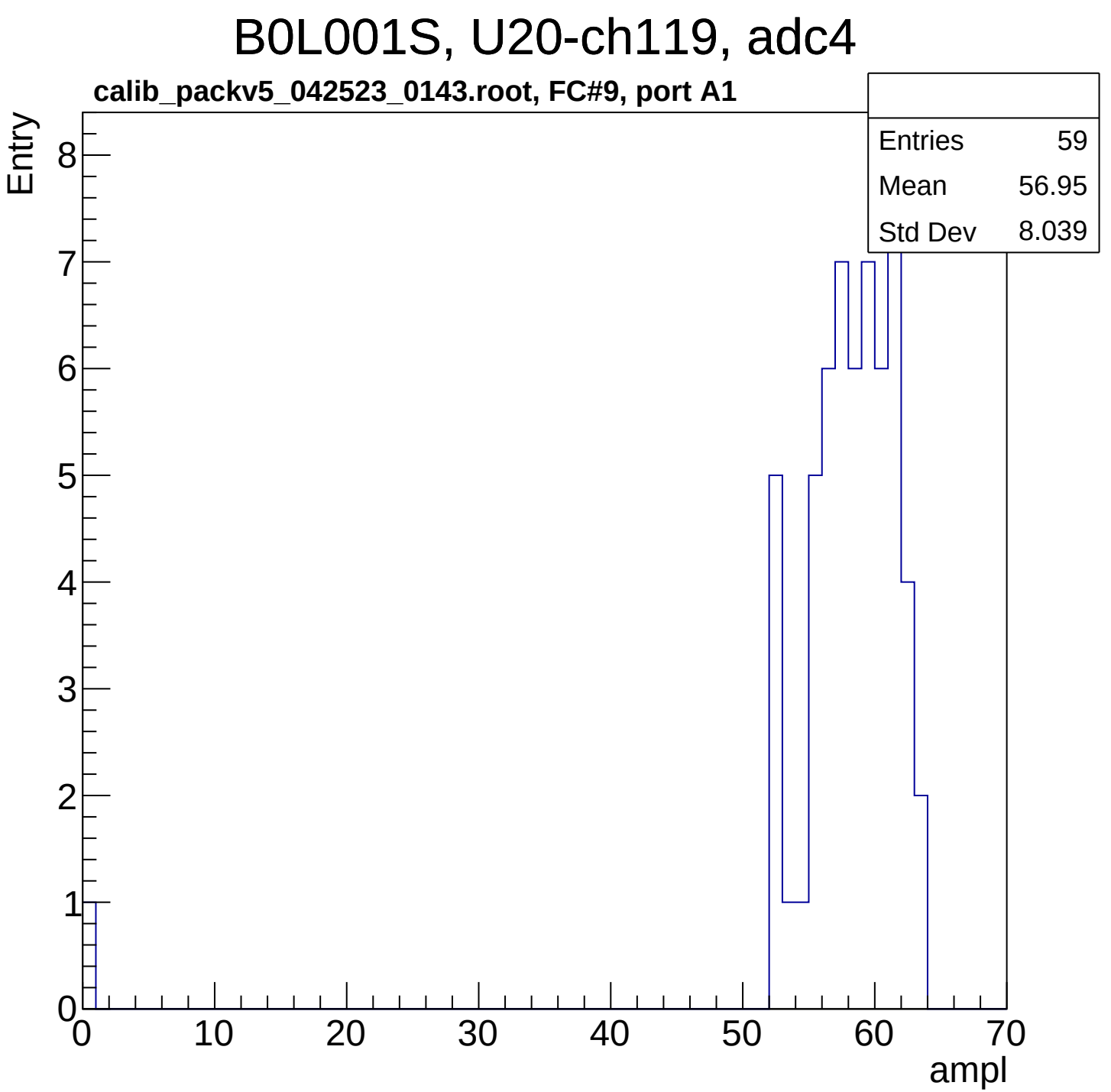

B0L001S, U20-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 51.77
Std Dev 3.819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.95
Std Dev 8.039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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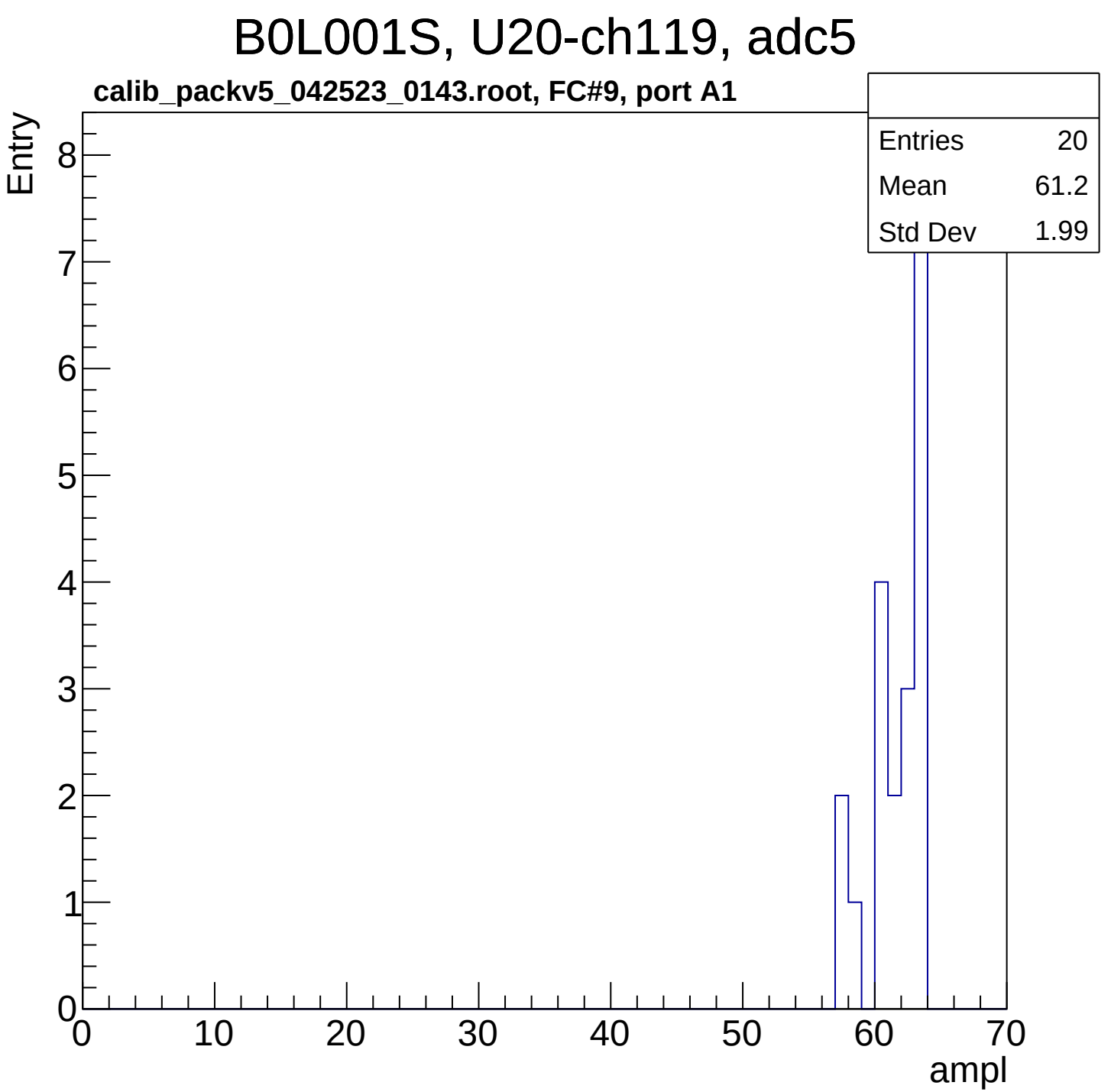
B0L001S, U20-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 61.2
Std Dev 1.99
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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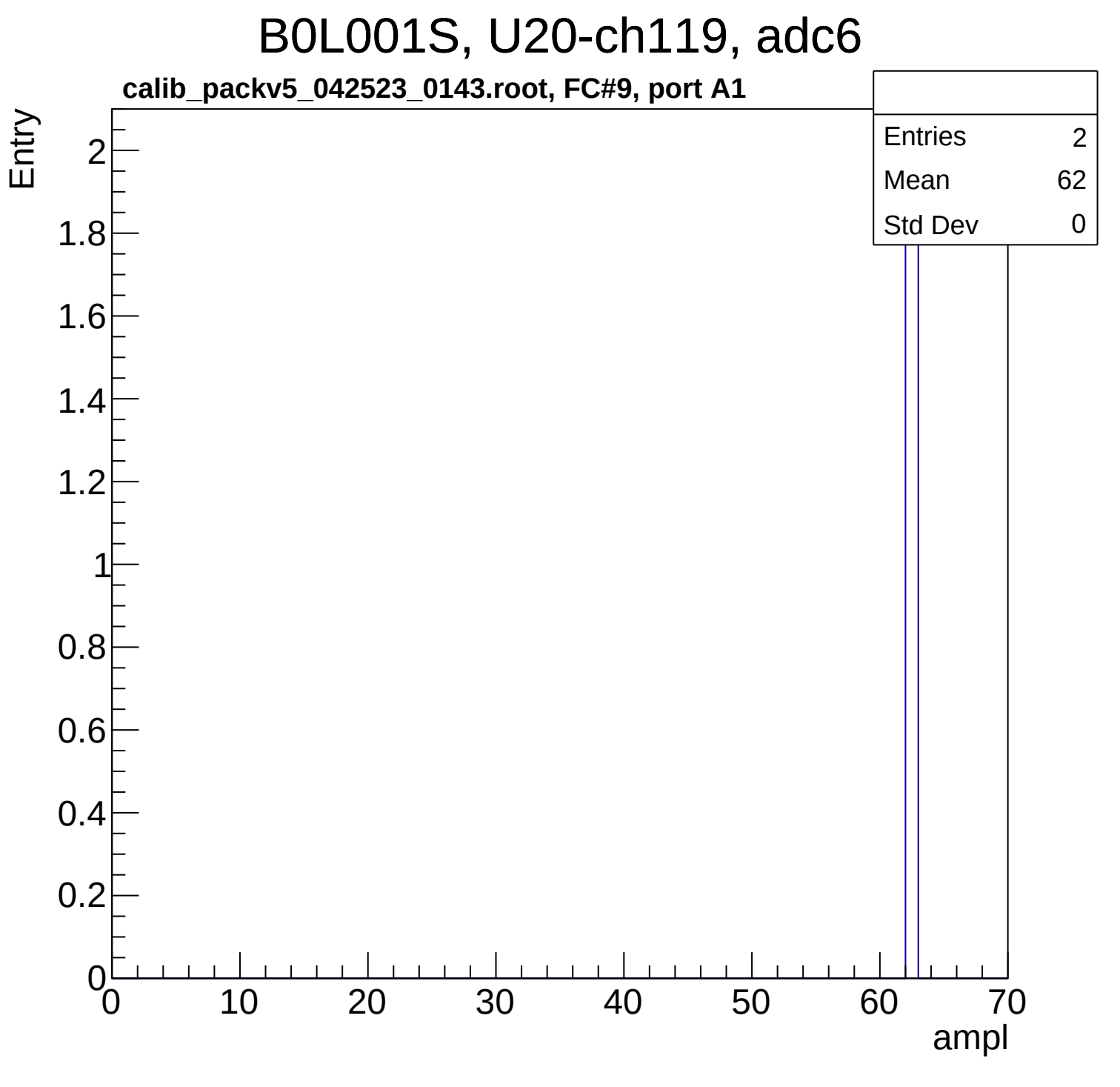
B0L001S, U20-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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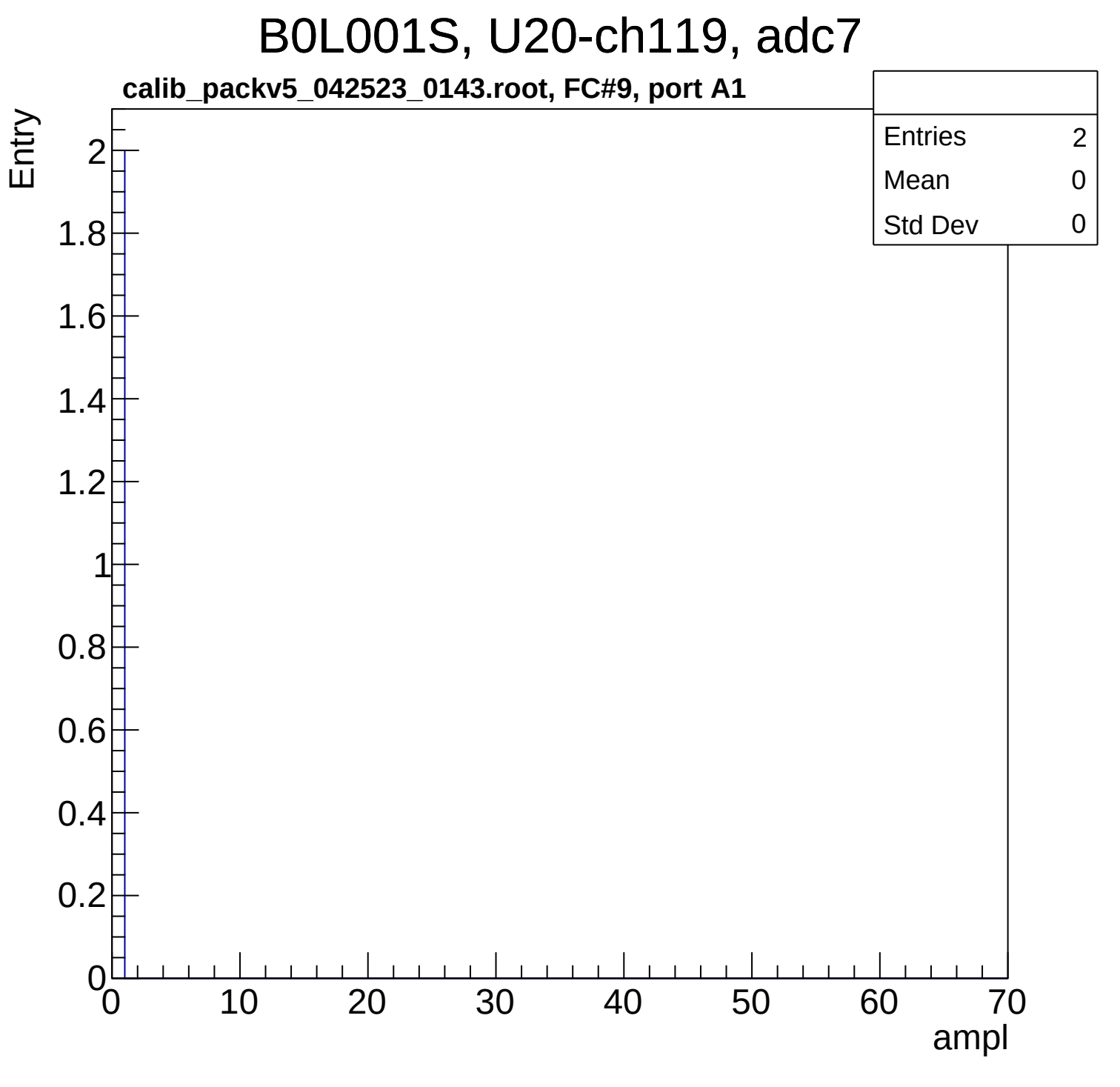
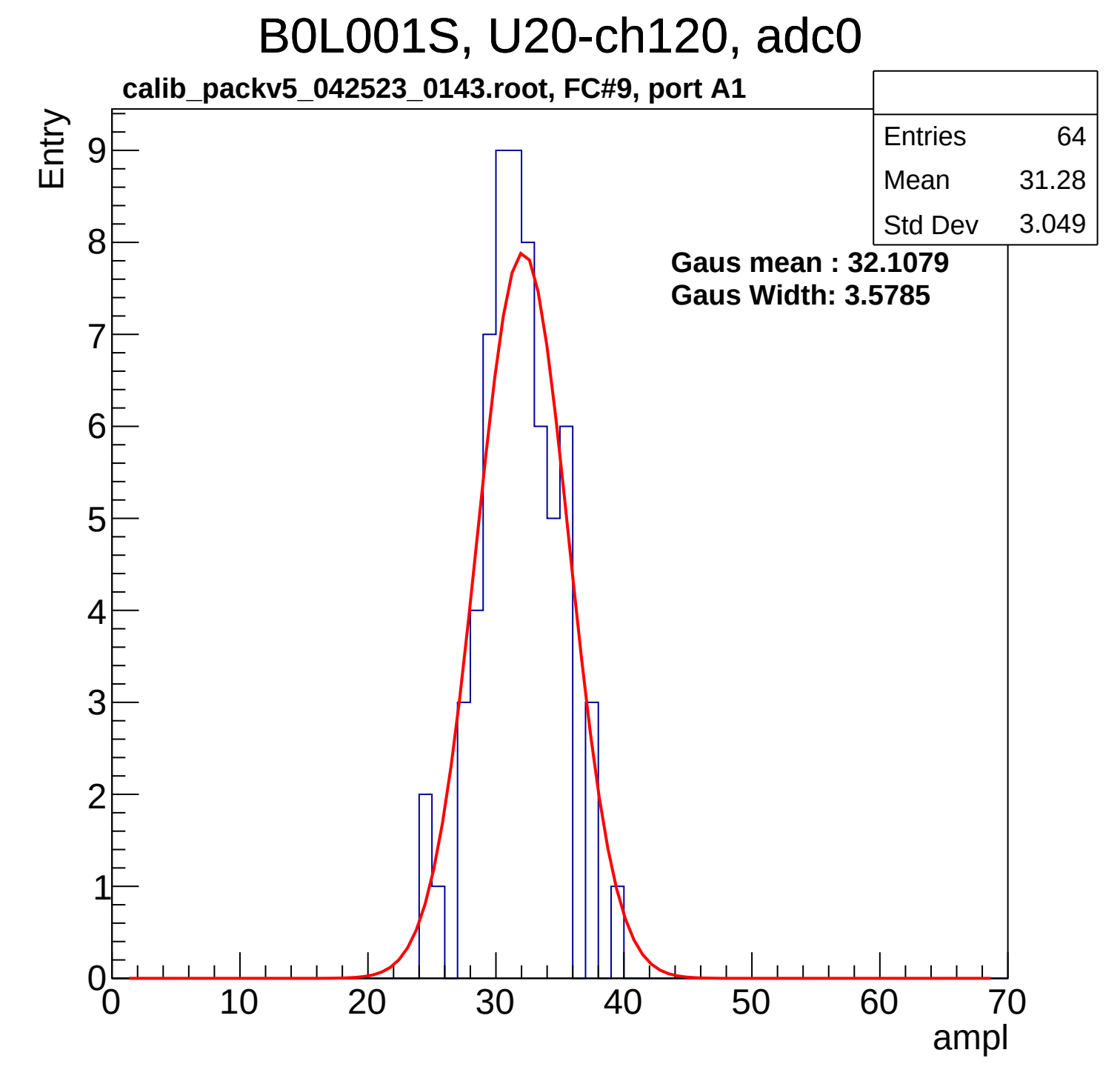

B0L001S, U20-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 31.28
Std Dev 3.049
Gaus mean : 32.1079
Gaus Width: 3.5785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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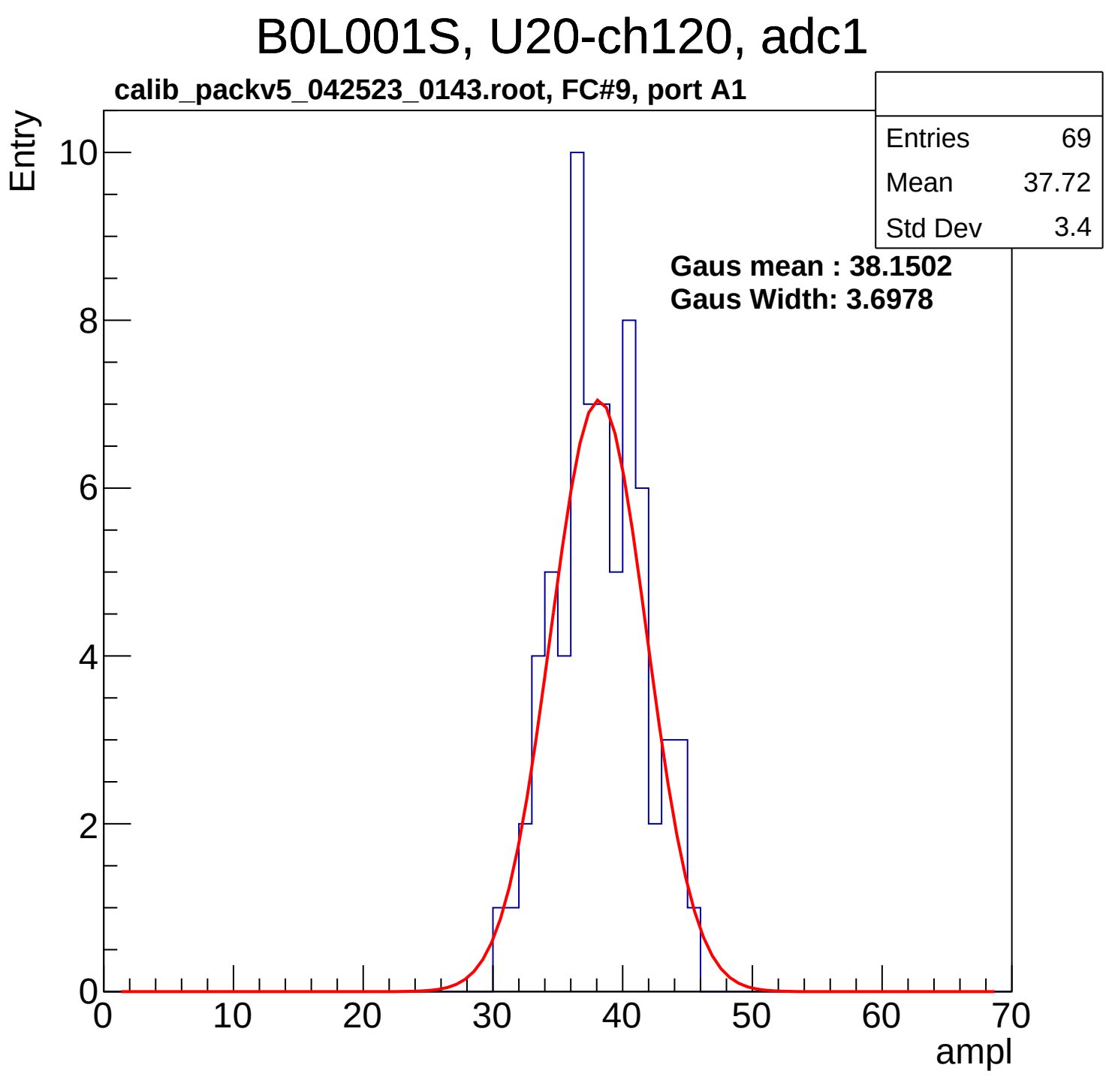
B0L001S, U20-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.72
Std Dev 3.4
Gaus mean : 38.1502
Gaus Width: 3.6978
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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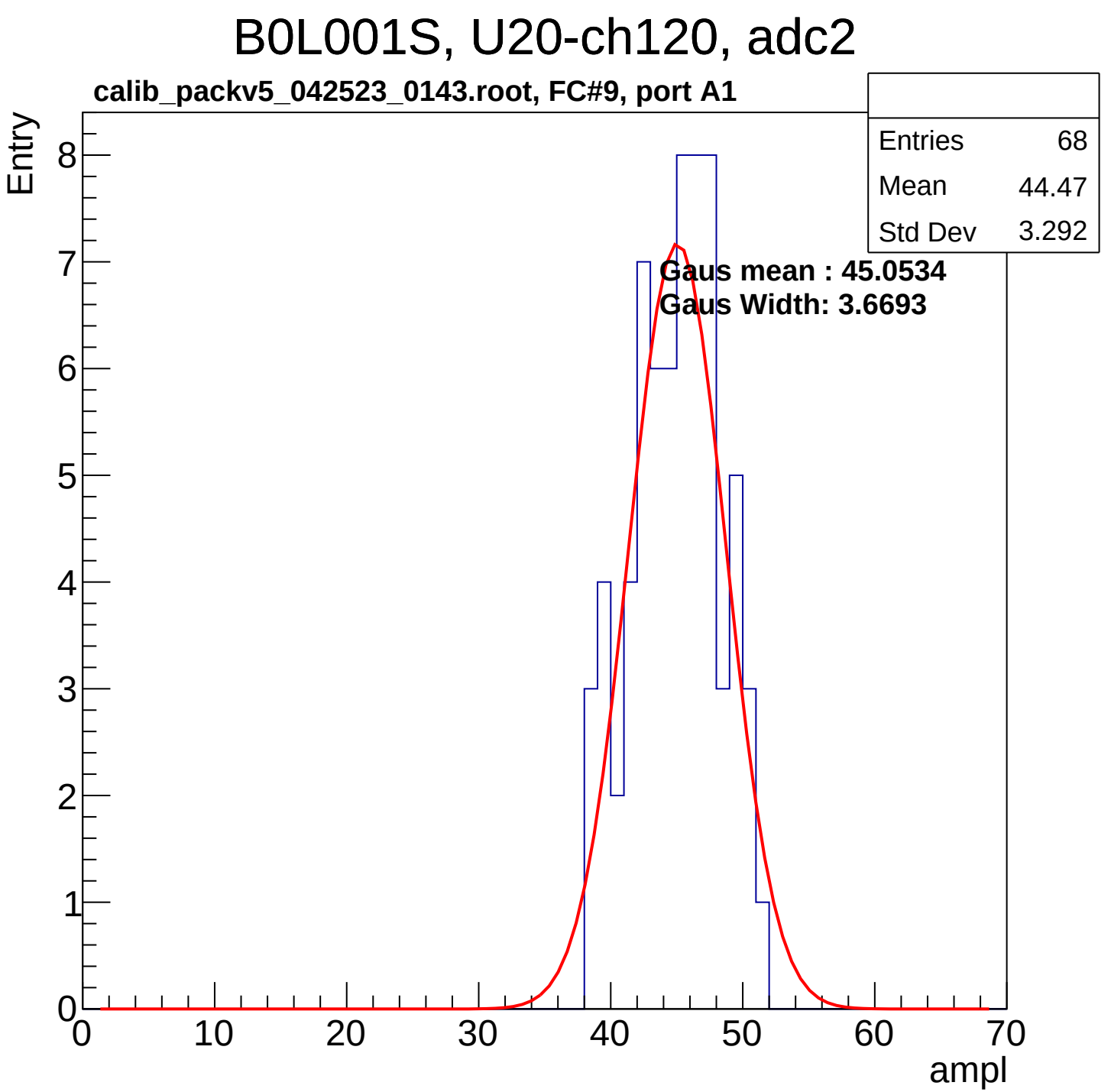
B0L001S, U20-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.47
Std Dev 3.292
Gaus mean : 45.0534
Gaus Width: 3.6693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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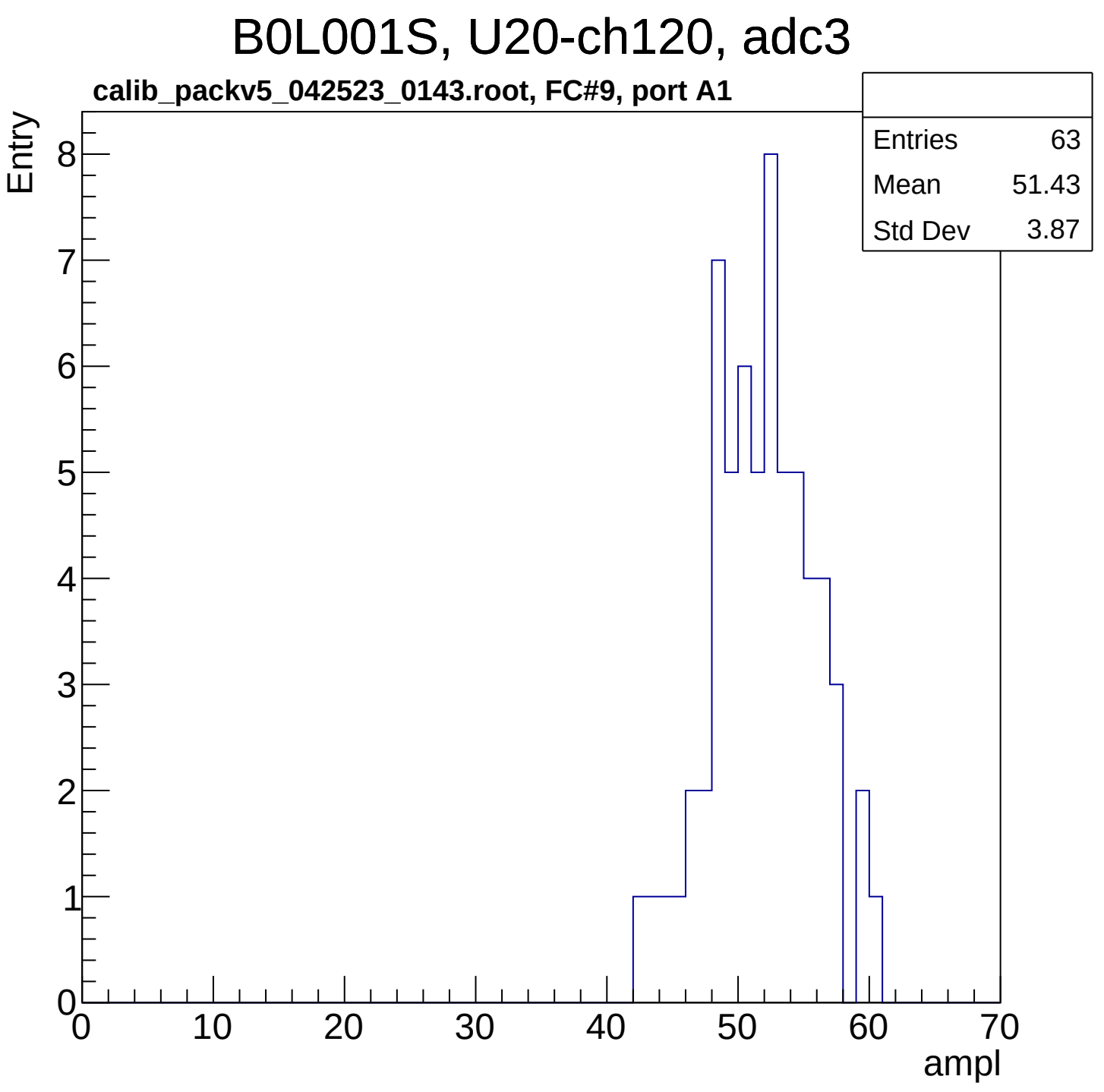
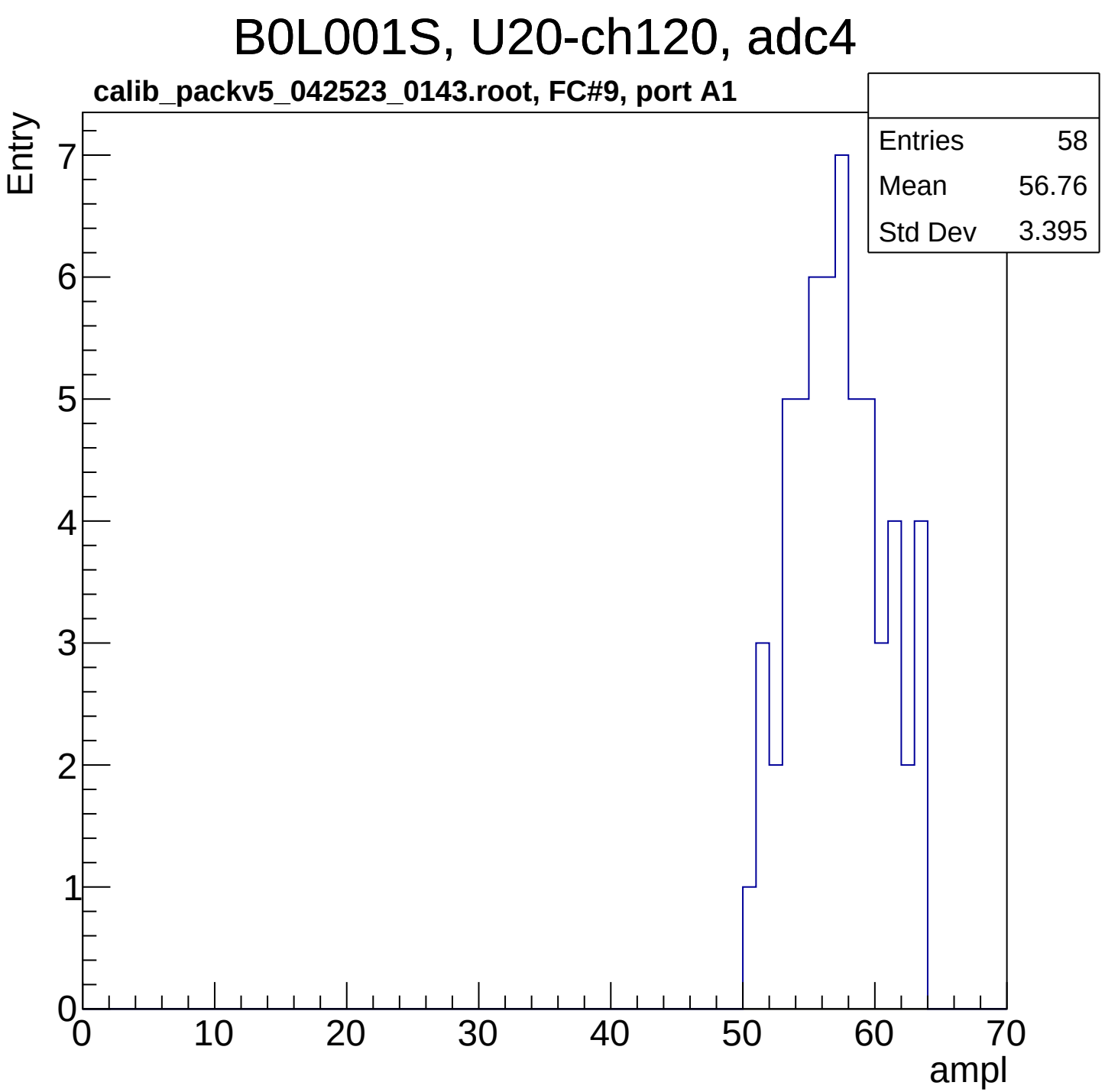
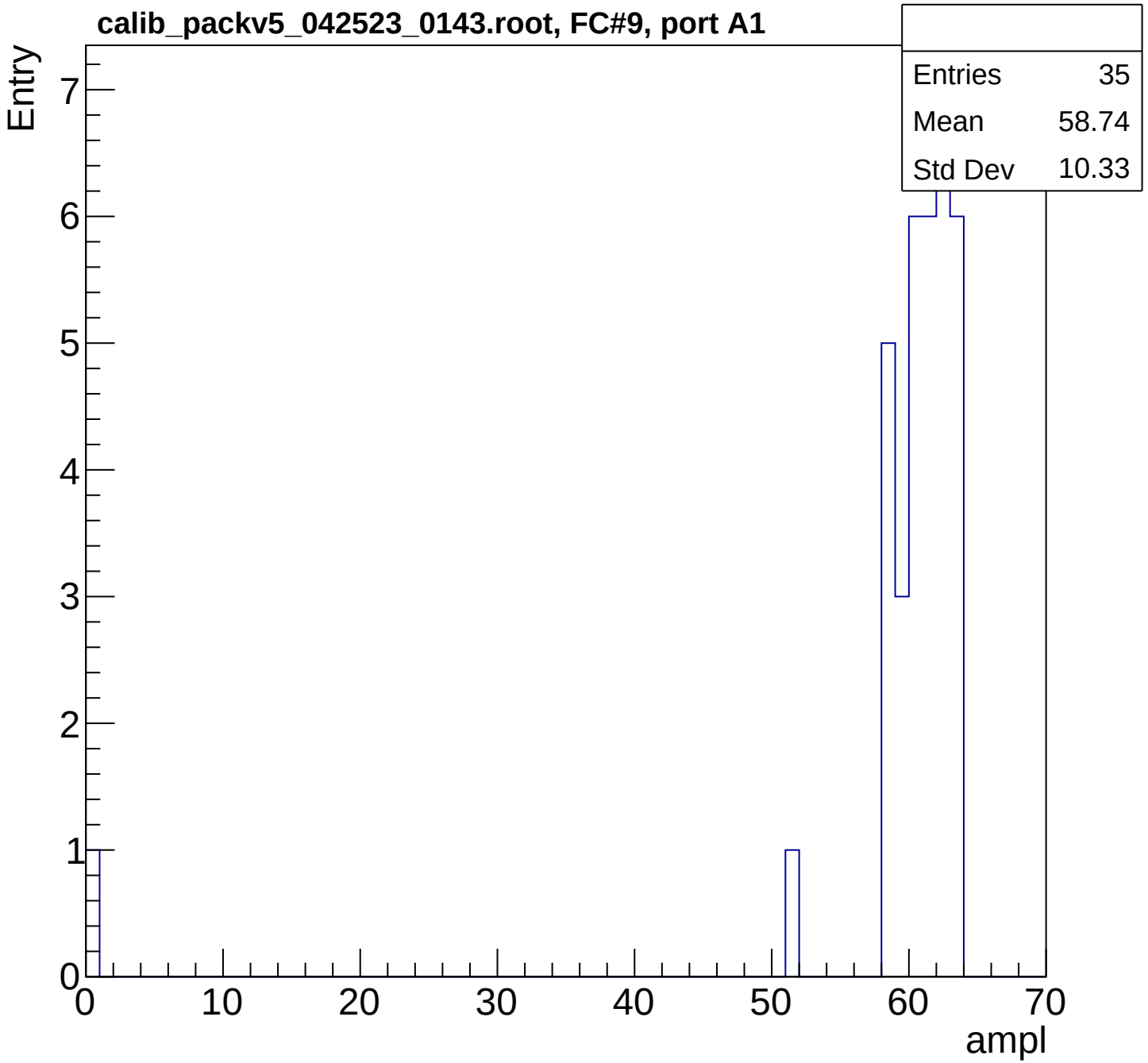

B0L001S, U20-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.43
Std Dev 3.87
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.76
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.74
Std Dev 10.33
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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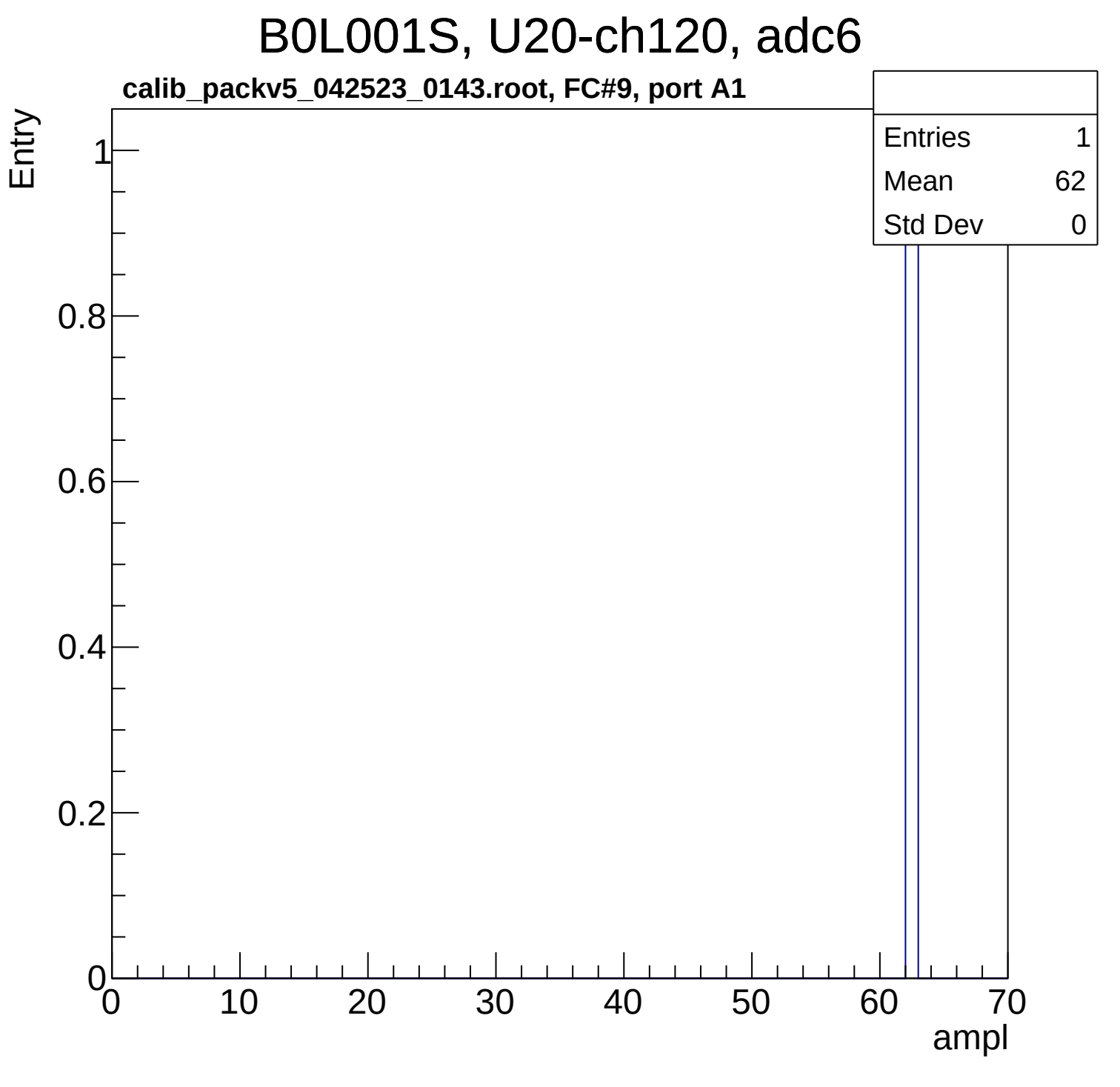
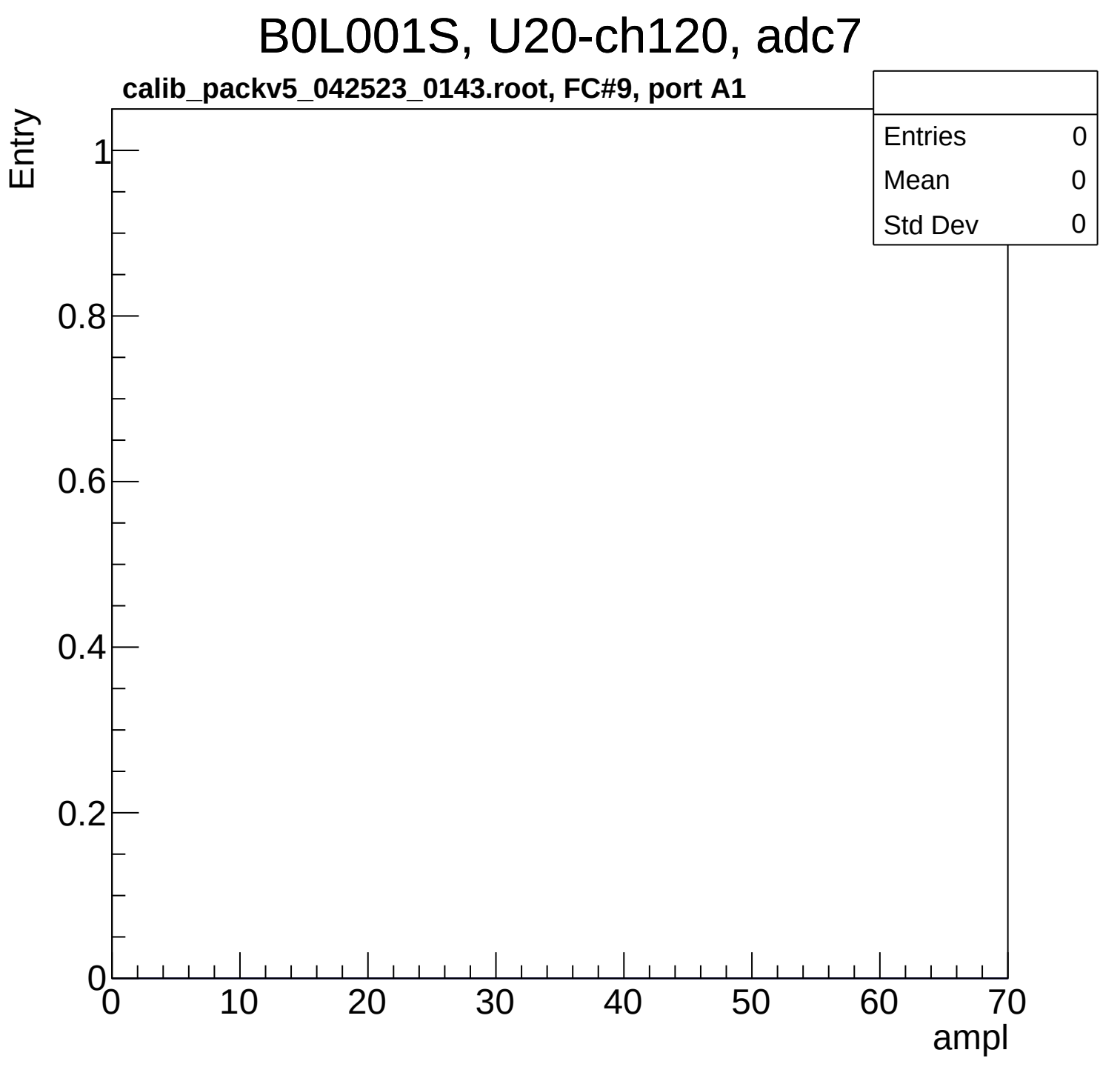
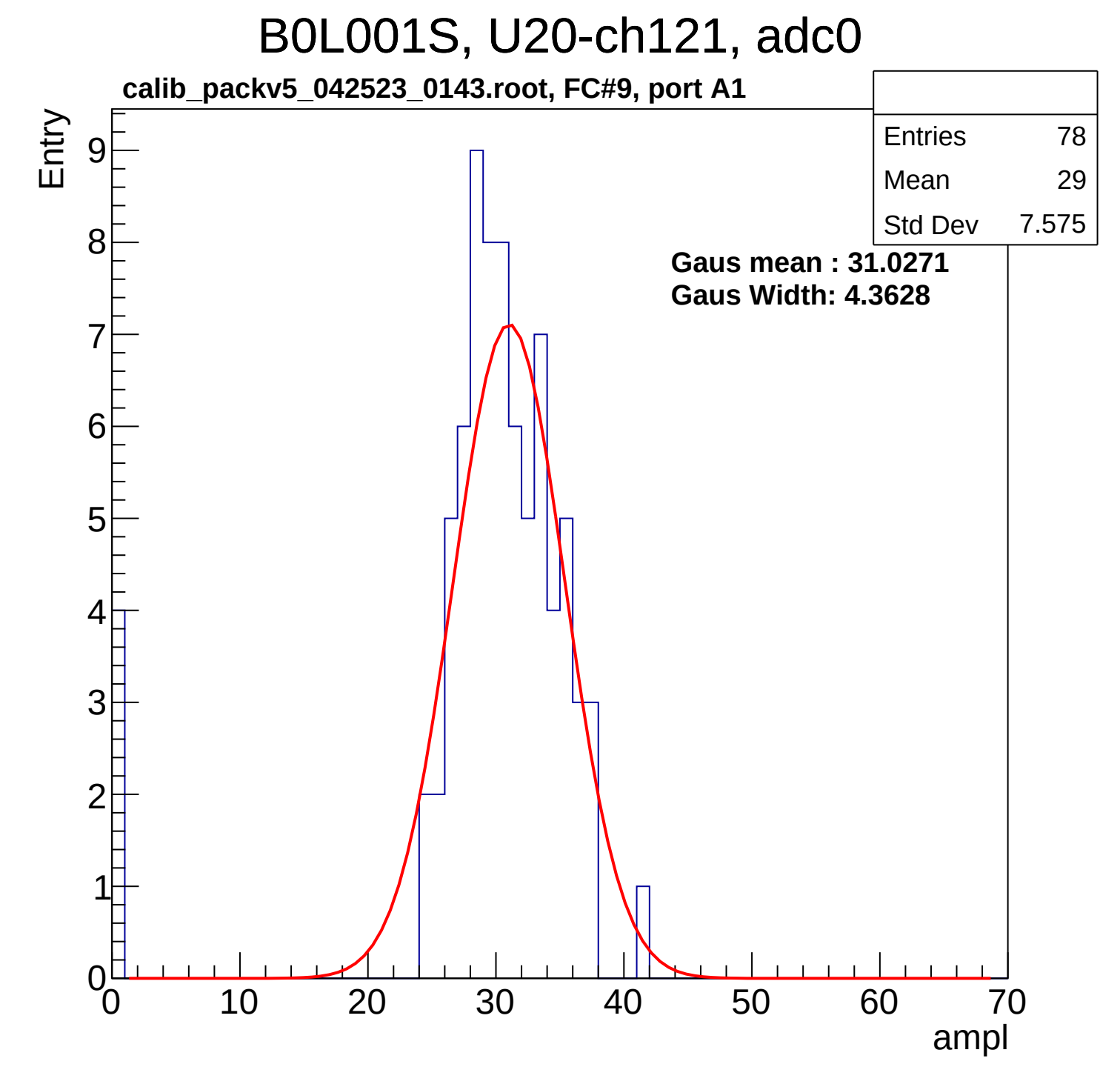
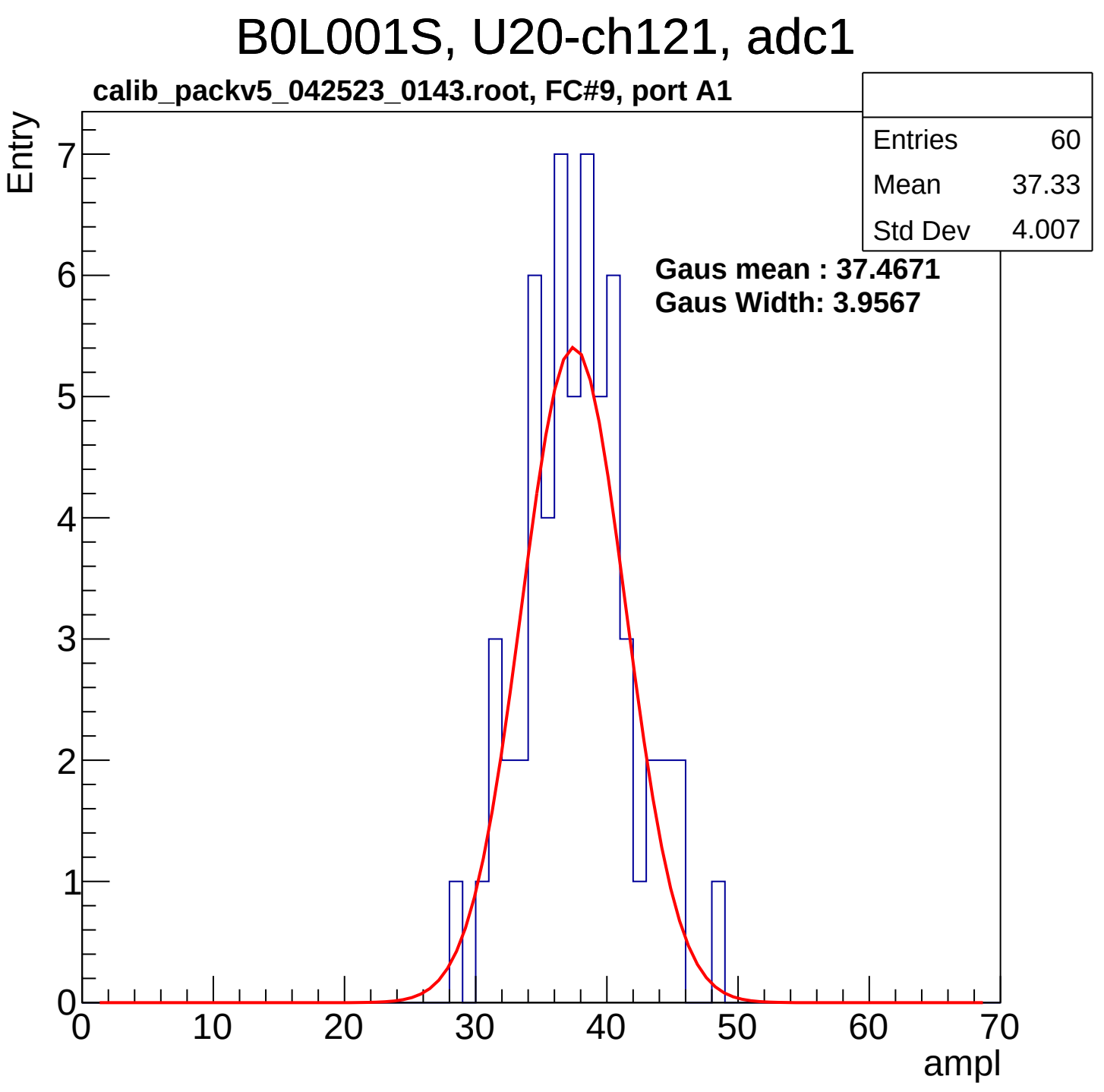

B0L001S, U20-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29
Std Dev 7.575
Gaus mean : 31.0271
Gaus Width: 4.3628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.33
Std Dev 4.007
Gaus mean : 37.4671
Gaus Width: 3.9567
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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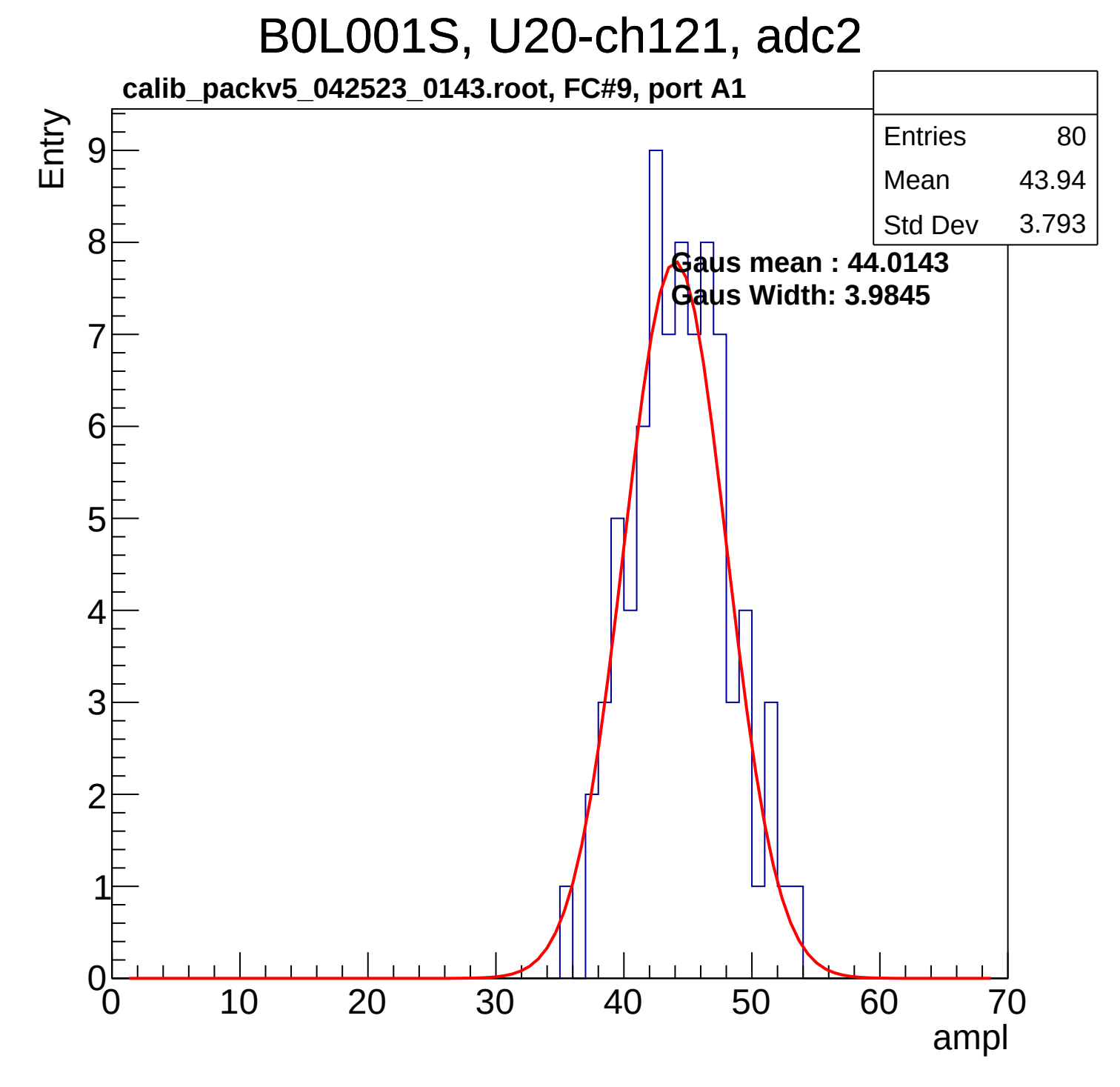
B0L001S, U20-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.94
Std Dev 3.793
Gaus mean : 44.0143
Gaus Width: 3.9845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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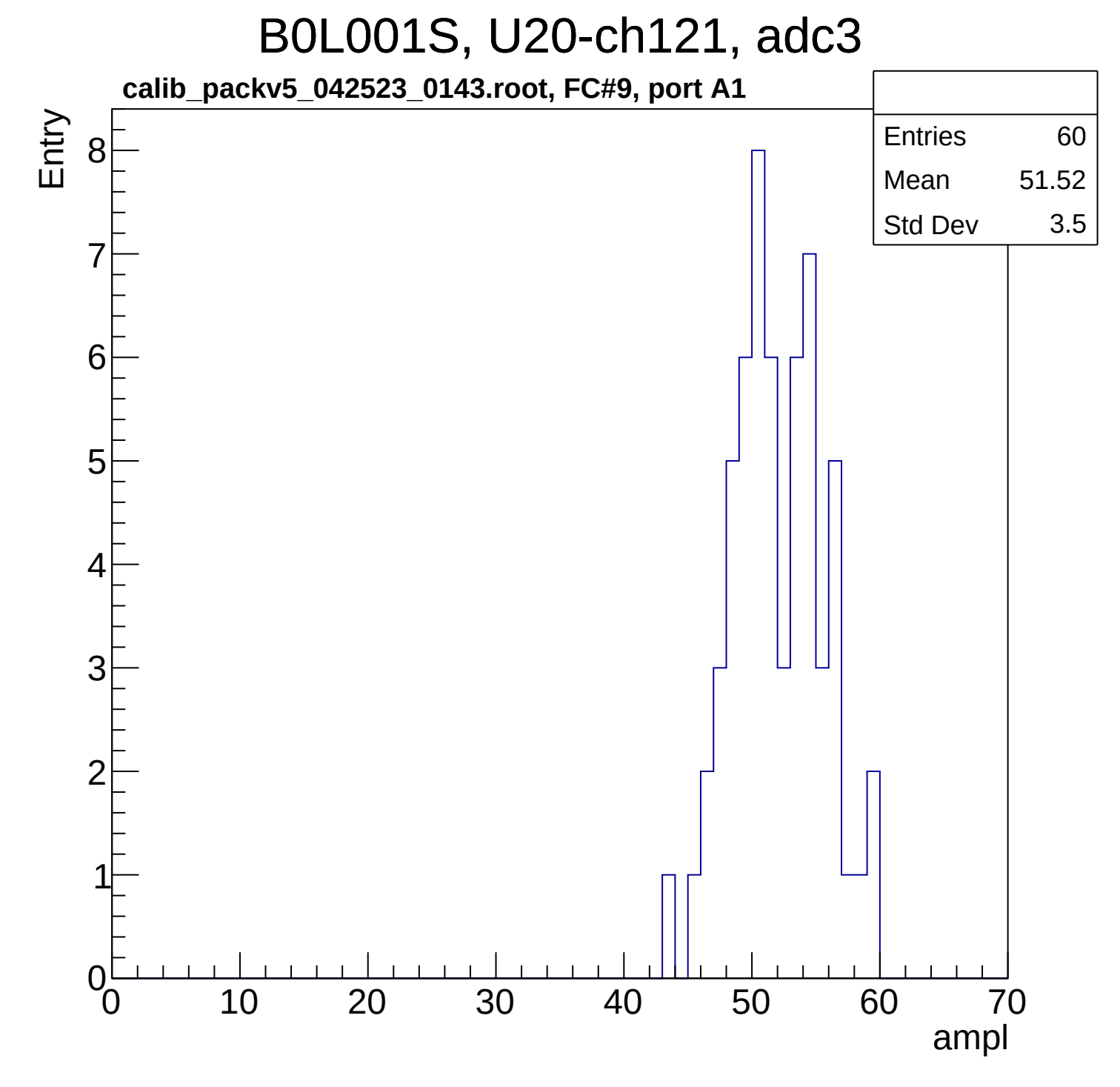
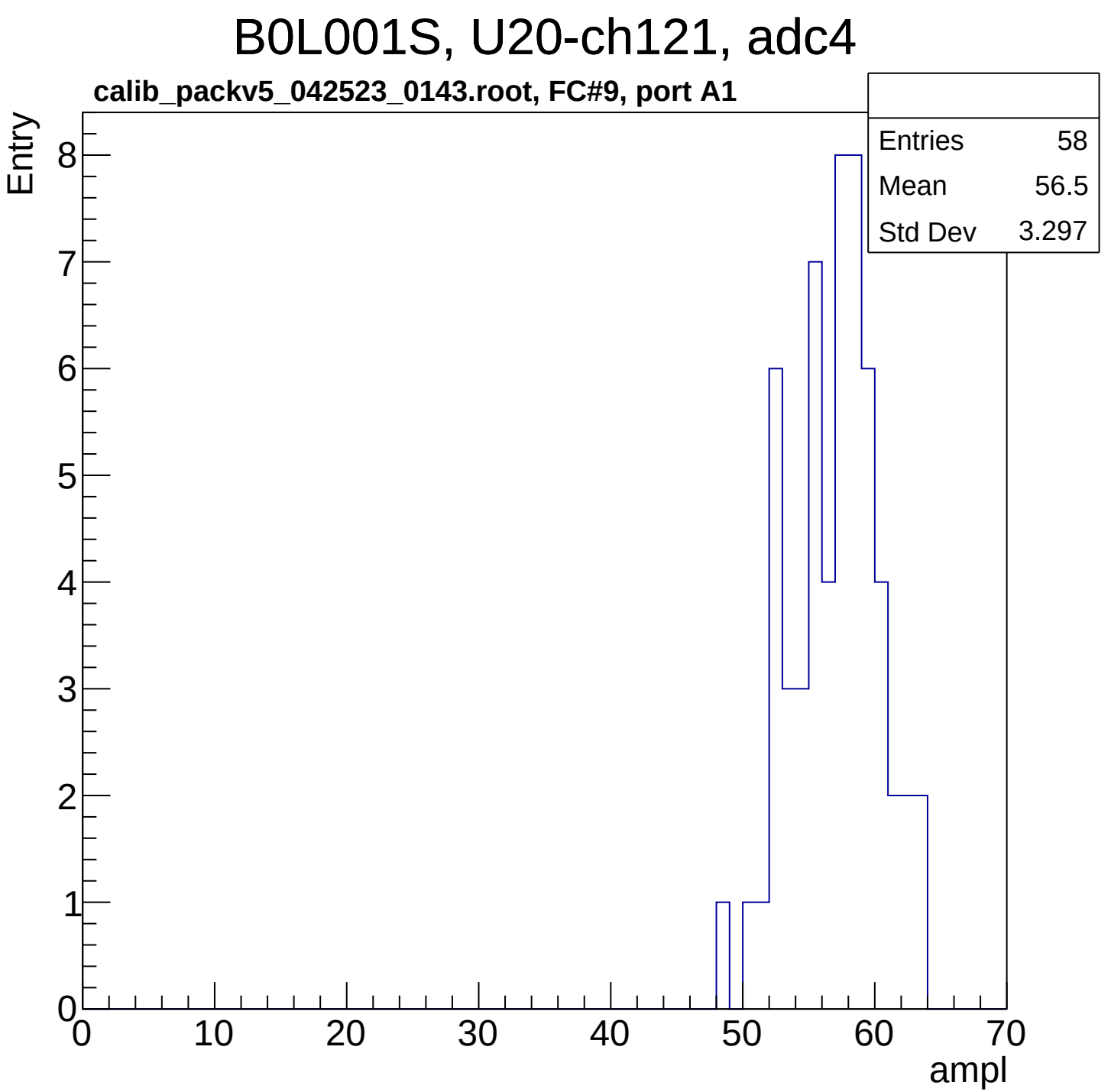

B0L001S, U20-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.52
Std Dev 3.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.5
Std Dev 3.297
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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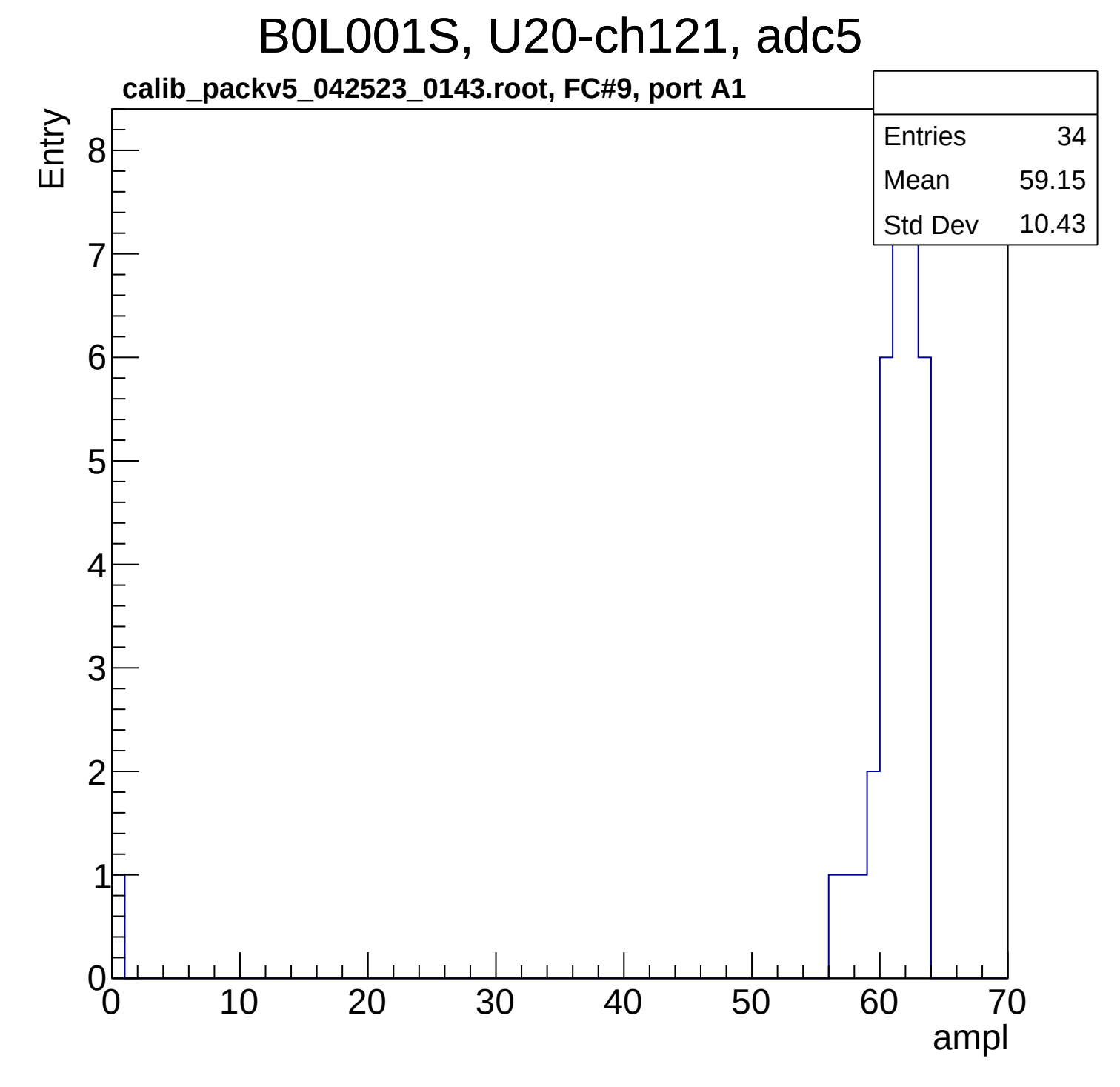
40
50
60
70


B0L001S, U20-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.15
Std Dev 10.43
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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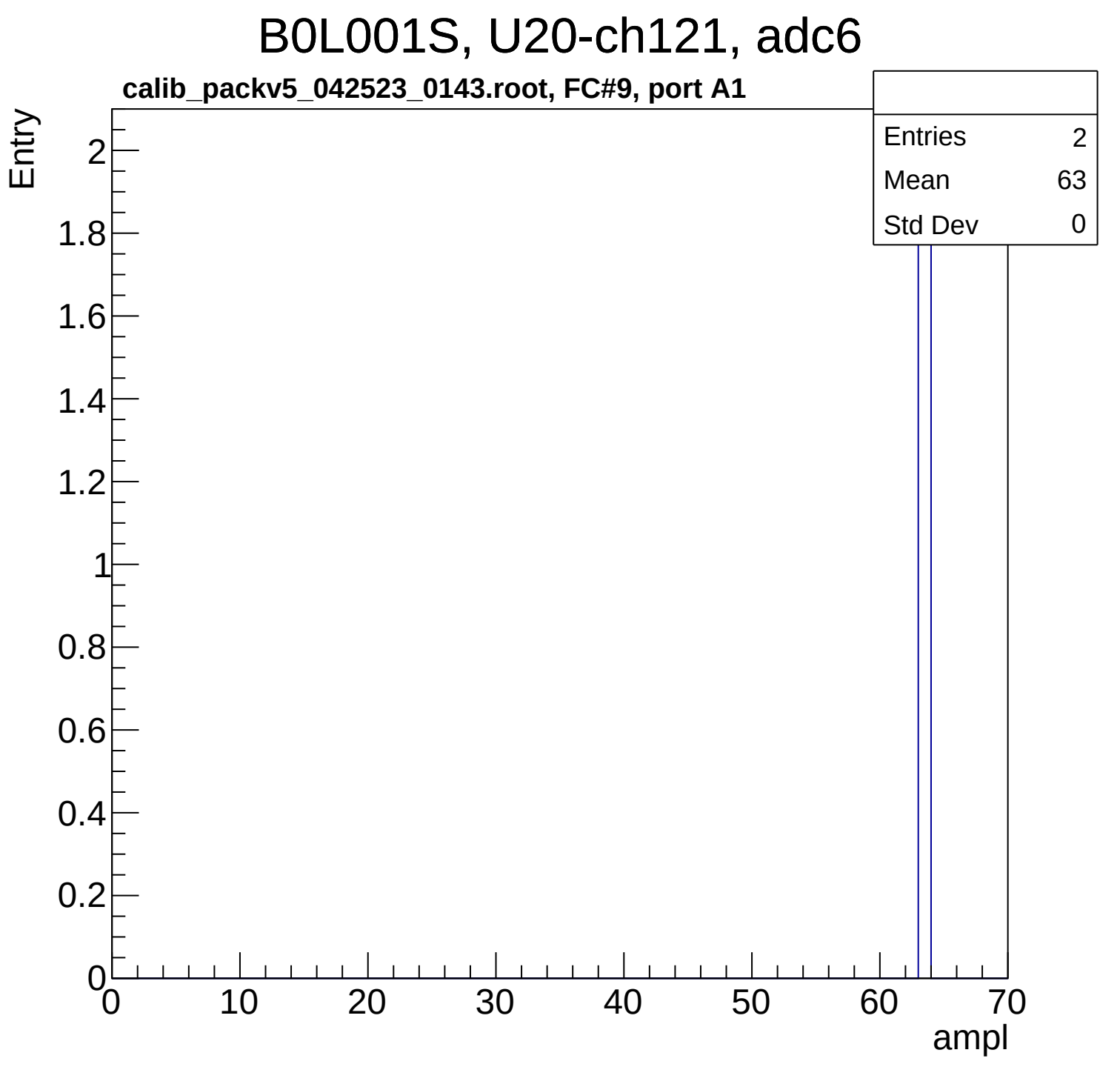
40
50
60
70


B0L001S, U20-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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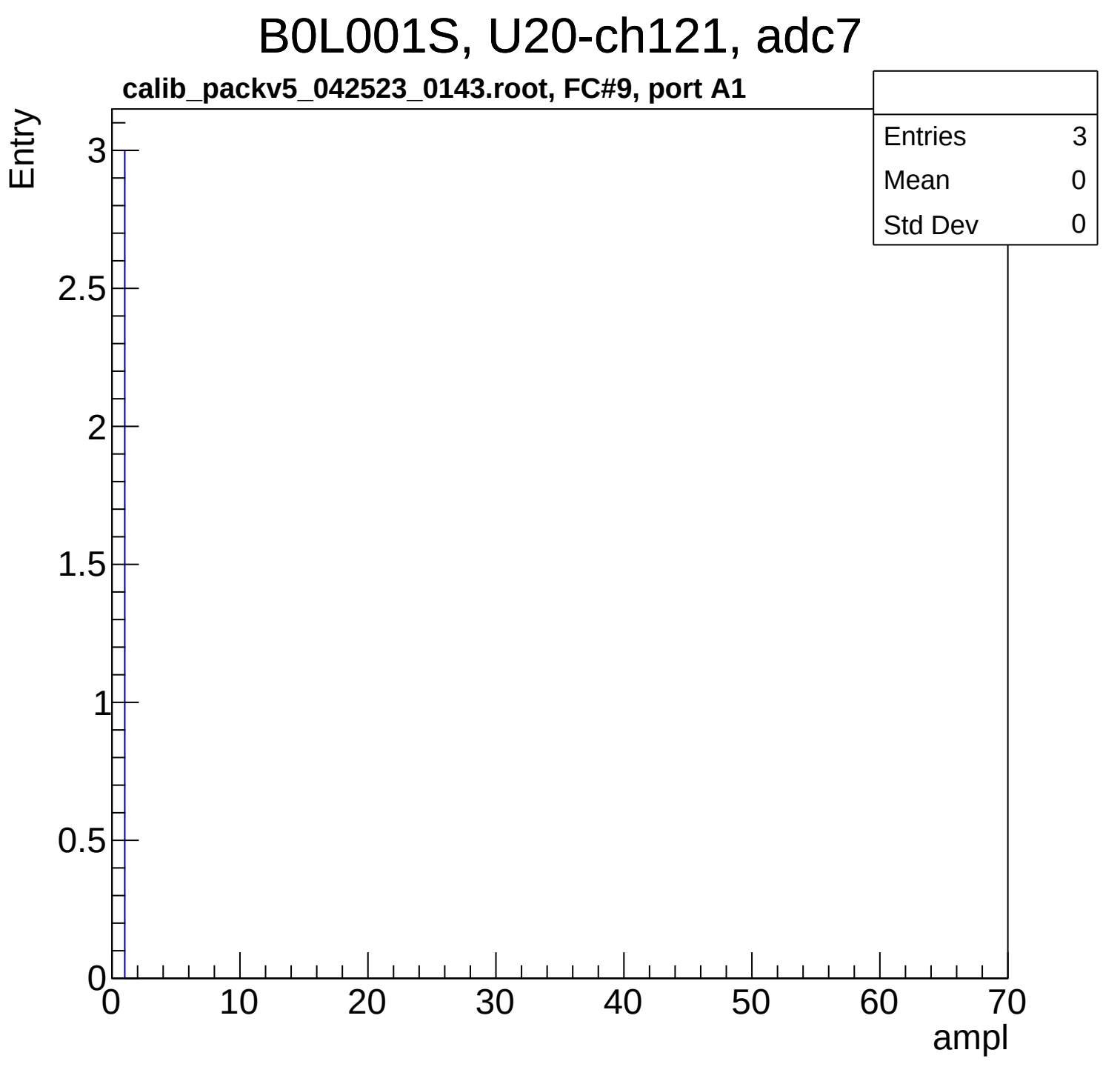
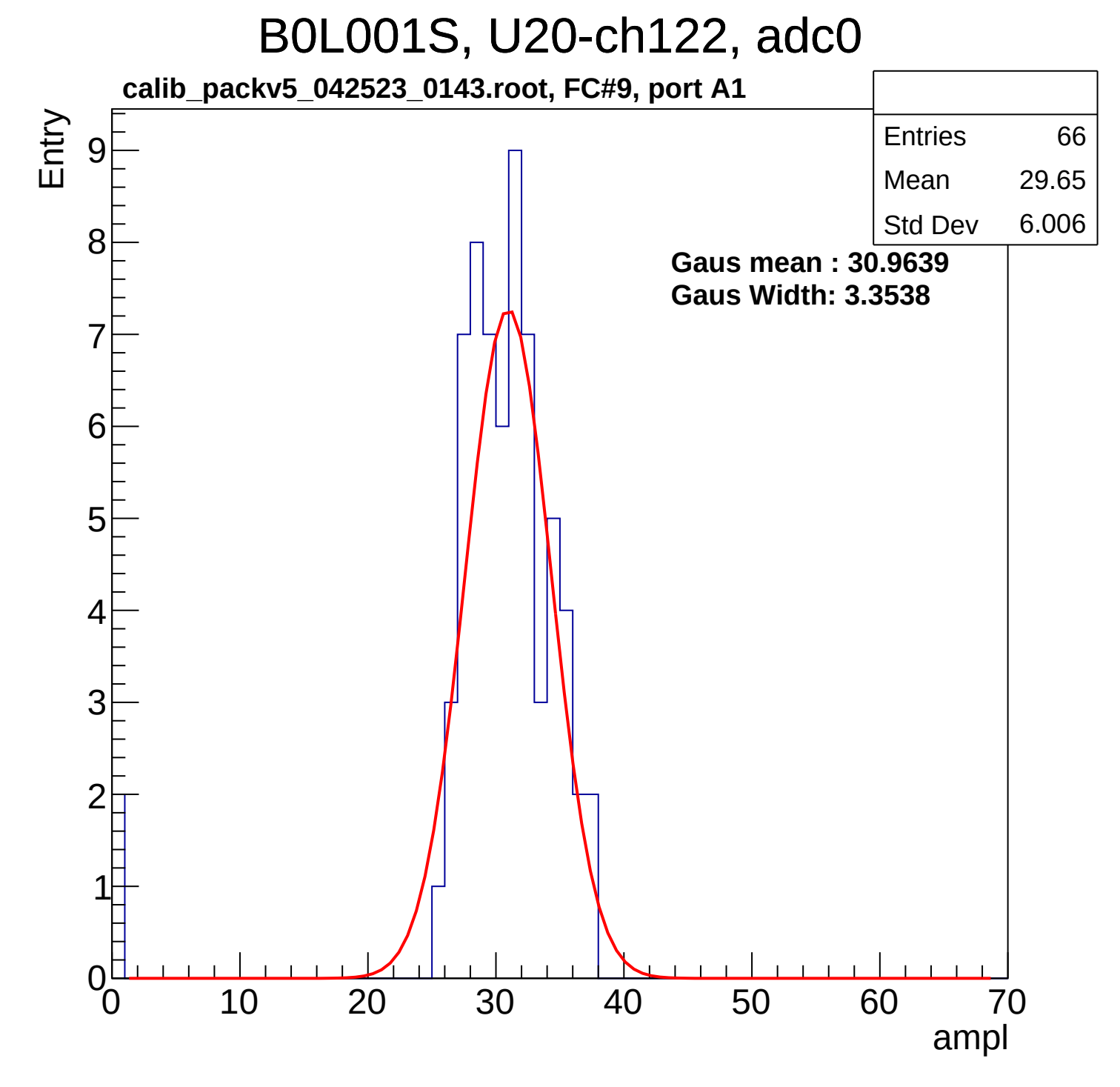
40
50
60
70


B0L001S, U20-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.65
Std Dev 6.006
Gaus mean : 30.9639
Gaus Width: 3.3538
Entry
ampl
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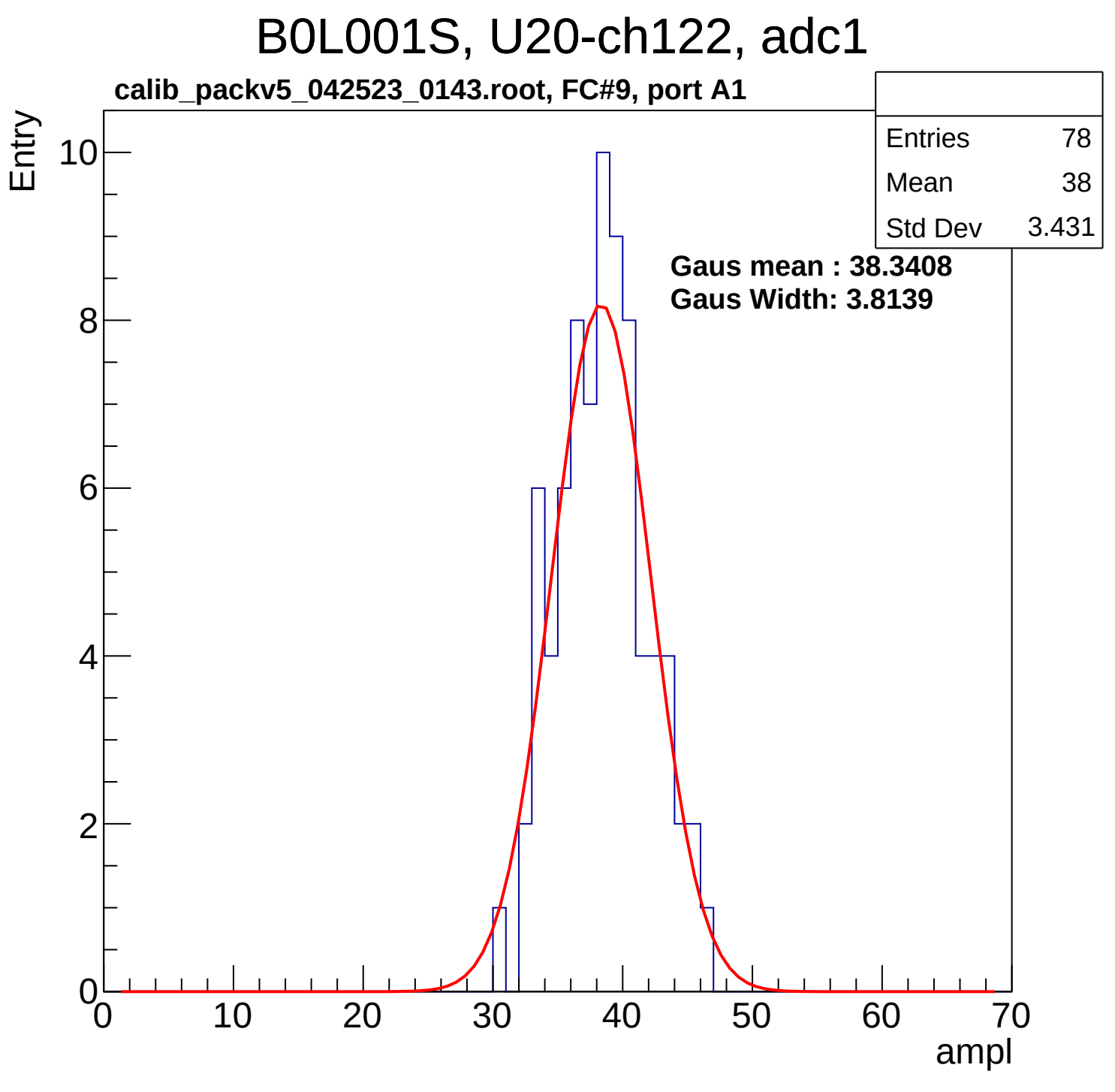
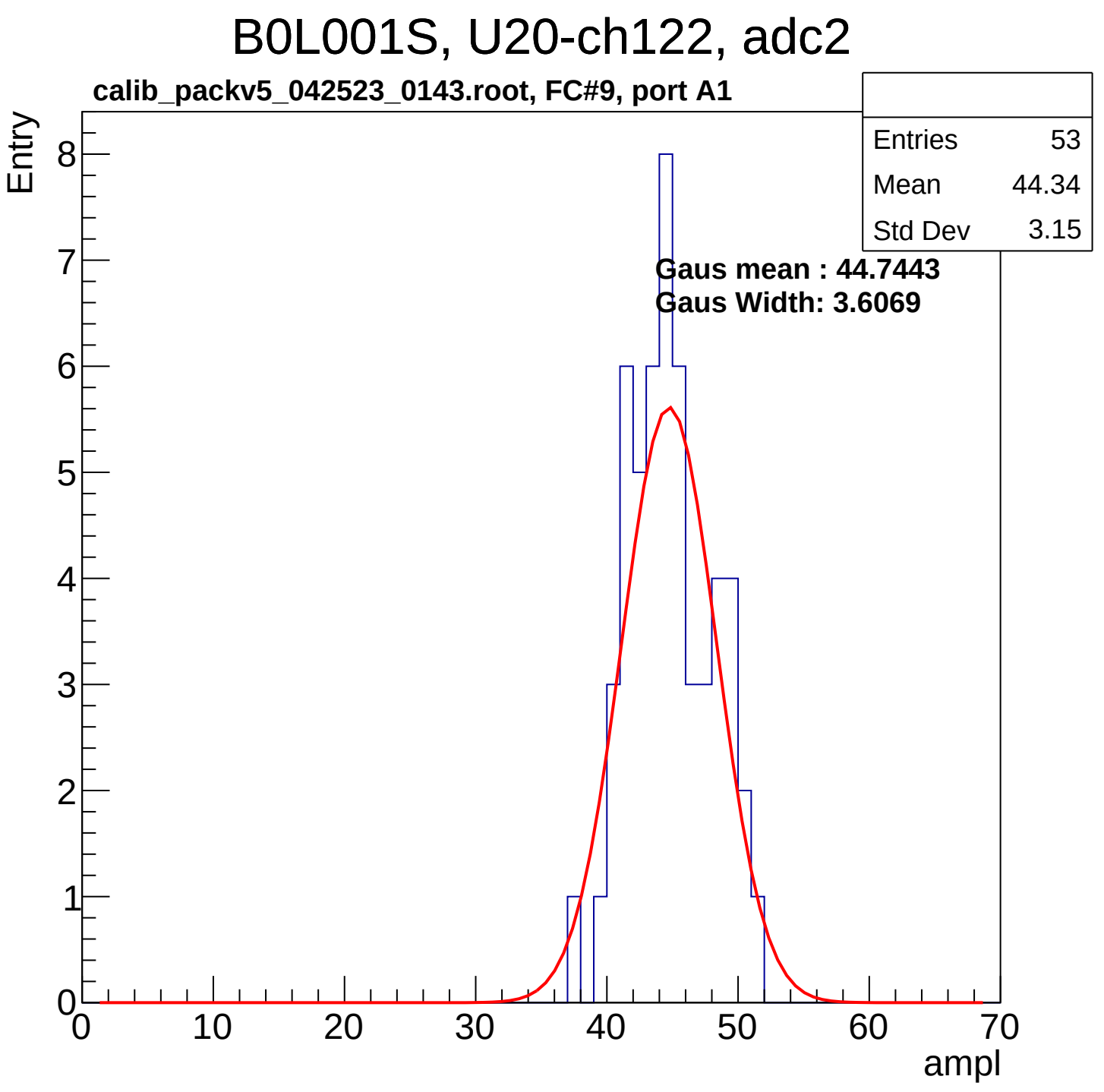
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38
Std Dev 3.431
Gaus mean : 38.3408
Gaus Width: 3.8139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.34
Std Dev 3.15
Gaus mean : 44.7443
Gaus Width: 3.6069
Entry
ampl
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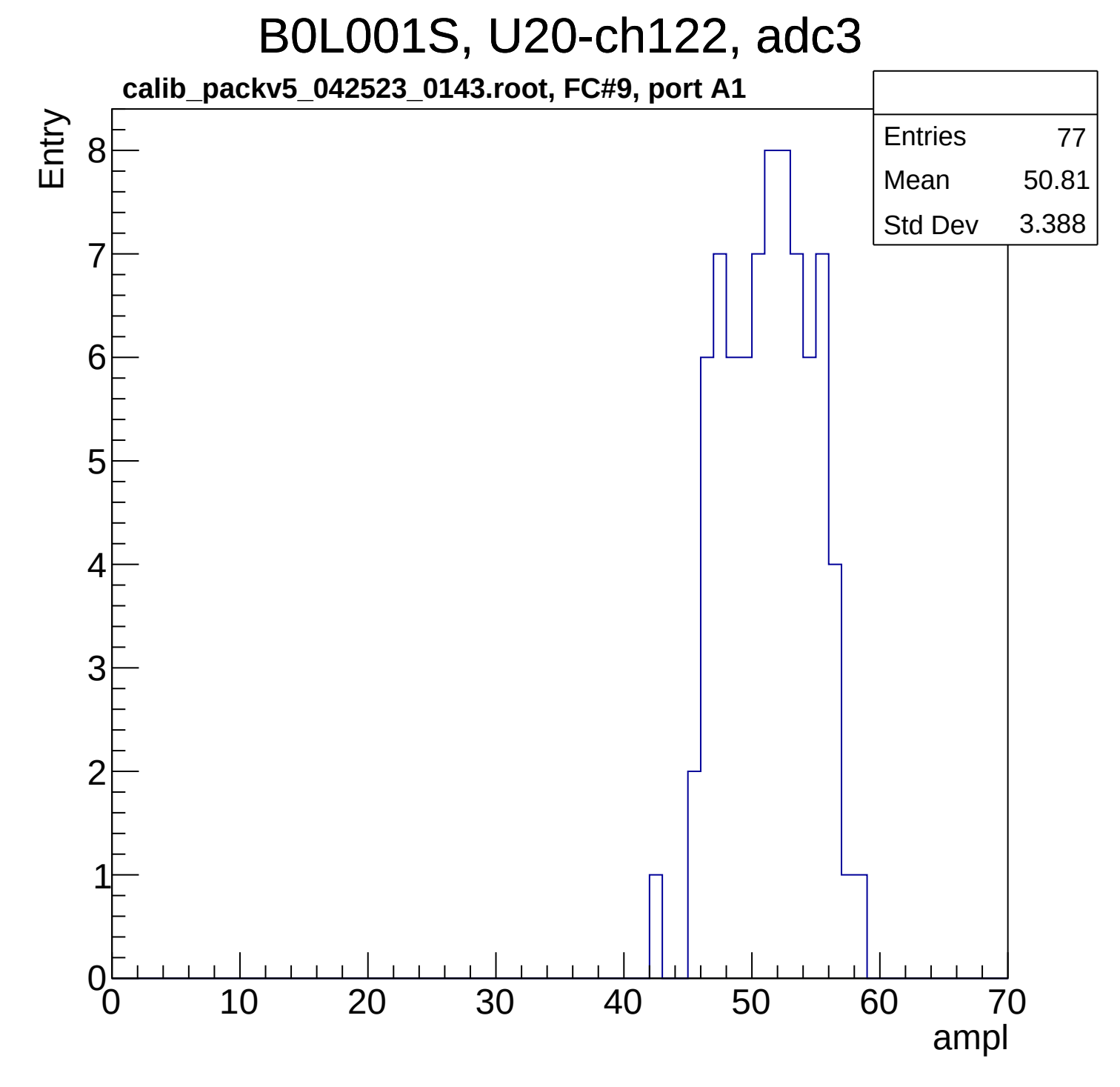
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 50.81
Std Dev 3.388
Entry
ampl
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1
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3
4
5
6
7
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0
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20
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40
50
60
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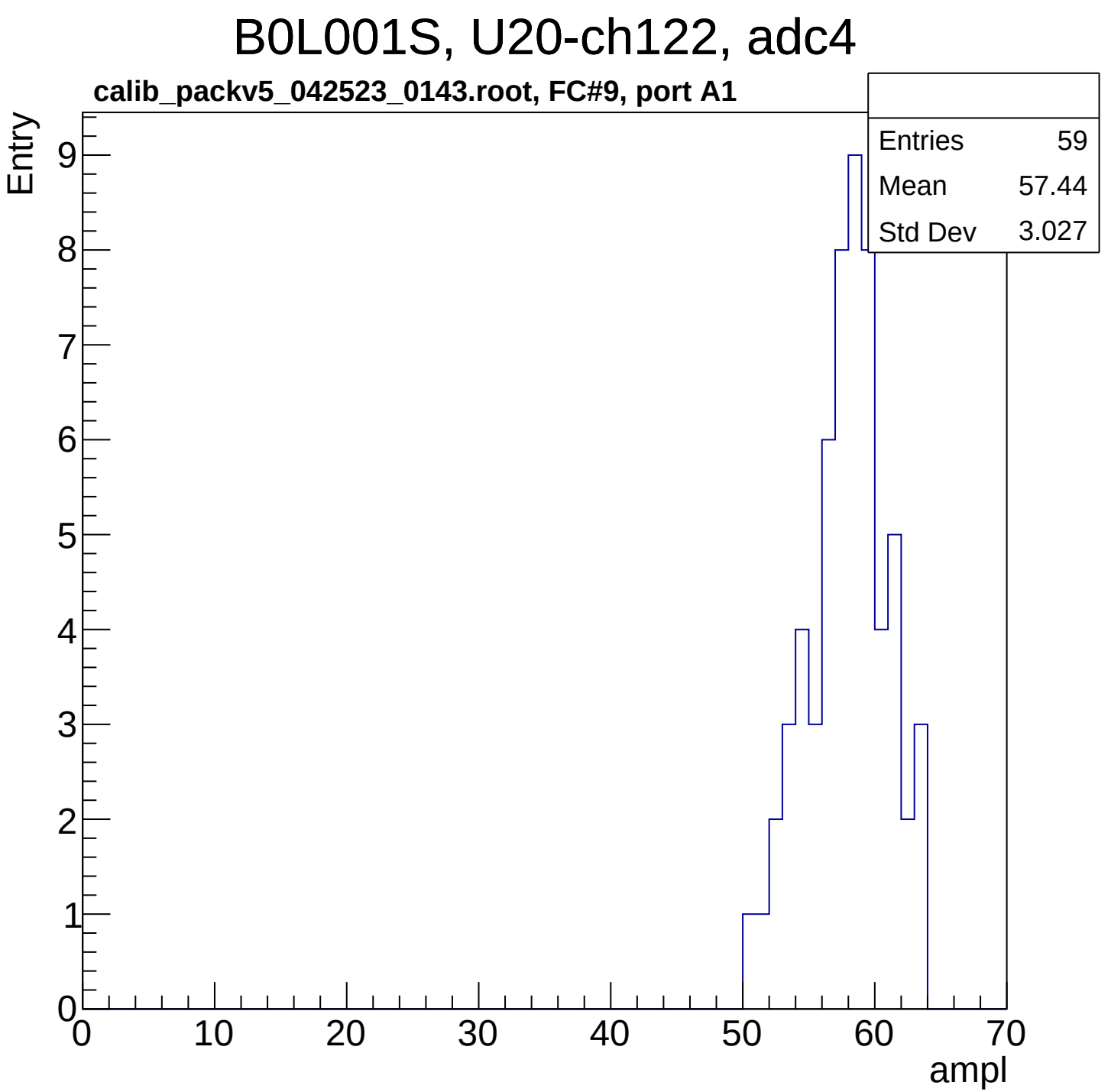
B0L001S, U20-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.44
Std Dev 3.027
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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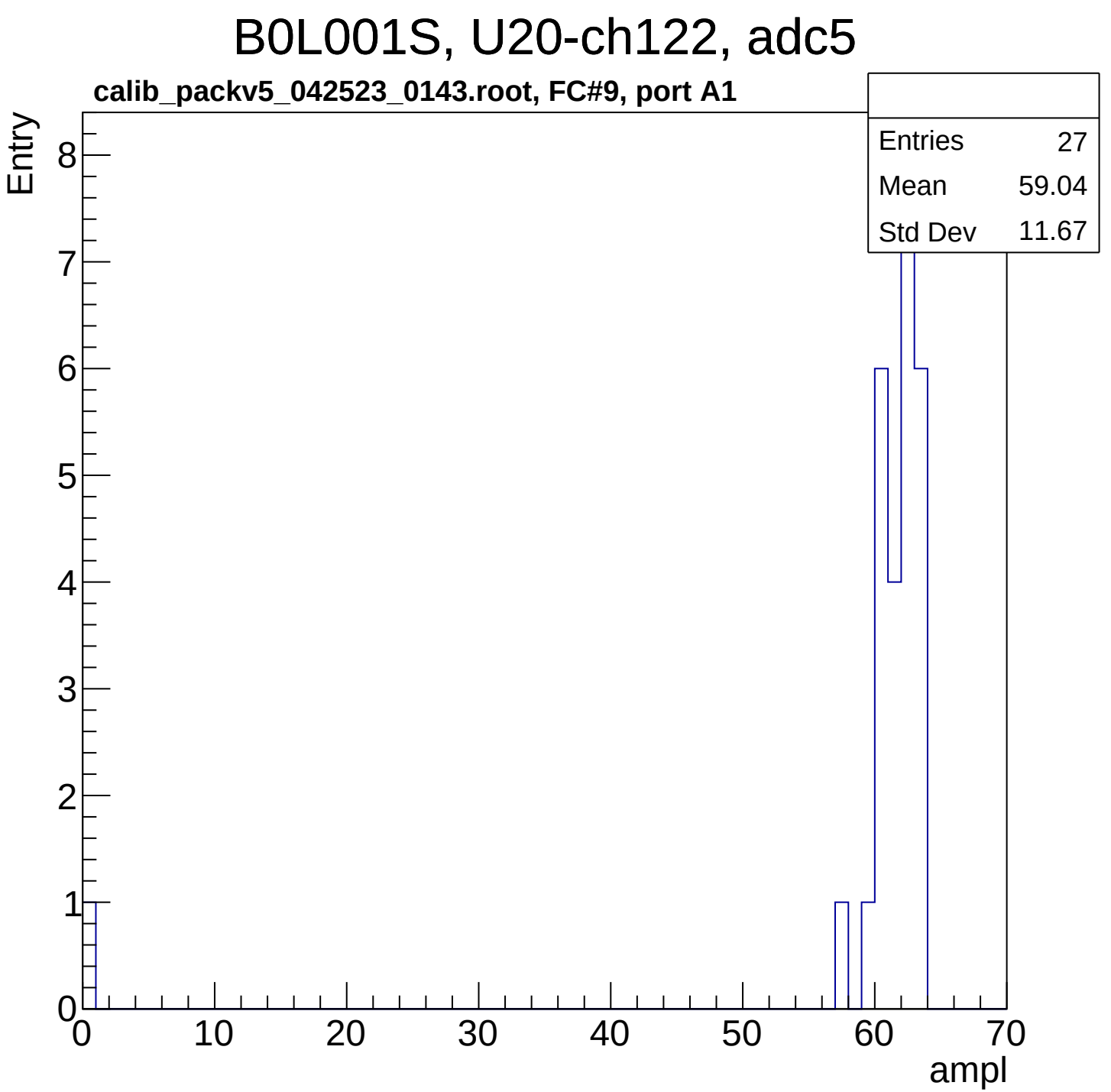
B0L001S, U20-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 59.04
Std Dev 11.67
Entry
ampl
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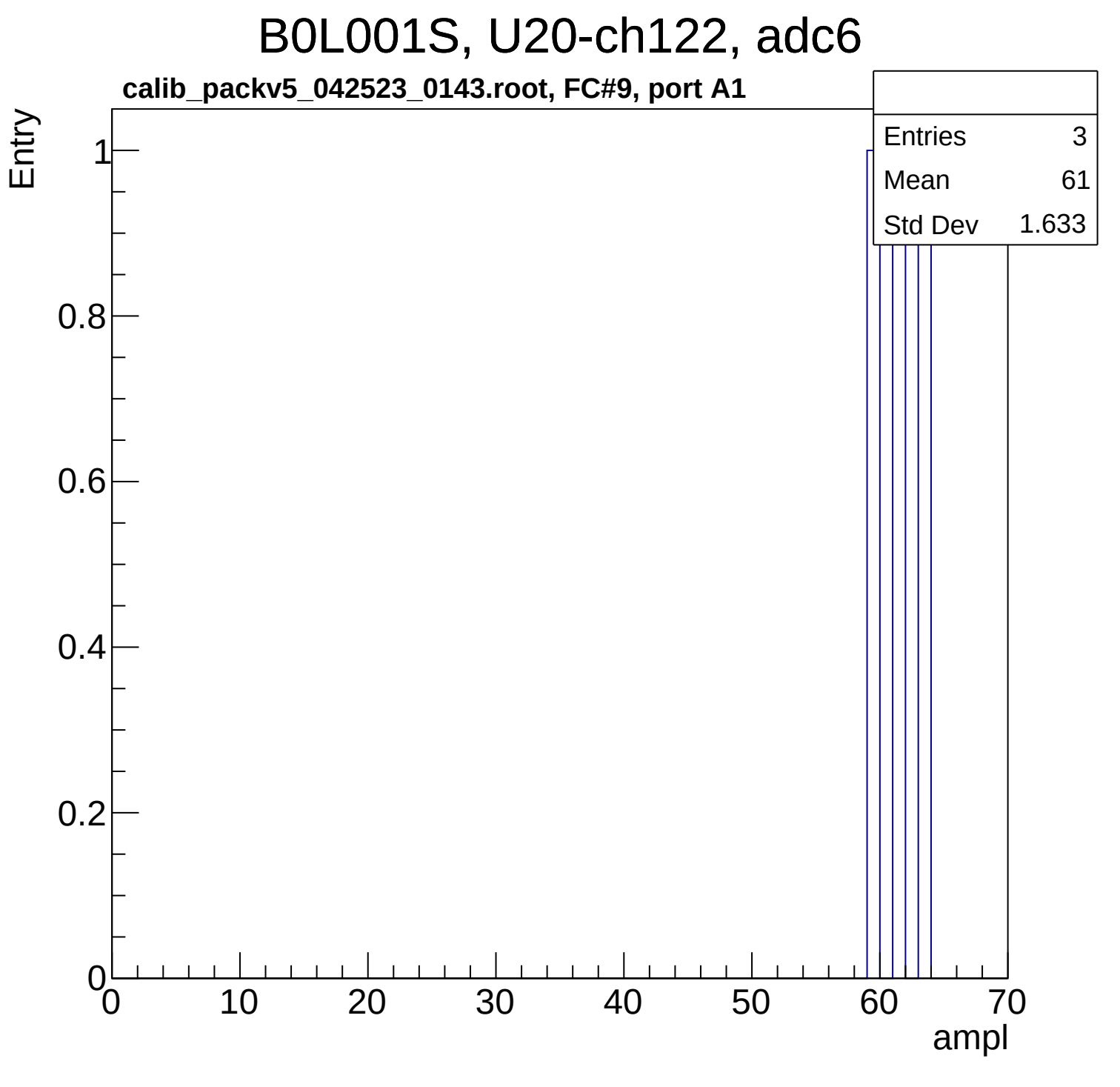
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 1.633
Entry
1
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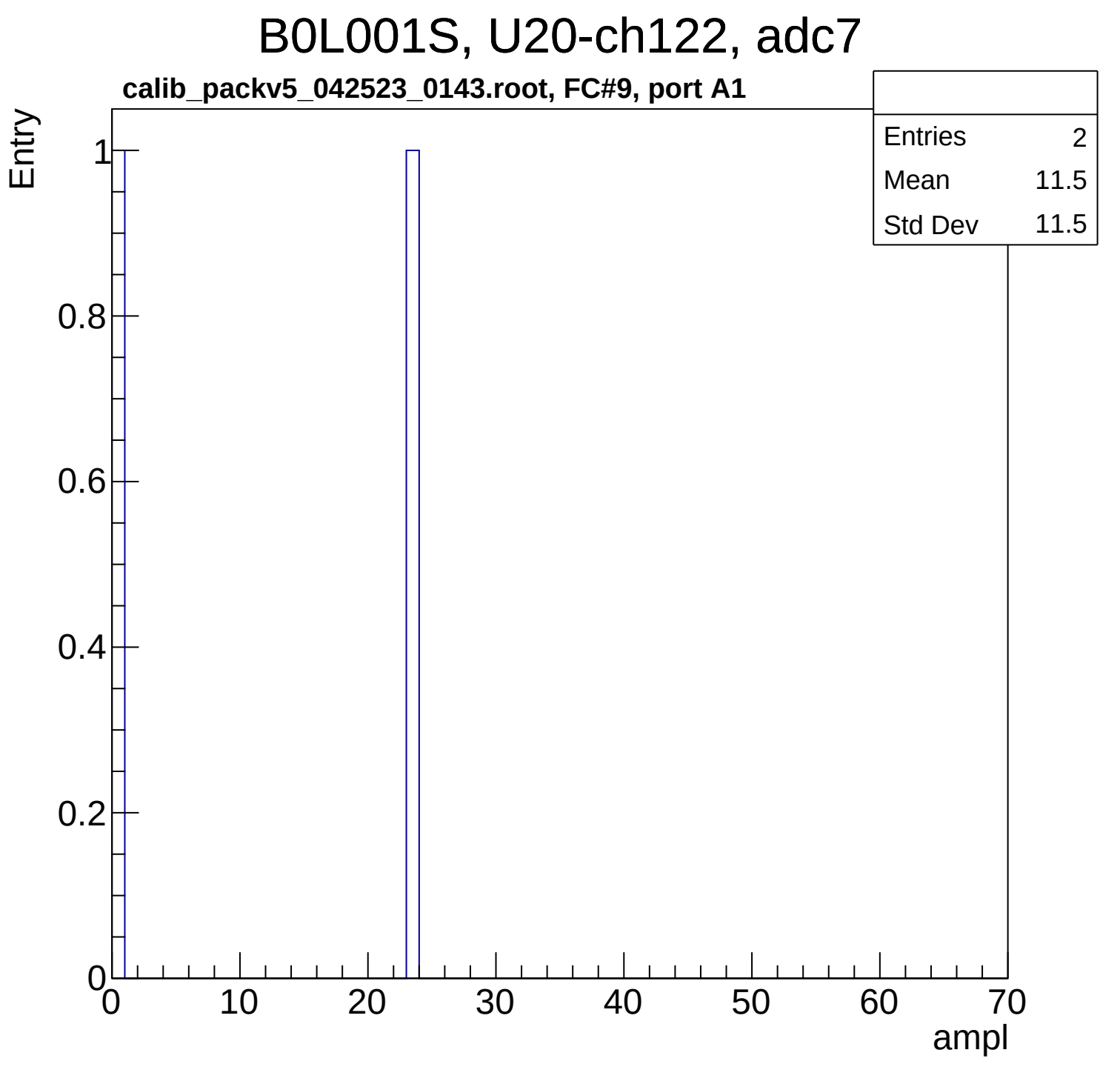
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
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10
20
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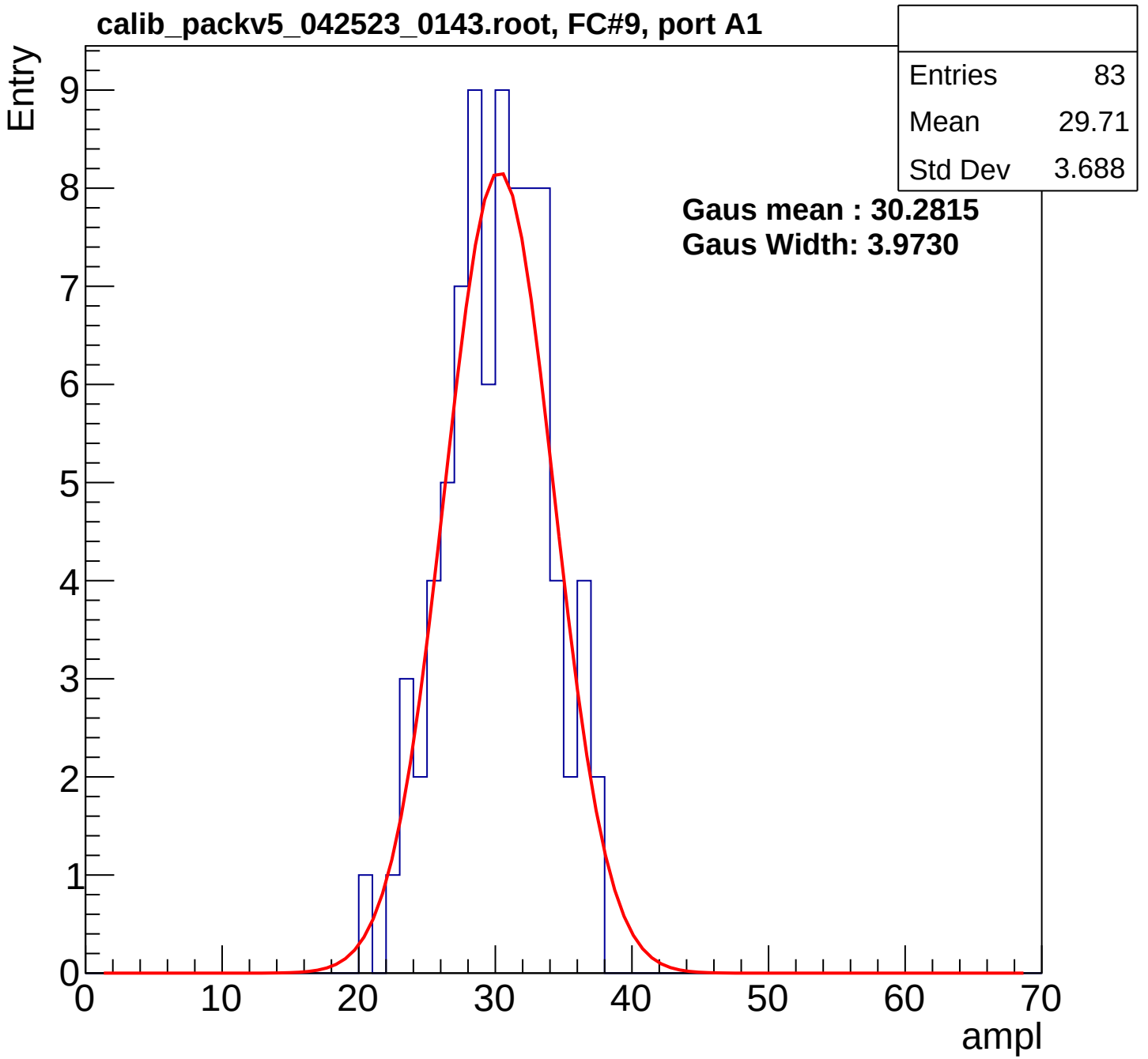
50
60
70
ampl


B0L001S, U20-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 29.71
Std Dev 3.688
Gaus mean : 30.2815
Gaus Width: 3.9730
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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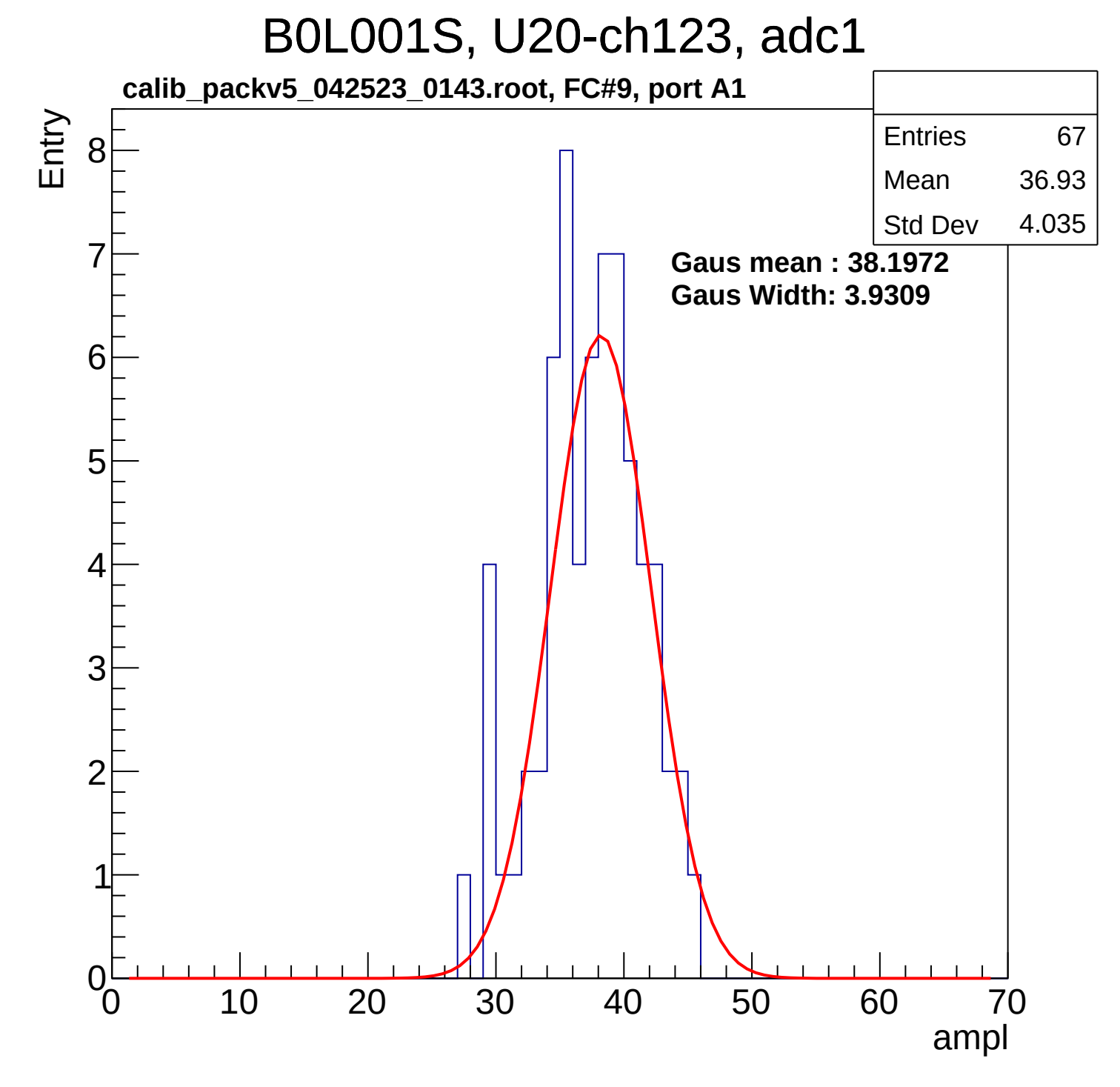
B0L001S, U20-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.93
Std Dev 4.035
Gaus mean : 38.1972
Gaus Width: 3.9309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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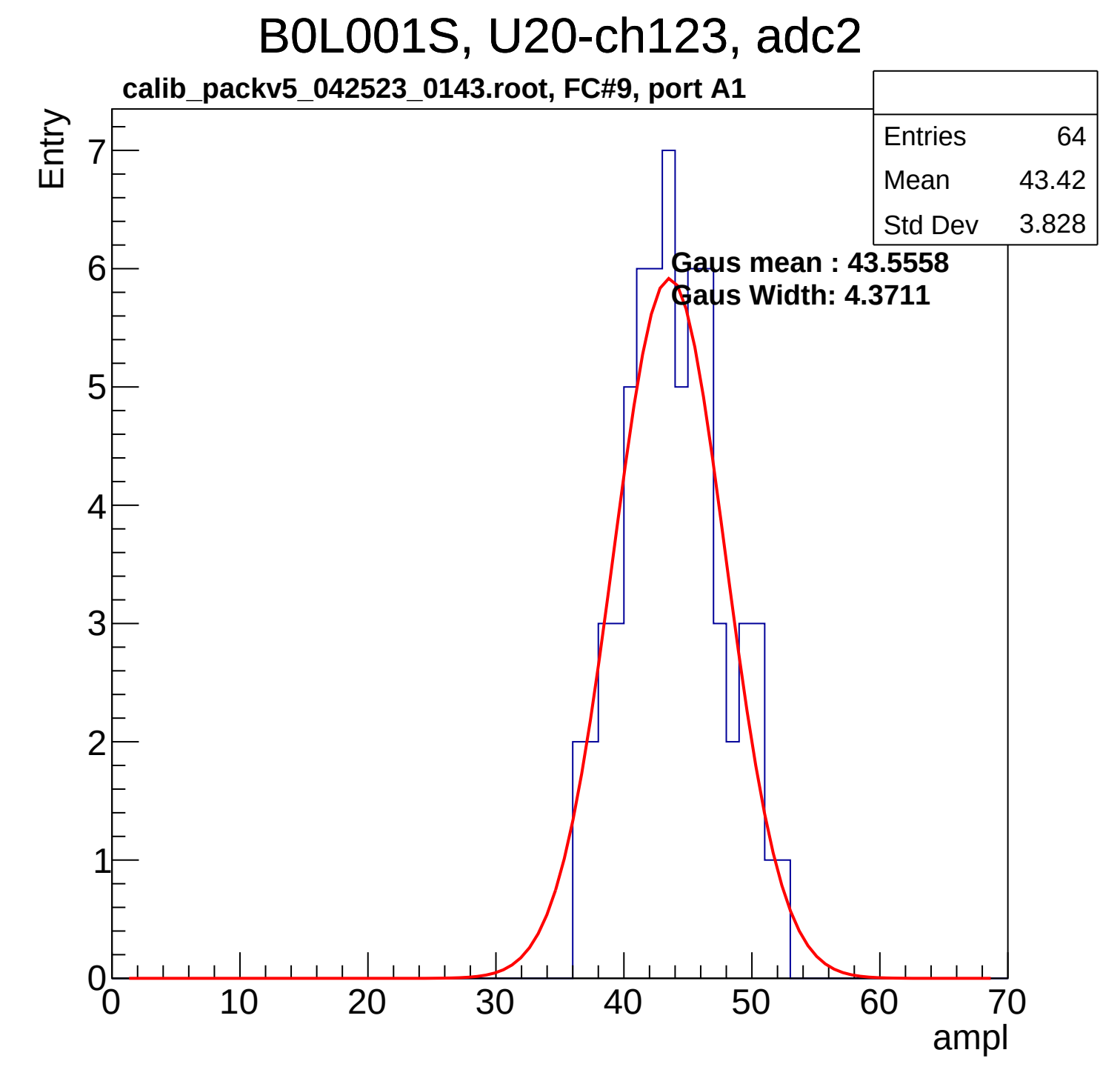
B0L001S, U20-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.42
Std Dev 3.828
Gaus mean : 43.5558
Gaus Width: 4.3711
Entry
ampl
0
1
2
3
4
5
6
7
0
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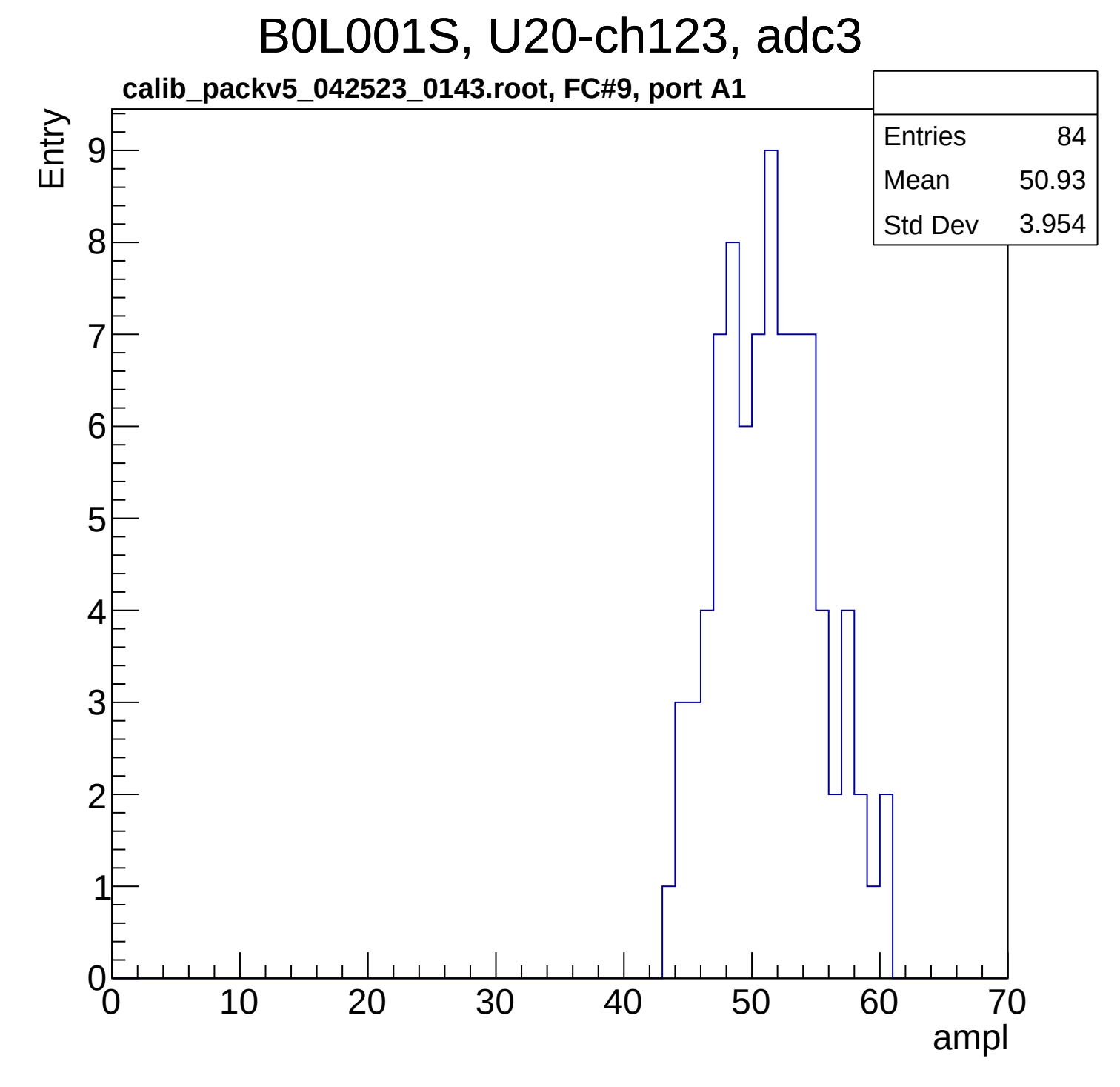
B0L001S, U20-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 50.93
Std Dev 3.954
Entry
ampl
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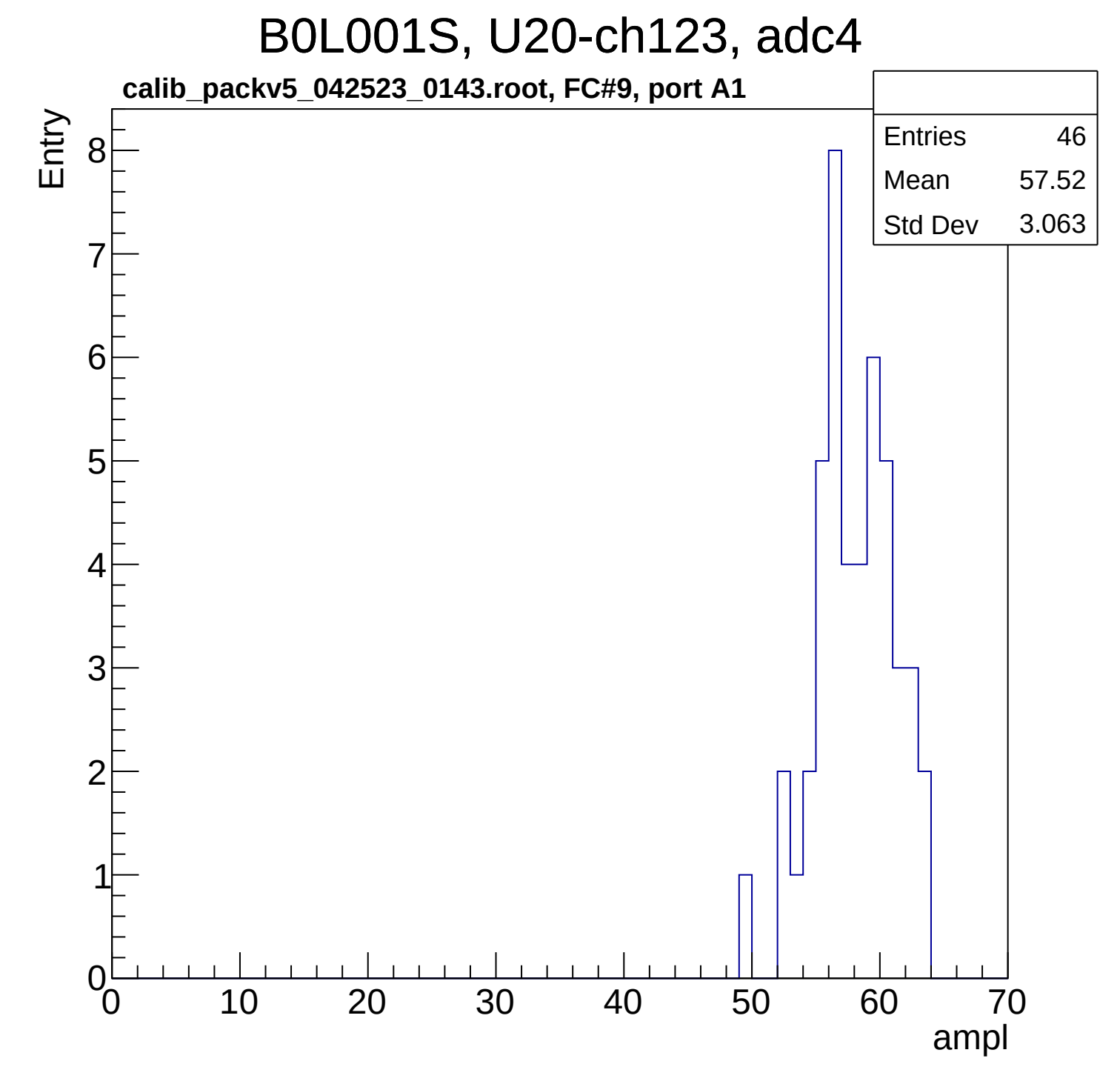
B0L001S, U20-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.52
Std Dev 3.063
Entry
ampl
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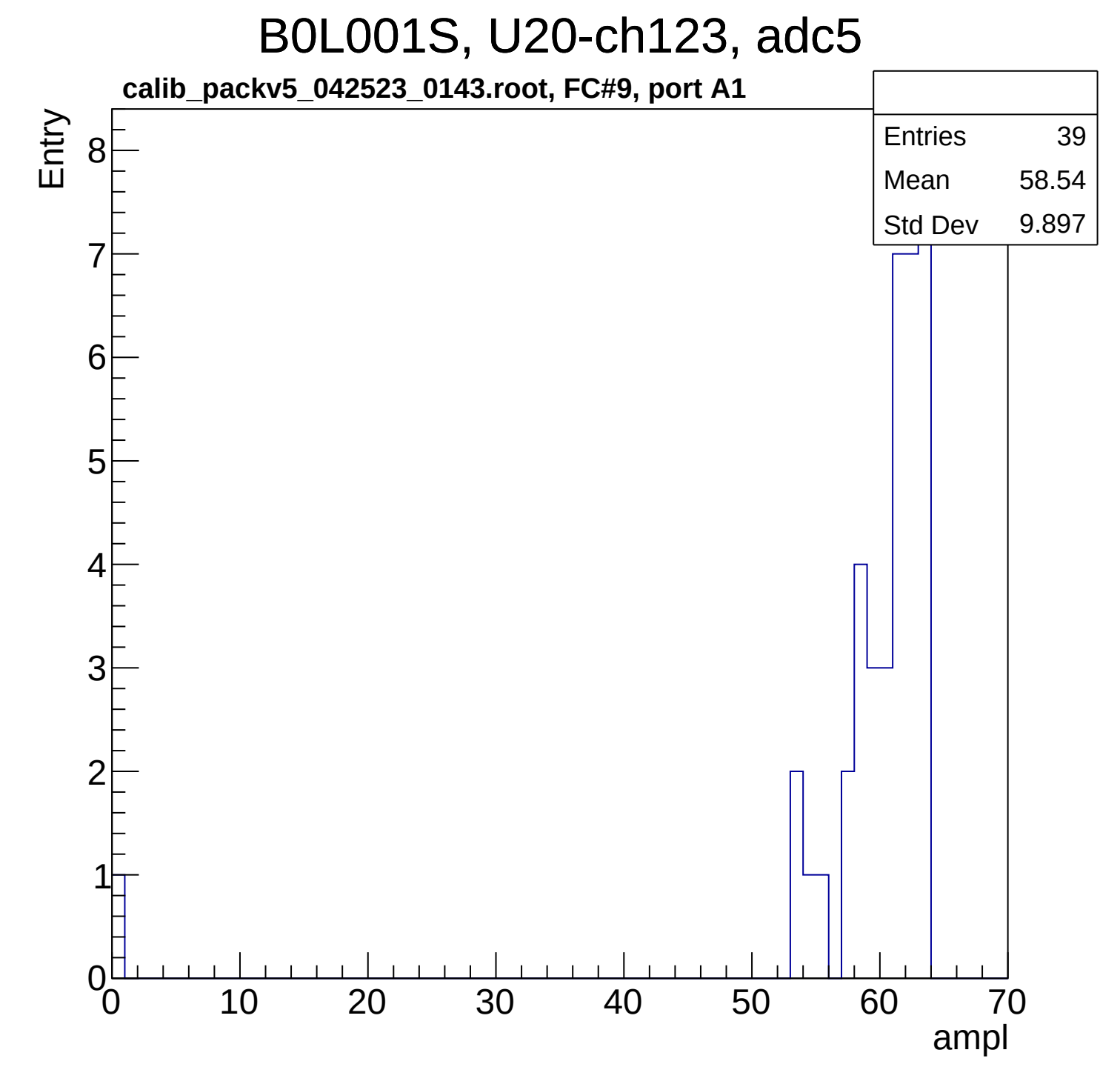
B0L001S, U20-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.54
Std Dev 9.897
Entry
ampl
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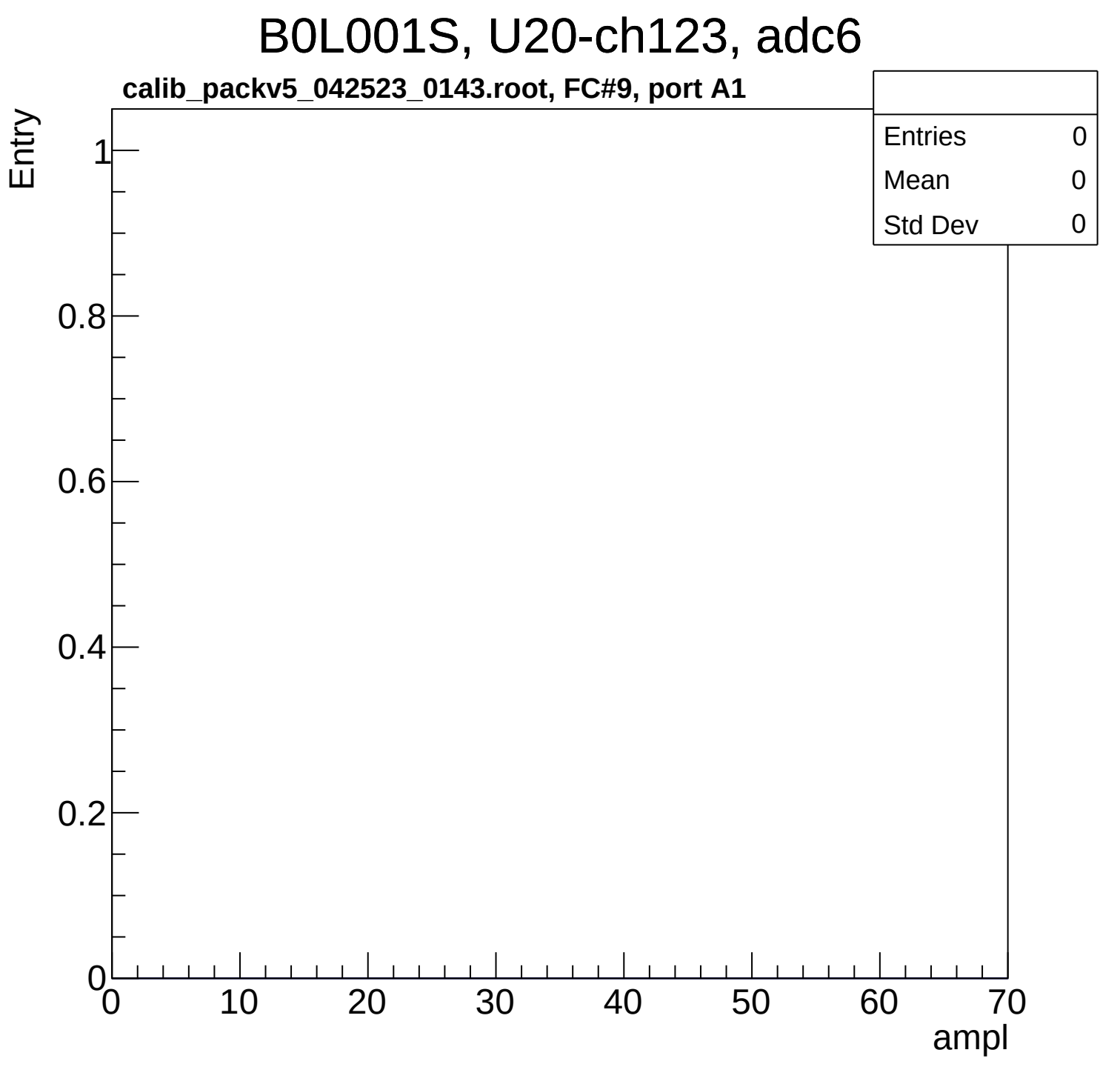
B0L001S, U20-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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30
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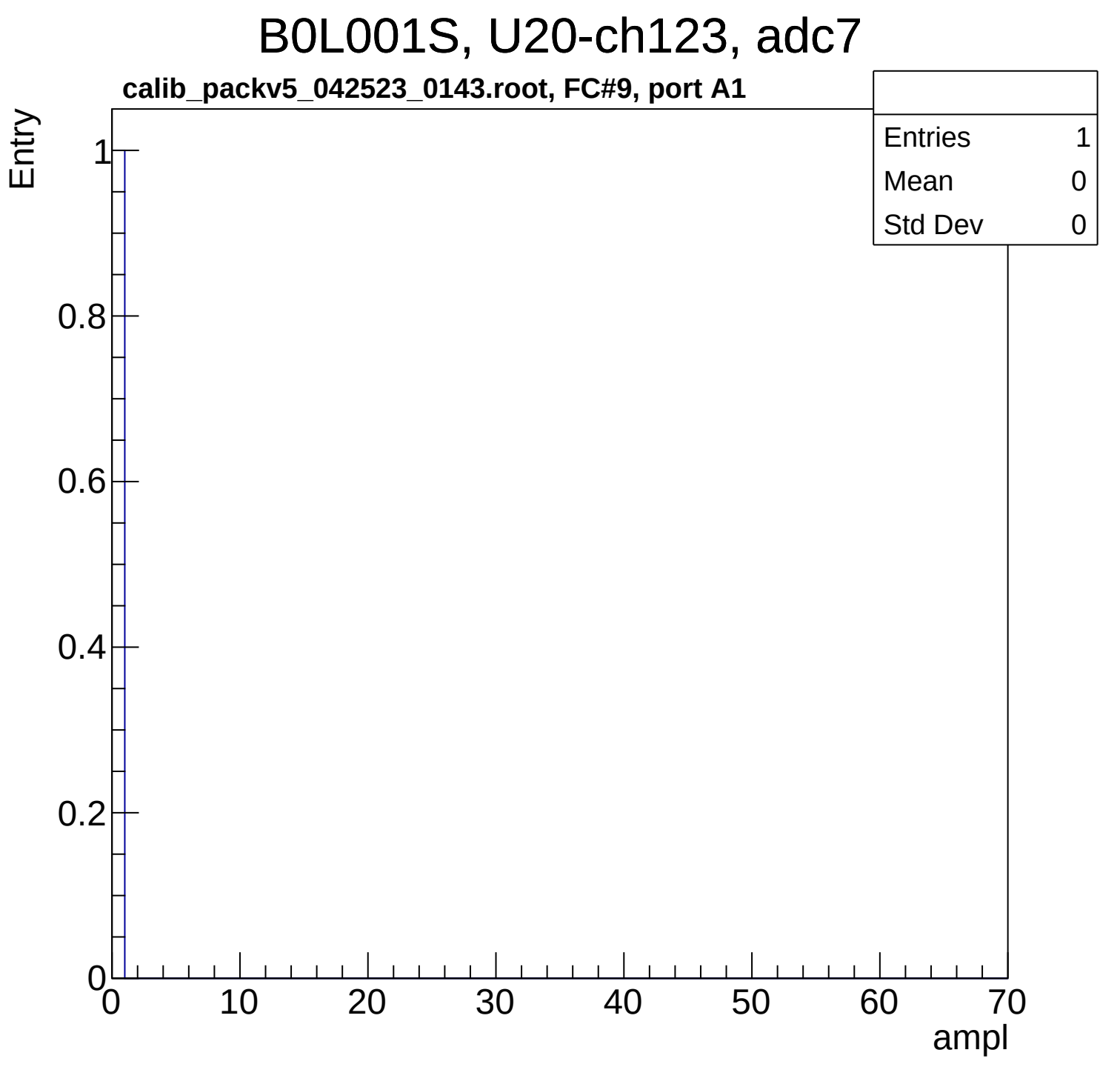
50
60
70
ampl


B0L001S, U20-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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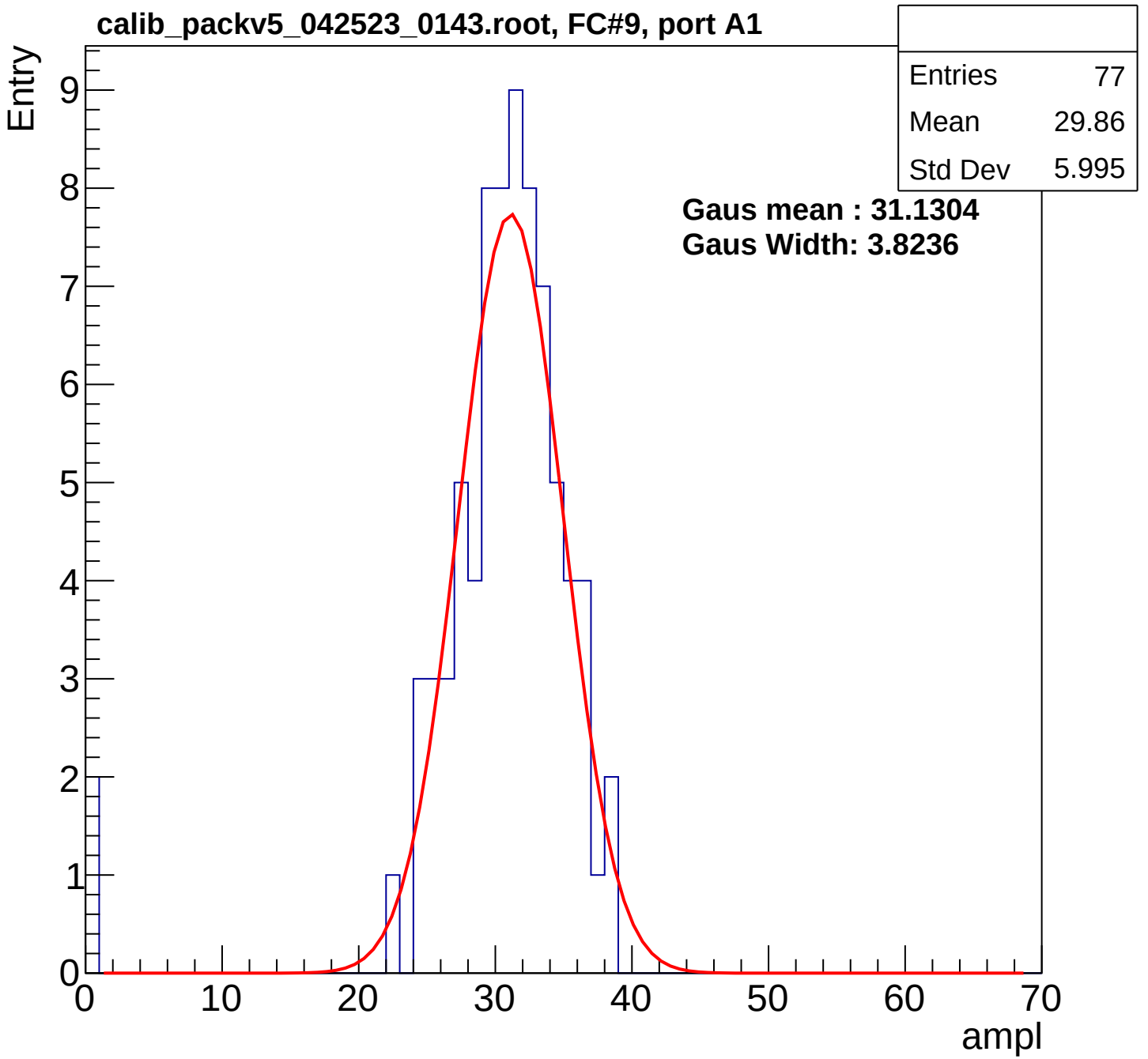
50
60
70
ampl


B0L001S, U20-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.86
Std Dev 5.995
Gaus mean : 31.1304
Gaus Width: 3.8236
Entry
ampl
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3
4
5
6
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0
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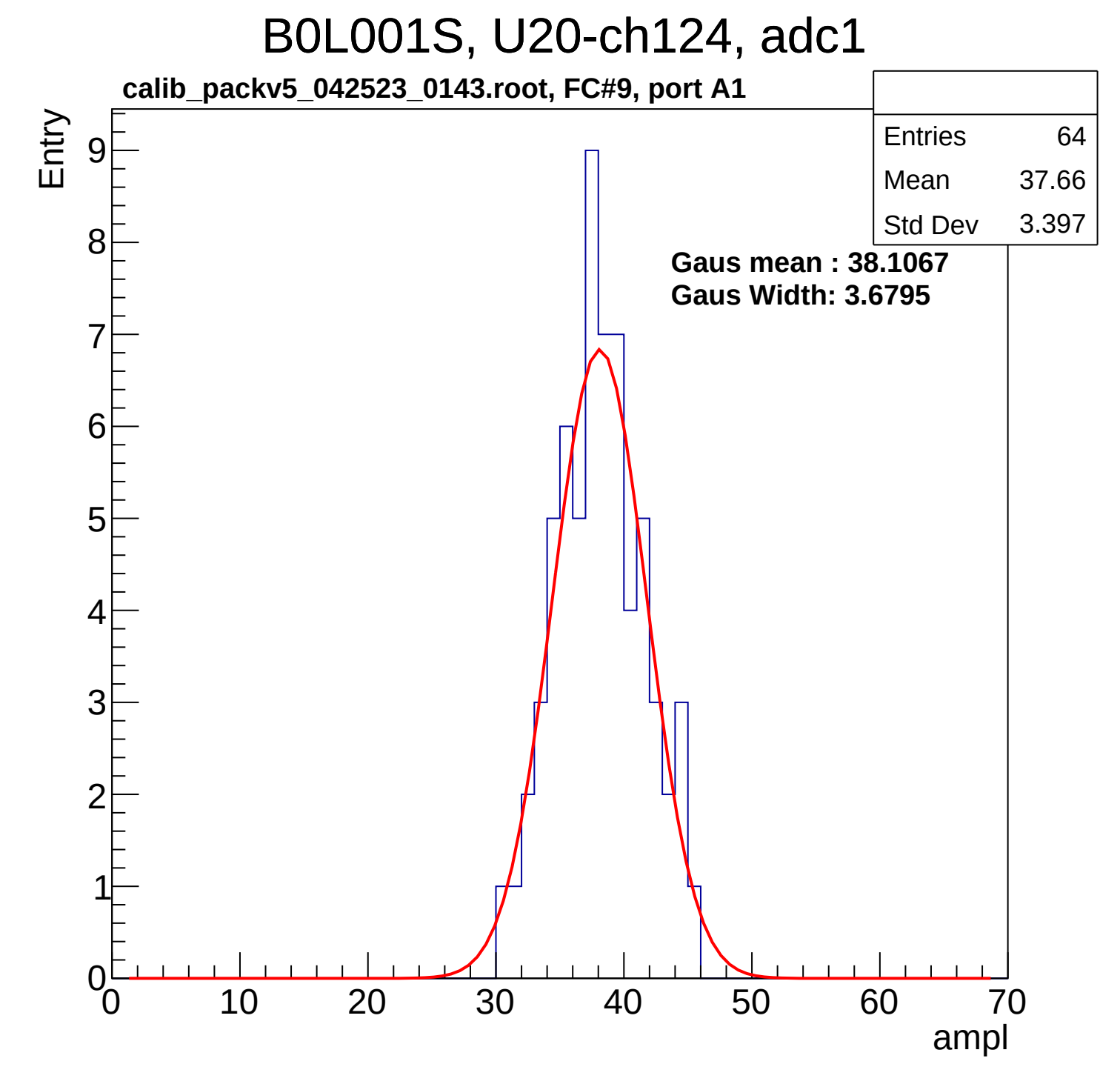
B0L001S, U20-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.66
Std Dev 3.397
Gaus mean : 38.1067
Gaus Width: 3.6795
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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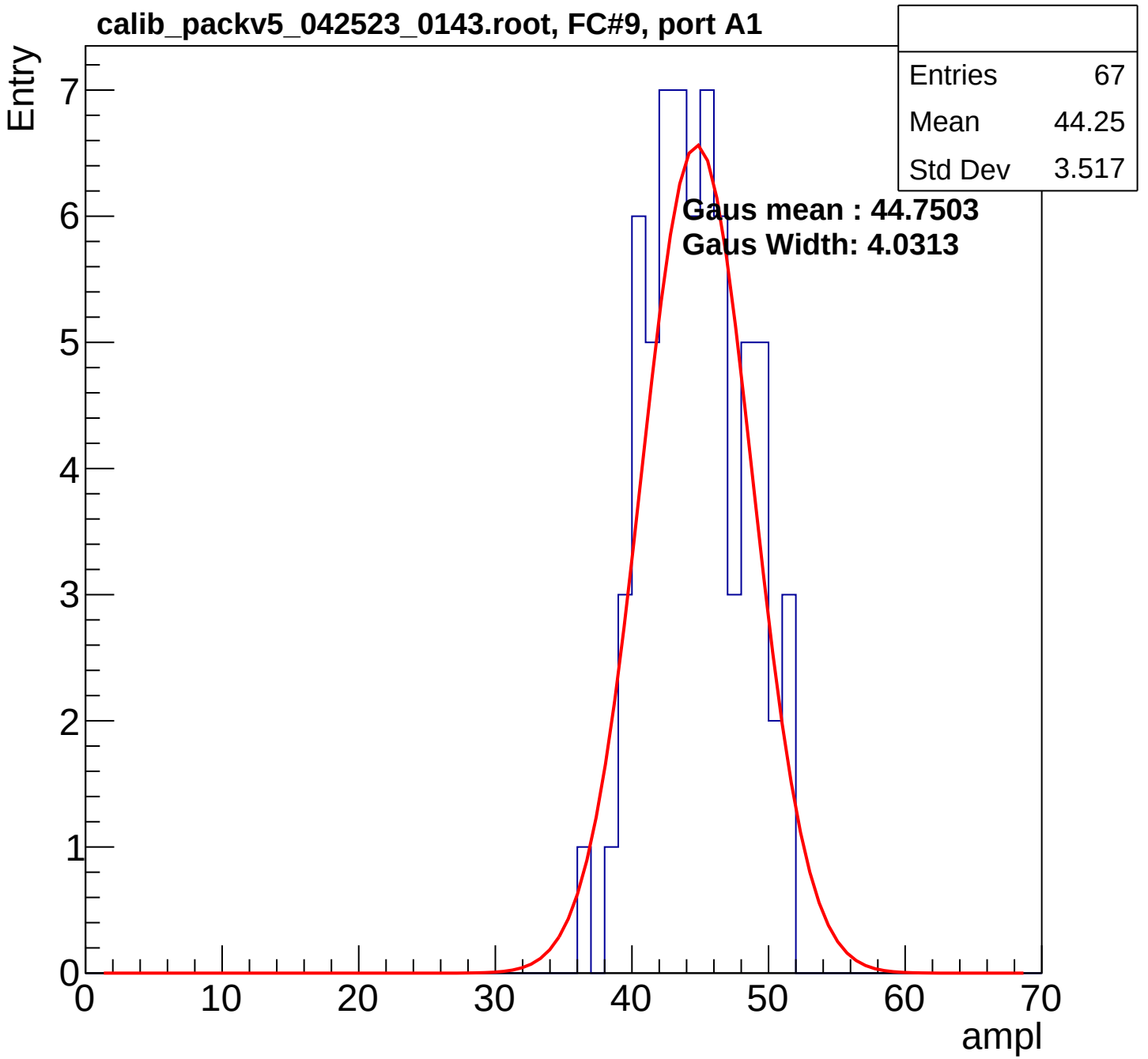
B0L001S, U20-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.25
Std Dev 3.517
Gaus mean : 44.7503
Gaus Width: 4.0313
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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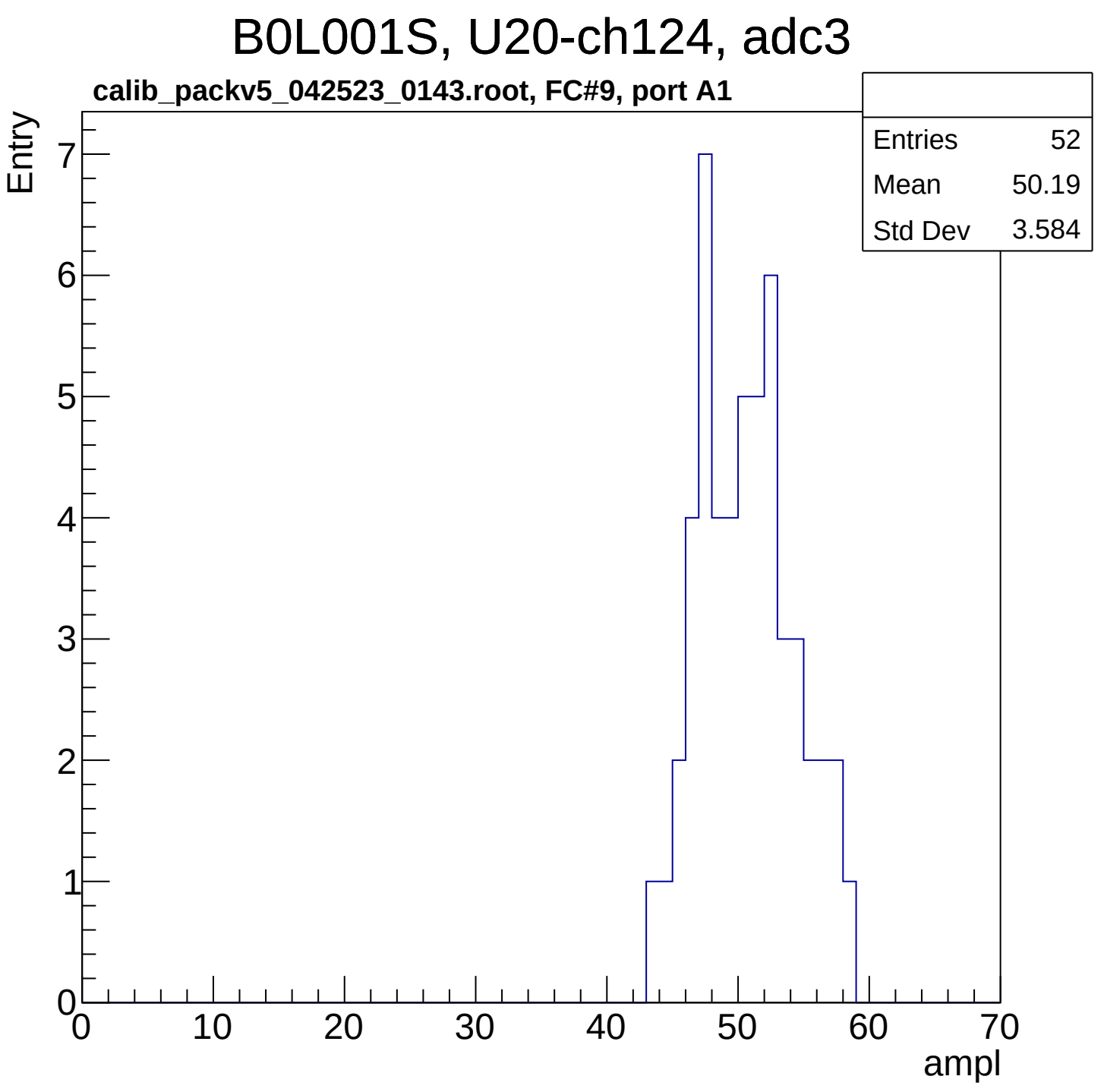
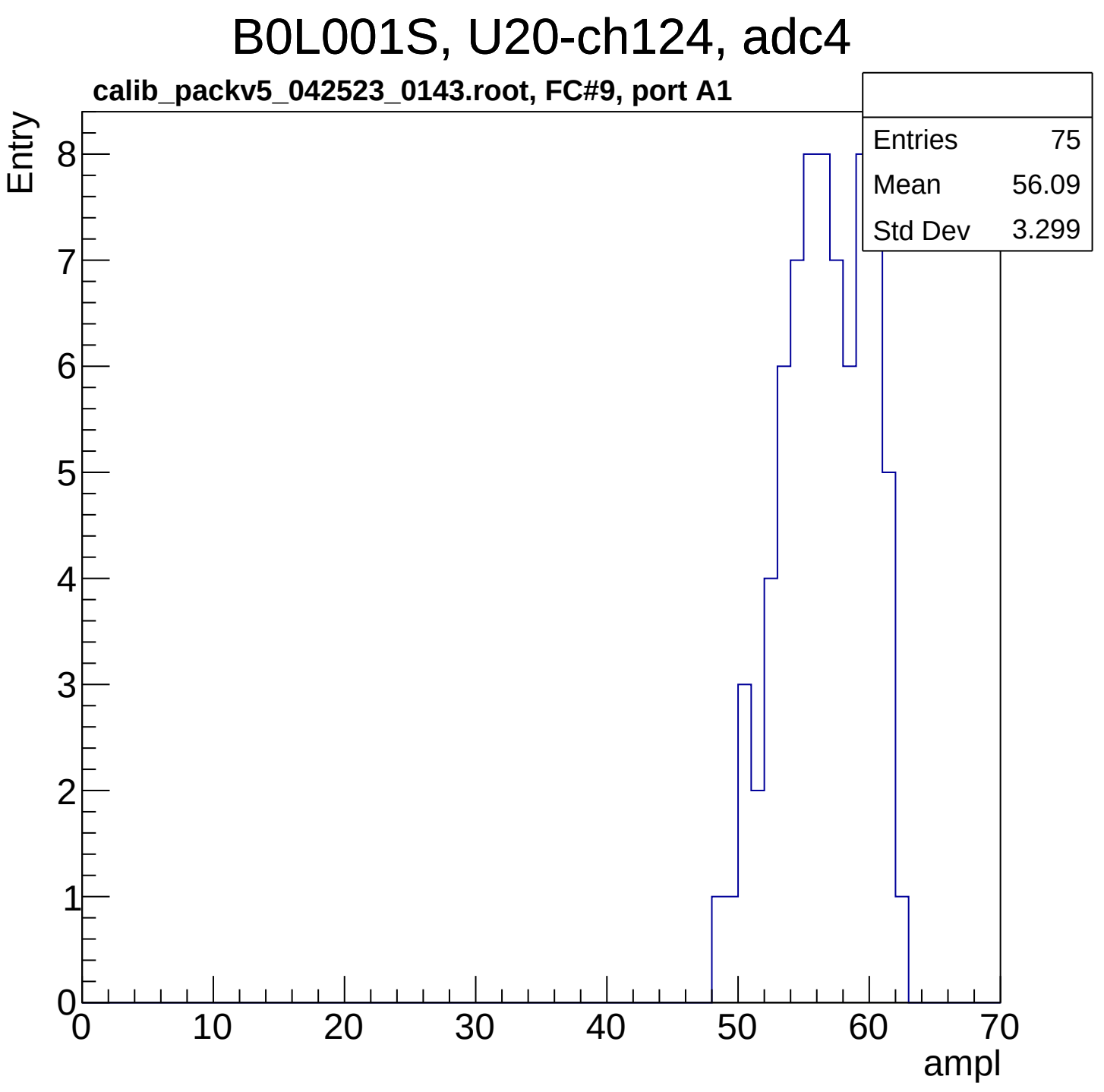

B0L001S, U20-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.19
Std Dev 3.584
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 56.09
Std Dev 3.299
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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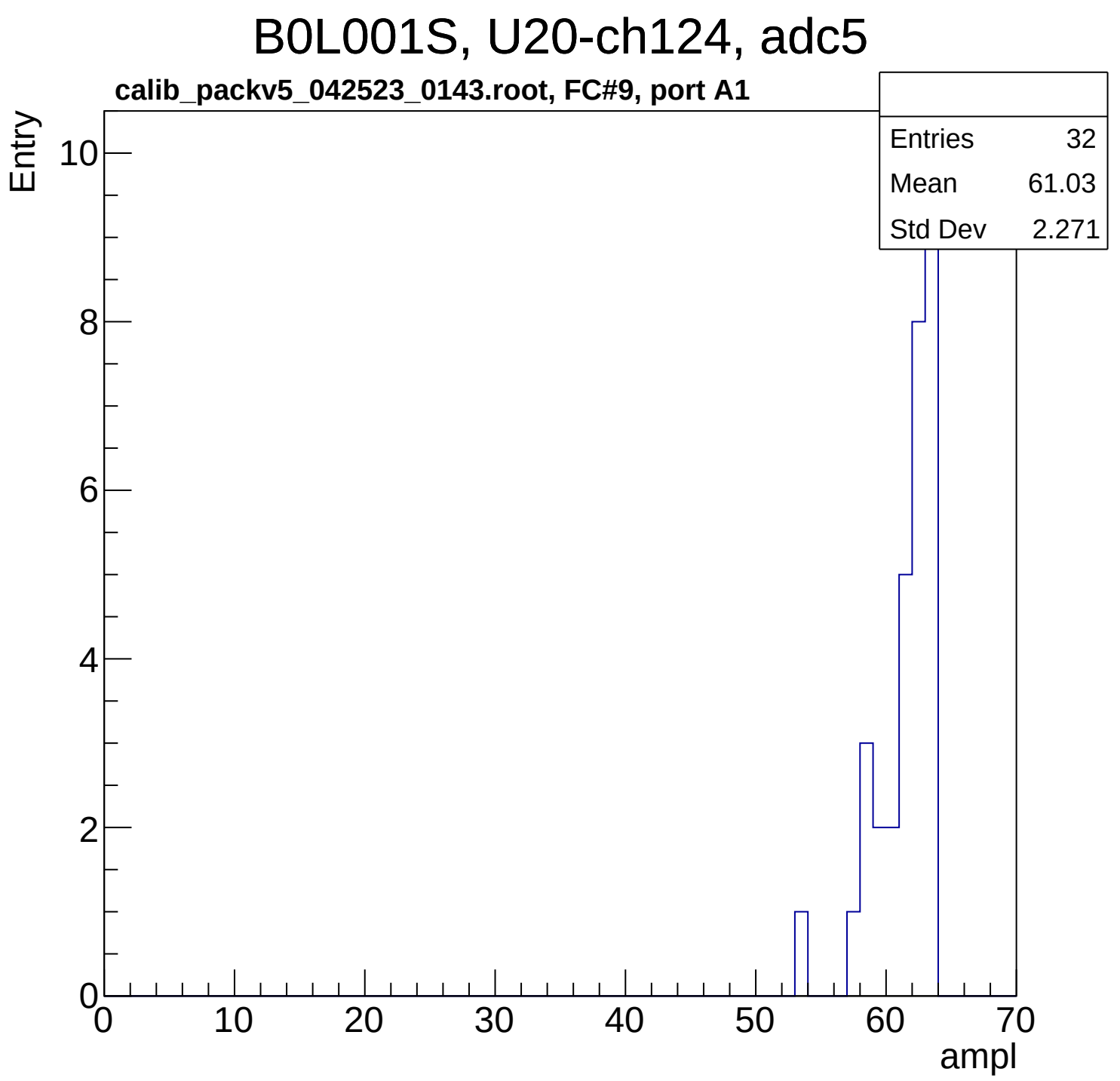
B0L001S, U20-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 61.03
Std Dev 2.271
Entry
0
2
4
6
8
10
0
10
20
30
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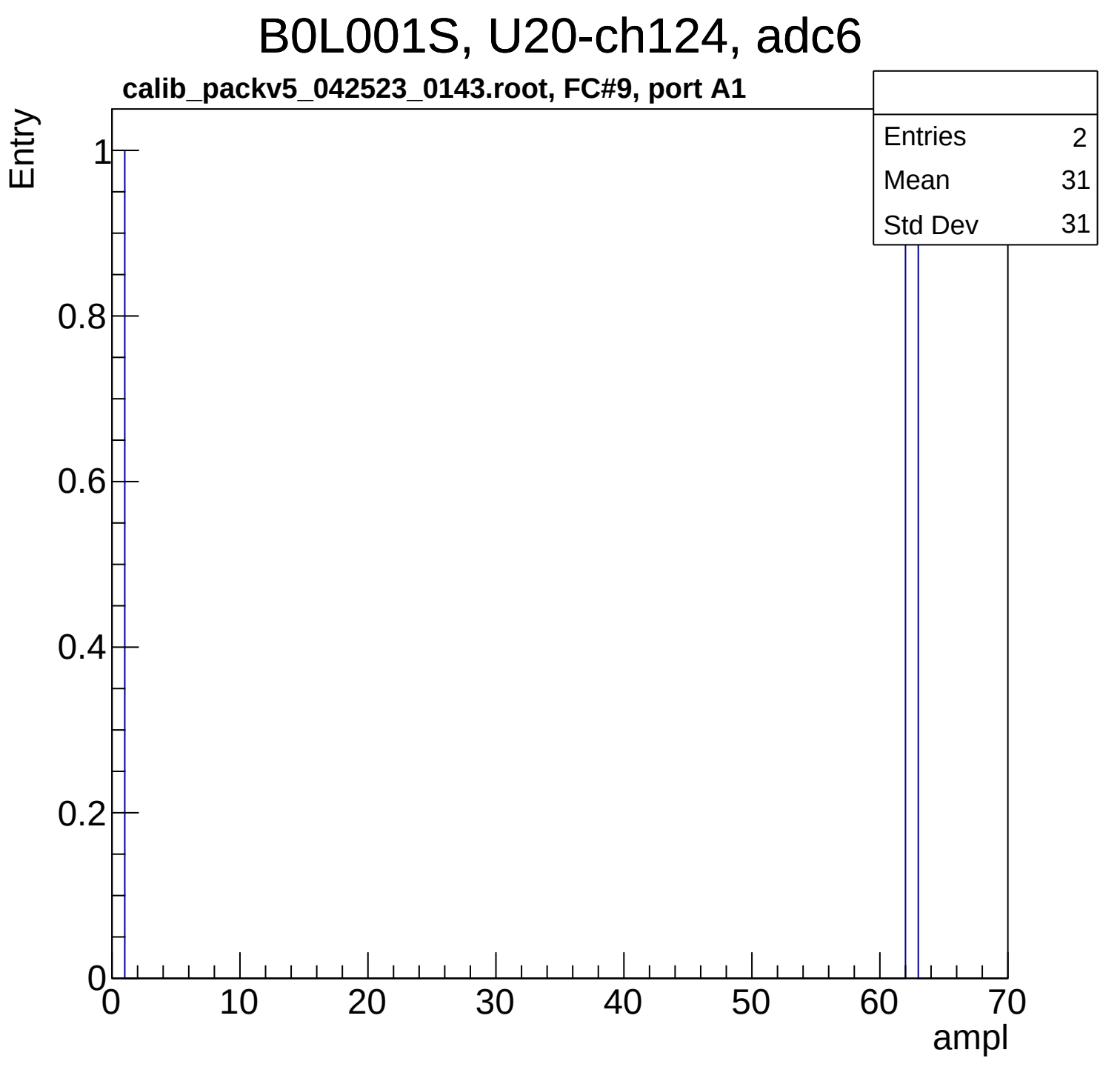
50
60
70
ampl


B0L001S, U20-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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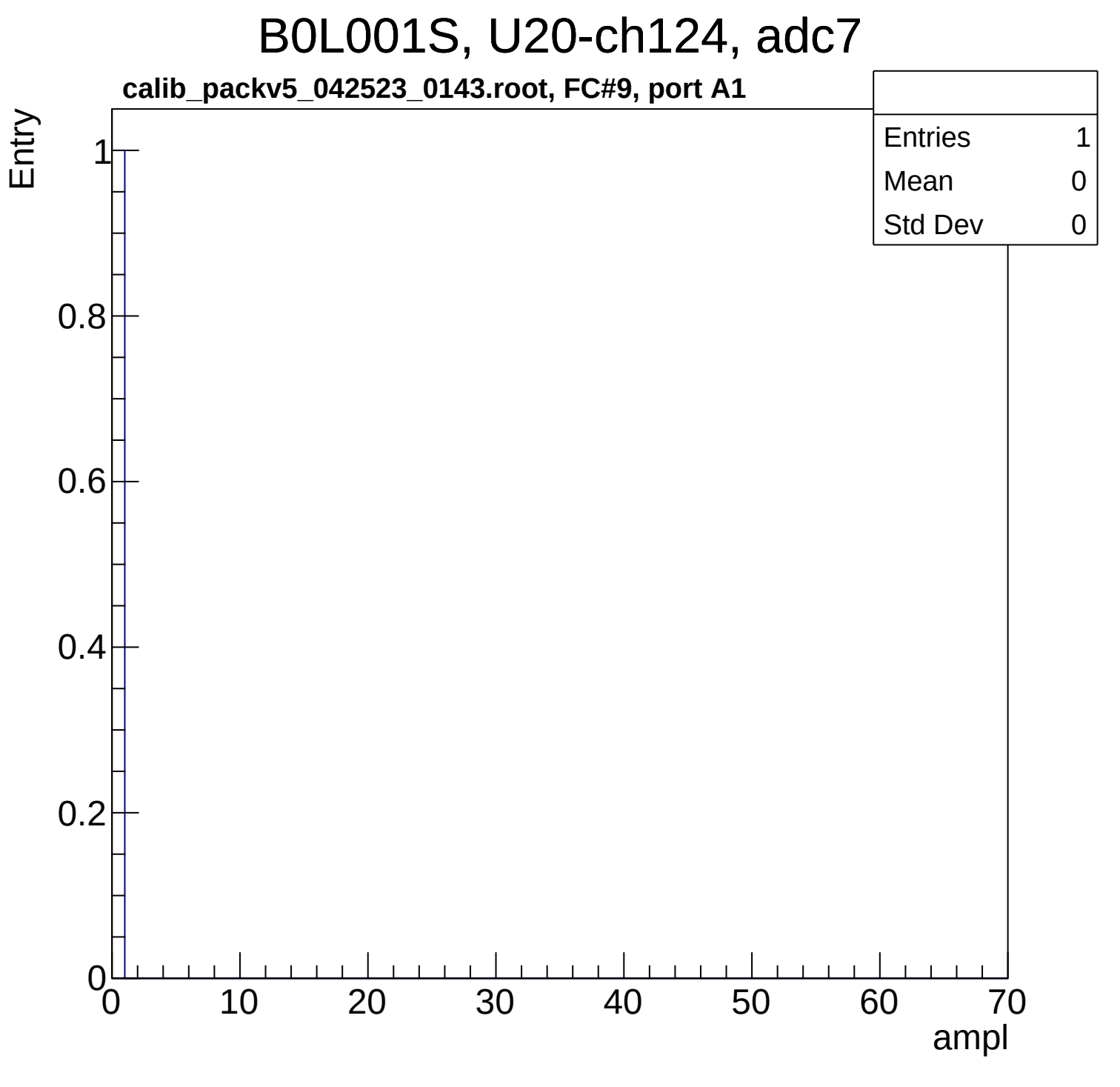
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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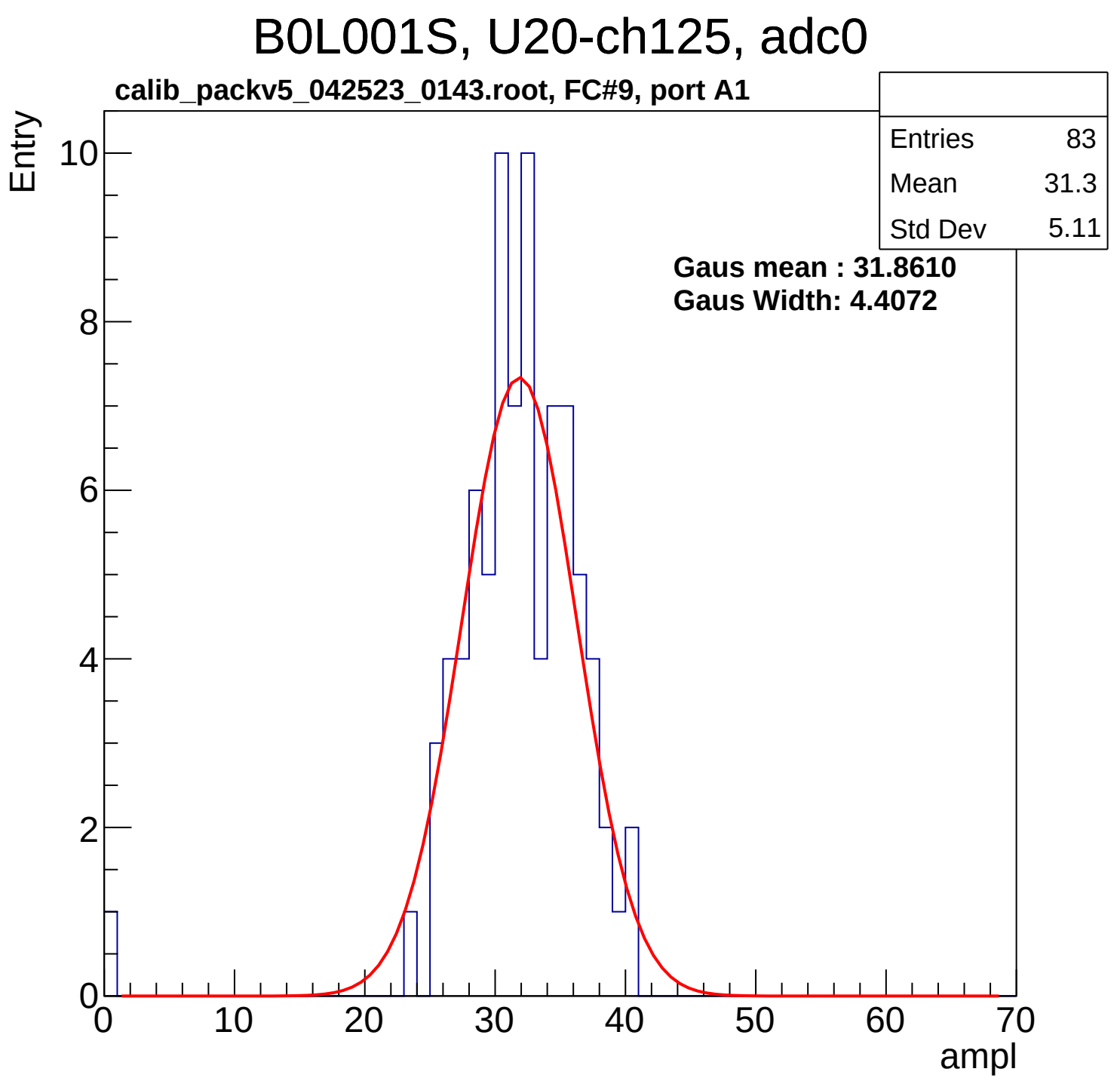
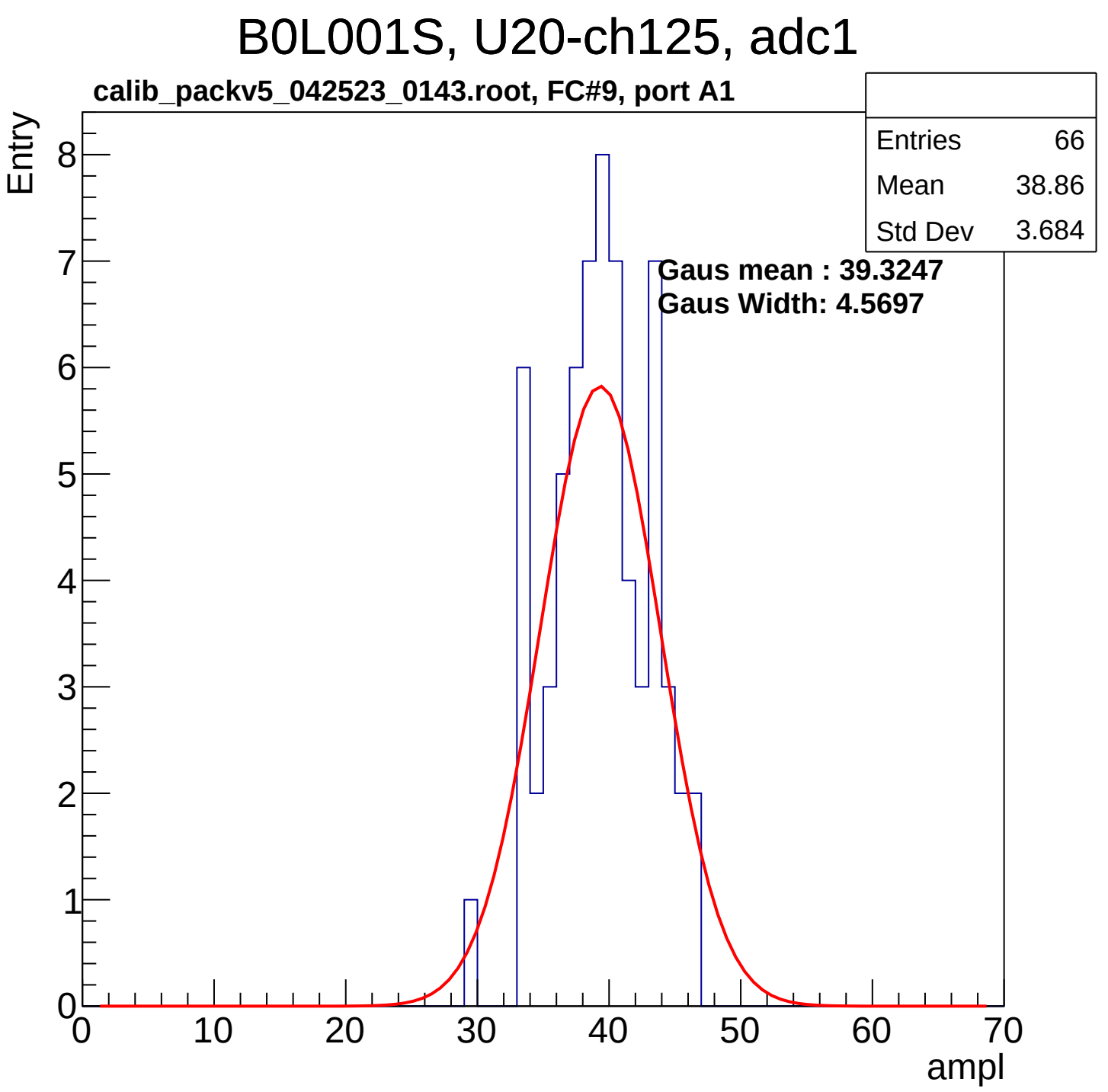
50
60
70
ampl


B0L001S, U20-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 31.3
Std Dev 5.11
Gaus mean : 31.8610
Gaus Width: 4.4072
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U20-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.86
Std Dev 3.684
Gaus mean : 39.3247
Gaus Width: 4.5697
Entry
ampl
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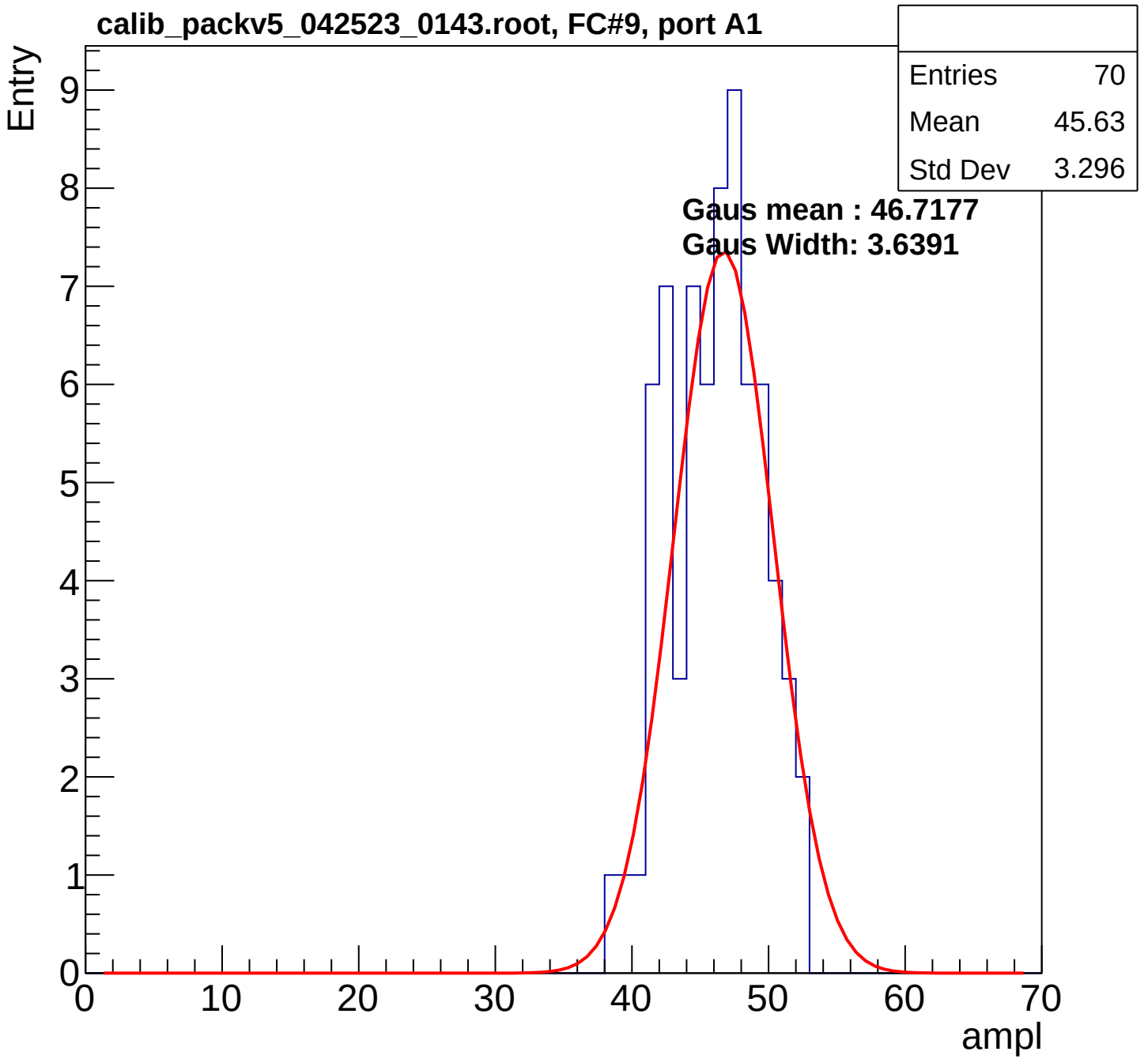
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 45.63
Std Dev 3.296
Gaus mean : 46.7177
Gaus Width: 3.6391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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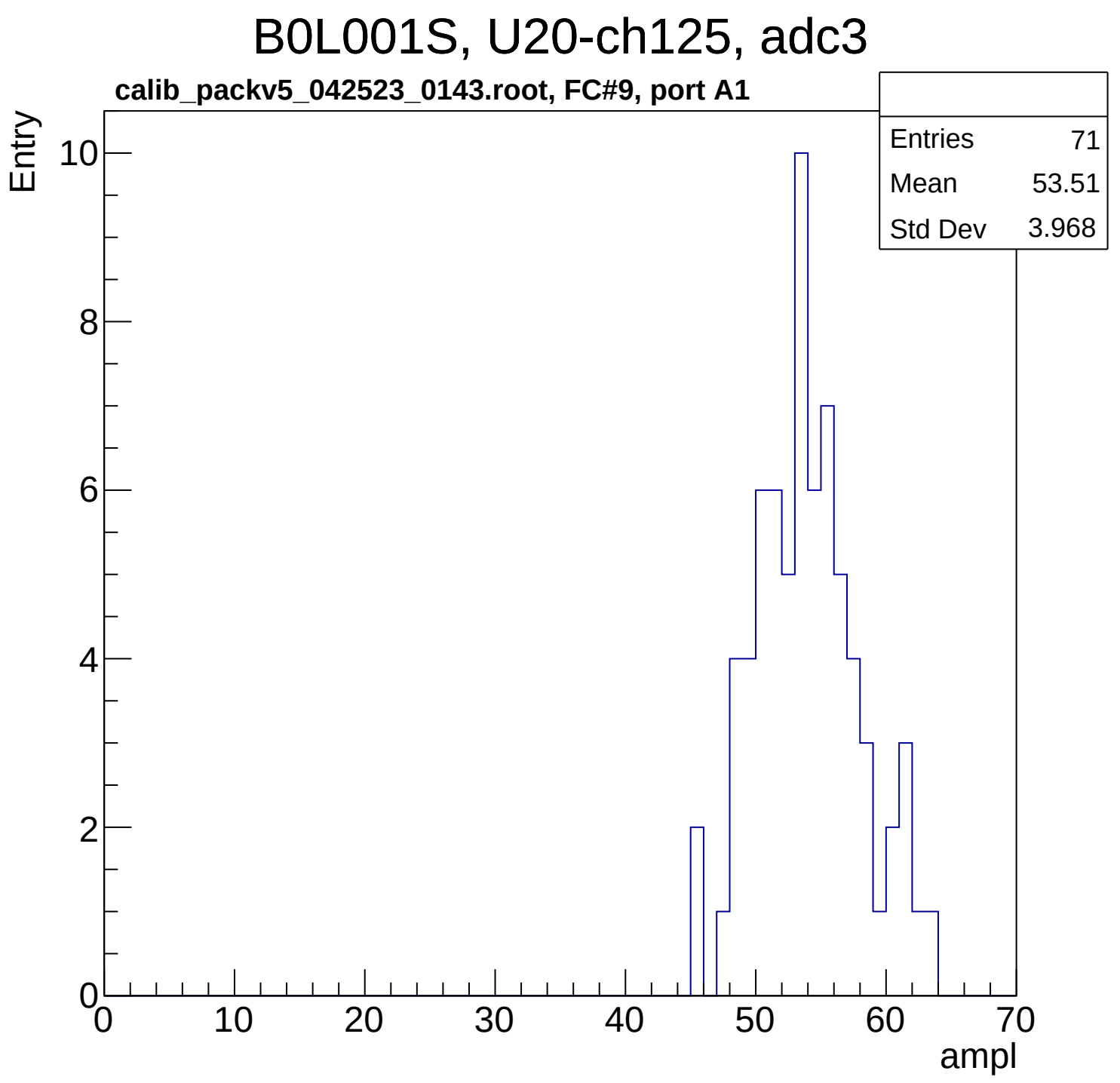
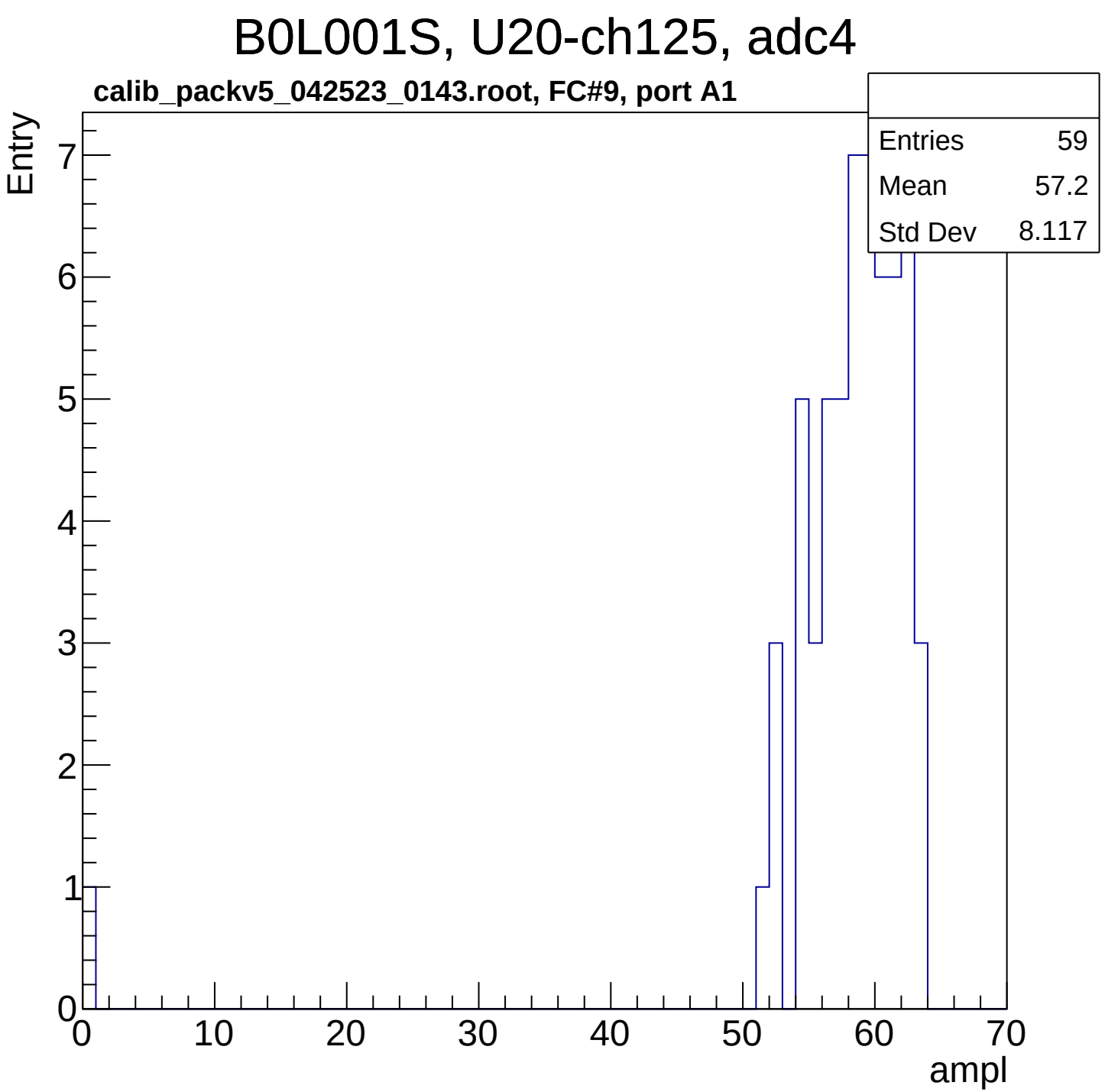

B0L001S, U20-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 53.51
Std Dev 3.968
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 57.2
Std Dev 8.117
Entry
ampl
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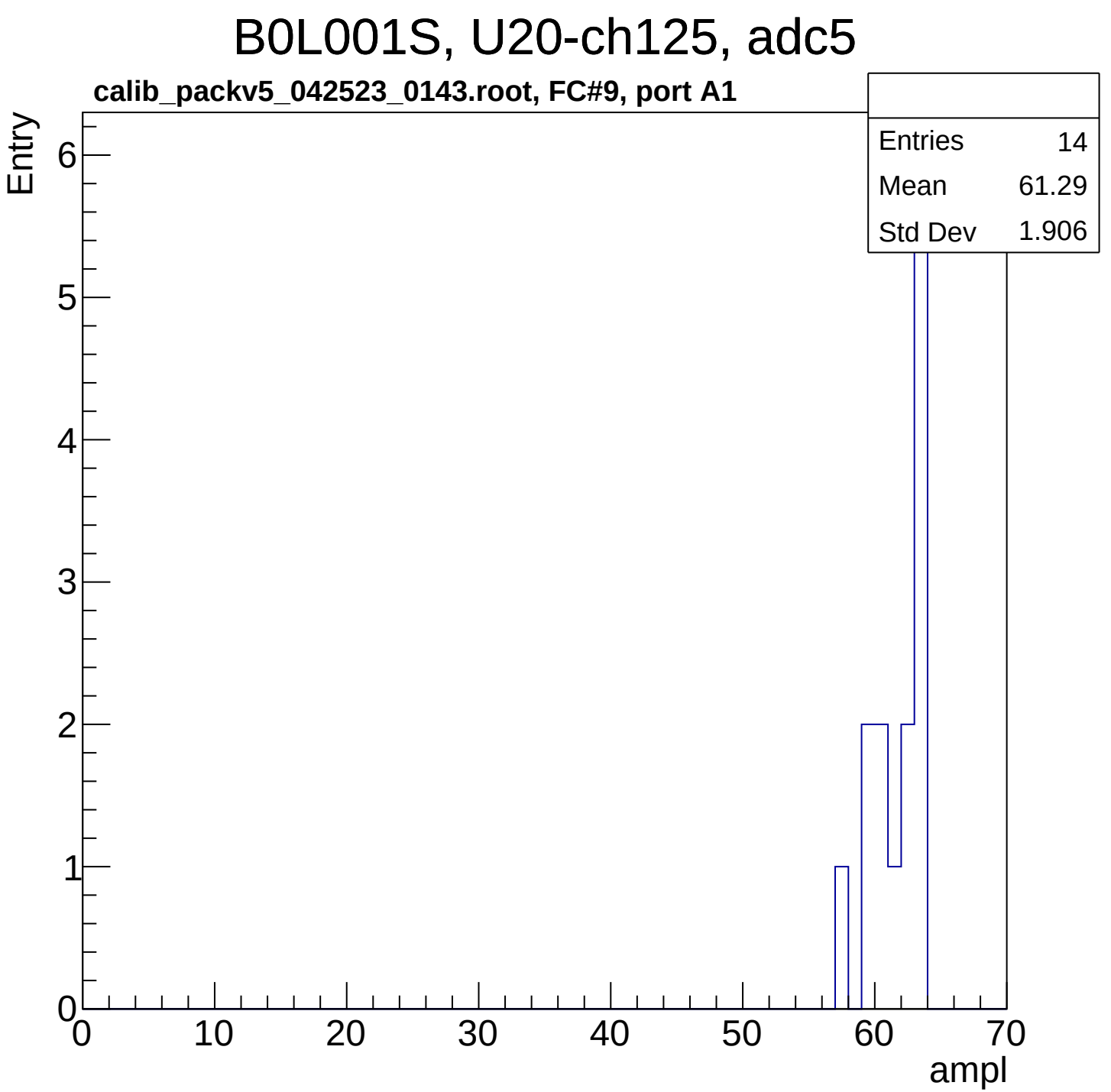
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U20-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 61.29
Std Dev 1.906
Entry
0
1
2
3
4
5
6
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20
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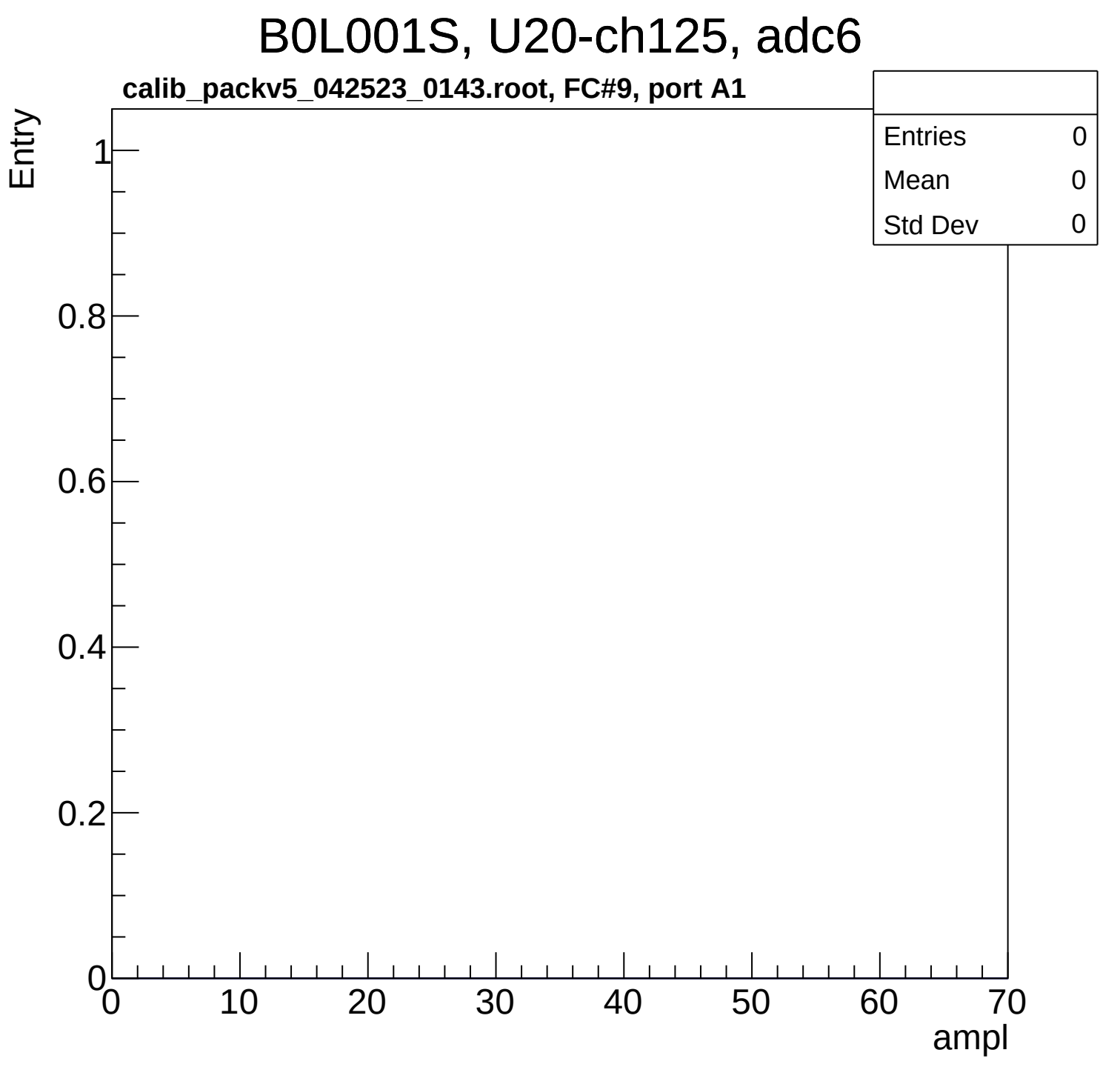
50
60
70
ampl


B0L001S, U20-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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10
20
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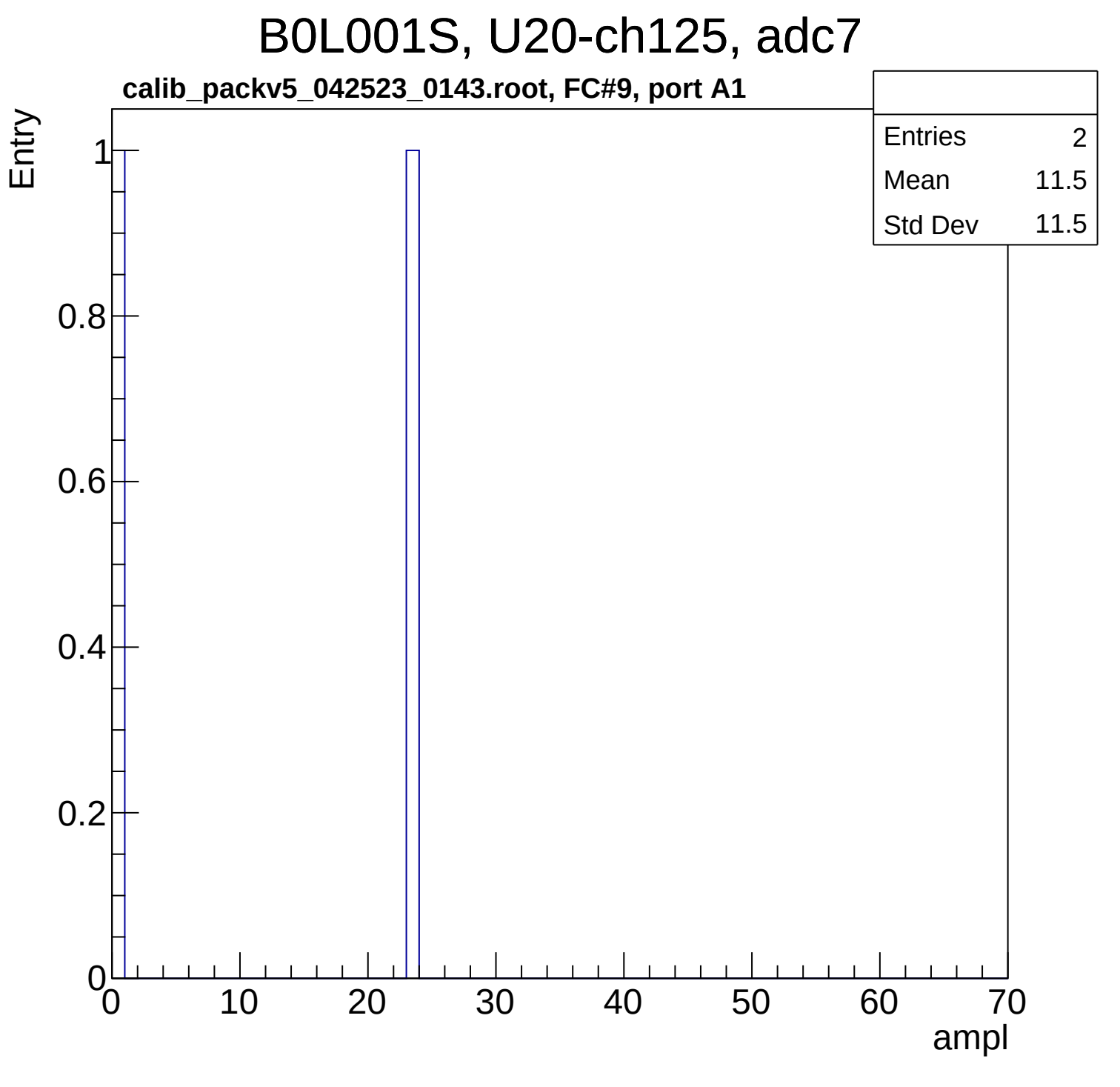
60
70
ampl


B0L001S, U20-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
0
0
10
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30
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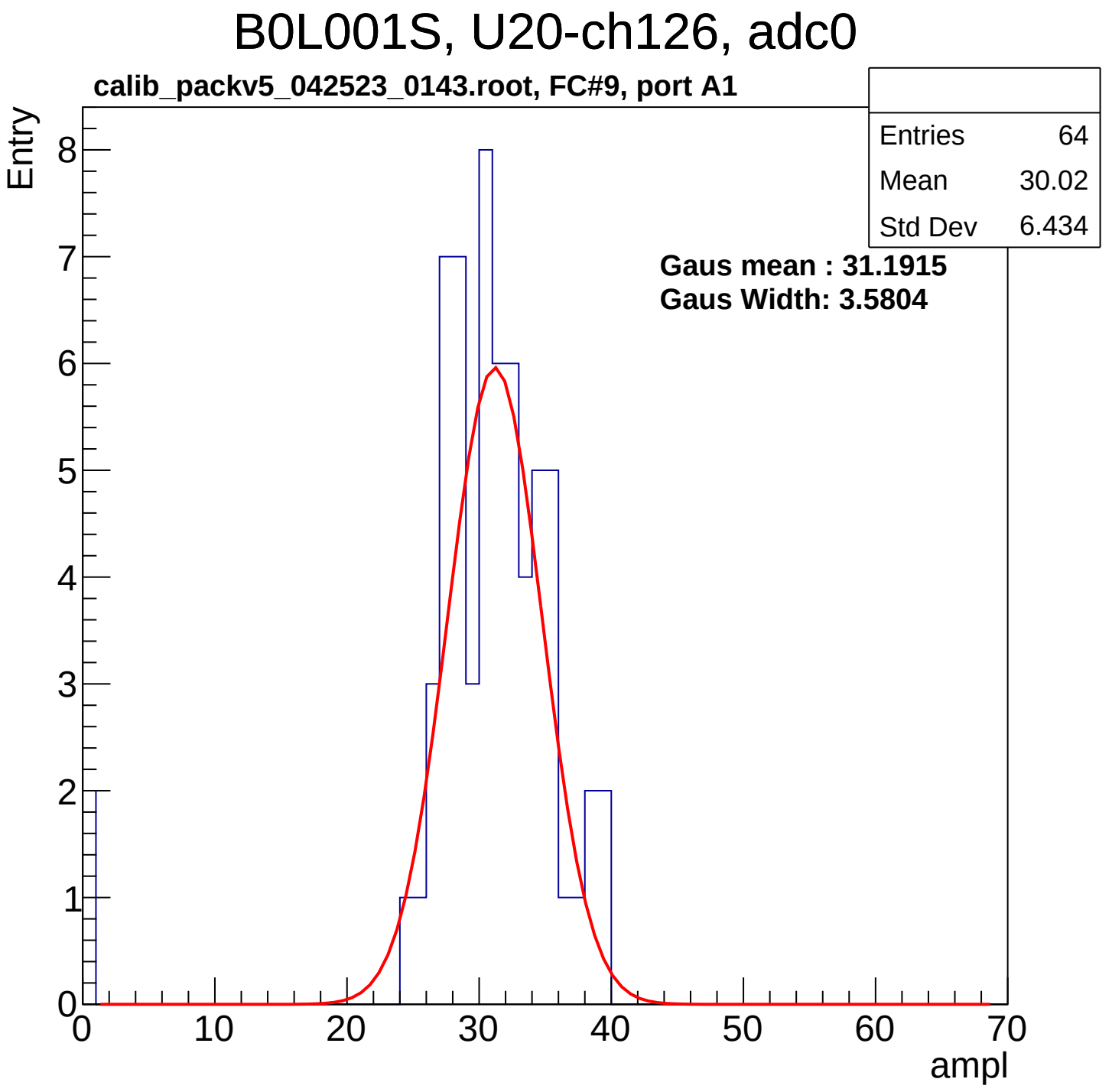
50
60
70
ampl


B0L001S, U20-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.02
Std Dev 6.434
Gaus mean : 31.1915
Gaus Width: 3.5804
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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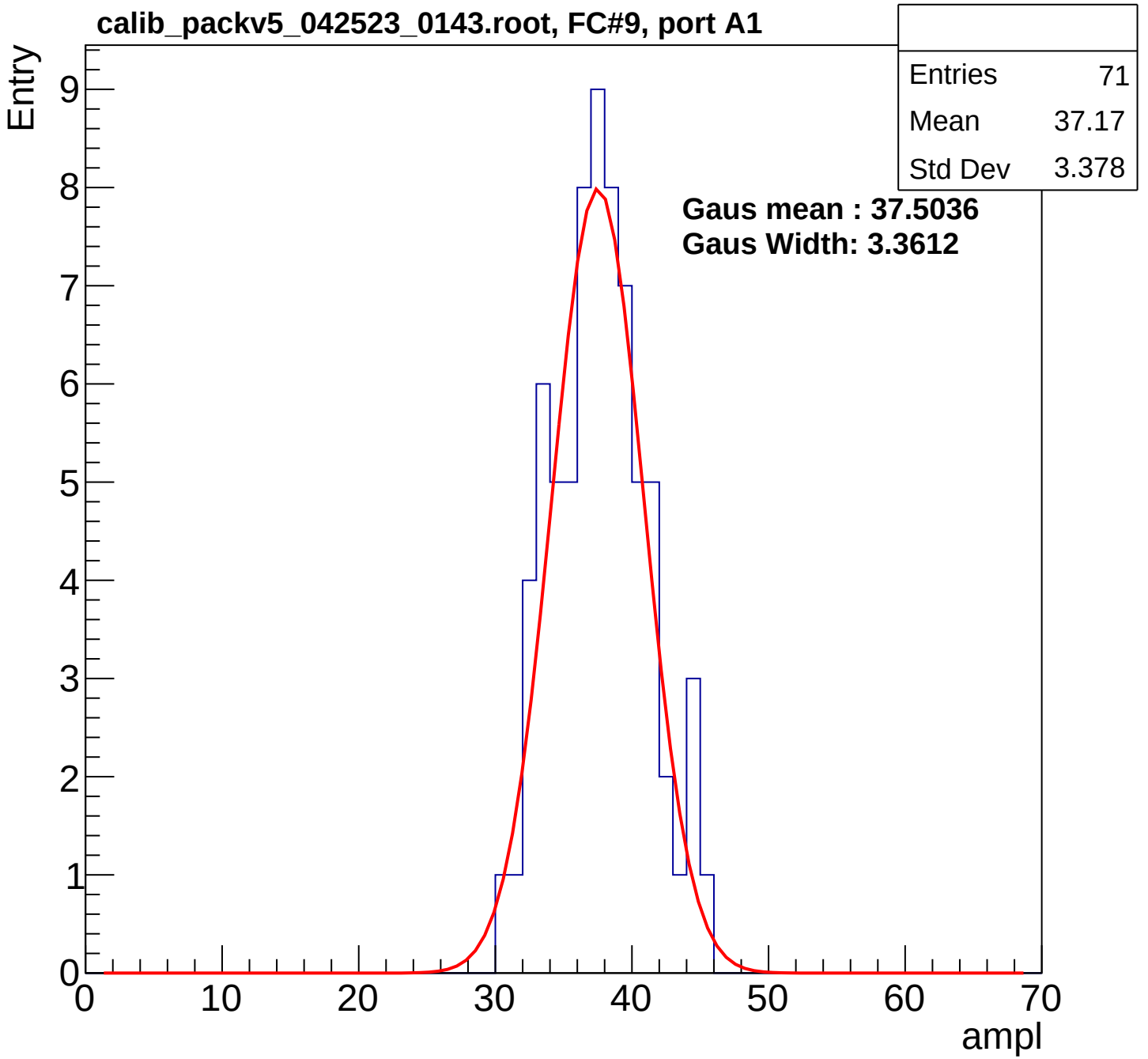
60
70


B0L001S, U20-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.17
Std Dev 3.378
Gaus mean : 37.5036
Gaus Width: 3.3612
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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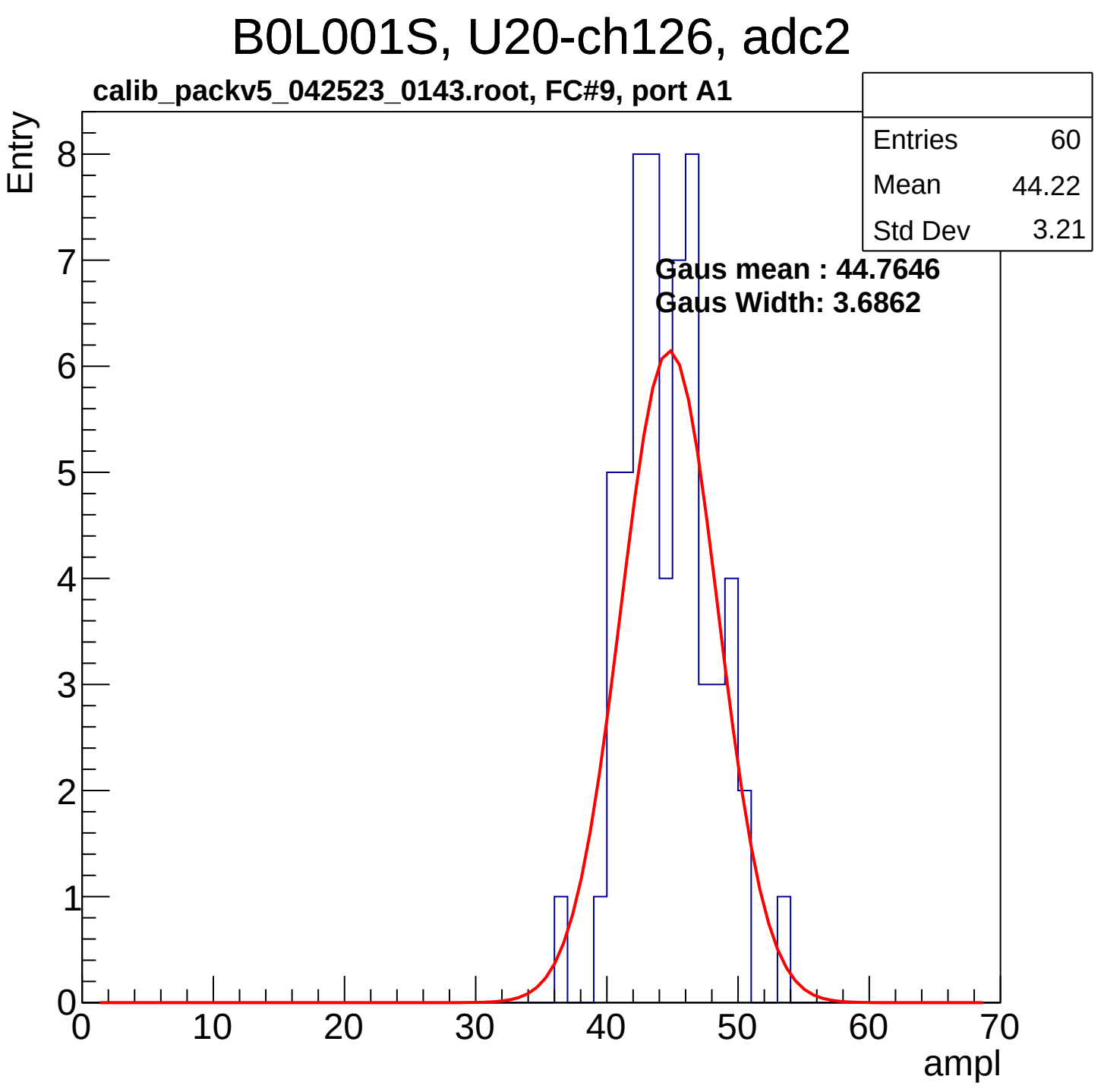
B0L001S, U20-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.22
Std Dev 3.21
Gaus mean : 44.7646
Gaus Width: 3.6862
Entry
ampl
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4
5
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0
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20
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60
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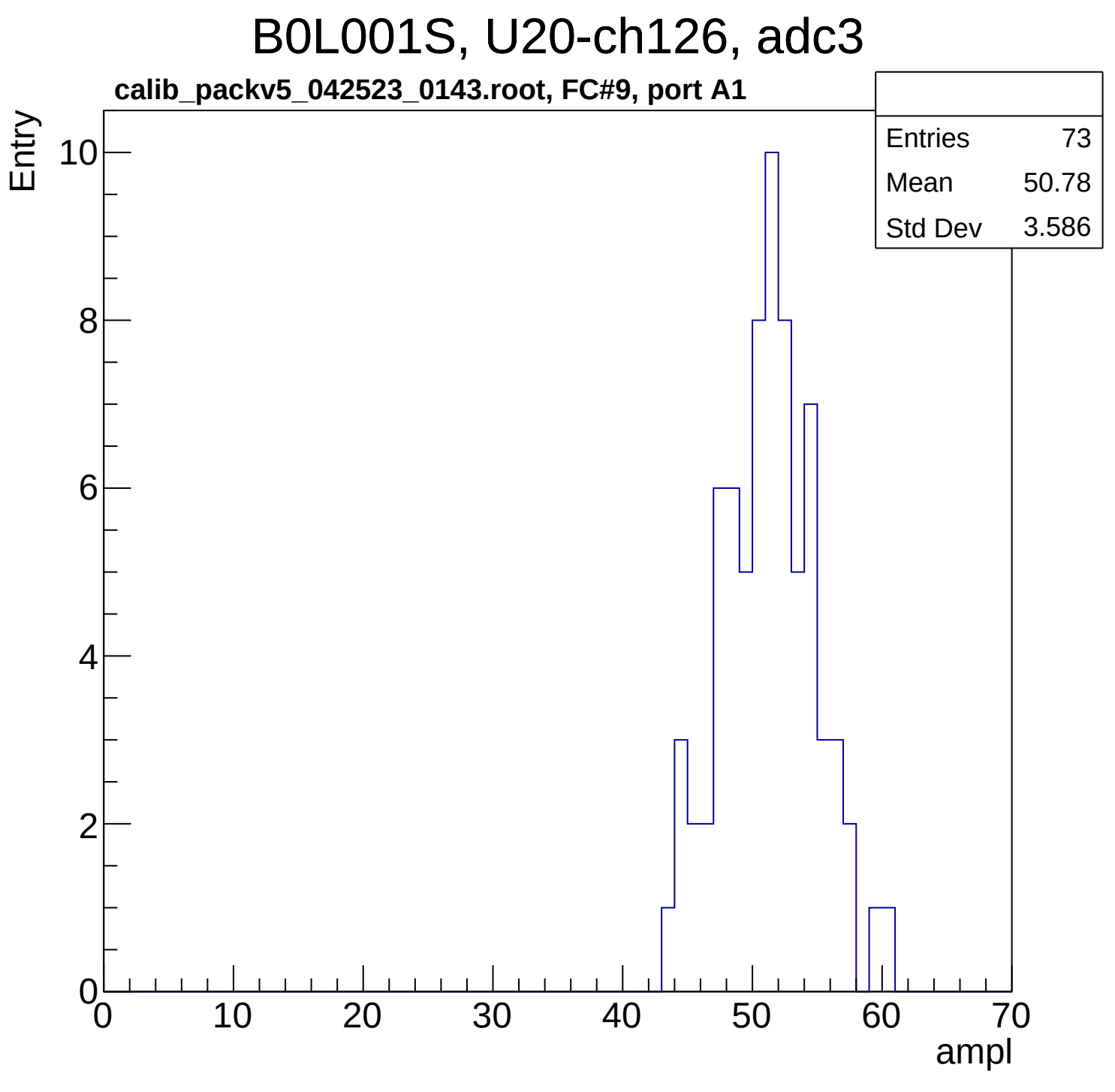
B0L001S, U20-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 50.78
Std Dev 3.586
Entry
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10
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30
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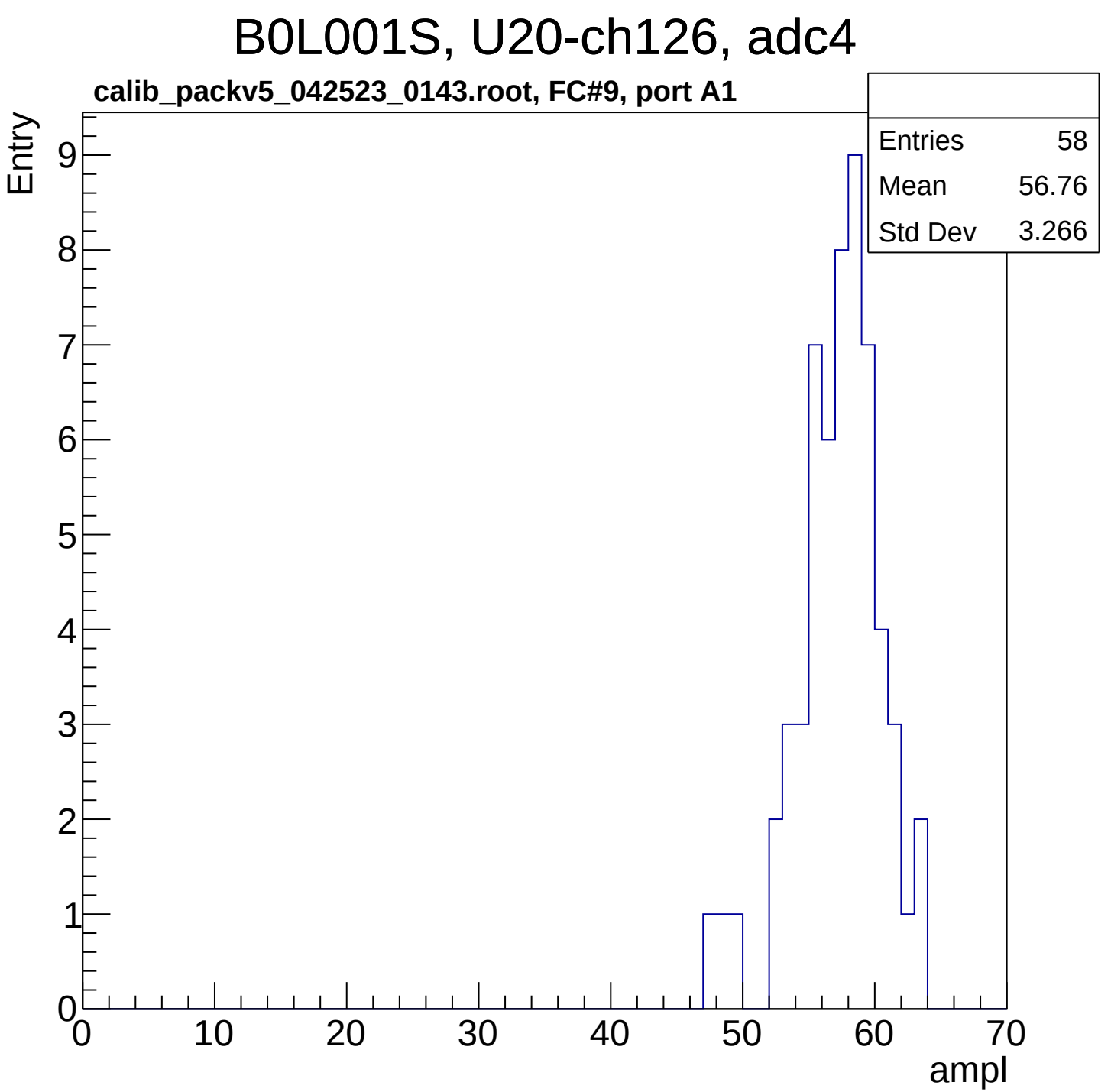
50
60
70
ampl


B0L001S, U20-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.76
Std Dev 3.266
Entry
ampl
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6
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0
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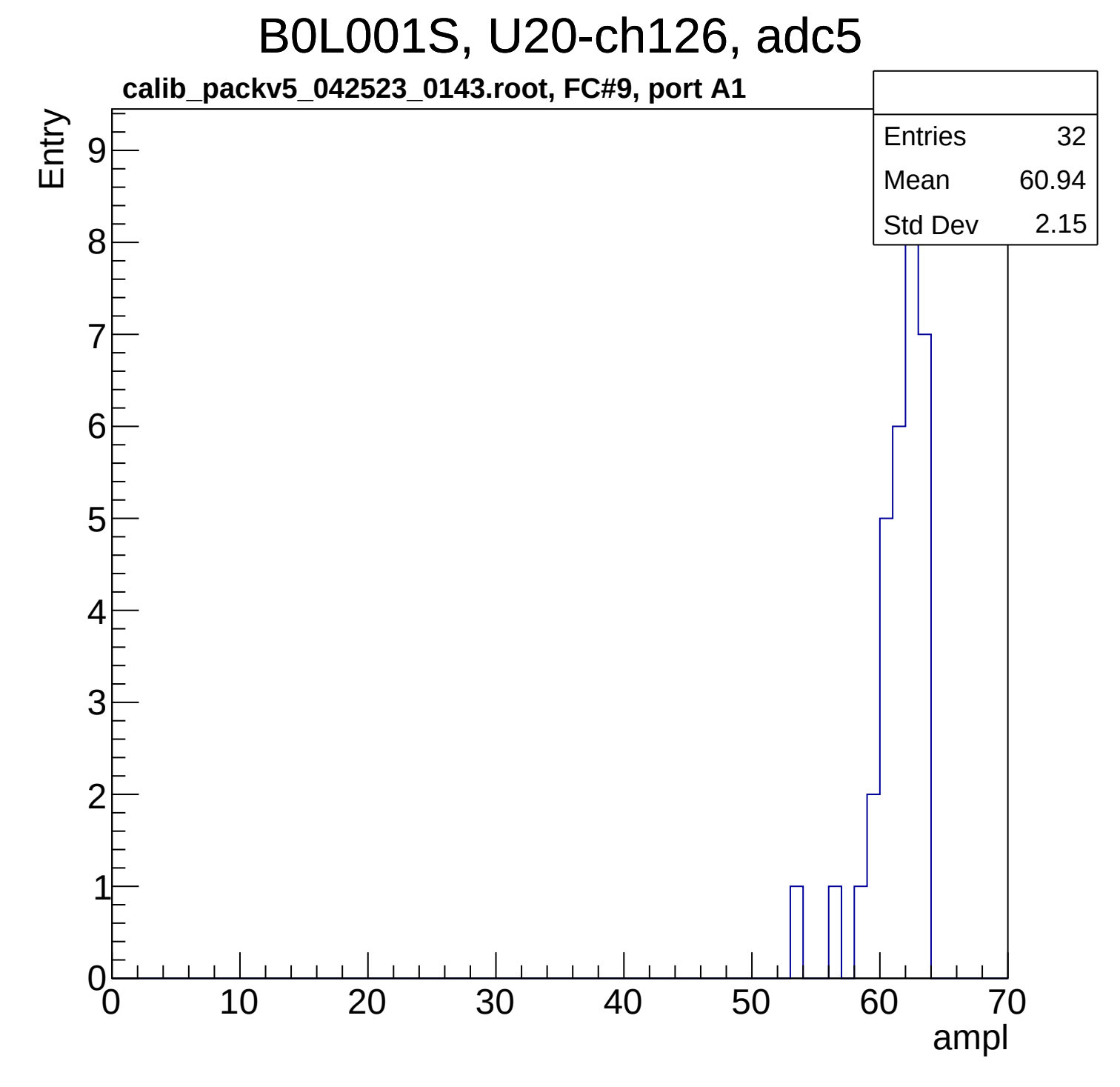
B0L001S, U20-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.94
Std Dev 2.15
Entry
ampl
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2
3
4
5
6
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0
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60
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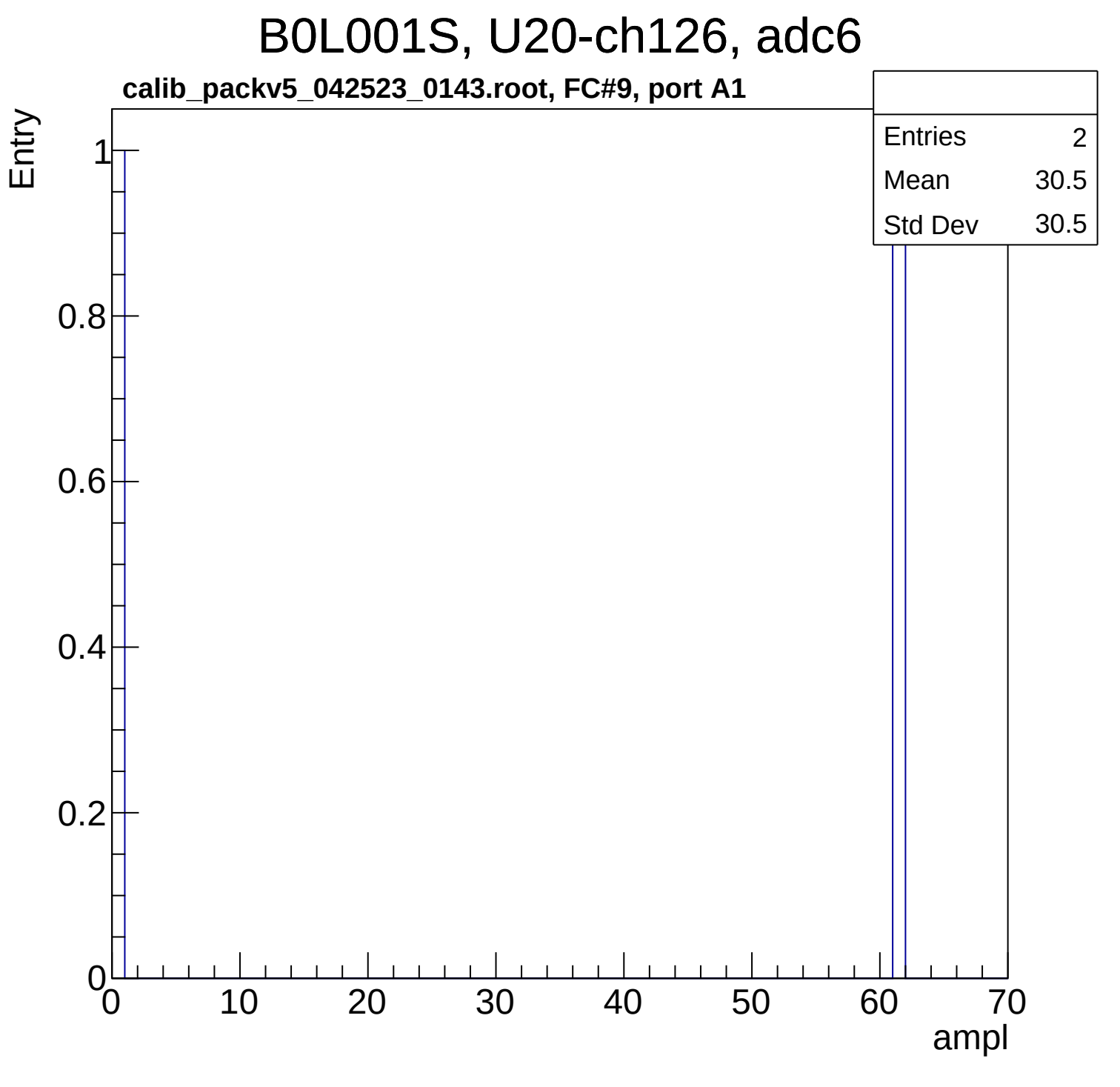
B0L001S, U20-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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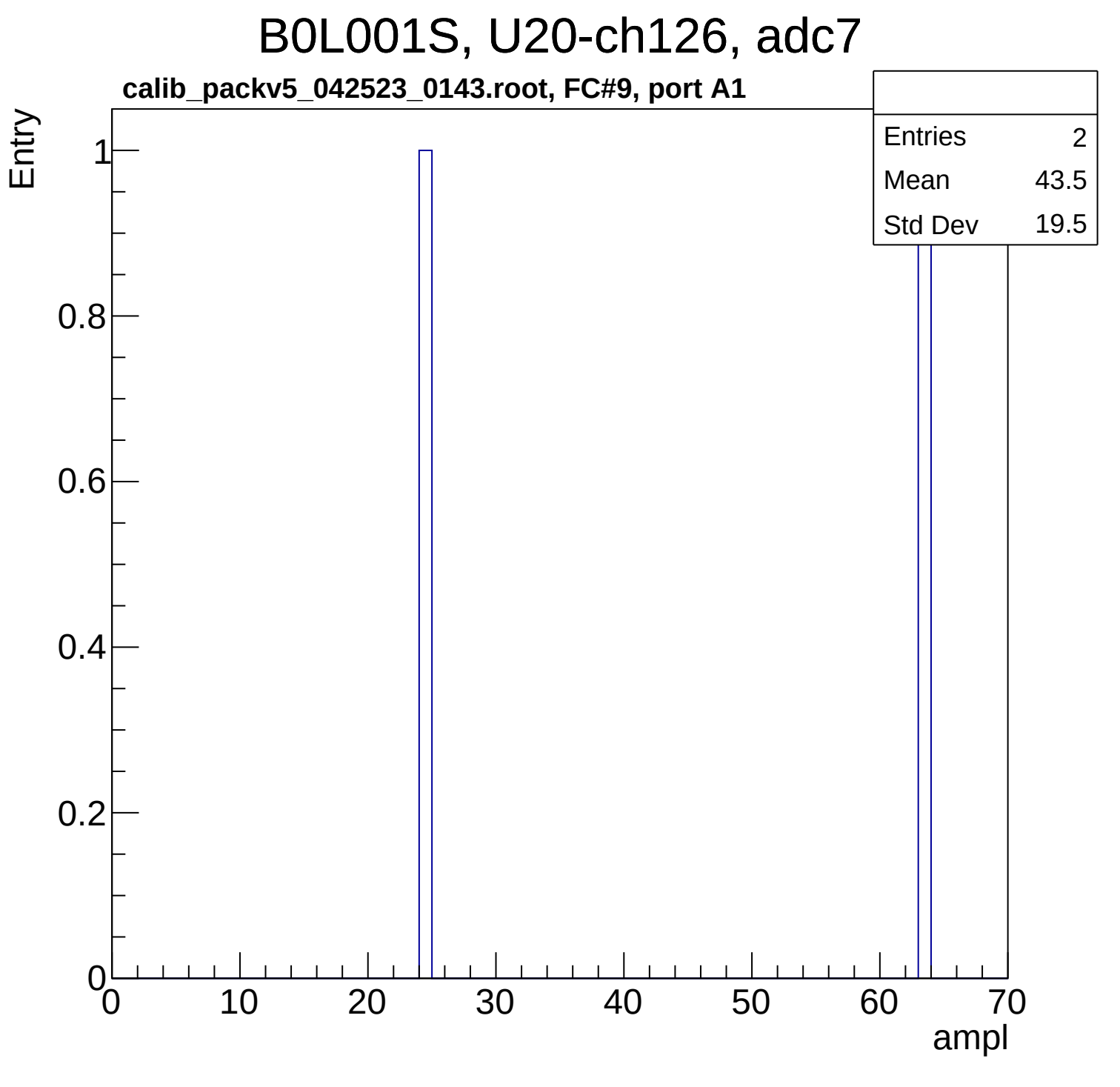
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 43.5
Std Dev 19.5
Entry
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0.6
0.4
0.2
0
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20
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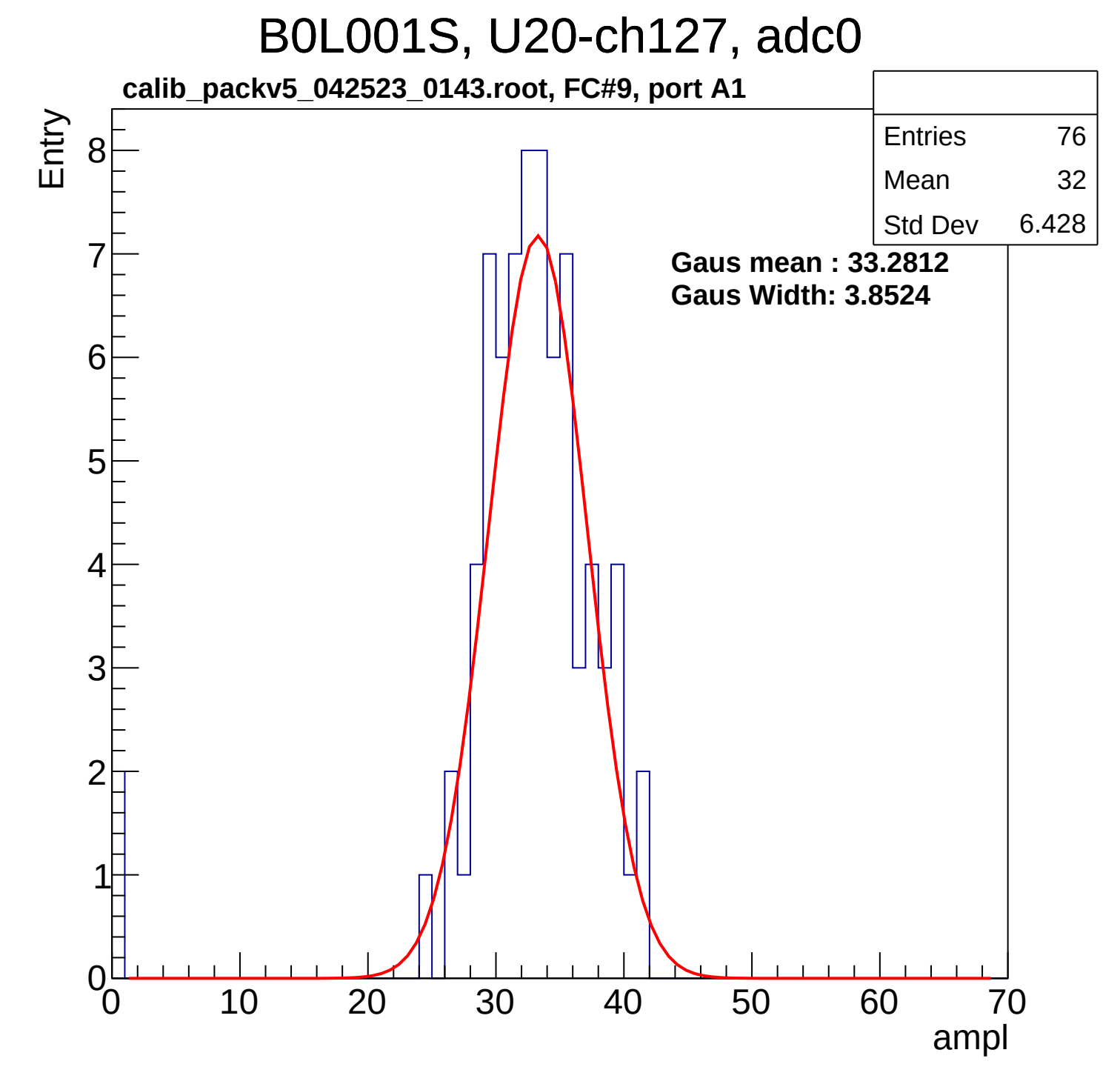
50
60
70
ampl


B0L001S, U20-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 32
Std Dev 6.428
Gaus mean : 33.2812
Gaus Width: 3.8524
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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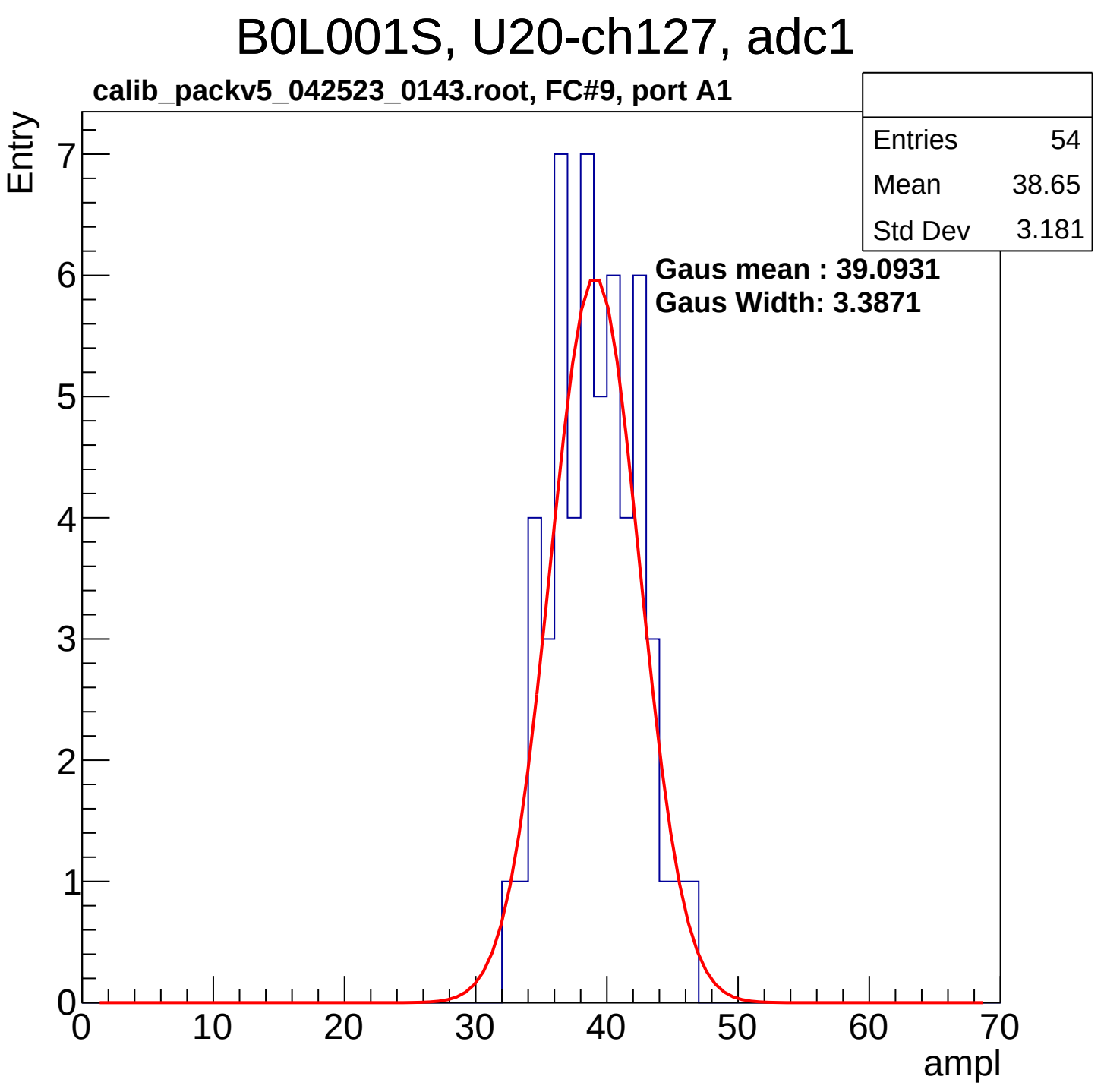
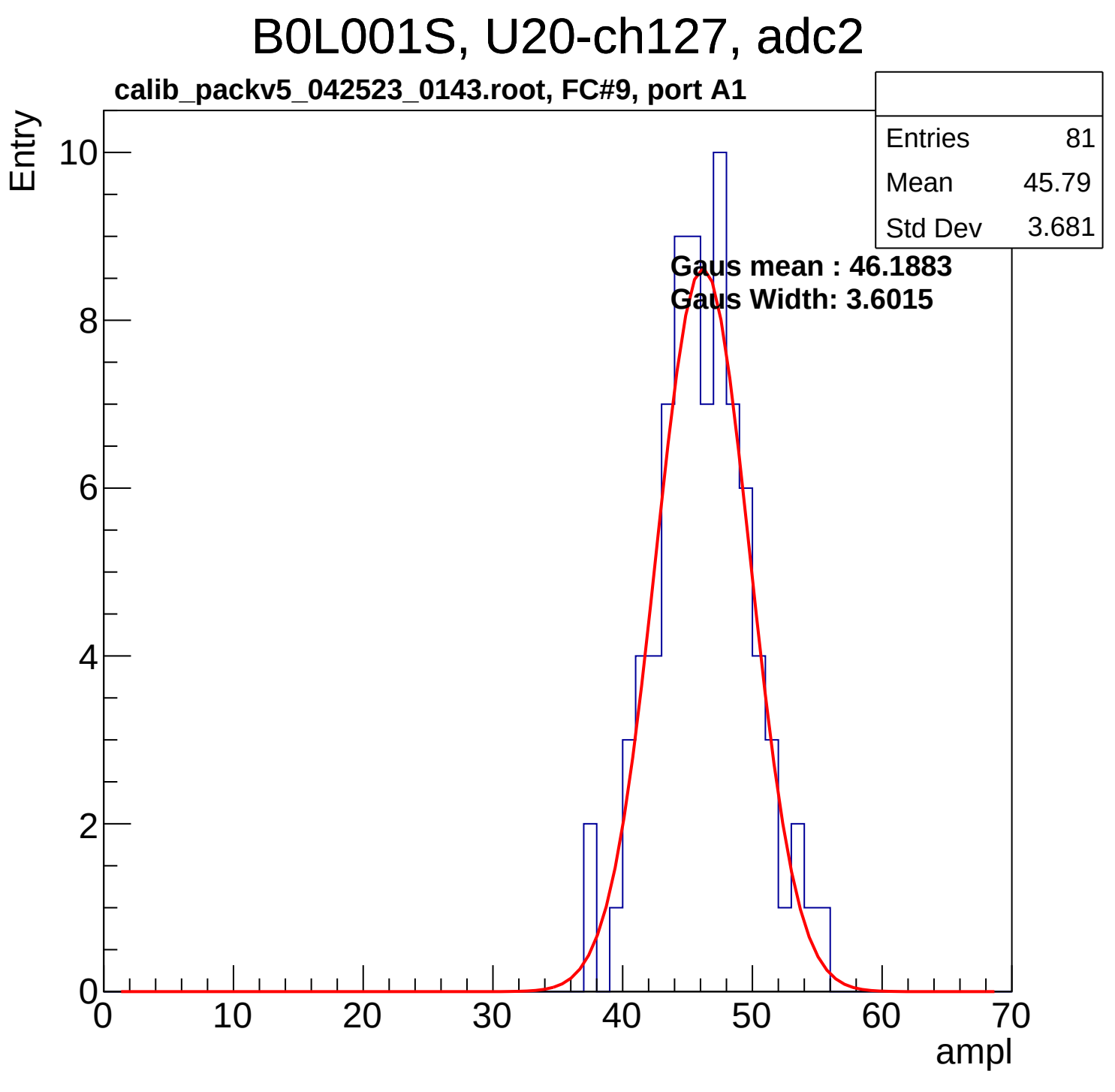
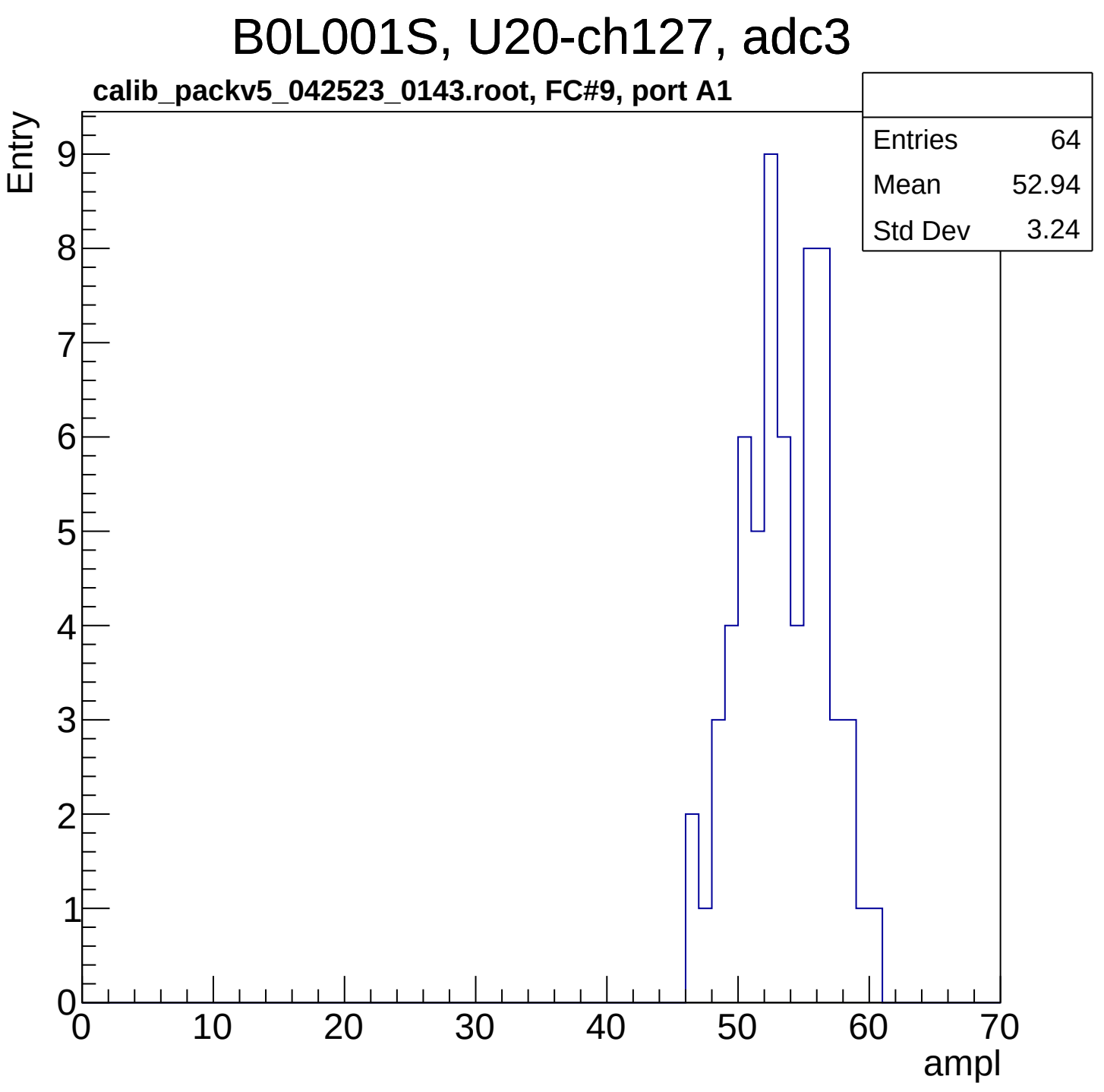
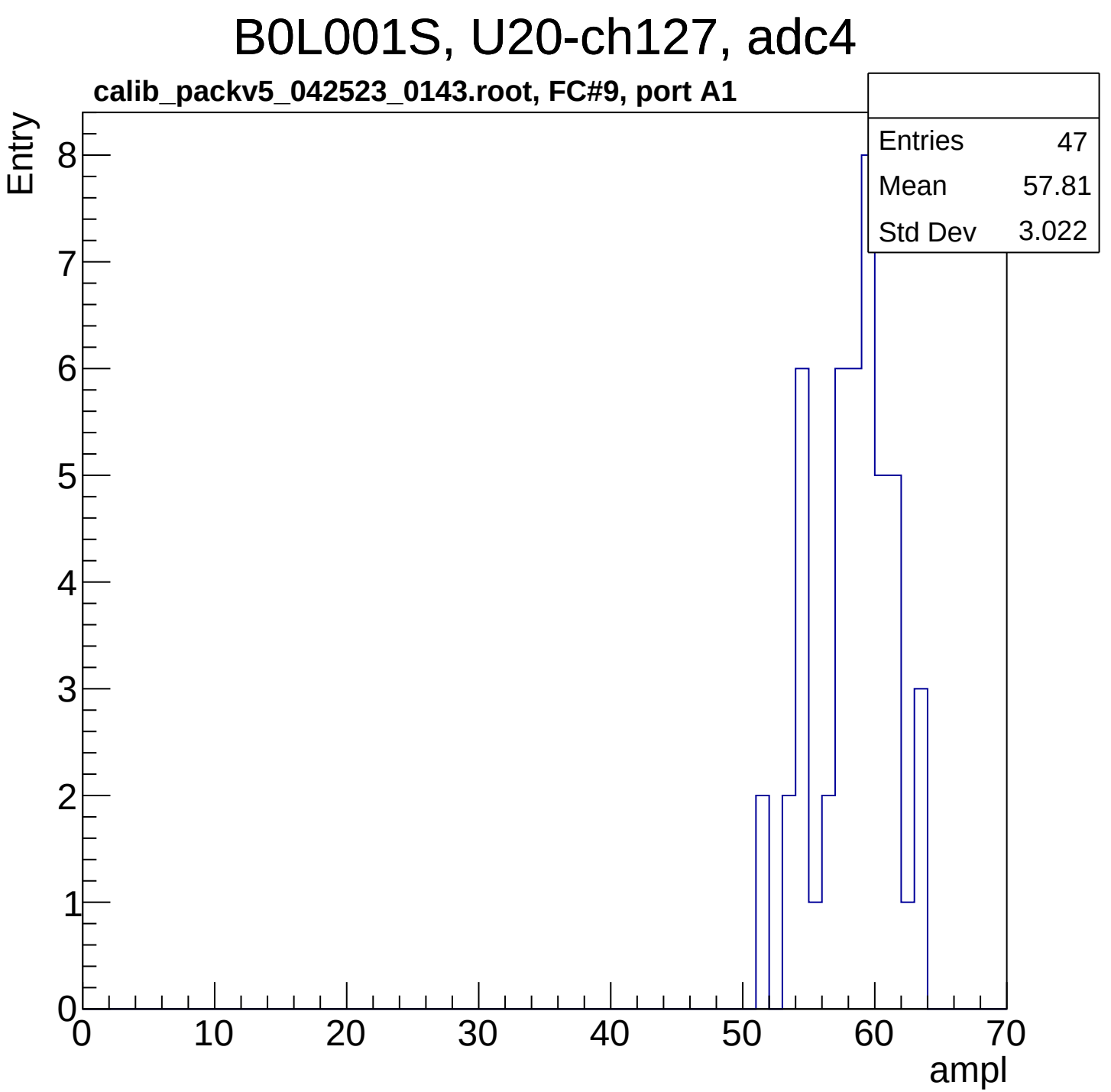
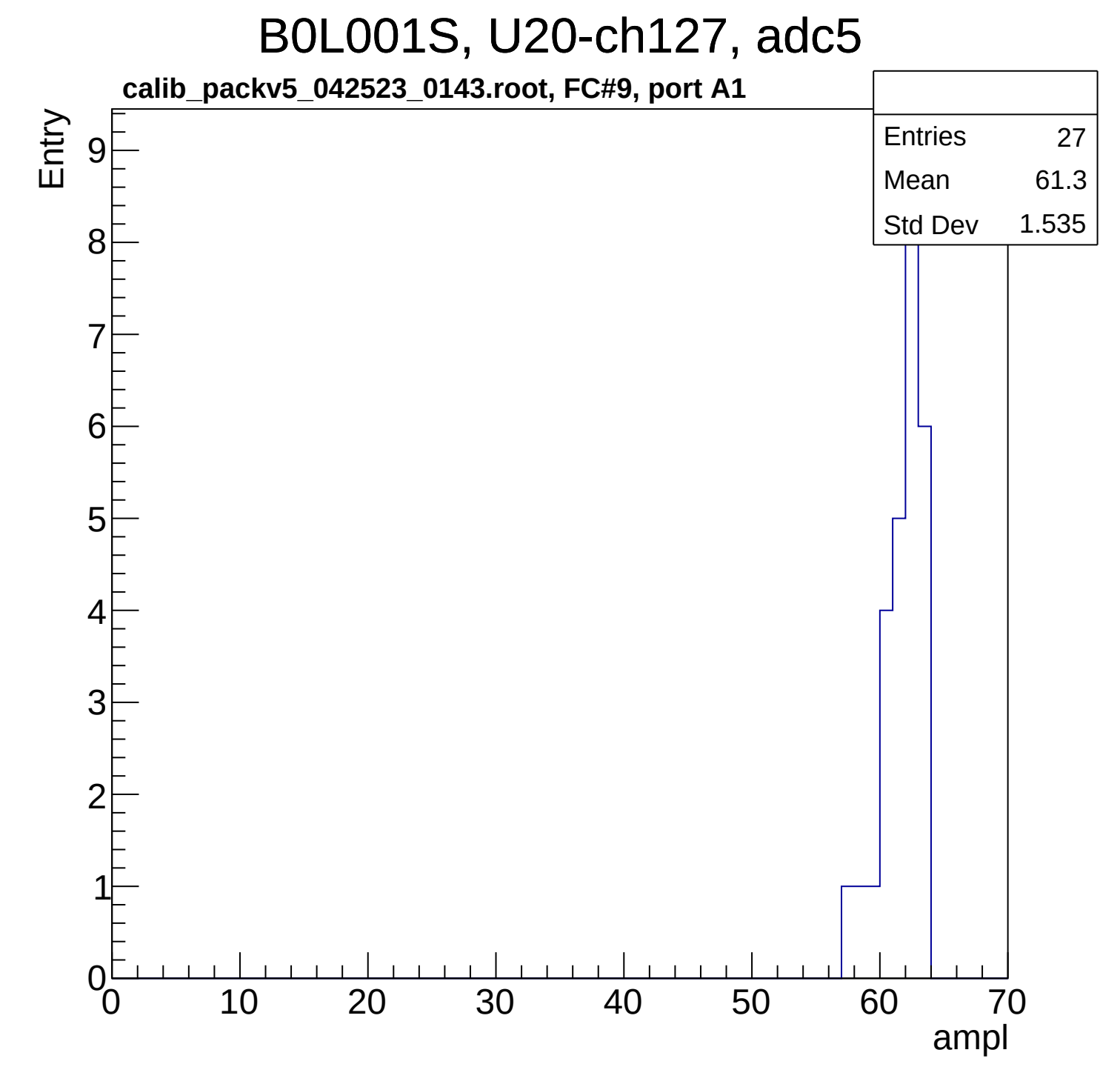
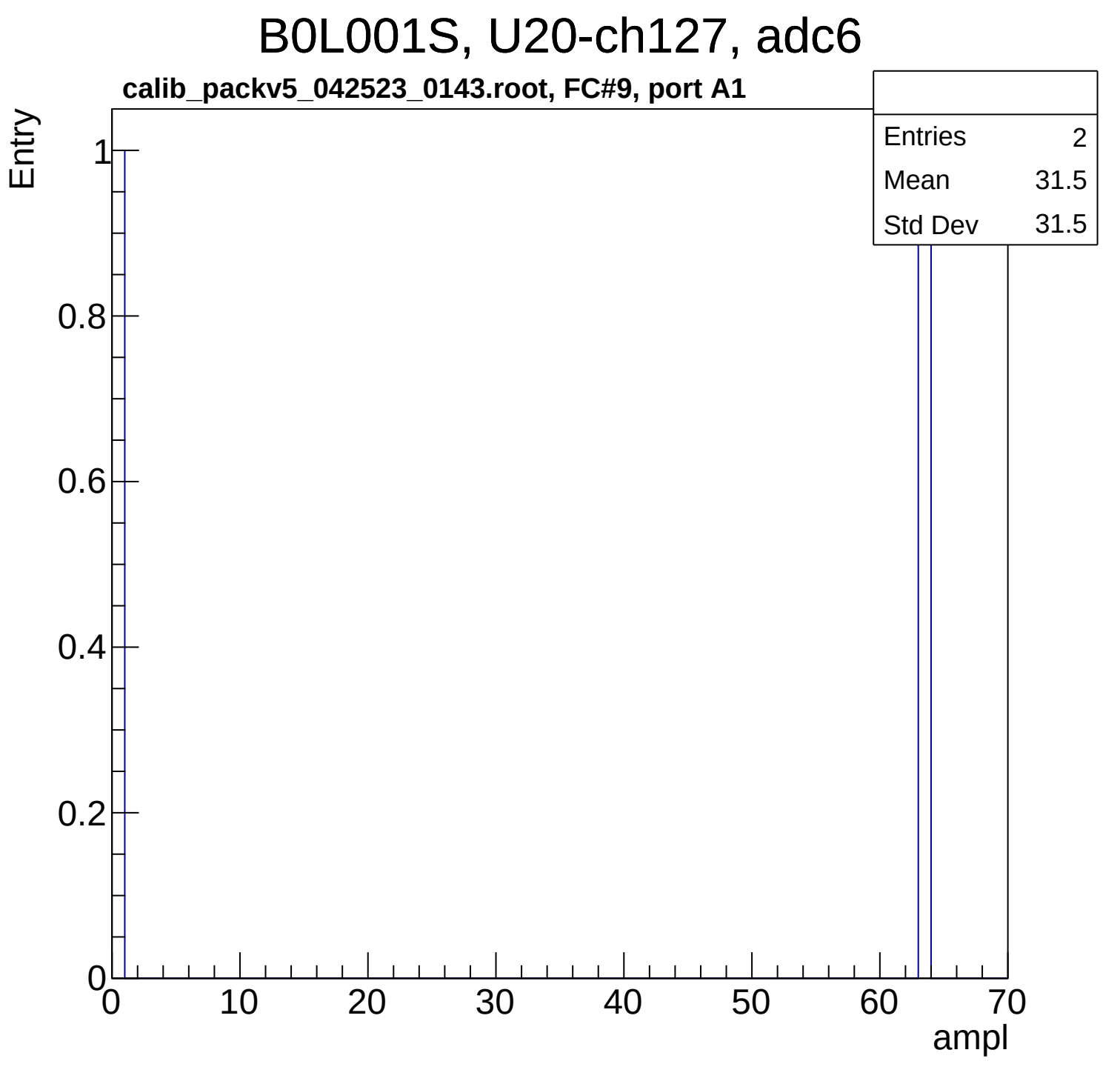

B0L001S, U20-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.65
Std Dev 3.181
Gaus mean : 39.0931
Gaus Width: 3.3871
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

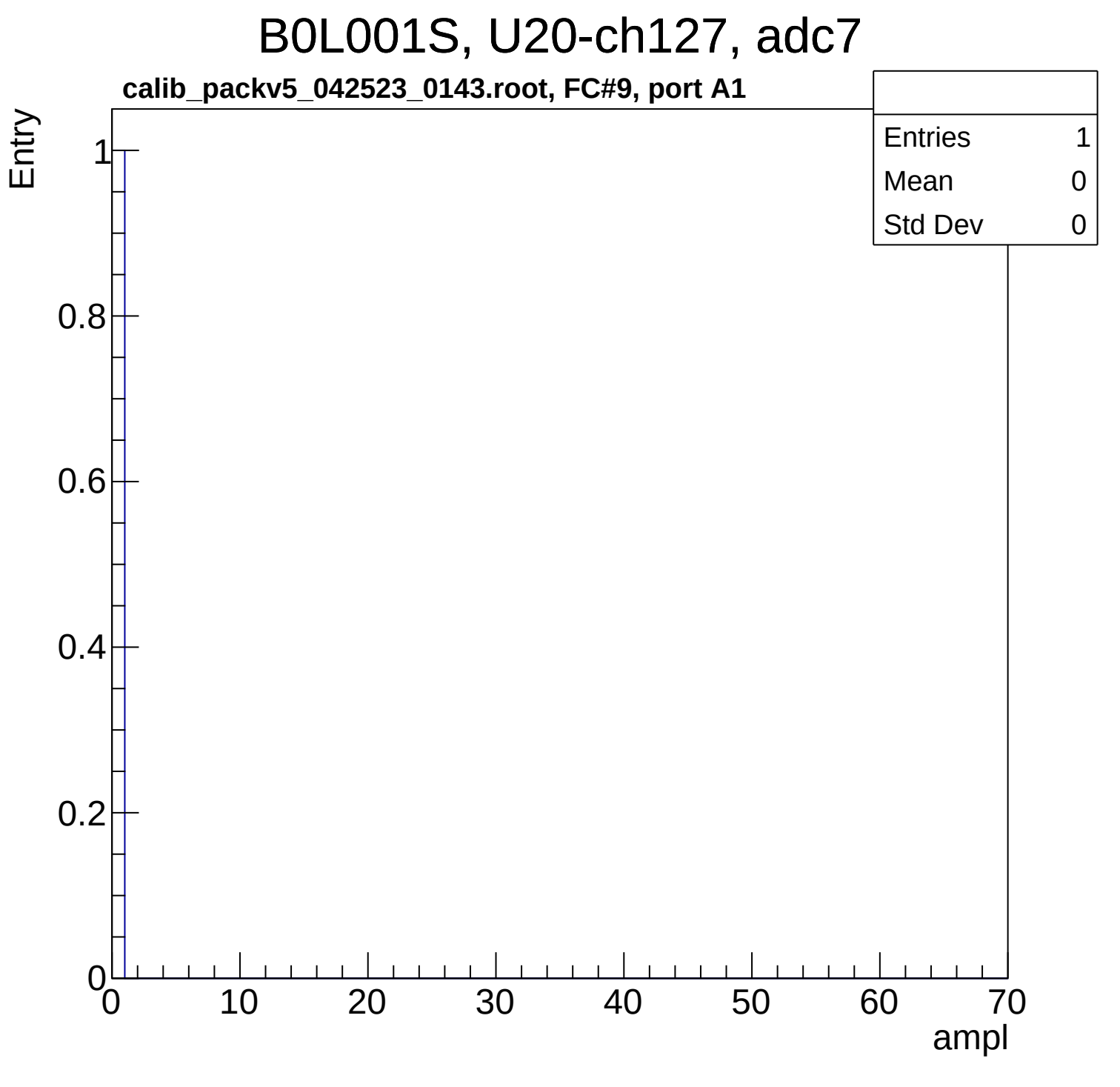

B0L001S, U20-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 45.79
Std Dev 3.681
Gaus mean : 46.1883
Gaus Width: 3.6015
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

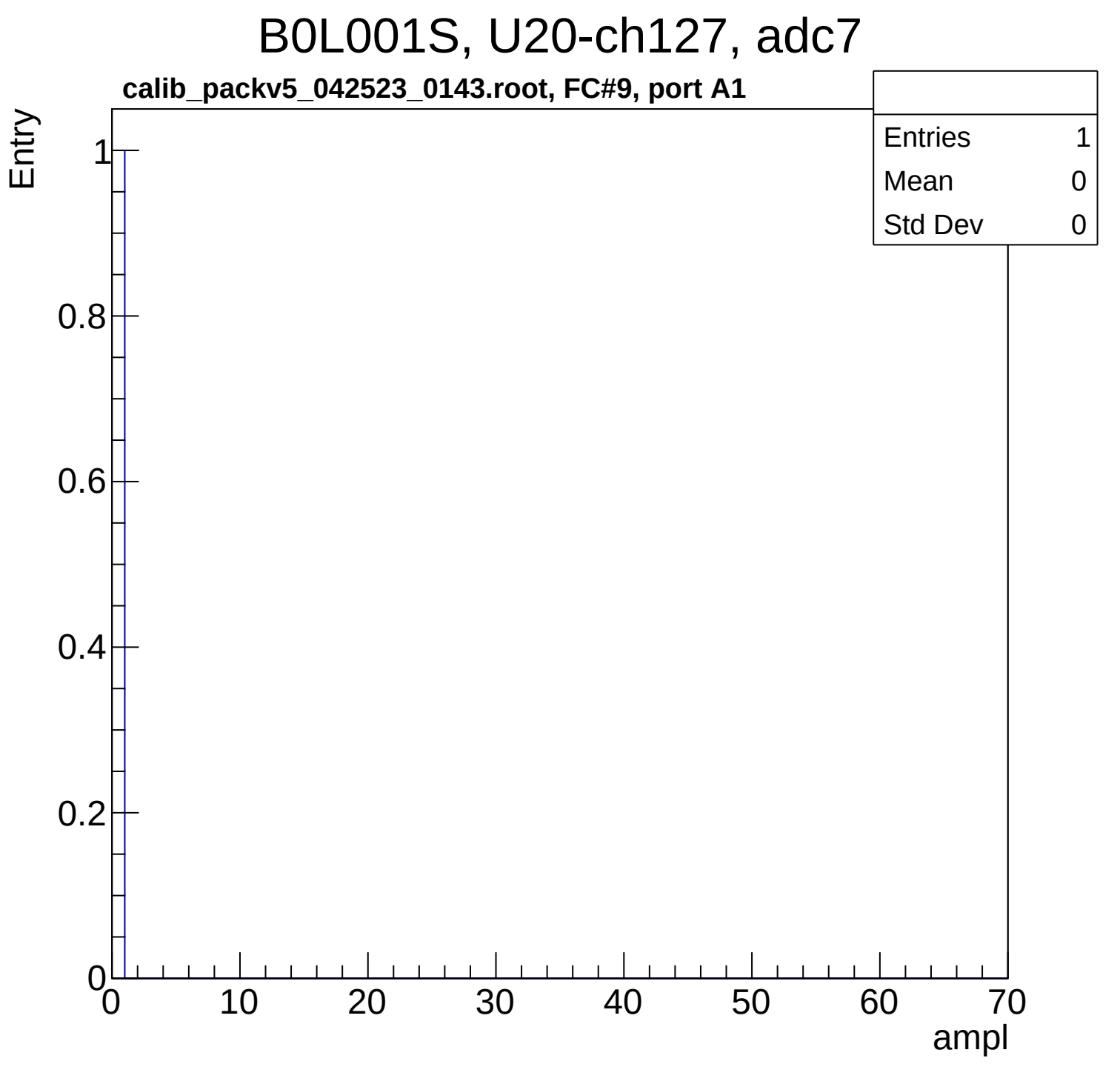

B0L001S, U20-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.94
Std Dev 3.24
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 57.81
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U20-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 61.3
Std Dev 1.535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U20-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U20-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl