



# B1L000S, U23-ch0, adc0

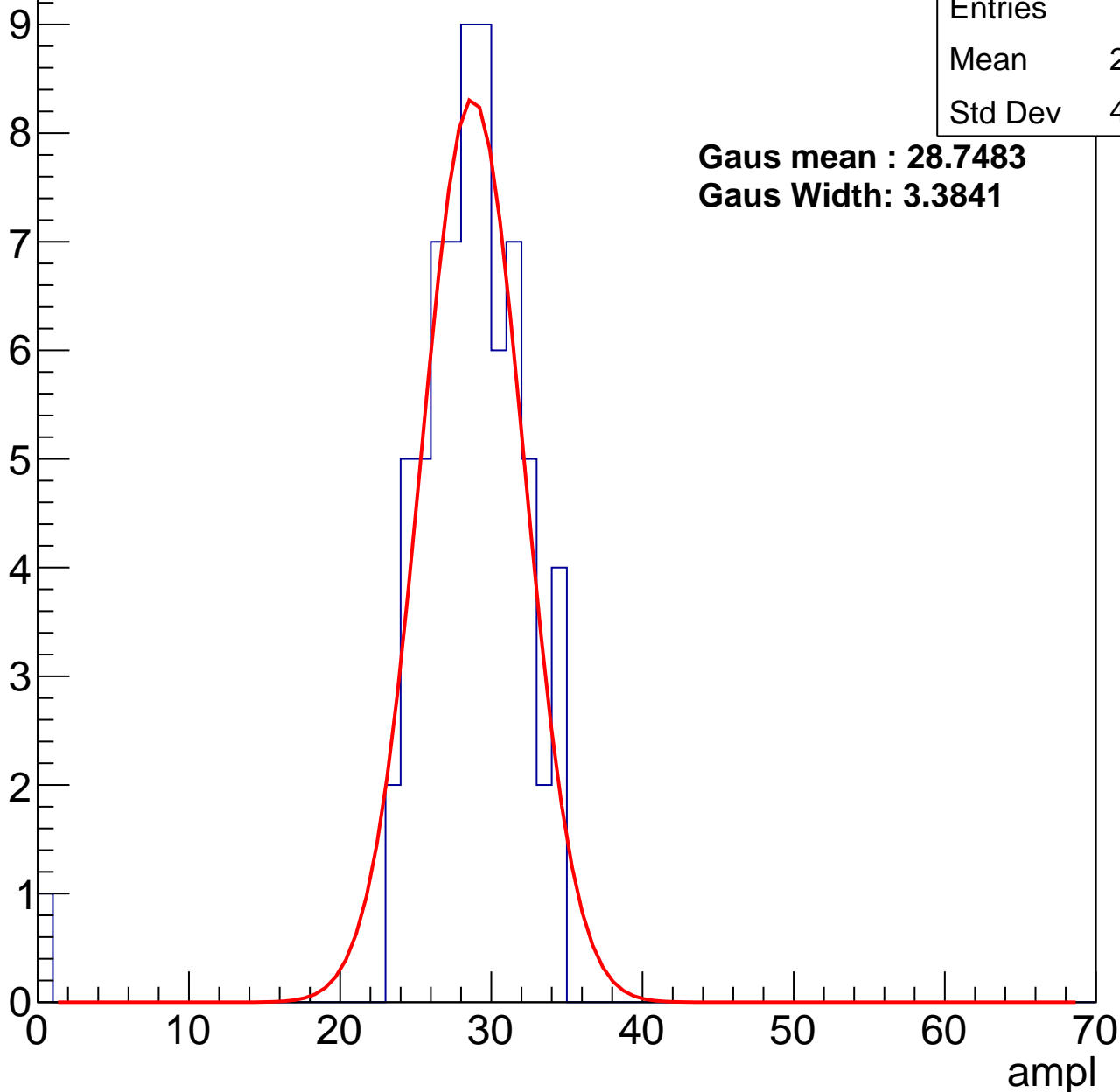
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.03
Std Dev	4.446

**Gaus mean : 28.7483**

**Gaus Width: 3.3841**



# B1L000S, U23-ch0, adc1

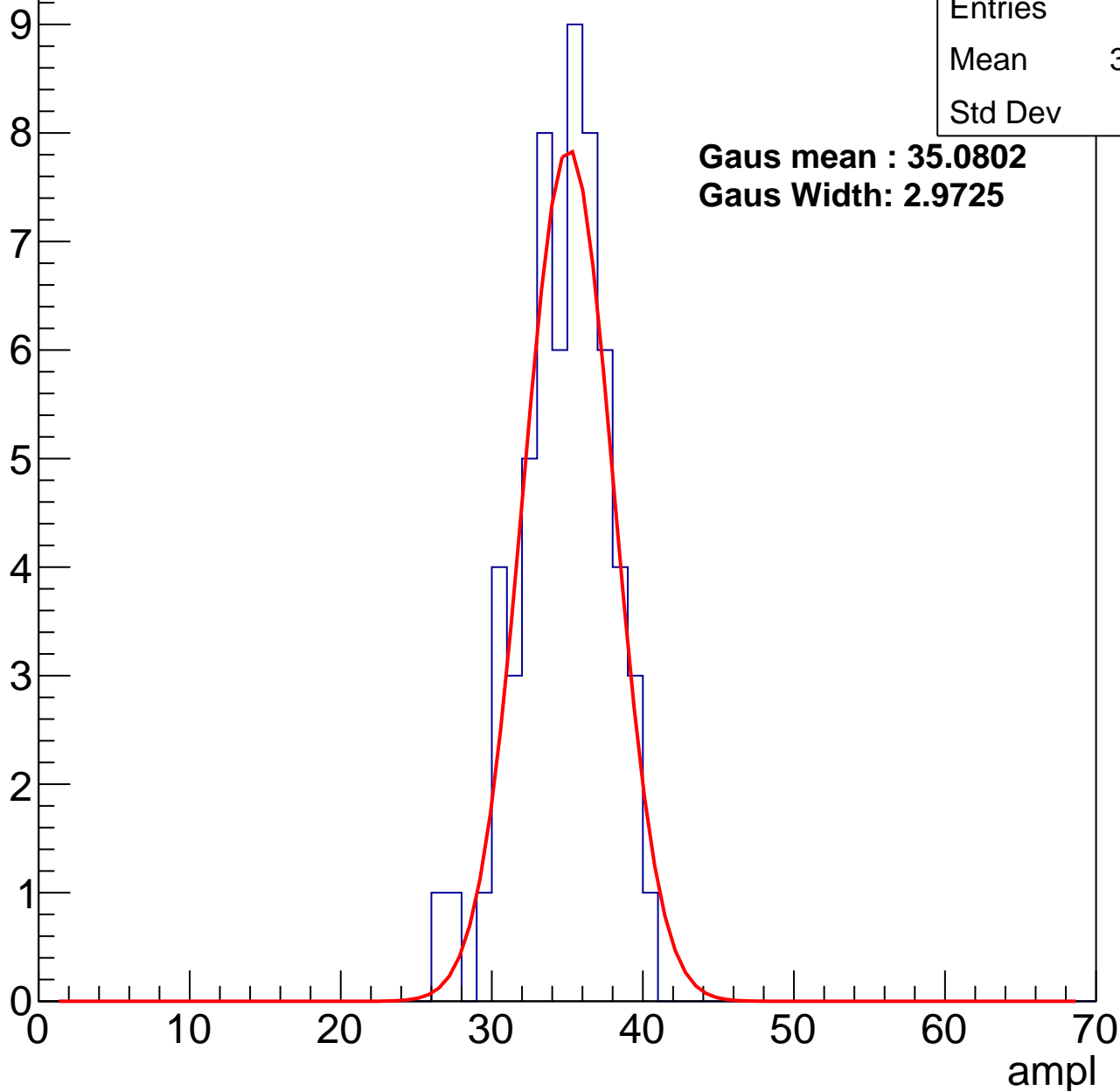
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.28
Std Dev	2.95

**Gaus mean : 35.0802**

**Gaus Width: 2.9725**



# B1L000S, U23-ch0, adc2

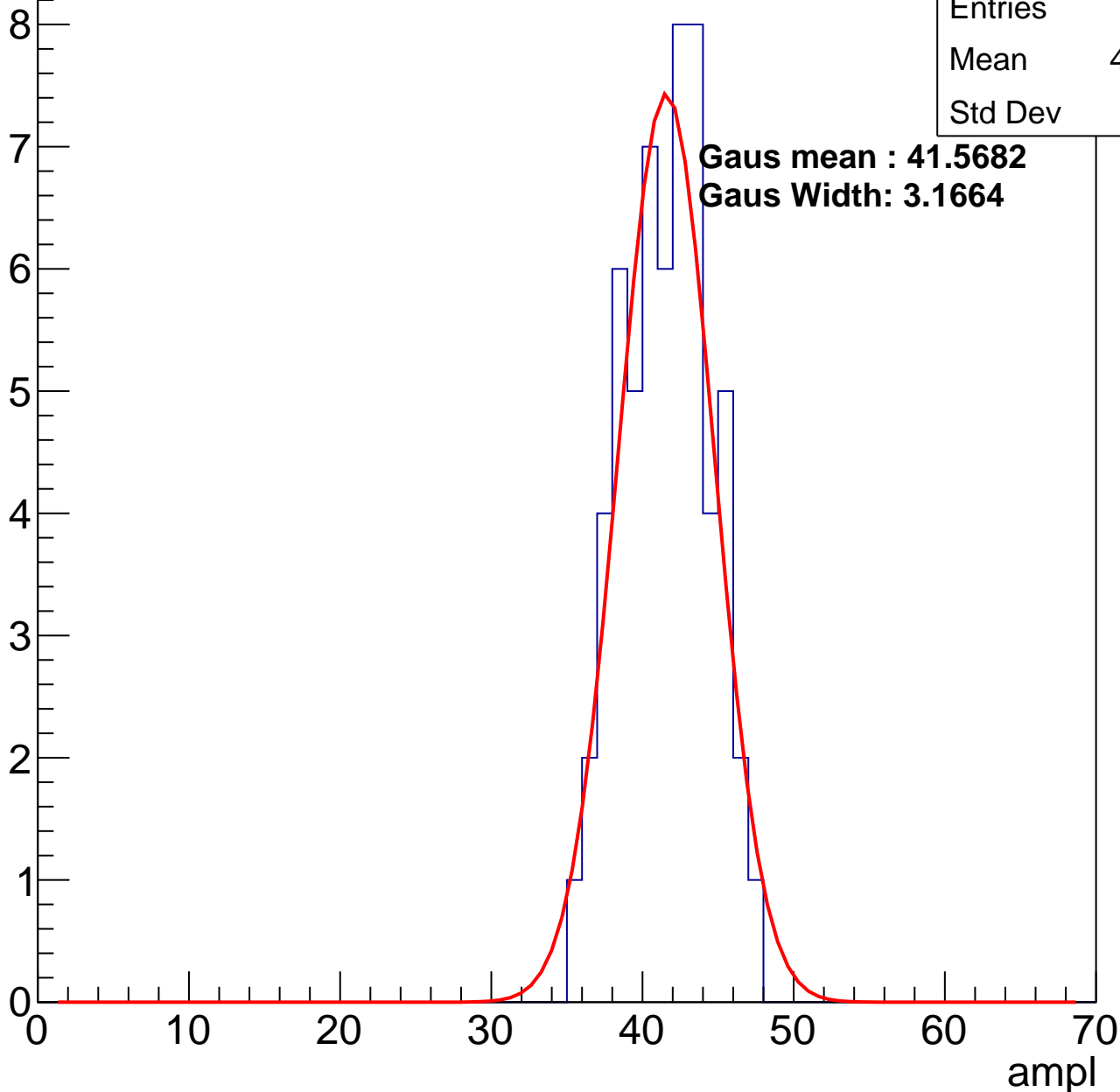
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.08
Std Dev	2.83

**Gaus mean : 41.5682**

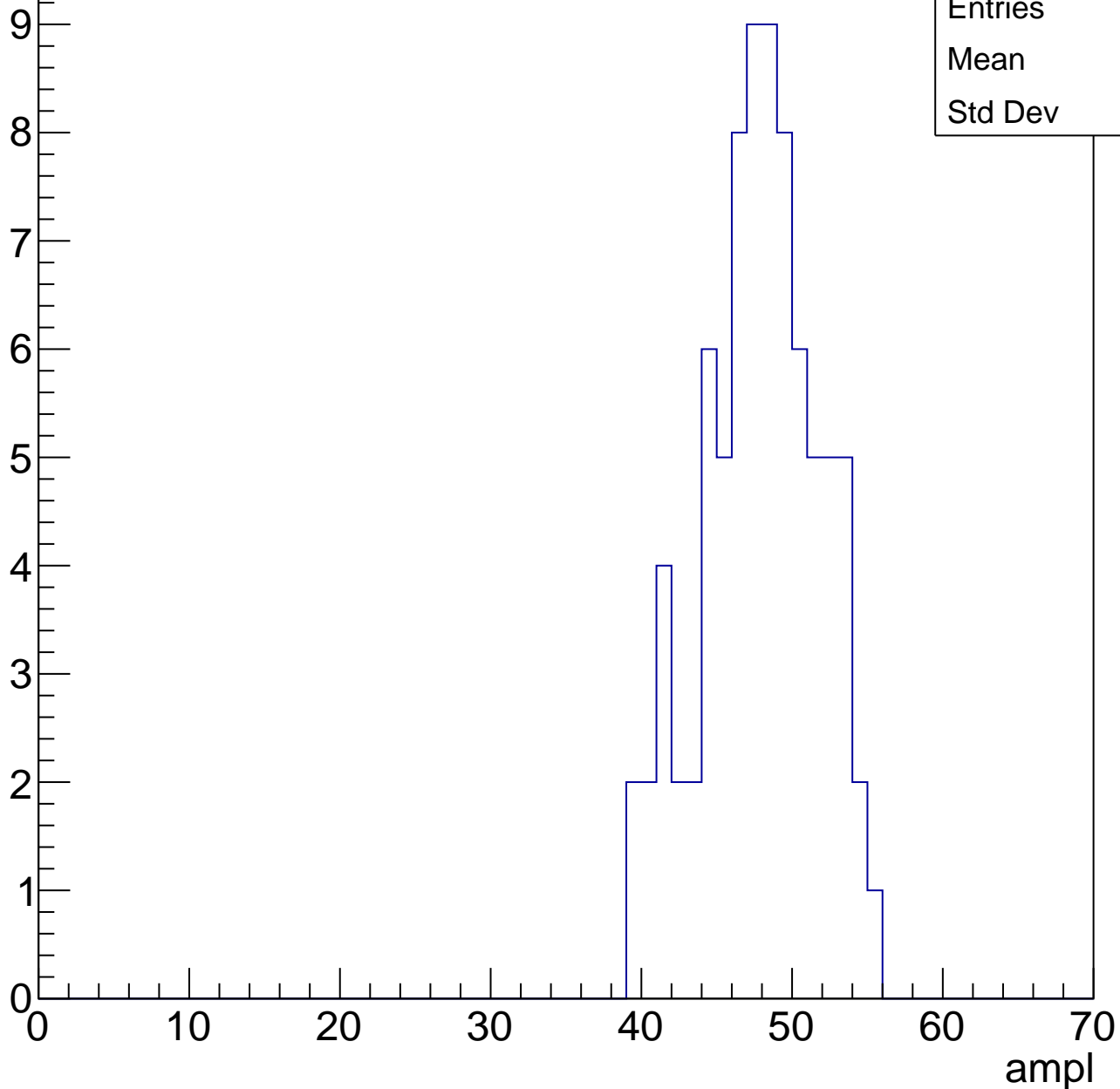
**Gaus Width: 3.1664**



# B1L000S, U23-ch0, adc3

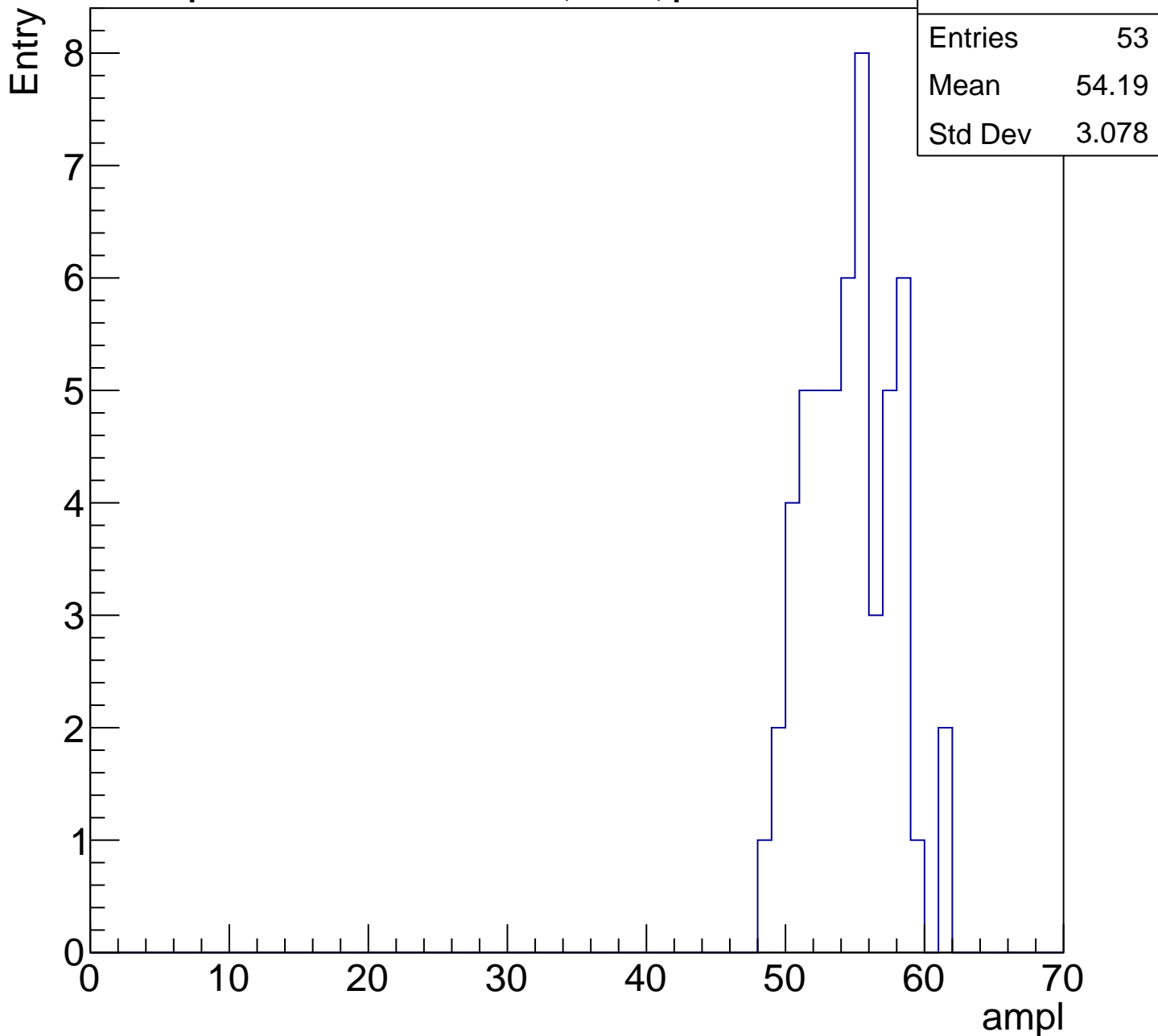
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 51

Mean 58.96

Std Dev 2.283

8

6

4

2

0

0

10

20

30

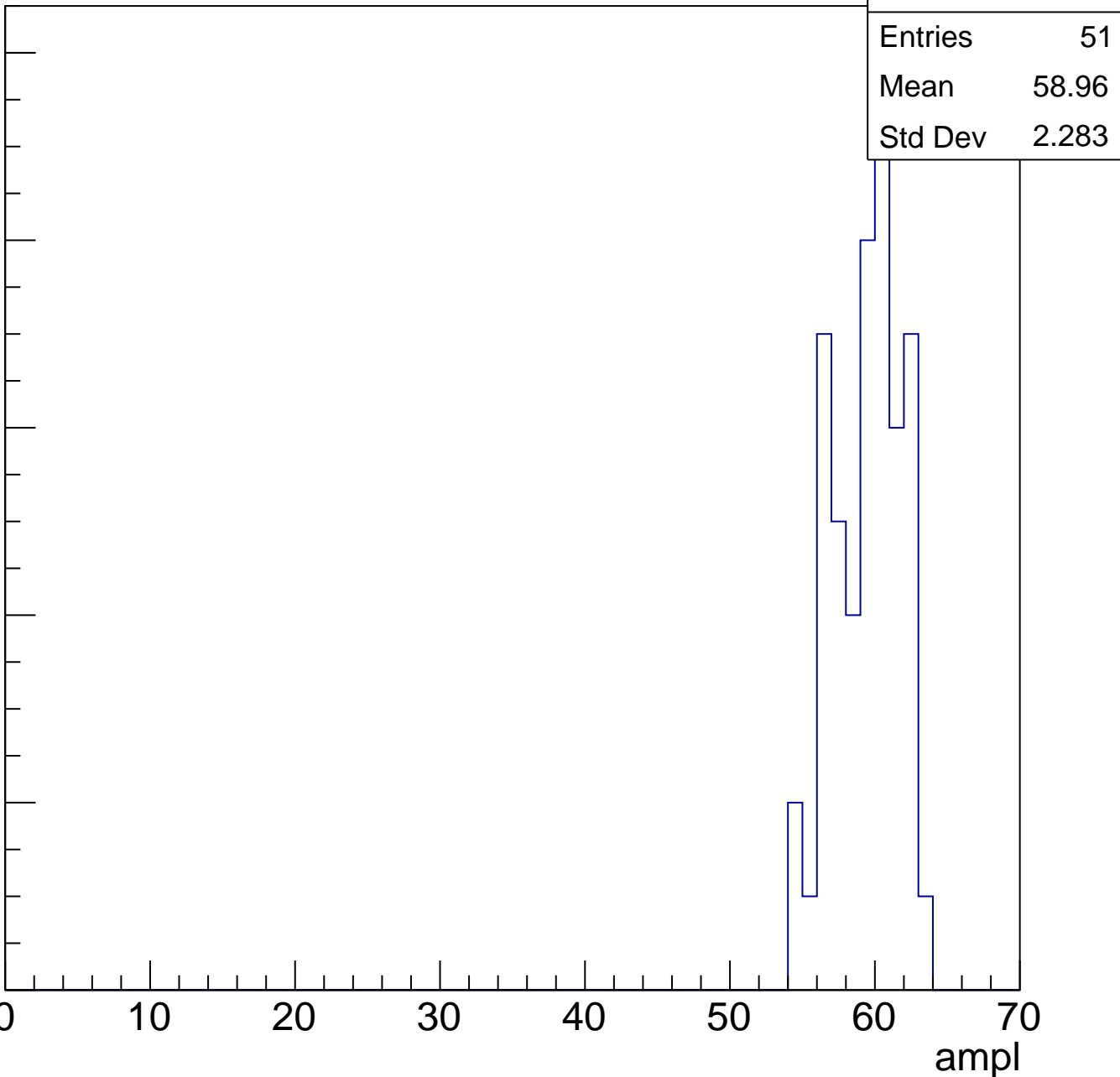
40

50

60

70

ampl

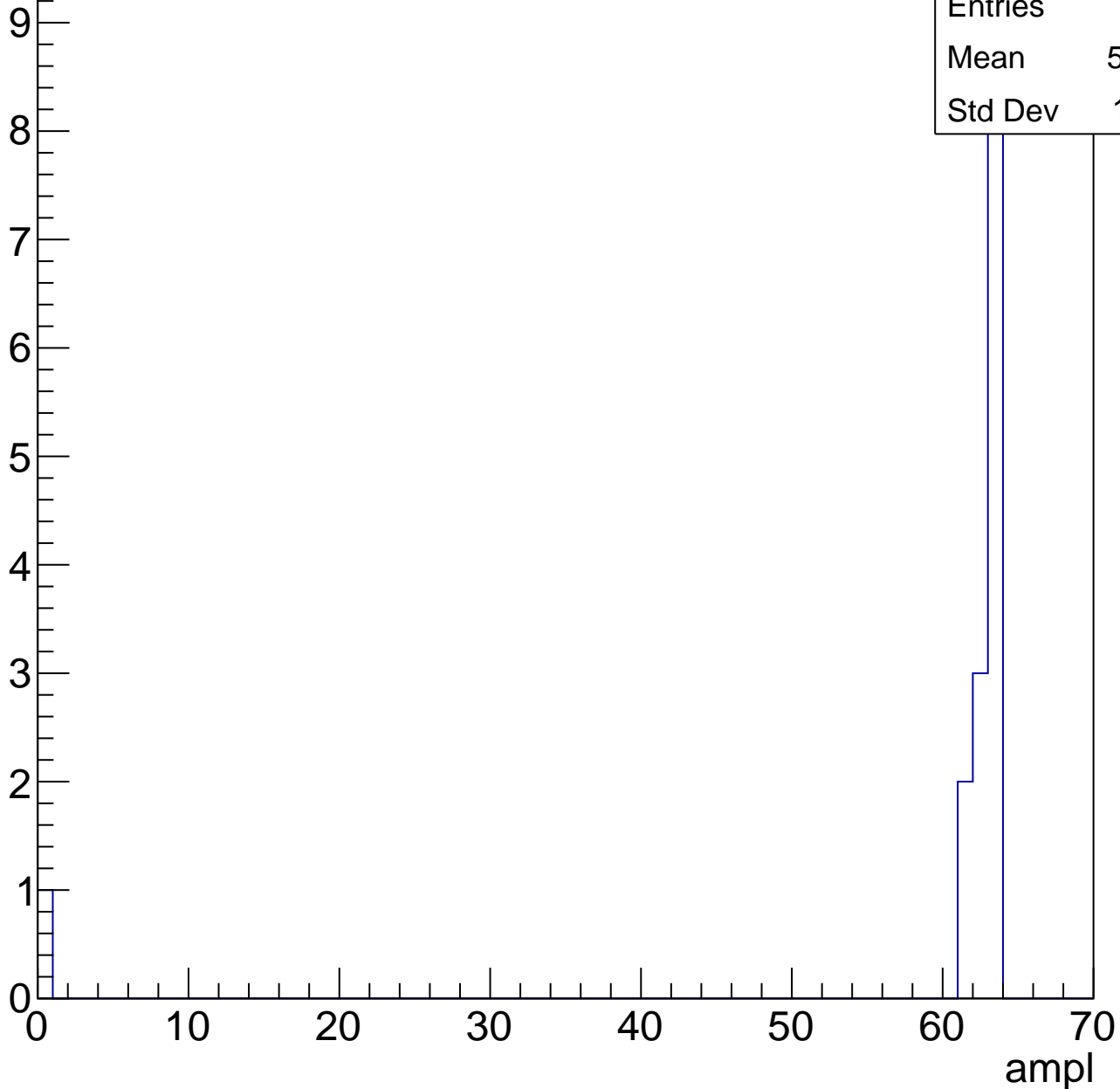


# B1L000S, U23-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	58.33
Std Dev	15.61

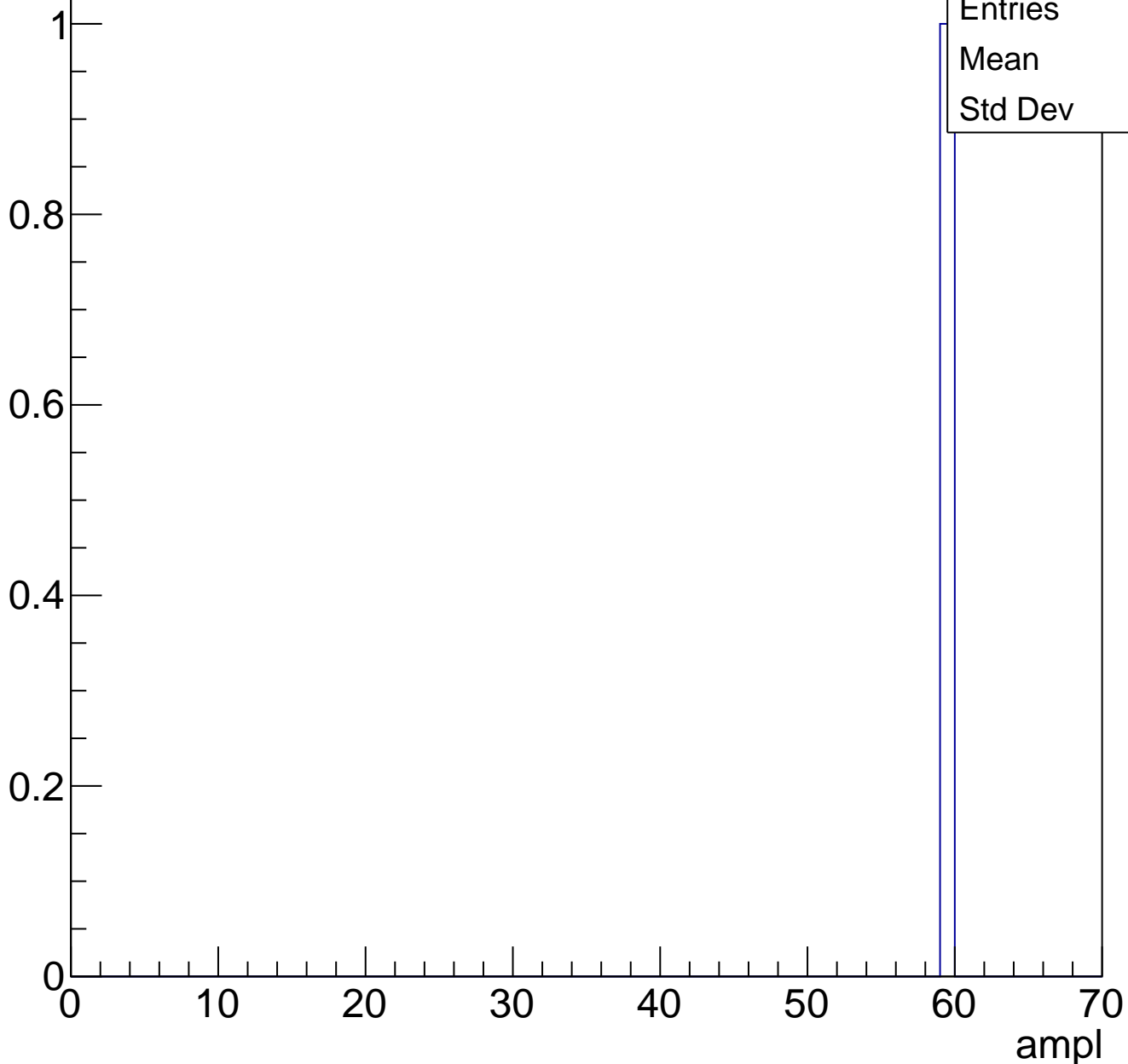




# B1L000S, U23-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch1, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	29.11
Std Dev	2.885

**Gaus mean : 29.9432**

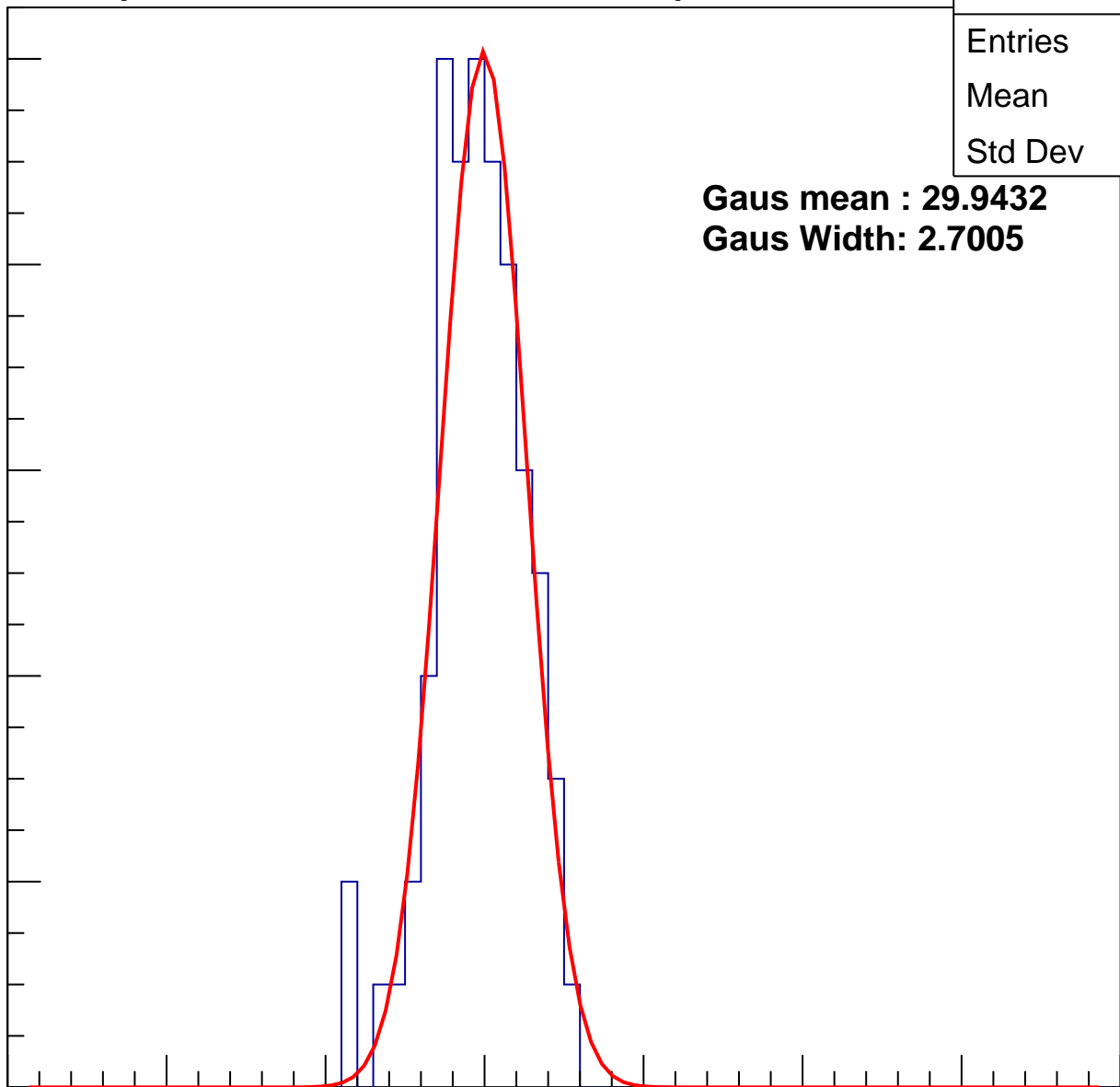
**Gaus Width: 2.7005**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch1, adc1

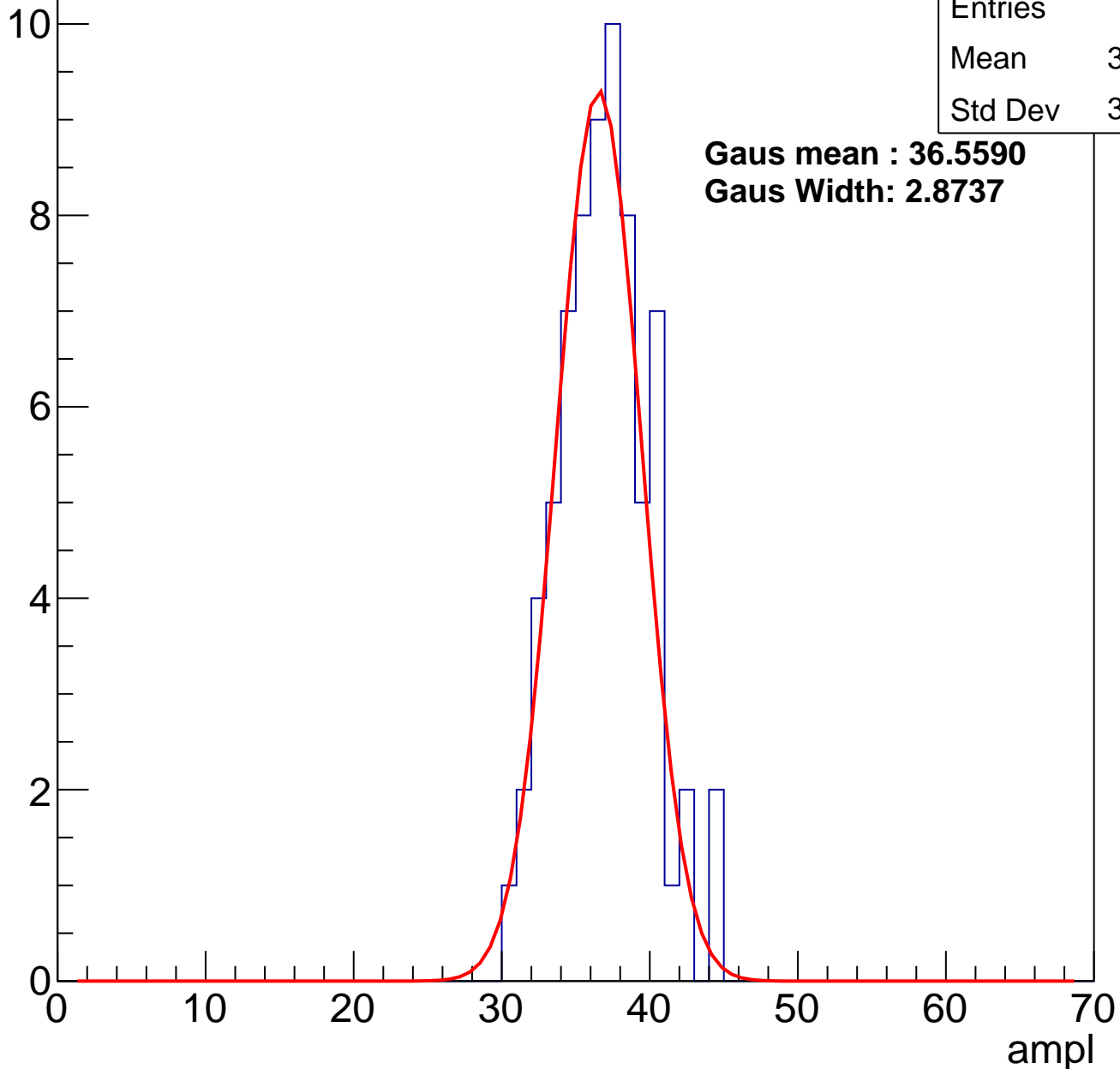
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	36.46
Std Dev	3.002

**Gaus mean : 36.5590**

**Gaus Width: 2.8737**

Entry



# B1L000S, U23-ch1, adc2

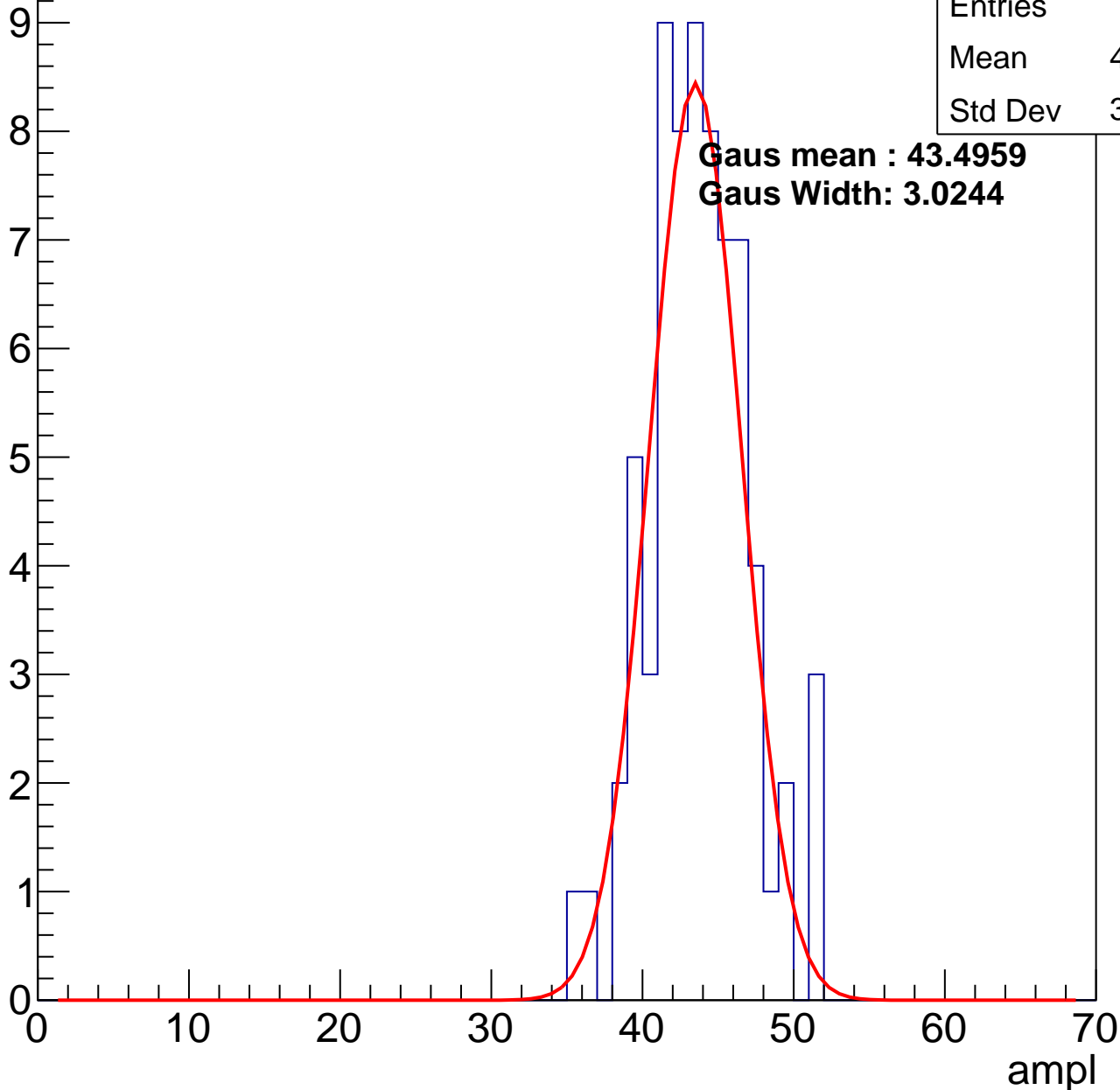
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	43.29
Std Dev	3.296

**Gaus mean : 43.4959**

**Gaus Width: 3.0244**



# B1L000S, U23-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

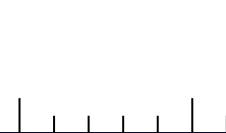
Entries	61
Mean	49.82
Std Dev	2.934

Entry

10  
8  
6  
4  
2  
0

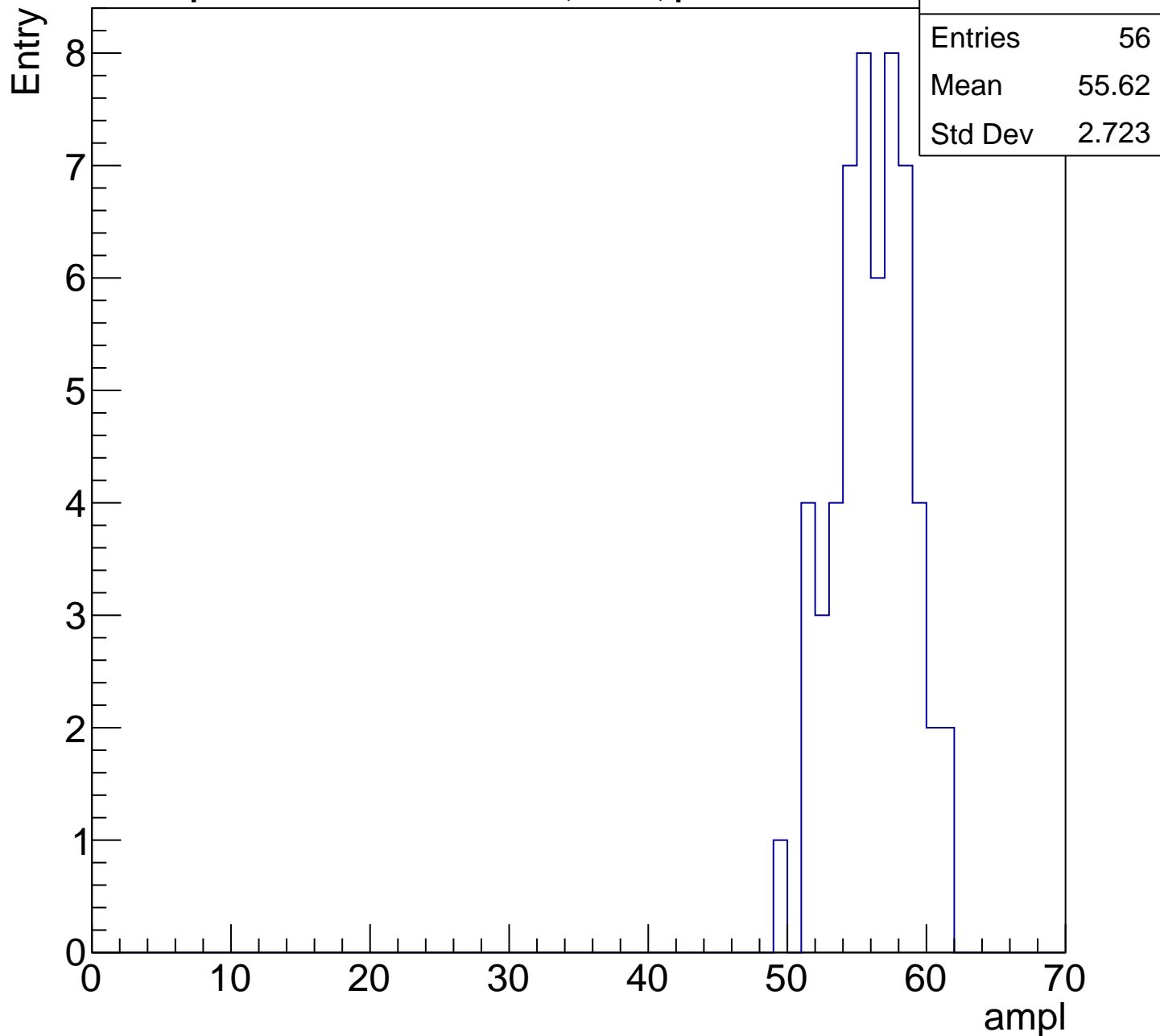
0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

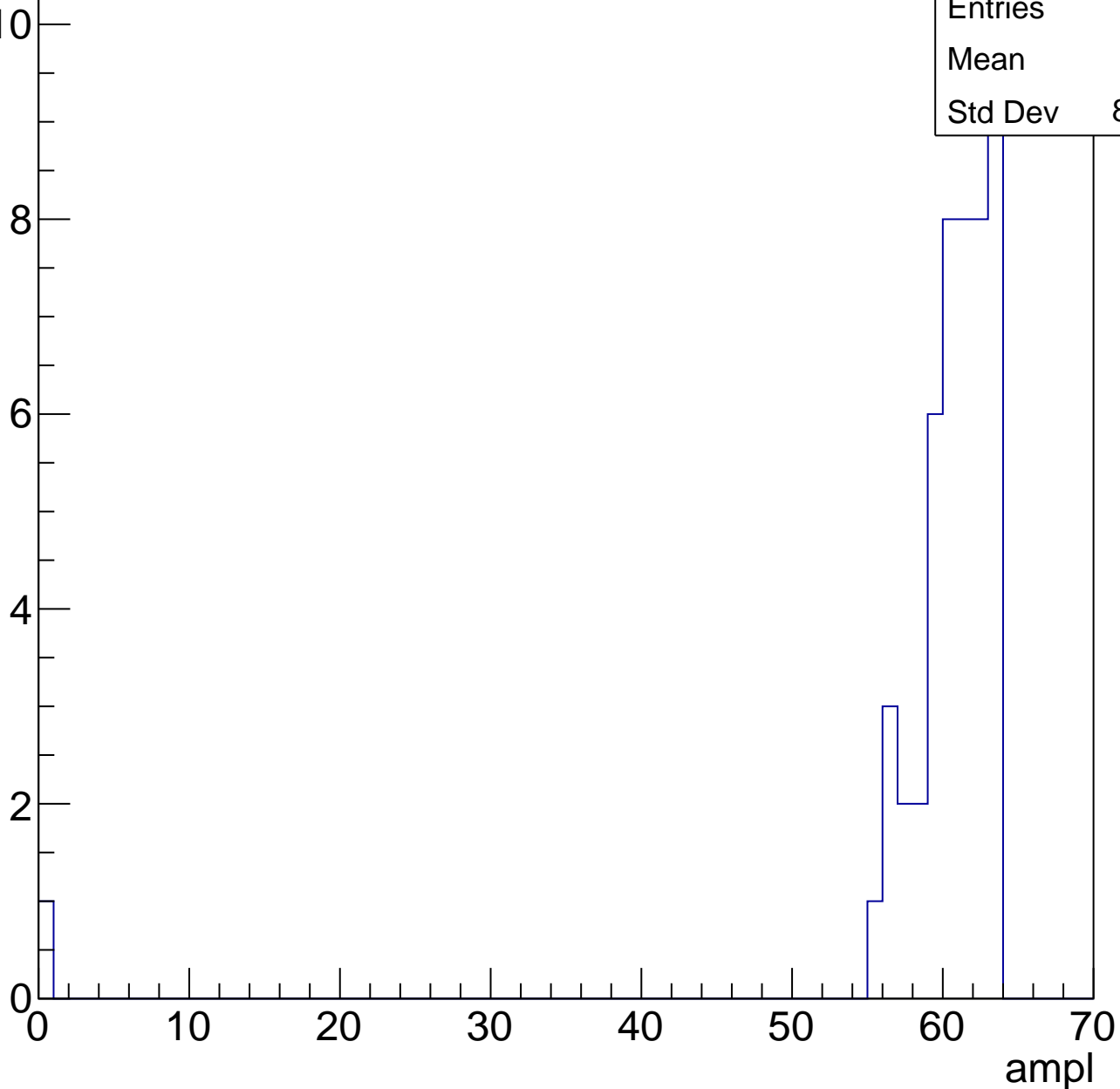


# B1L000S, U23-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	59.2
Std Dev	8.811



# B1L000S, U23-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

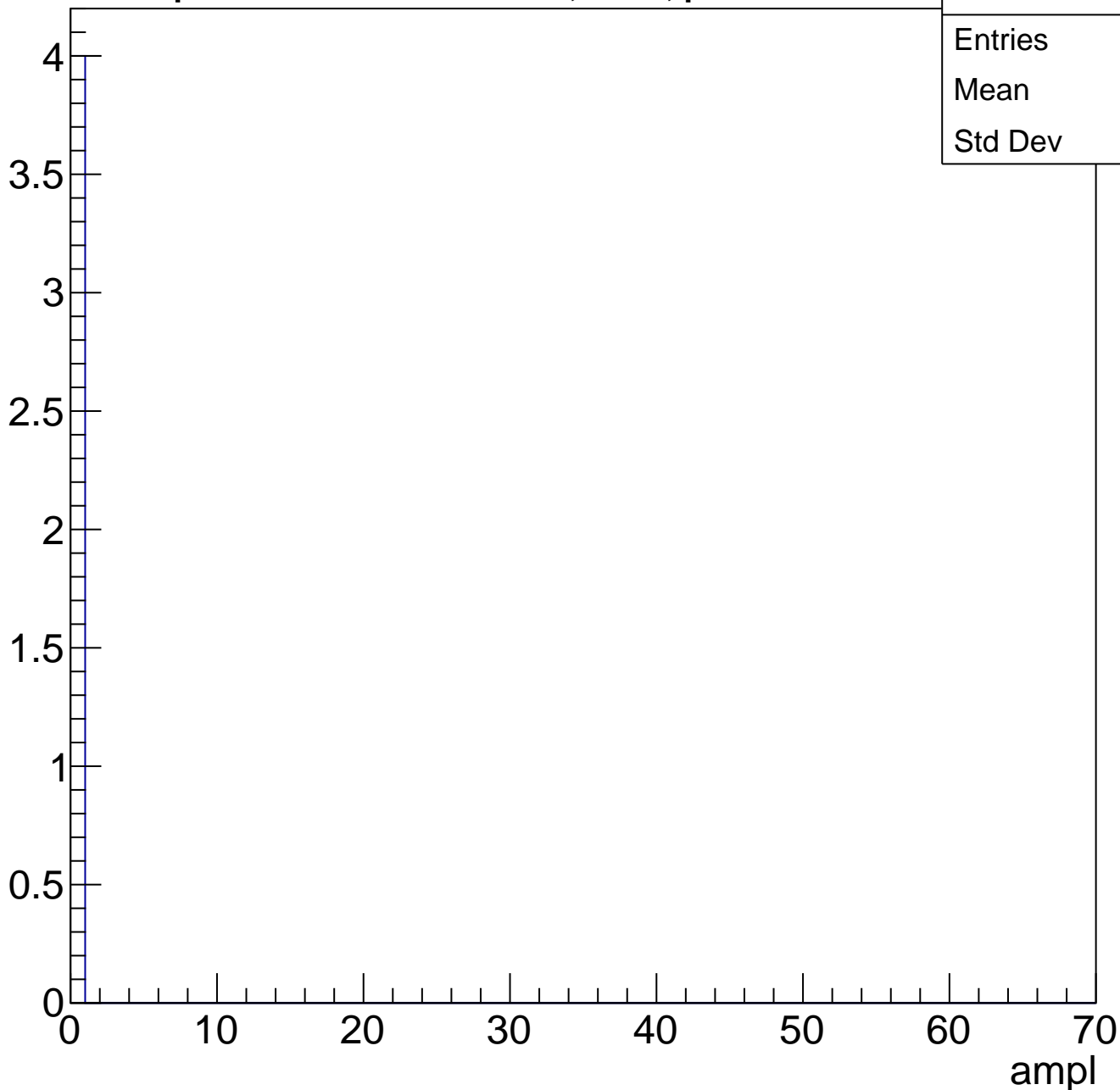




# B1L000S, U23-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	0
Std Dev	0

# B1L000S, U23-ch2, adc0

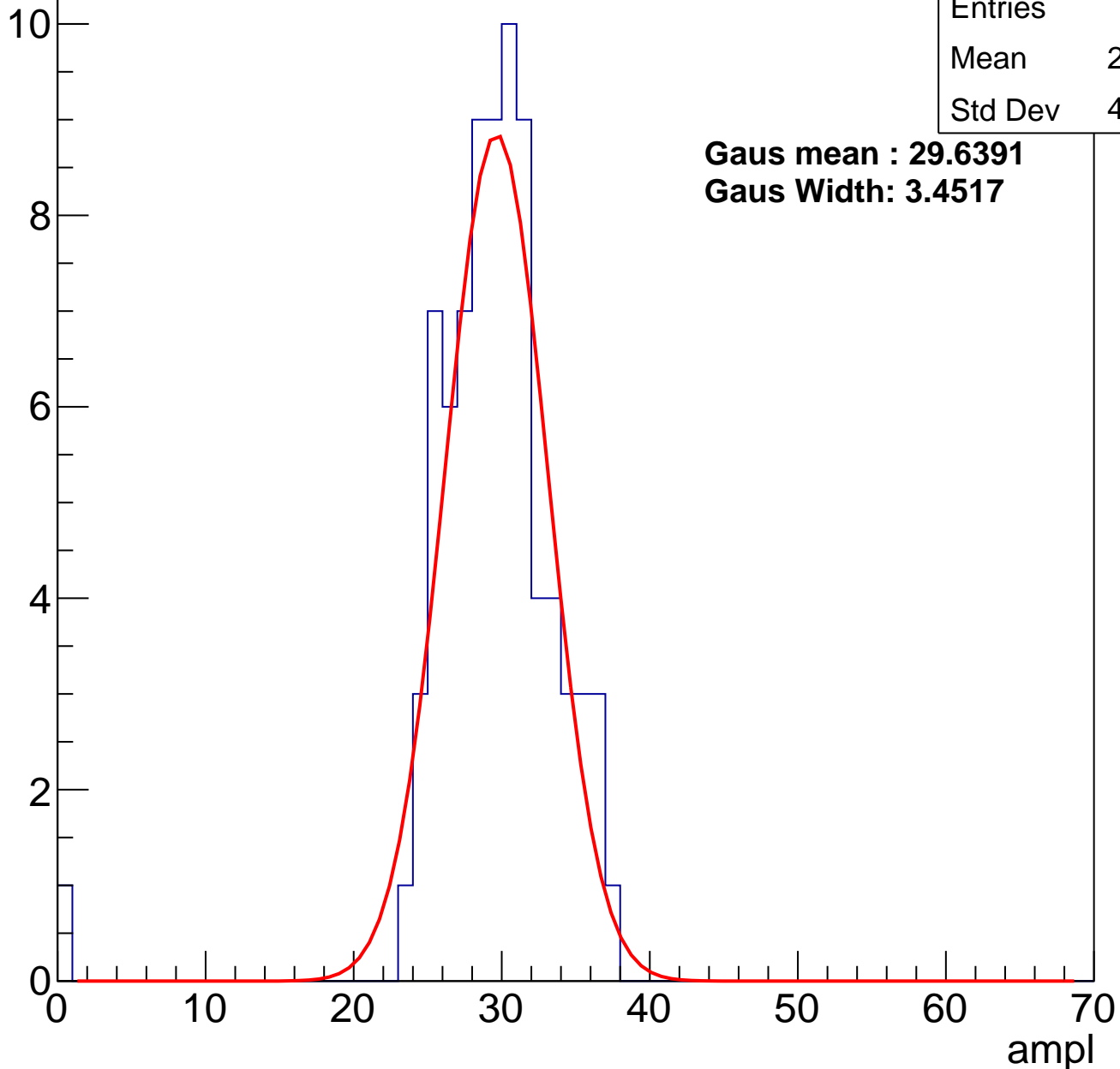
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	28.99
Std Dev	4.603

**Gaus mean : 29.6391**

**Gaus Width: 3.4517**

Entry



# B1L000S, U23-ch2, adc1

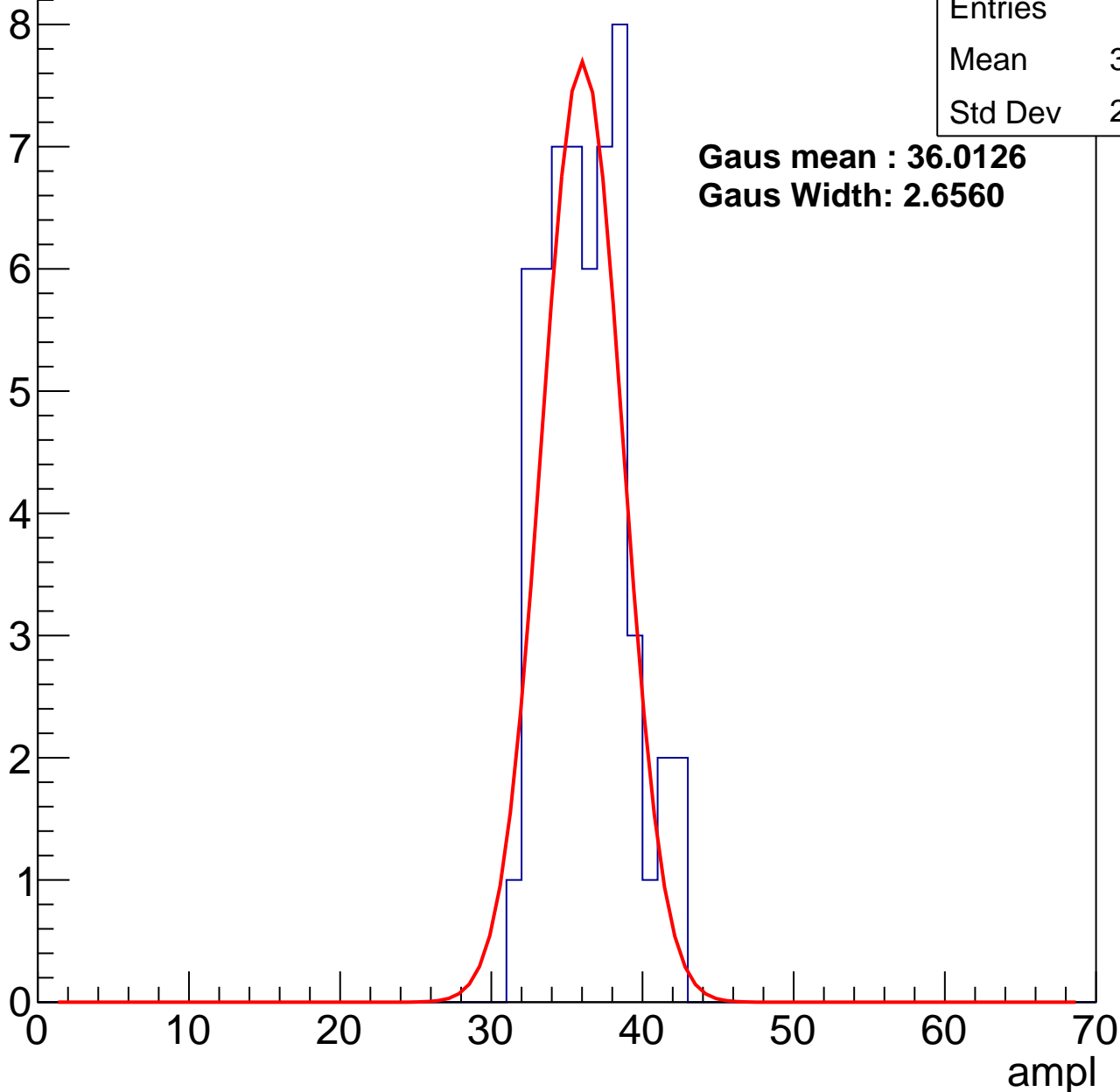
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.82
Std Dev	2.713

**Gaus mean : 36.0126**

**Gaus Width: 2.6560**



# B1L000S, U23-ch2, adc2

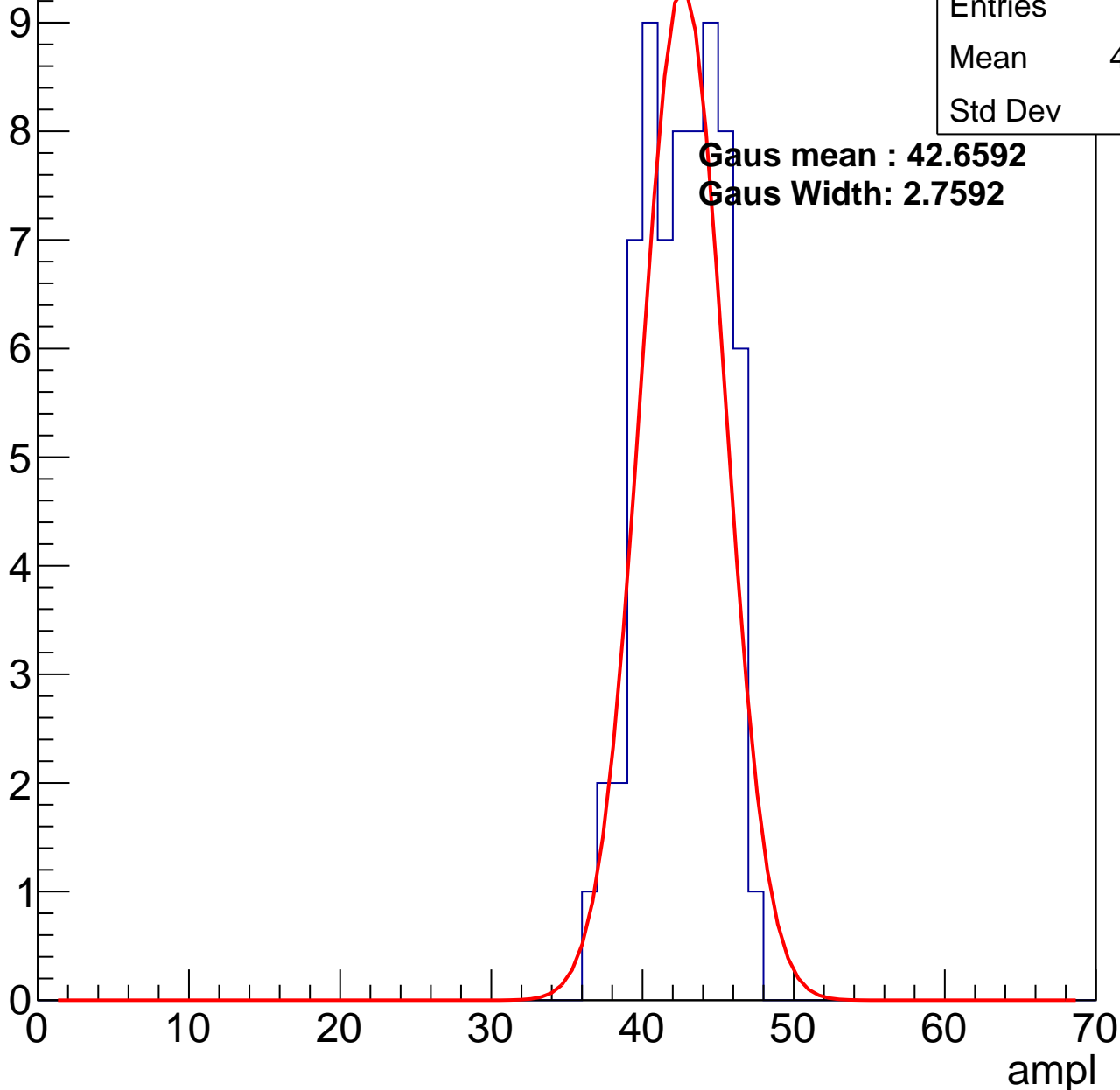
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.13
Std Dev	2.6

**Gaus mean : 42.6592**

**Gaus Width: 2.7592**

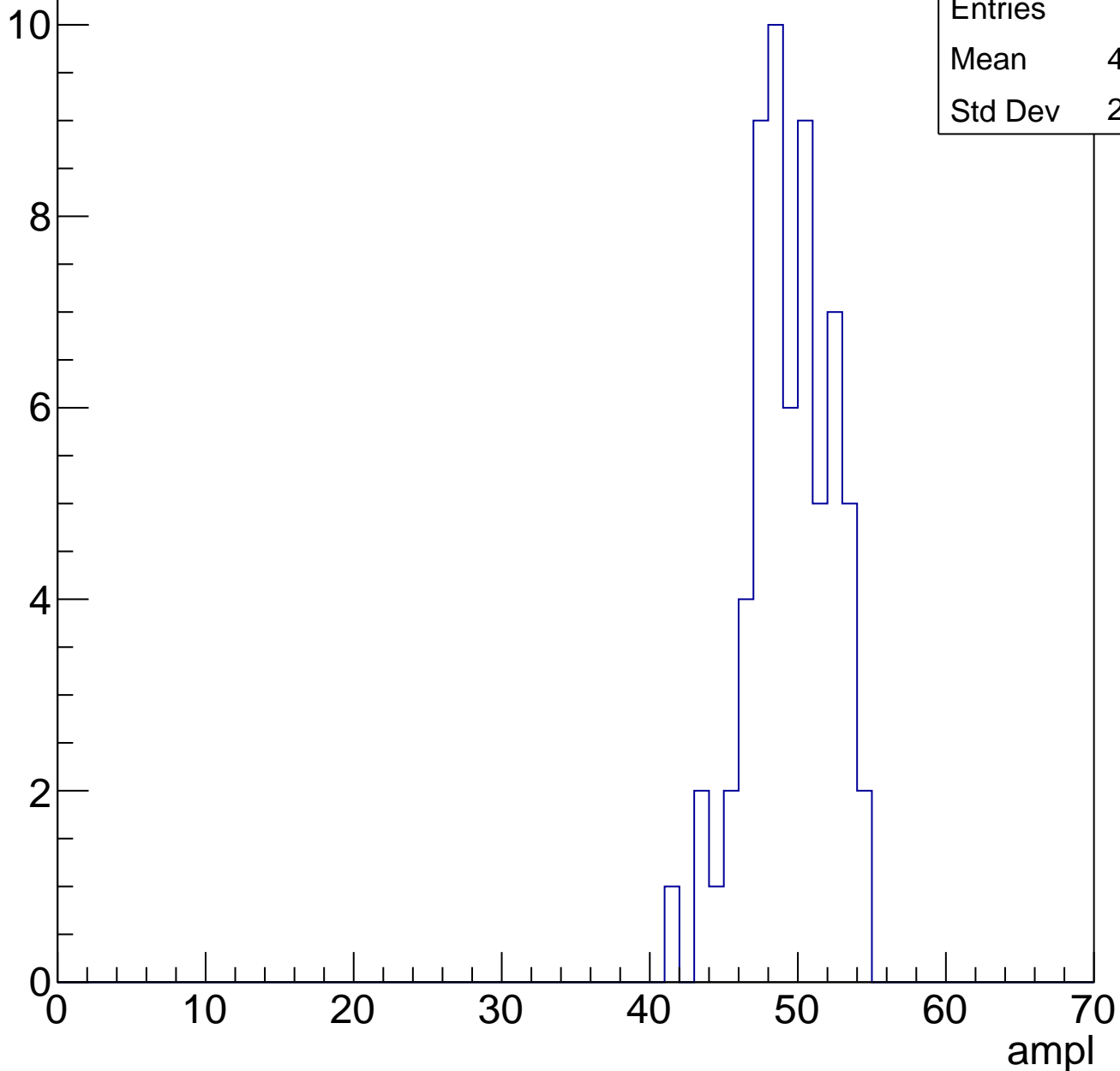


# B1L000S, U23-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

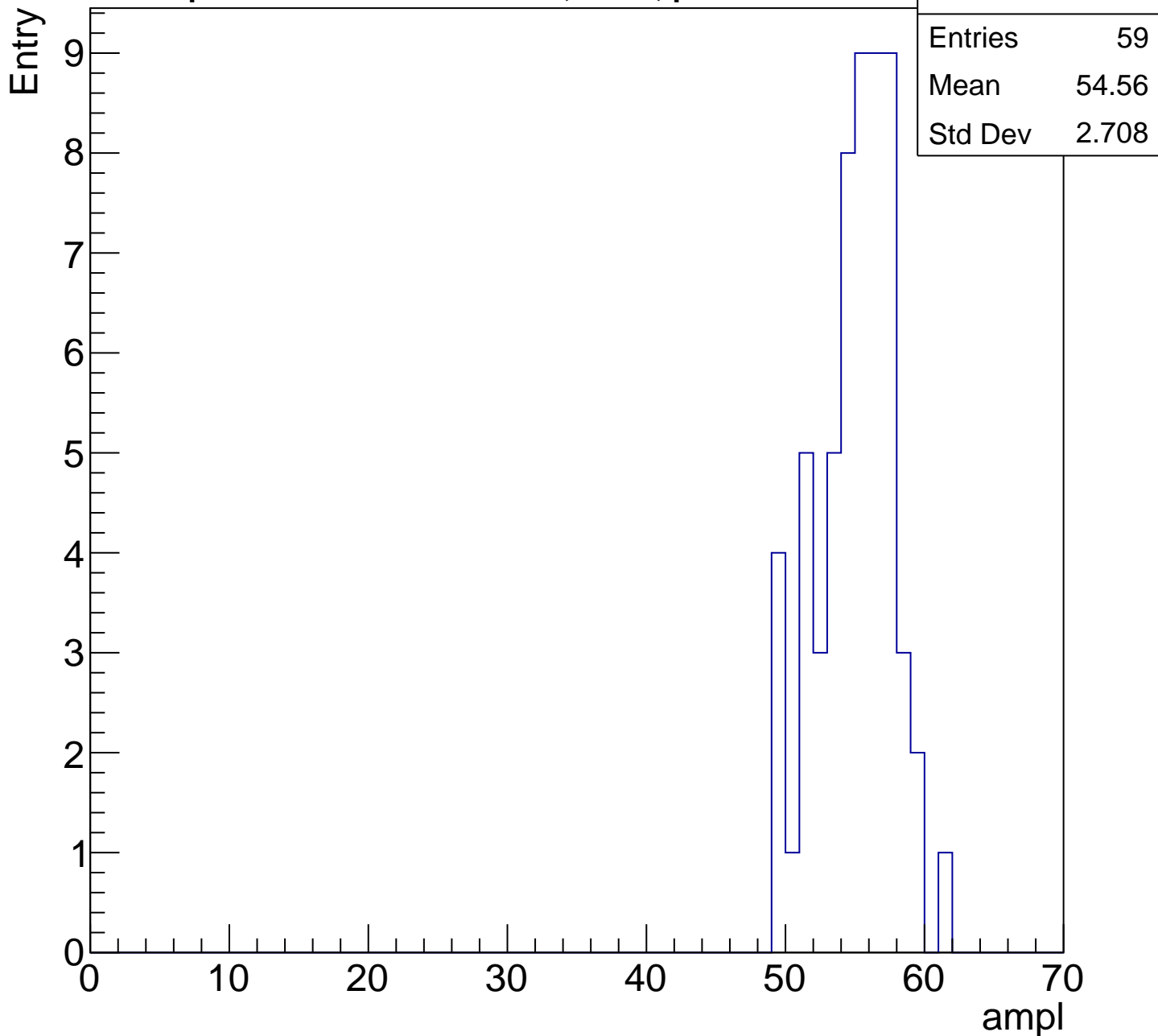
Entries	63
Mean	48.95
Std Dev	2.808

Entry



# B1L000S, U23-ch2, adc4

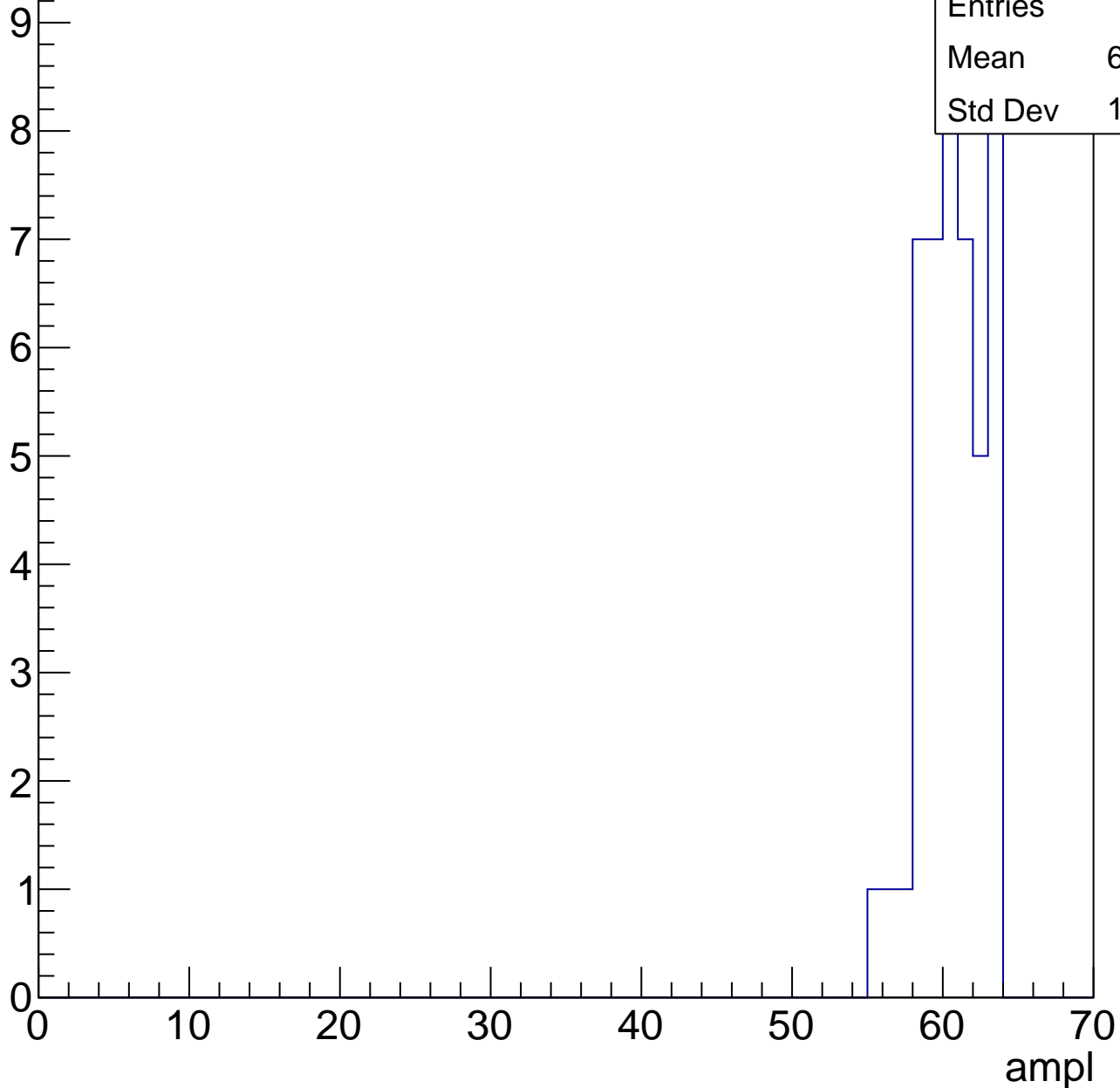
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

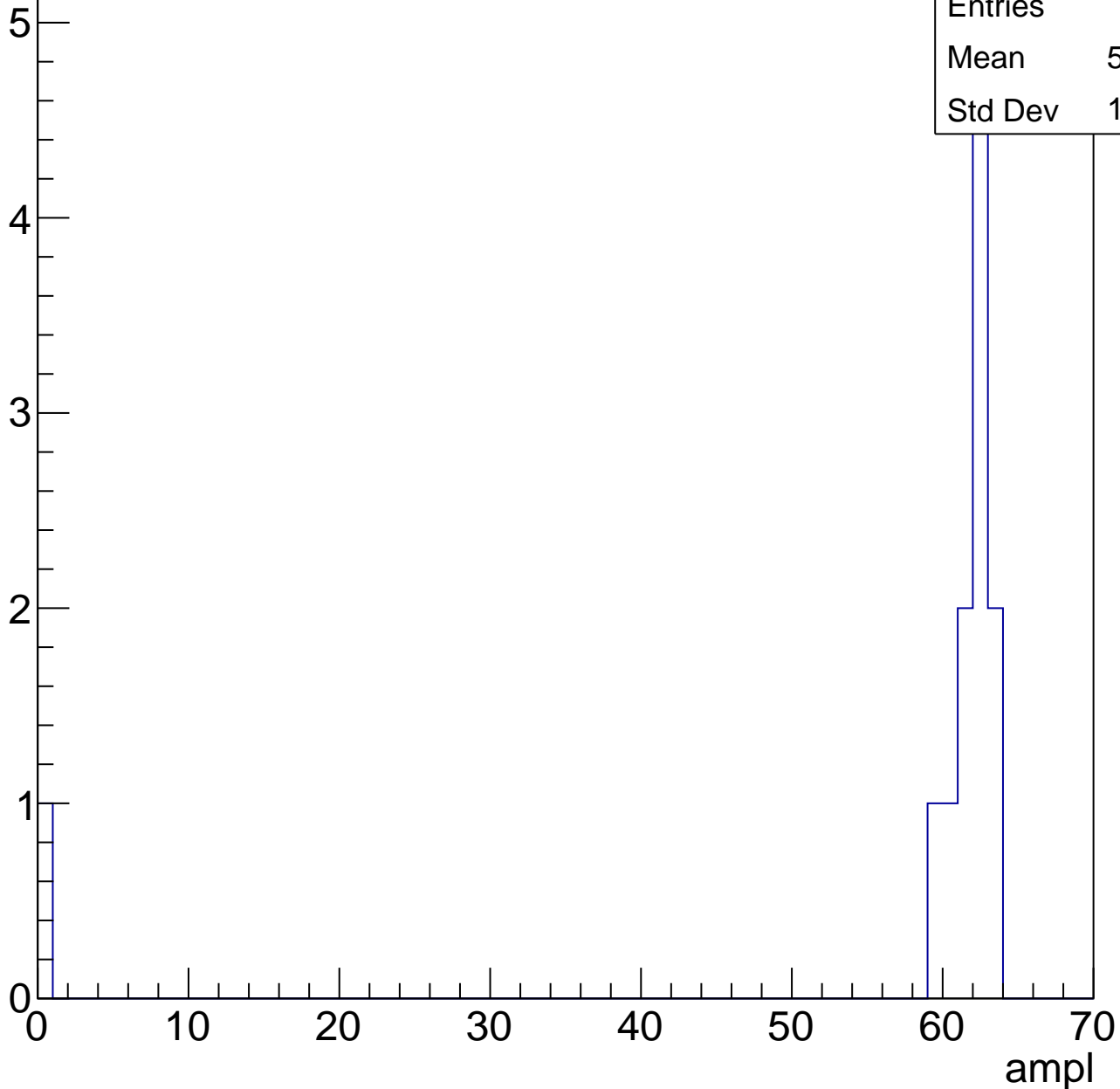


# B1L000S, U23-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.42
Std Dev	17.05





# B1L000S, U23-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch3, adc0

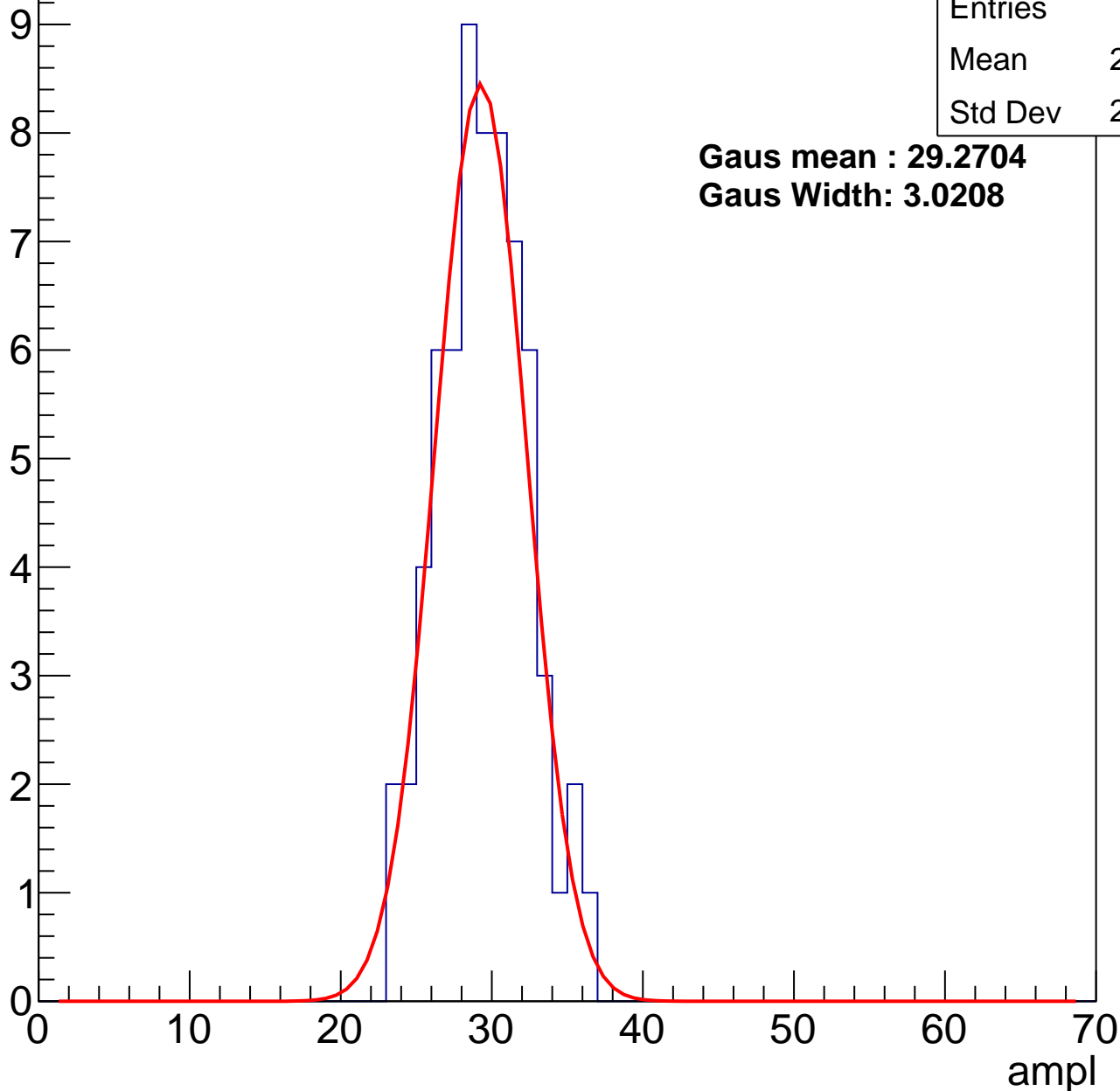
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.98
Std Dev	2.927

**Gaus mean : 29.2704**

**Gaus Width: 3.0208**



# B1L000S, U23-ch3, adc1

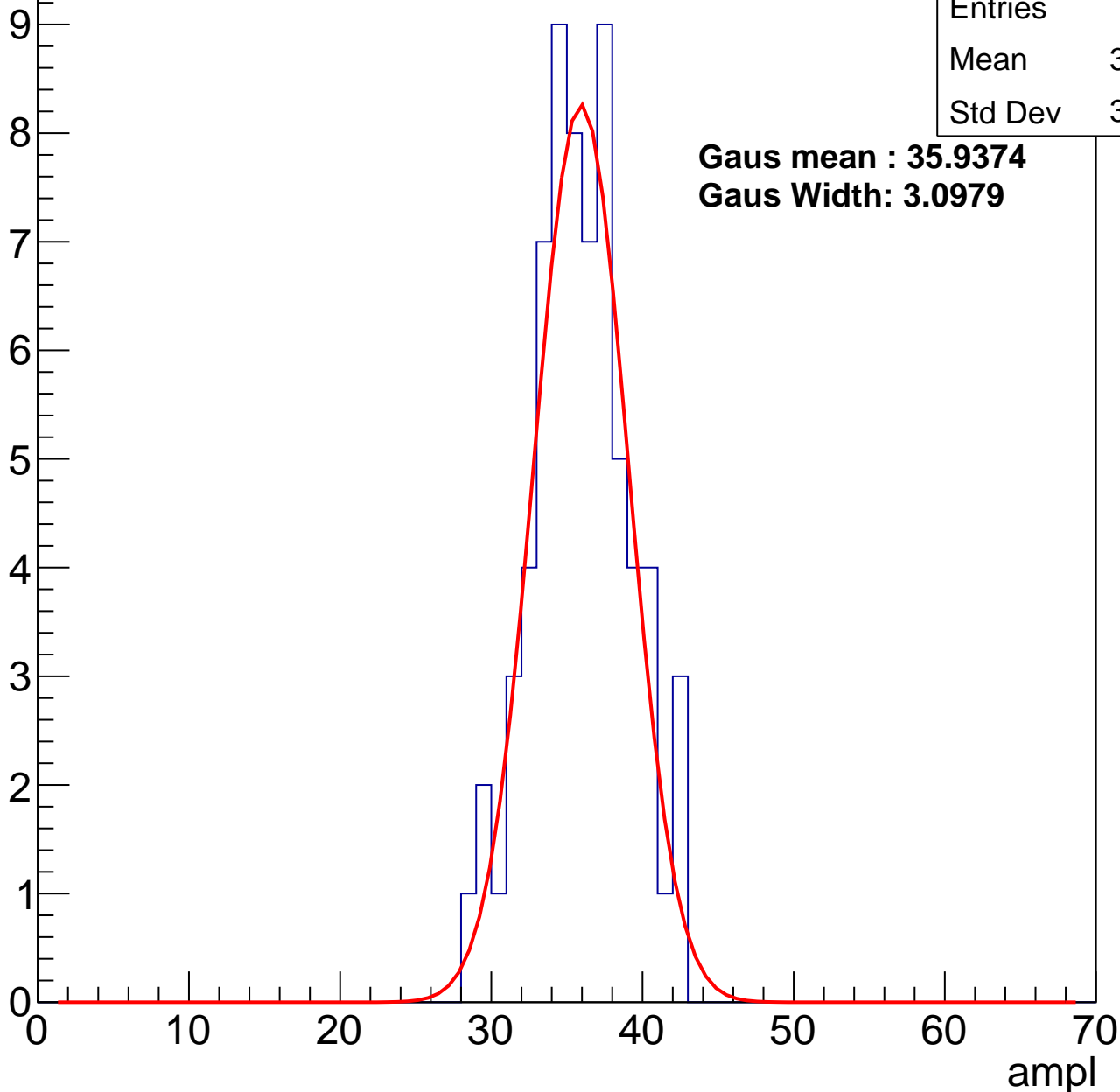
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.47
Std Dev	3.178

**Gaus mean : 35.9374**

**Gaus Width: 3.0979**



# B1L000S, U23-ch3, adc2

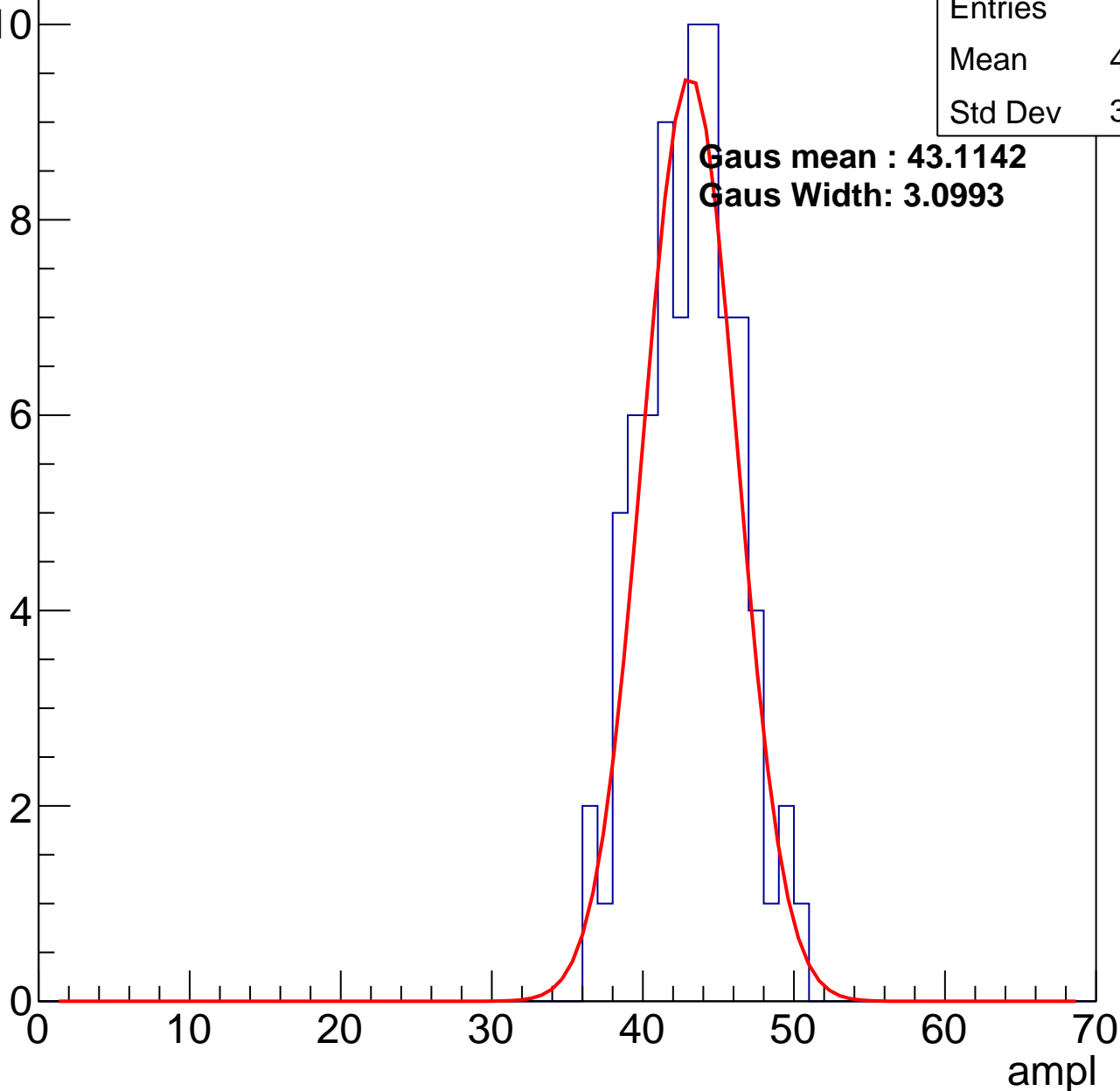
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	42.65
Std Dev	3.108

**Gaus mean : 43.1142**

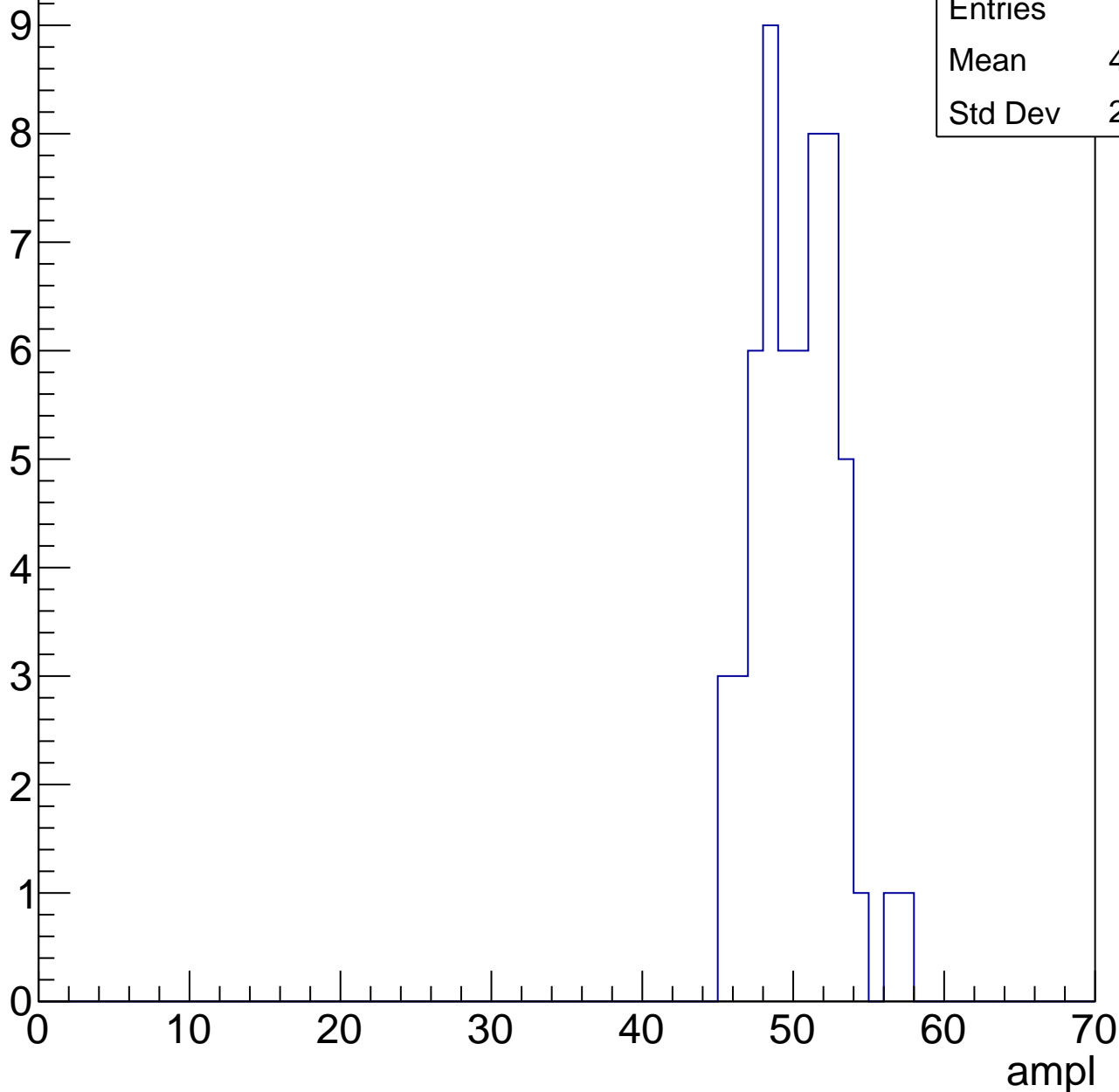
**Gaus Width: 3.0993**



# B1L000S, U23-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

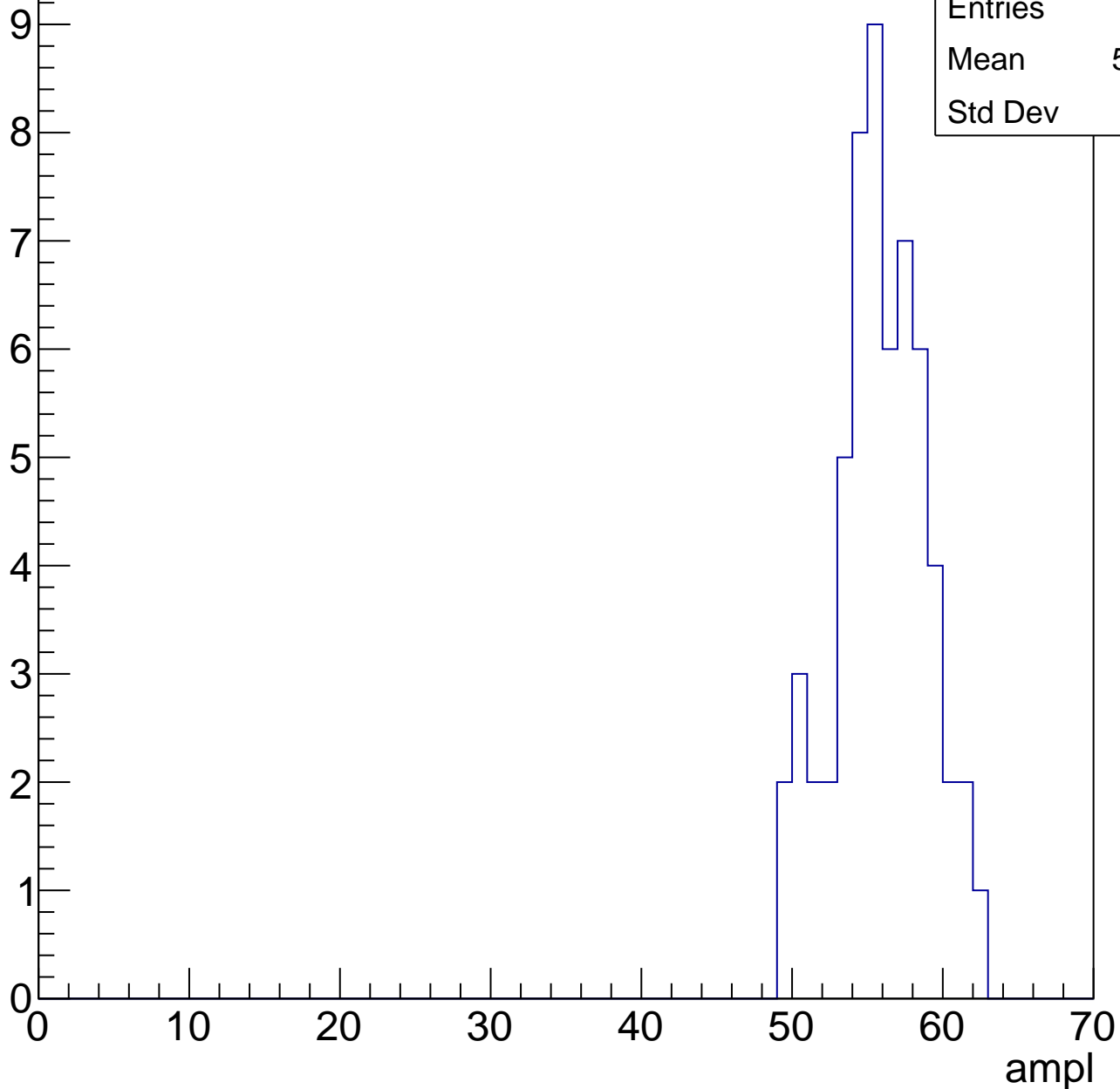
Entry



# B1L000S, U23-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch3, adc5

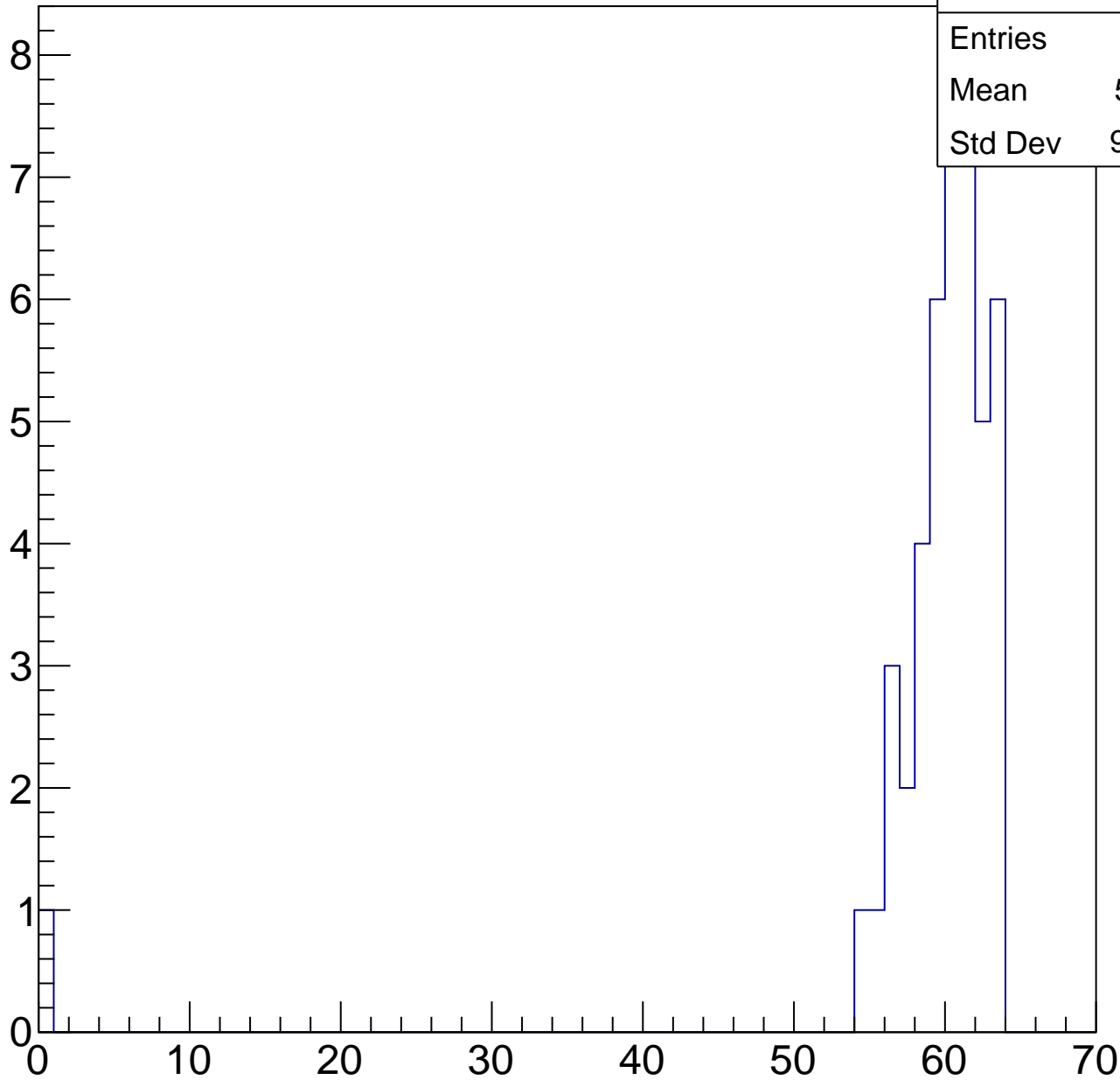
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.51
Std Dev	9.106

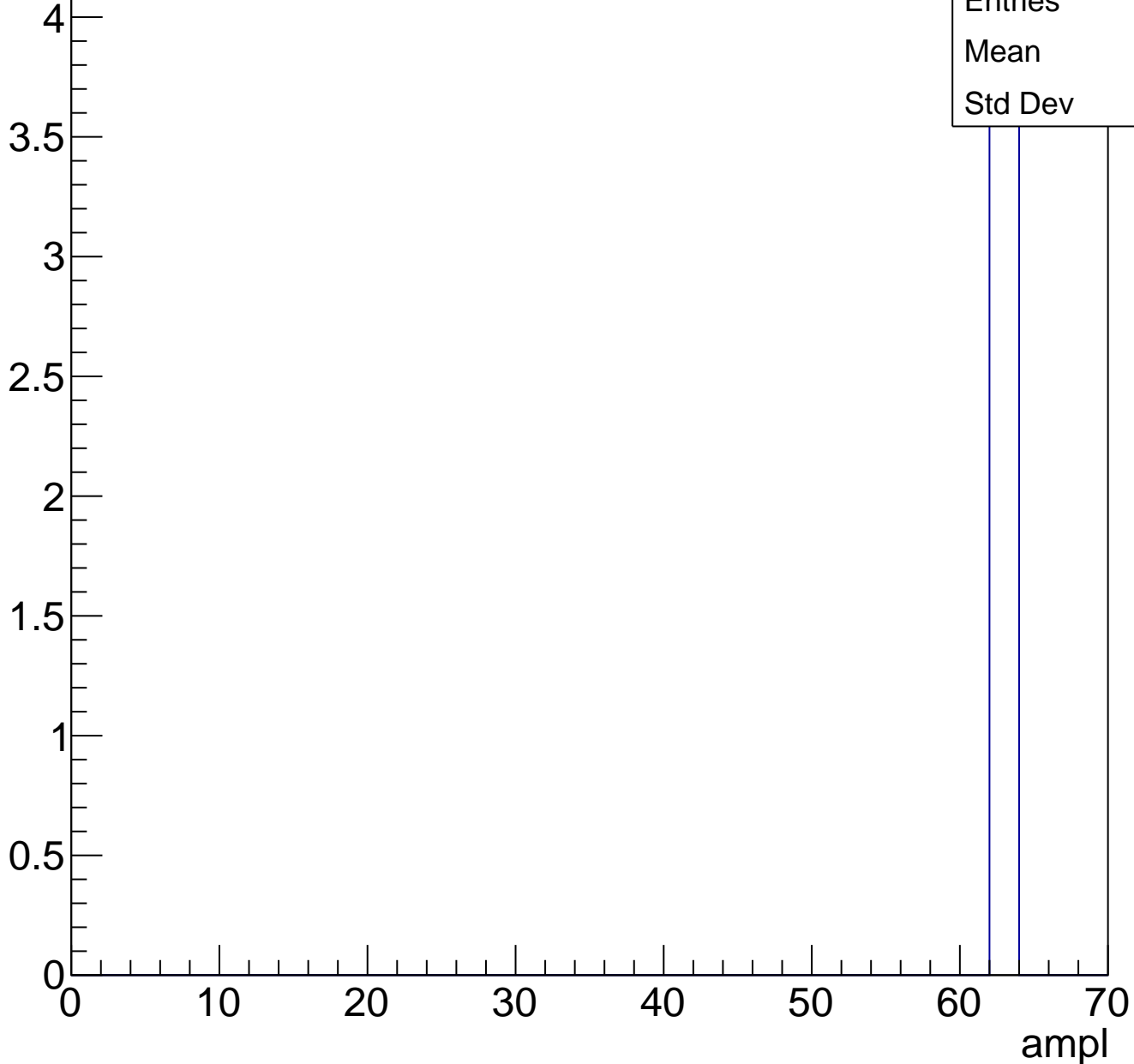
ampl



# B1L000S, U23-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

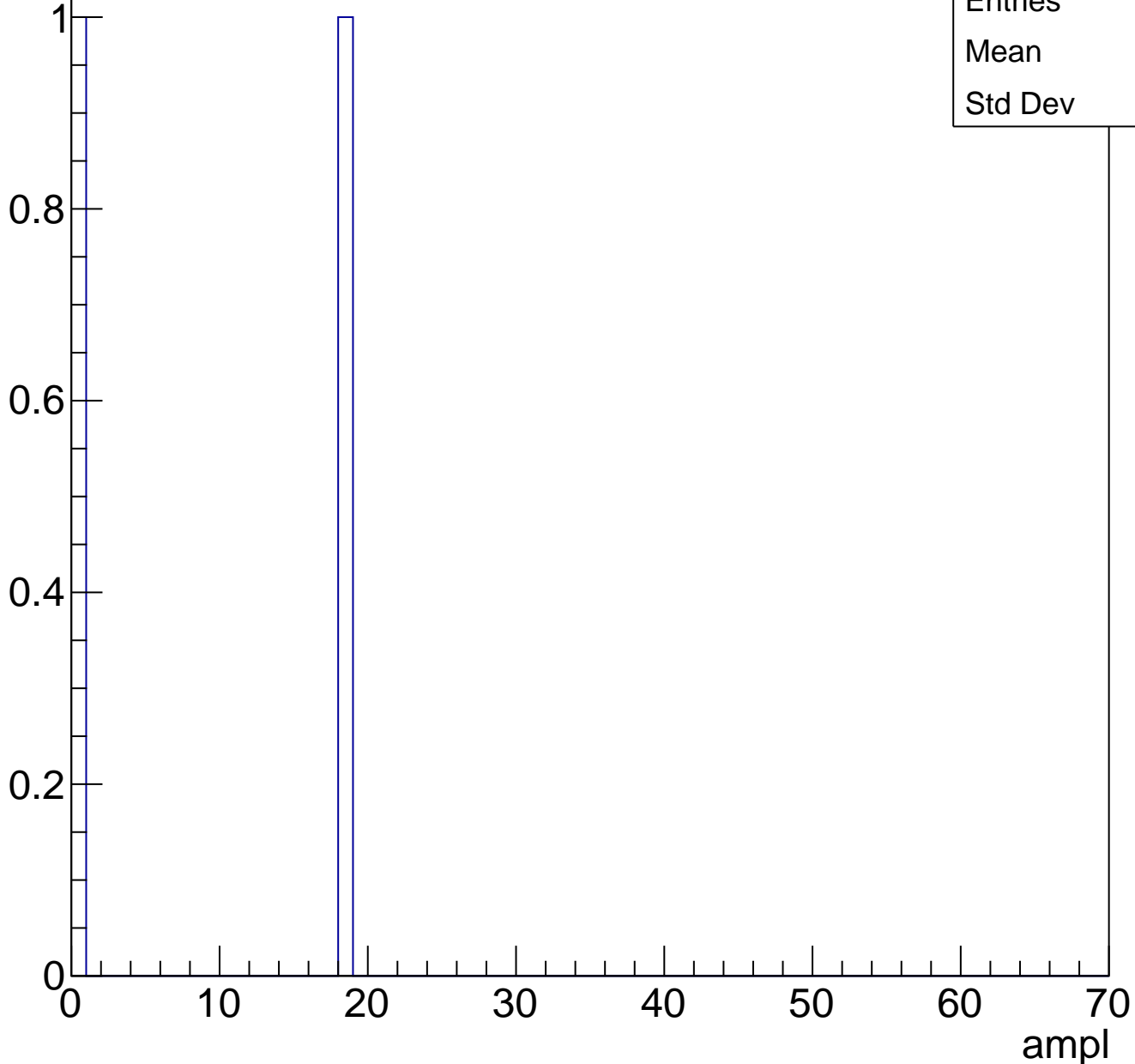




# B1L000S, U23-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	9
Std Dev	9

# B1L000S, U23-ch4, adc0

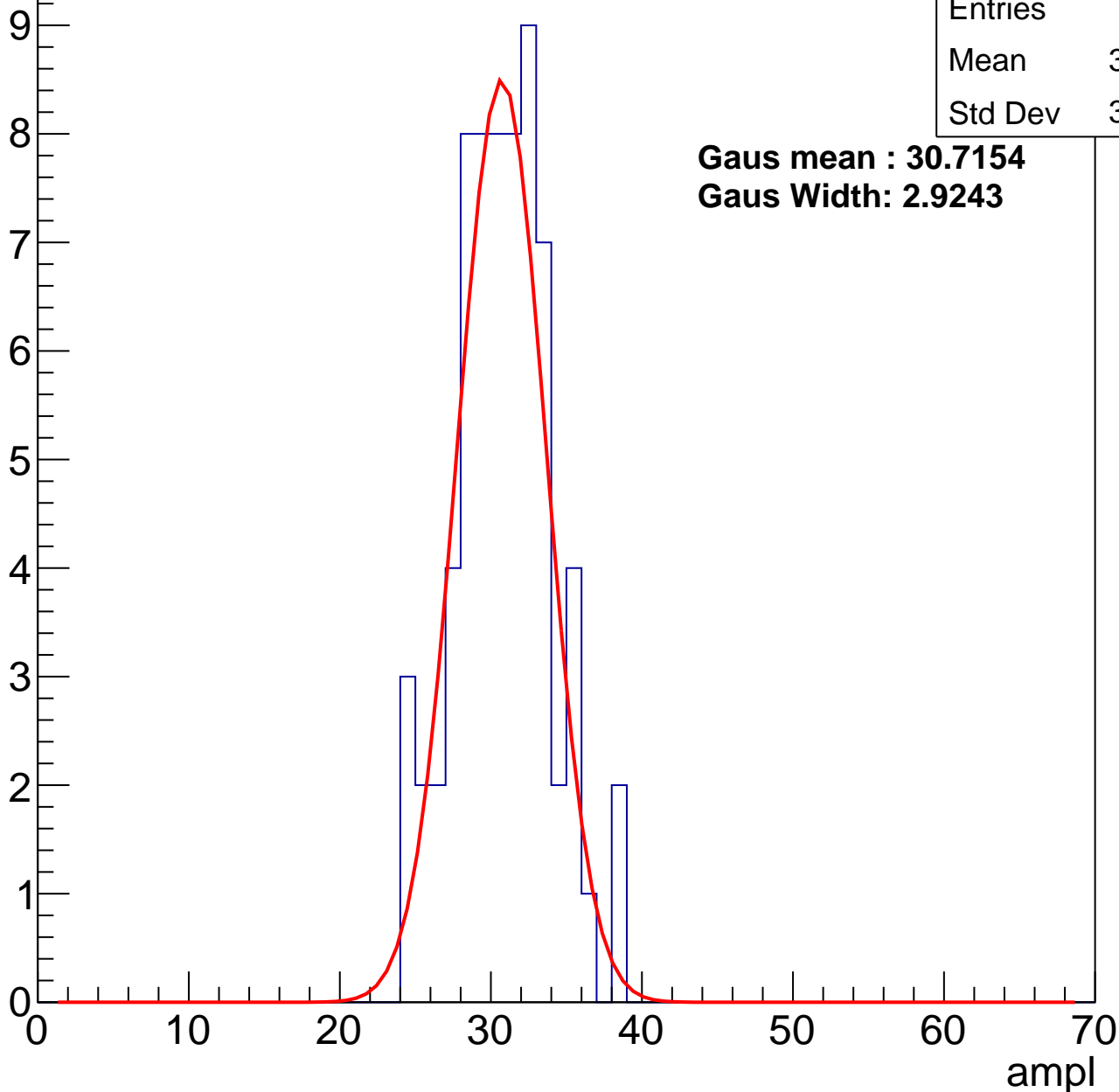
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.37
Std Dev	3.115

**Gaus mean : 30.7154**

**Gaus Width: 2.9243**



# B1L000S, U23-ch4, adc1

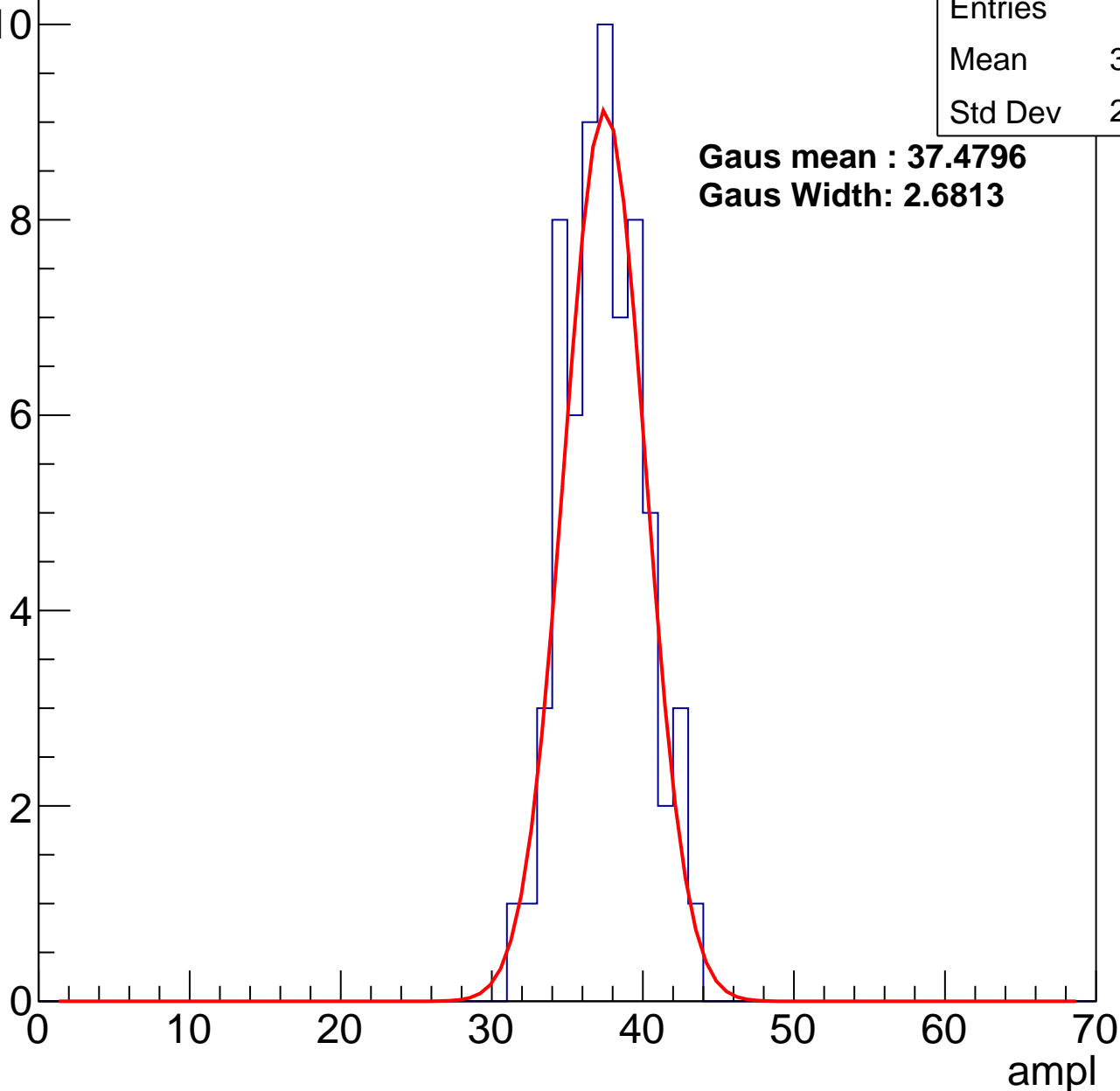
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.98
Std Dev	2.625

**Gaus mean : 37.4796**

**Gaus Width: 2.6813**



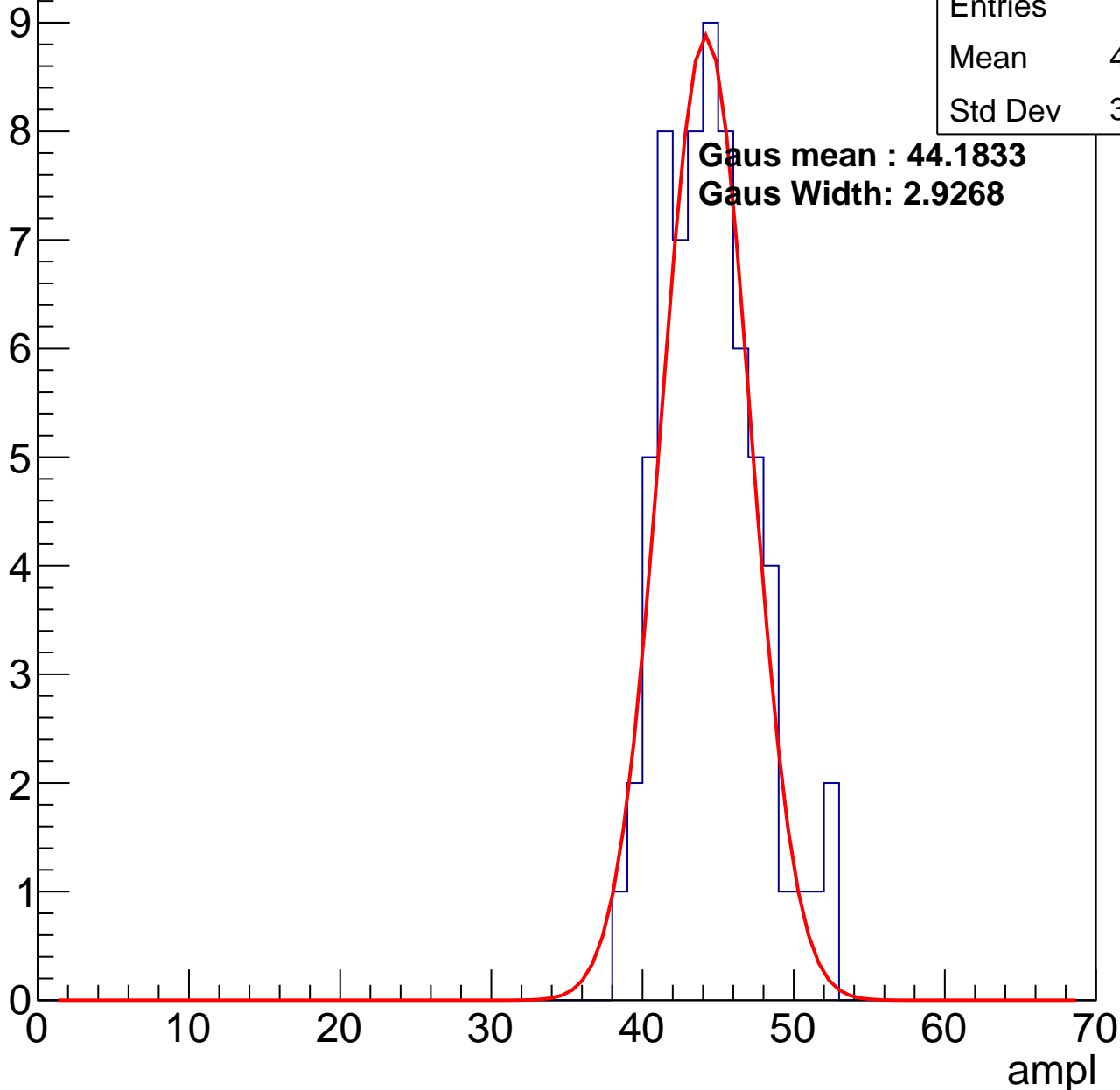
# B1L000S, U23-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	44.04
Std Dev	3.099

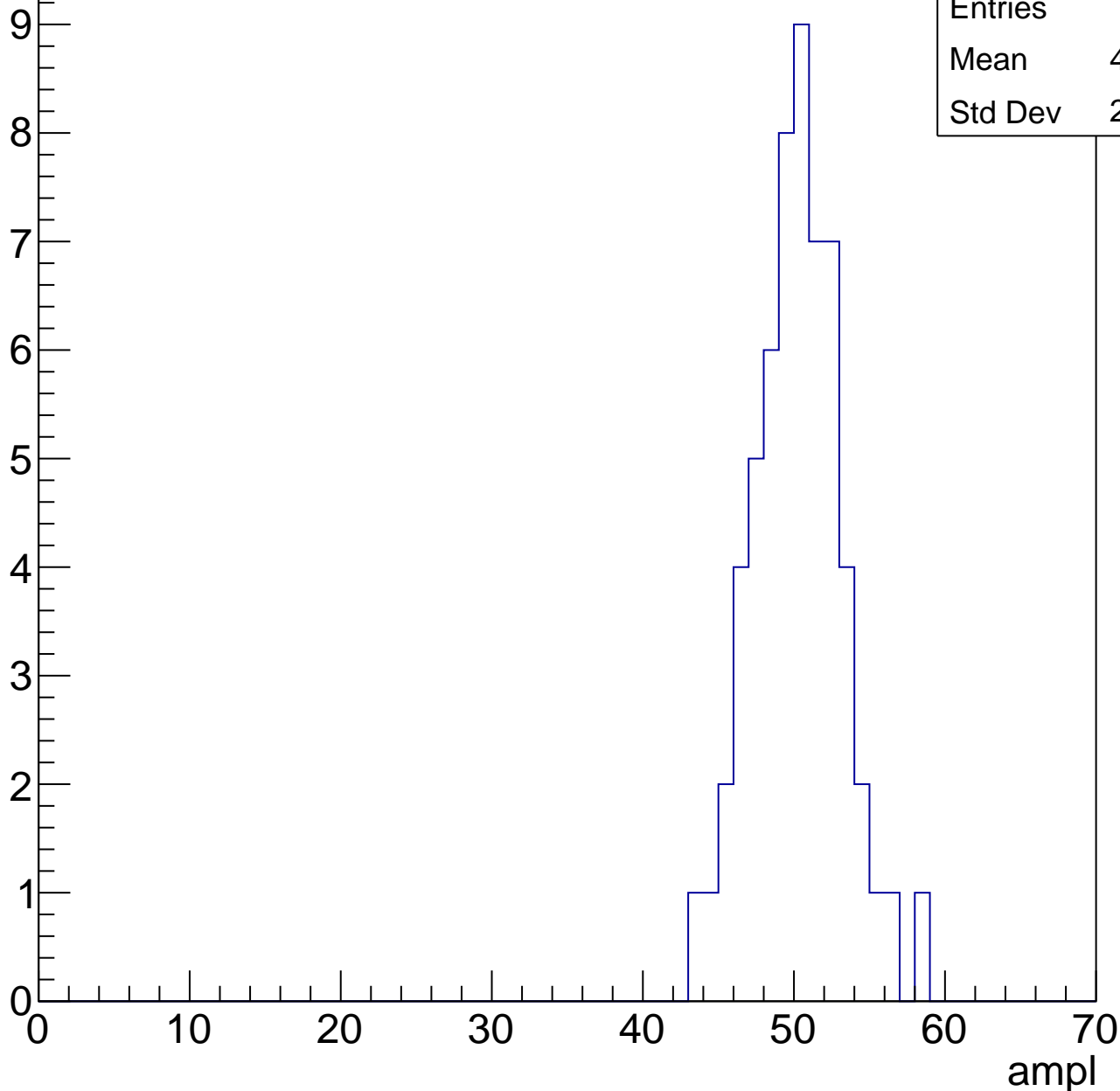
**Gaus mean : 44.1833**  
**Gaus Width: 2.9268**



# B1L000S, U23-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	59
Mean	55.78
Std Dev	2.373

Entry

10

8

6

4

2

0

0

10

20

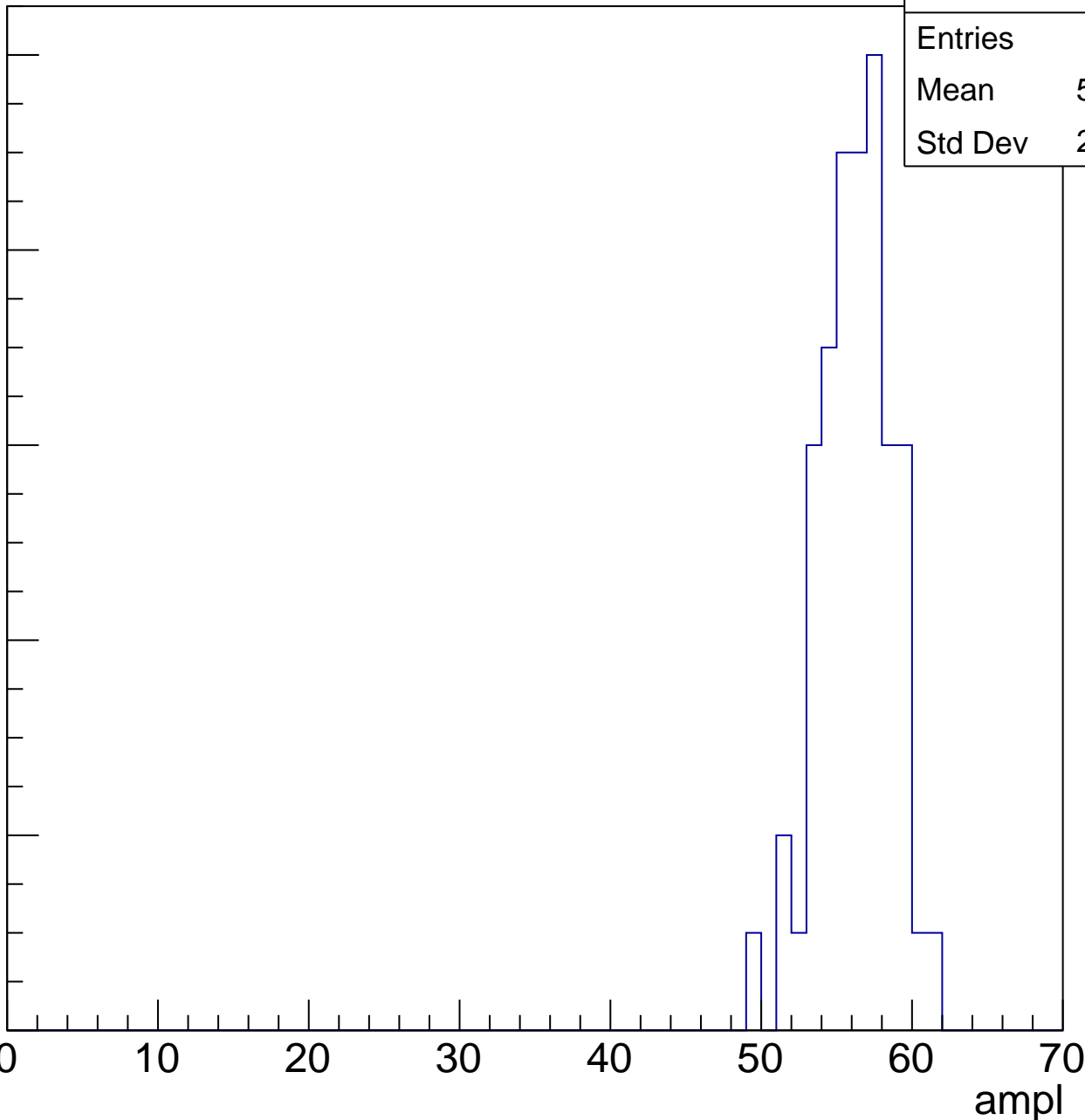
30

40

50

60

ampl



# B1L000S, U23-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 44

Mean 59.55

Std Dev 9.26

8

6

4

2

0

0

10

20

30

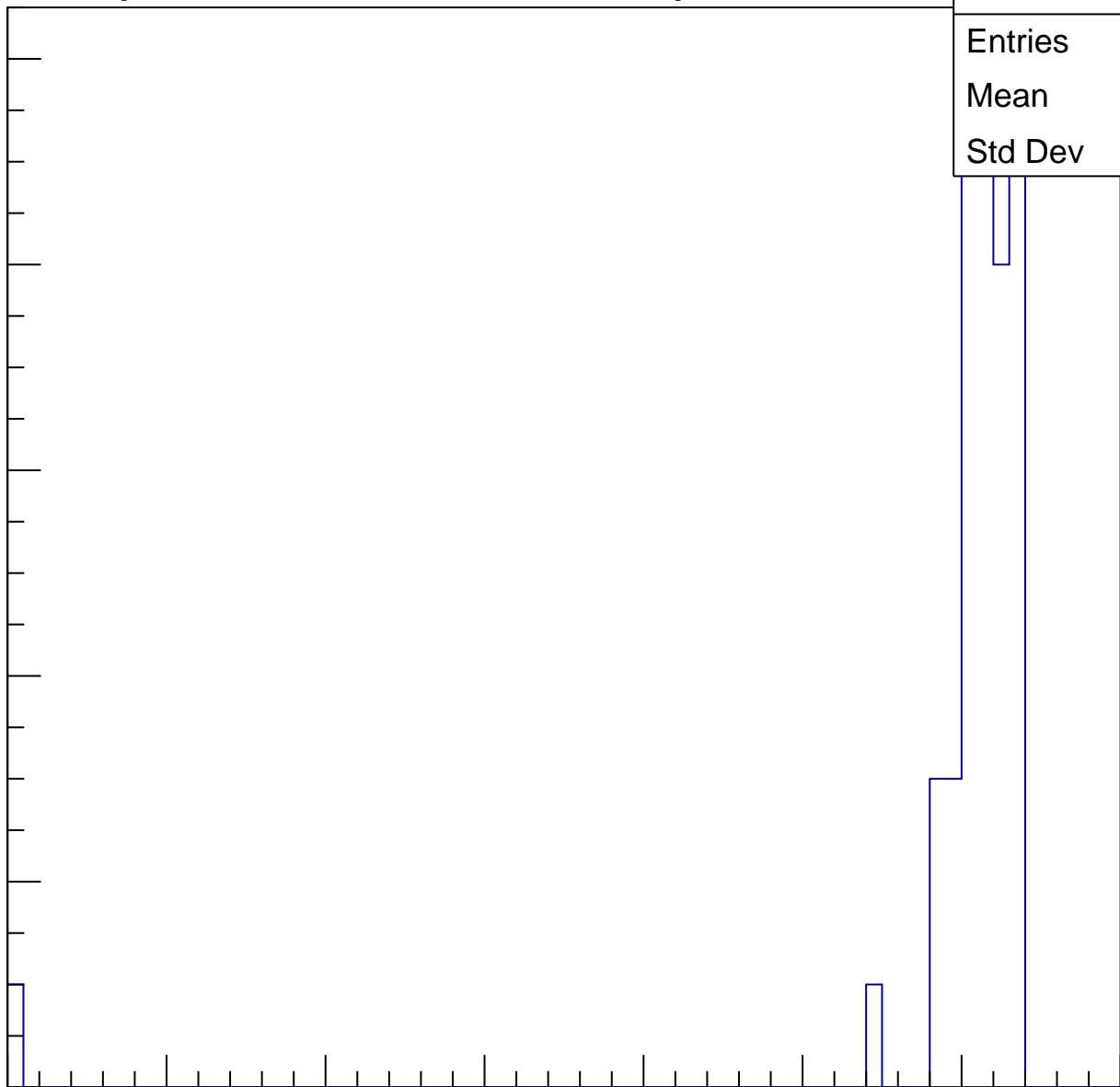
40

50

60

70

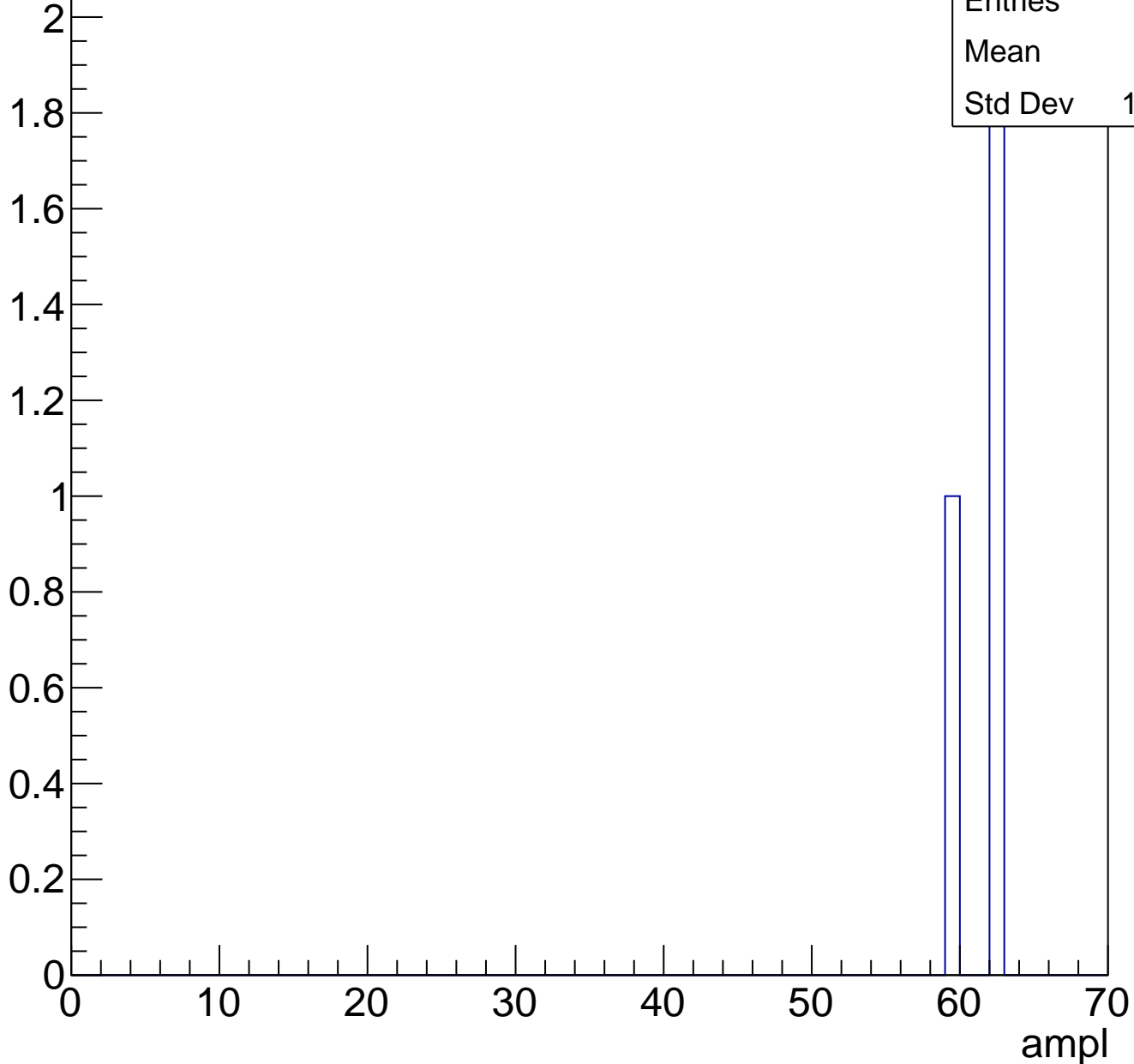
ampl



# B1L000S, U23-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

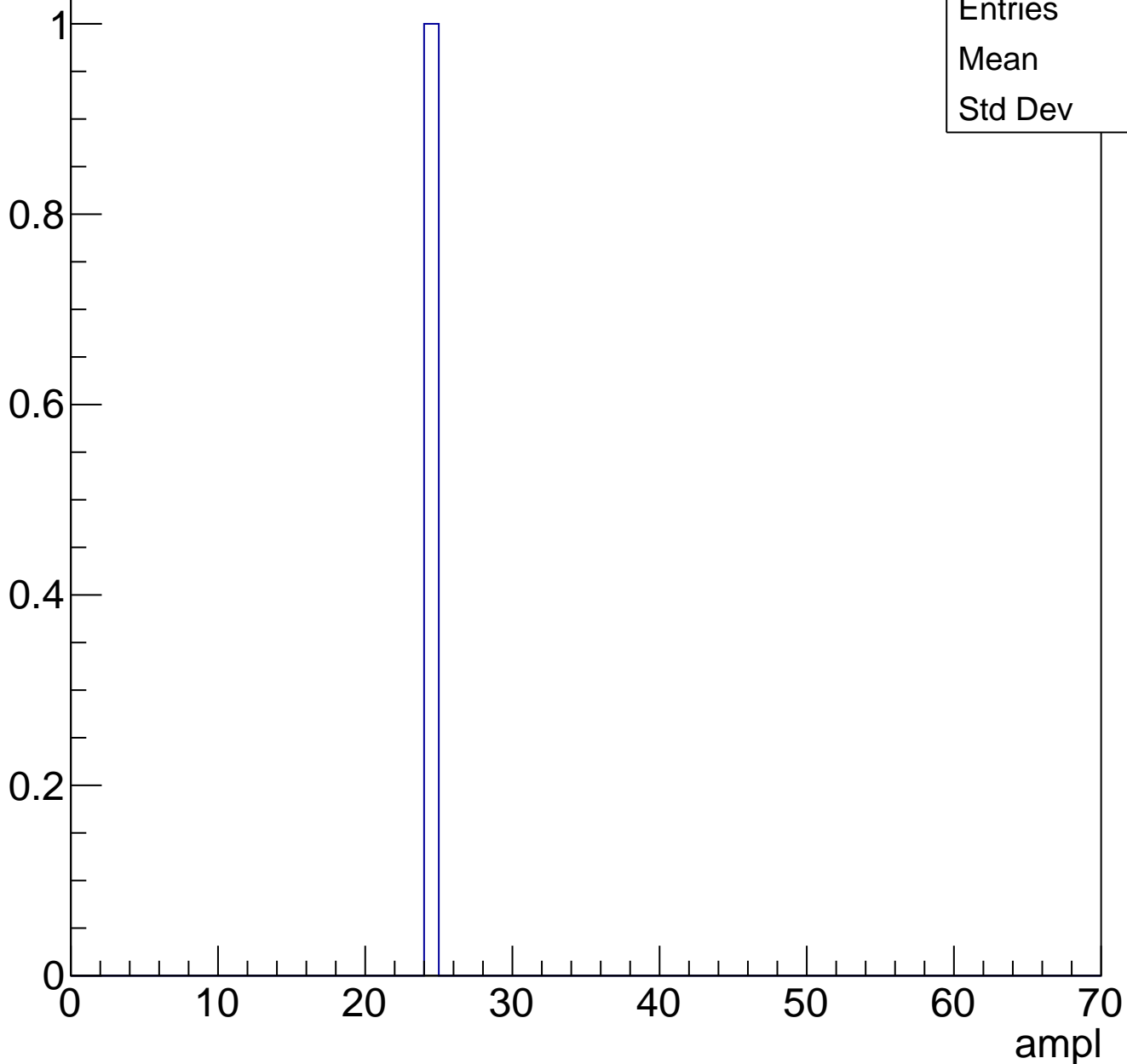




# B1L000S, U23-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch5, adc0

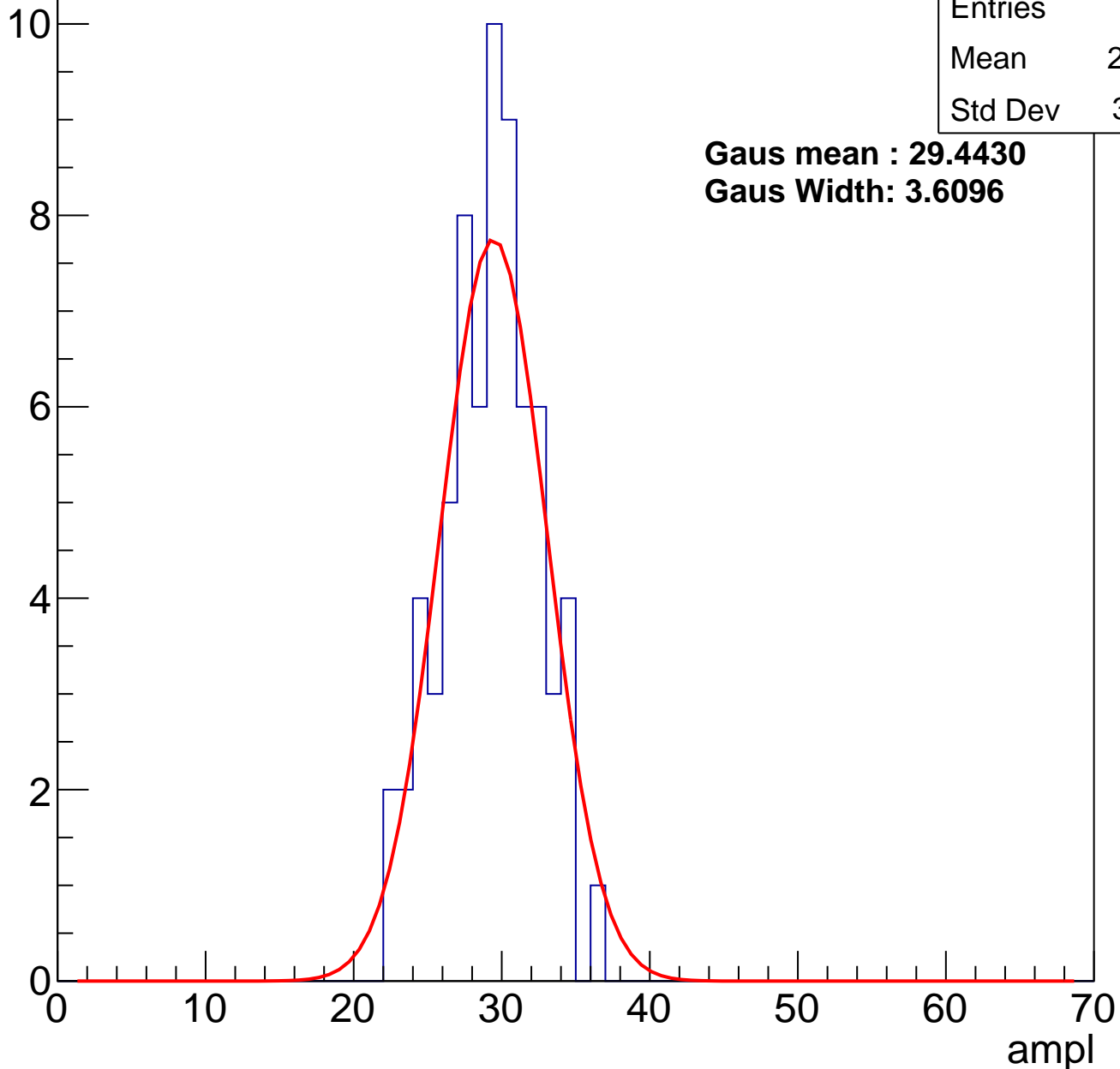
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.75
Std Dev	3.141

**Gaus mean : 29.4430**

**Gaus Width: 3.6096**

Entry



# B1L000S, U23-ch5, adc1

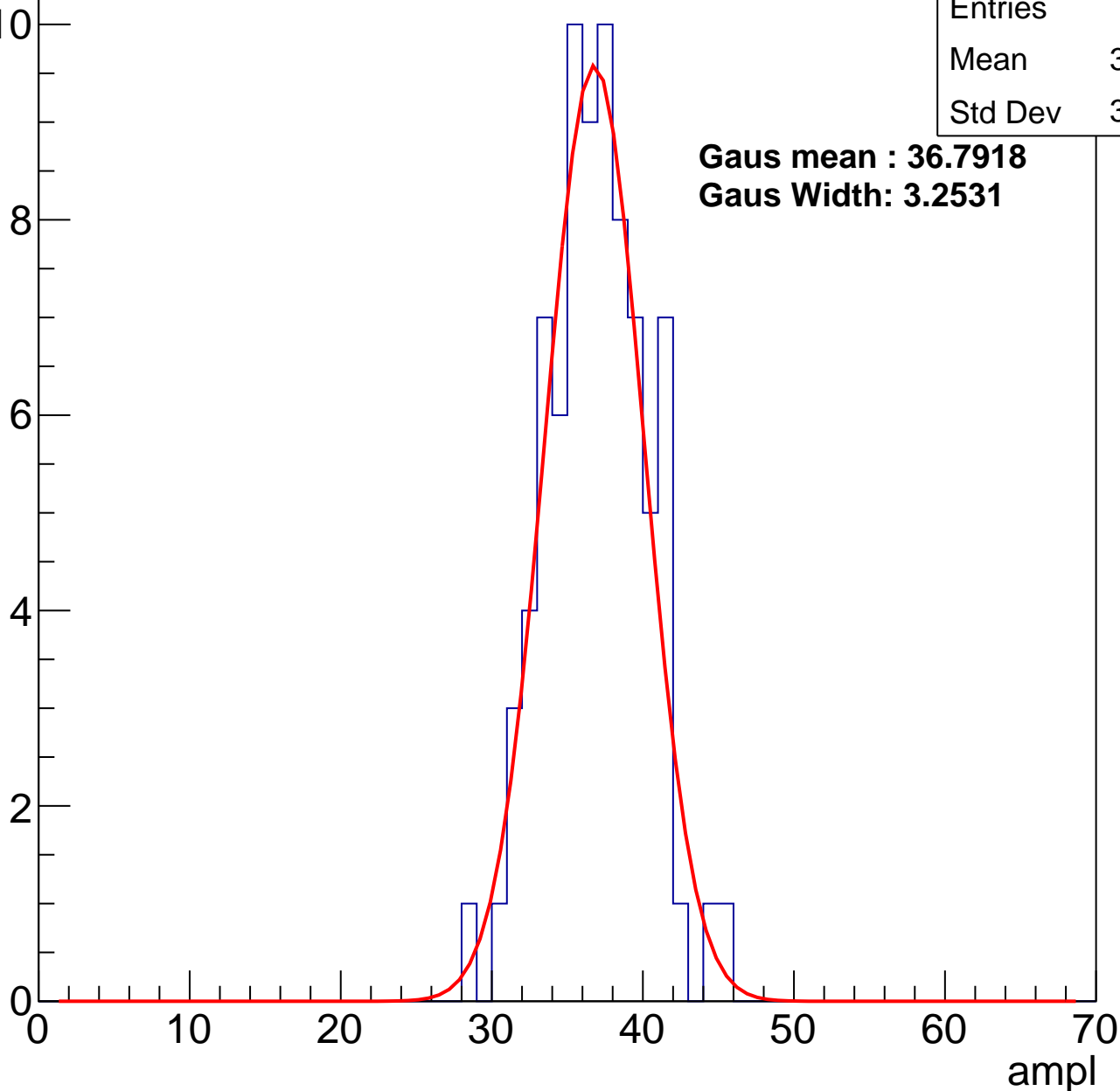
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	36.46
Std Dev	3.259

**Gaus mean : 36.7918**

**Gaus Width: 3.2531**



# B1L000S, U23-ch5, adc2

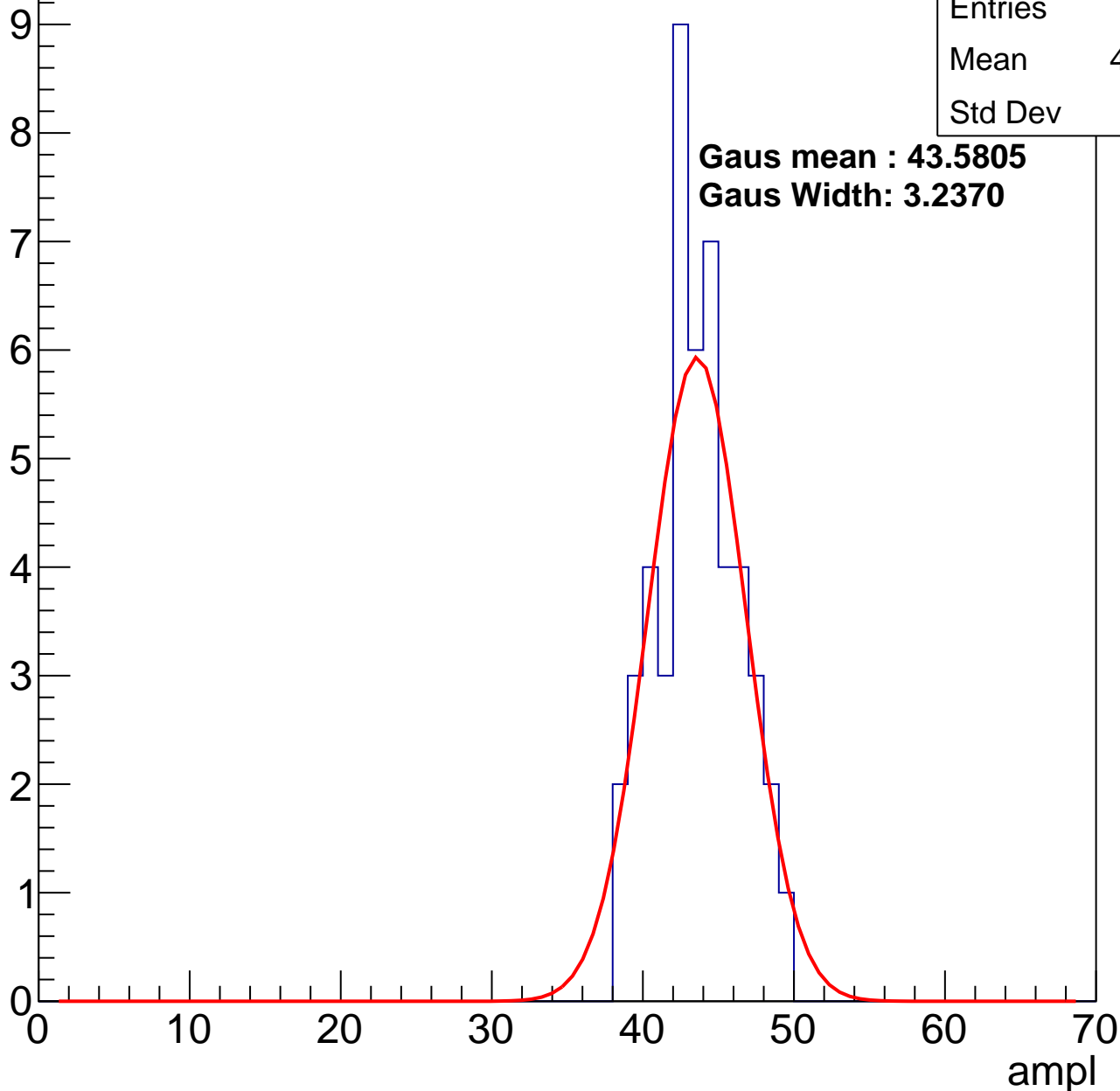
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	43.12
Std Dev	2.69

**Gaus mean : 43.5805**

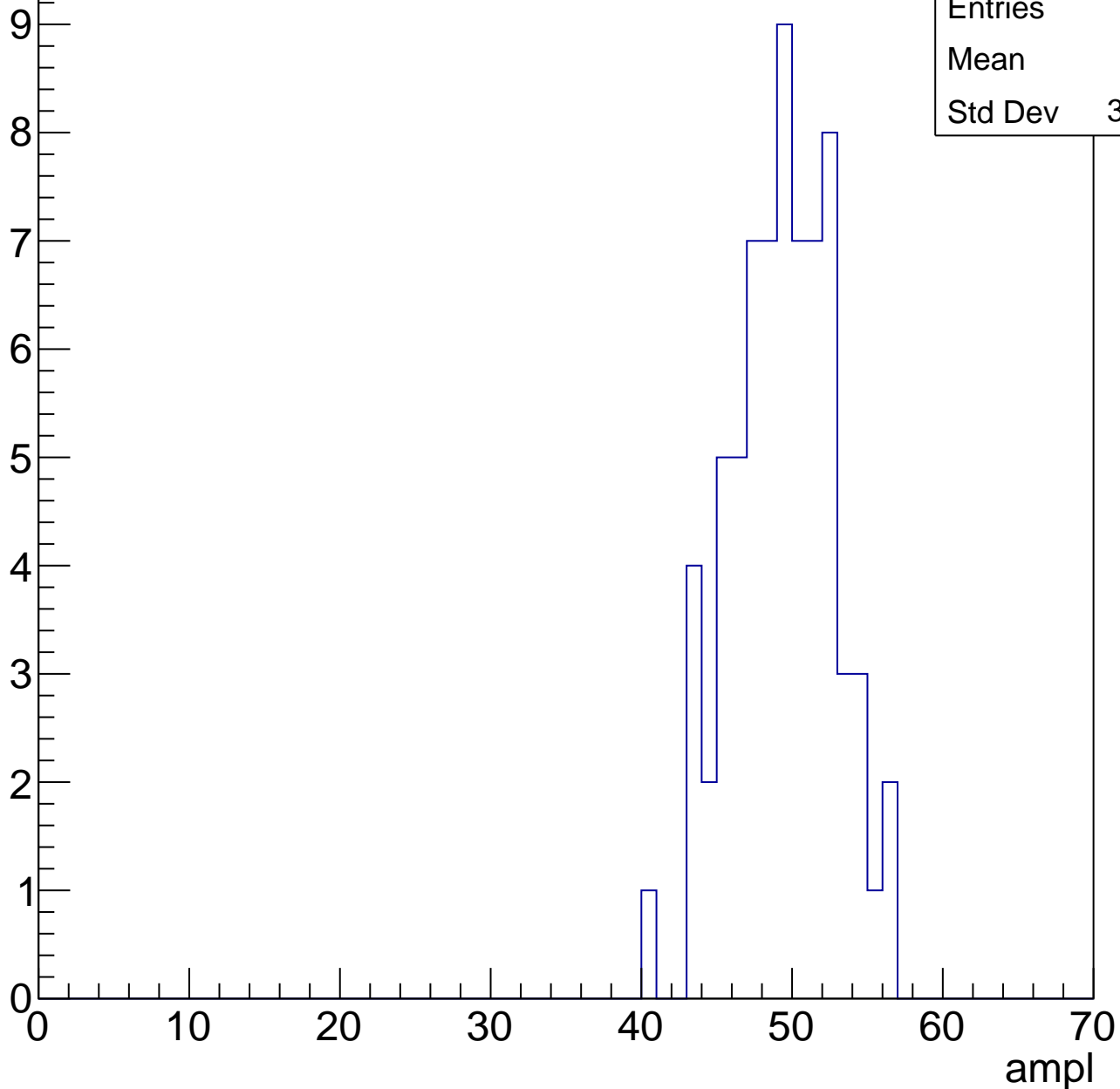
**Gaus Width: 3.2370**



# B1L000S, U23-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

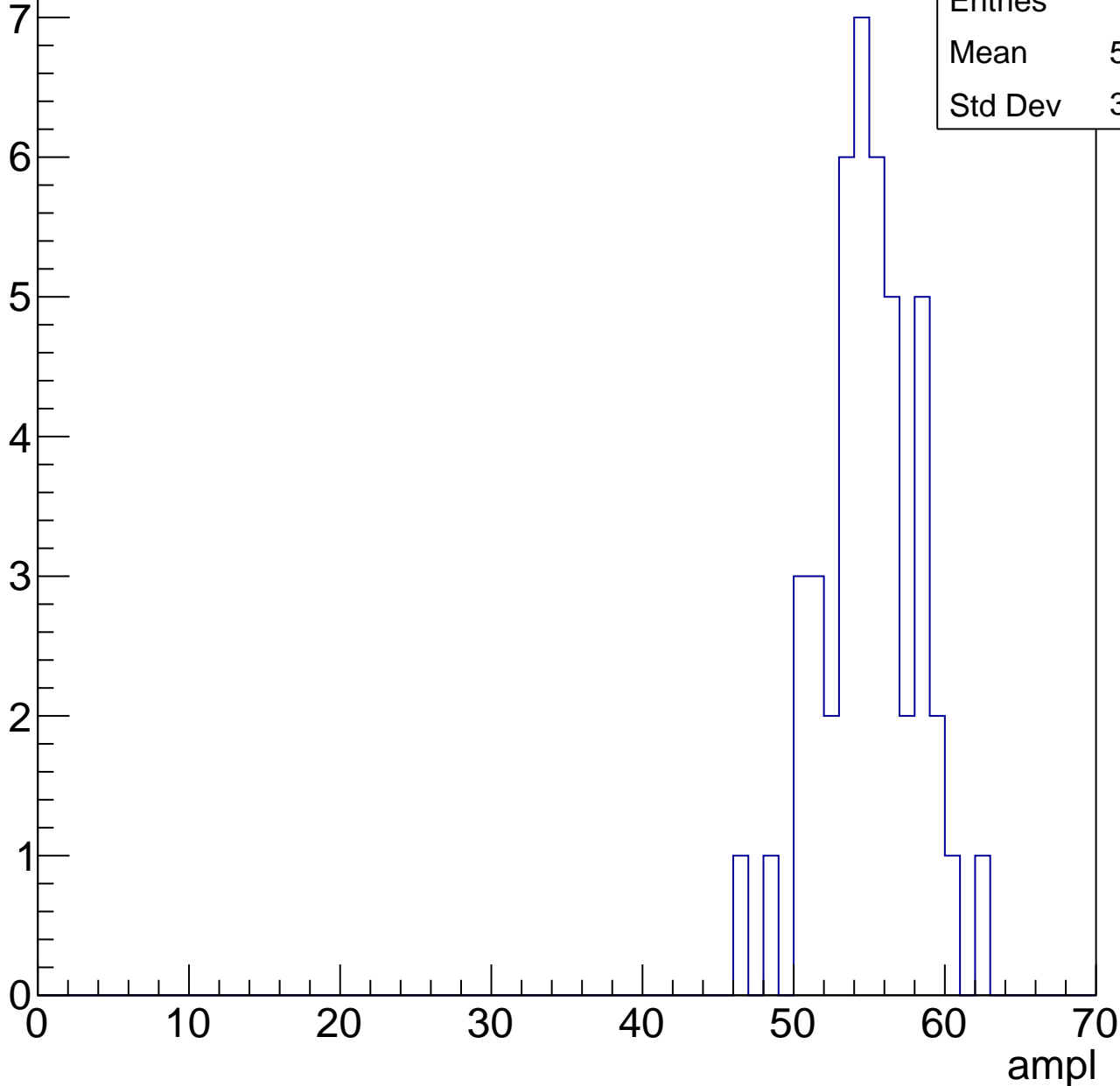


# B1L000S, U23-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	54.47
Std Dev	3.174



# B1L000S, U23-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	59.45
Std Dev	2.437

ampl

0

10

20

30

40

50

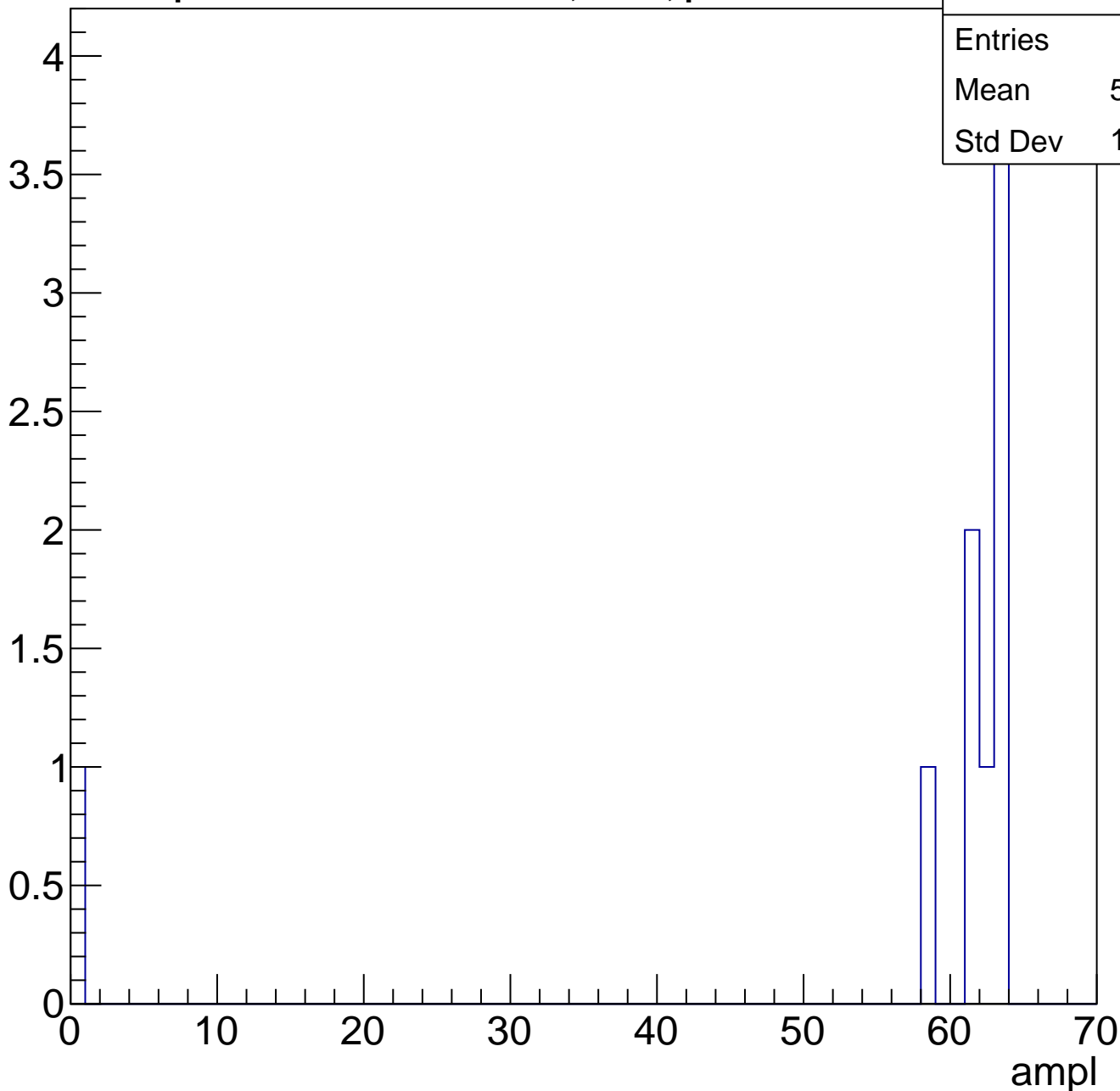
60

70

# B1L000S, U23-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch6, adc0

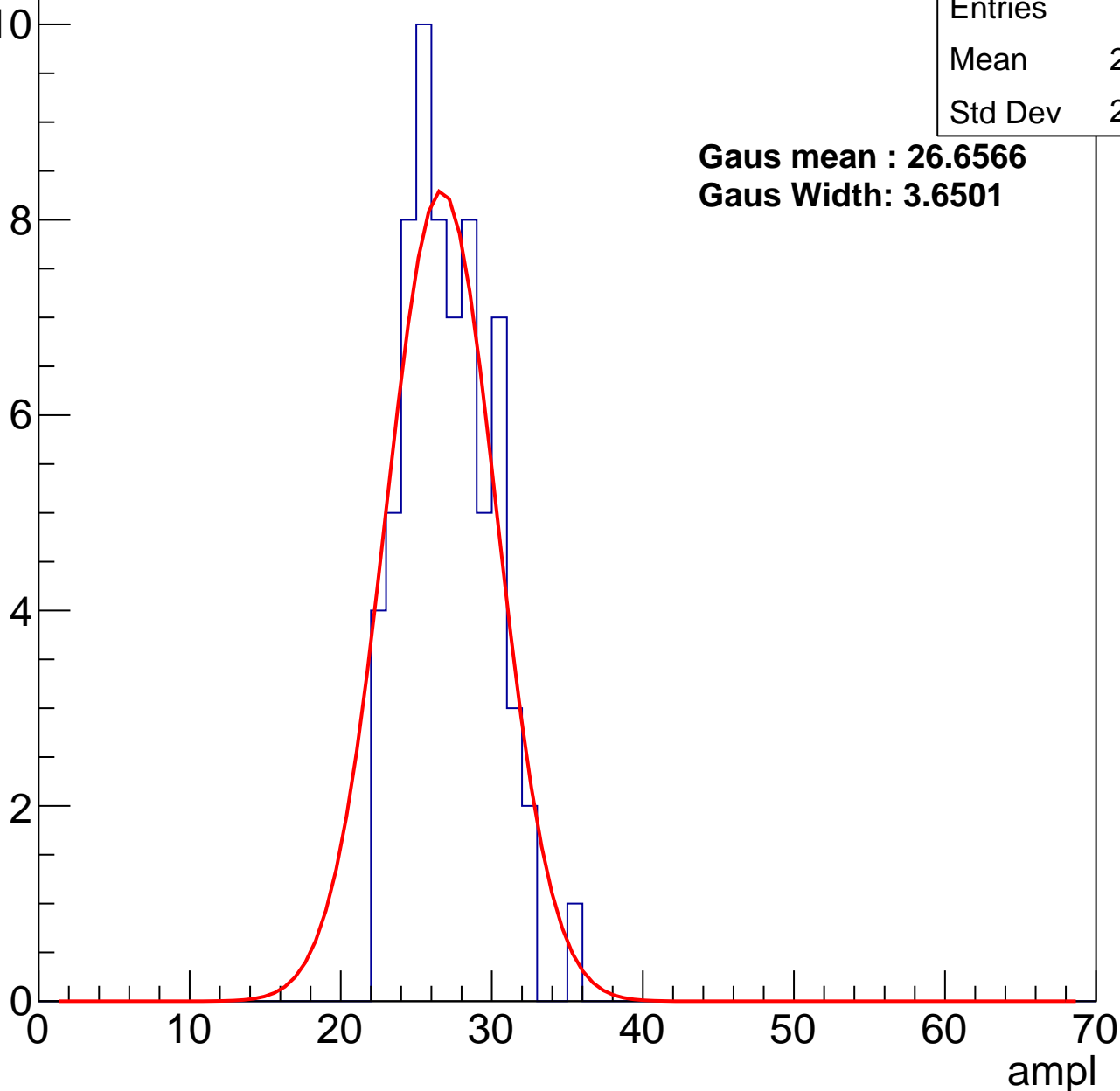
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26.66
Std Dev	2.832

**Gaus mean : 26.6566**

**Gaus Width: 3.6501**



# B1L000S, U23-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	33.53
Std Dev	3.32

**Gaus mean : 34.2529**

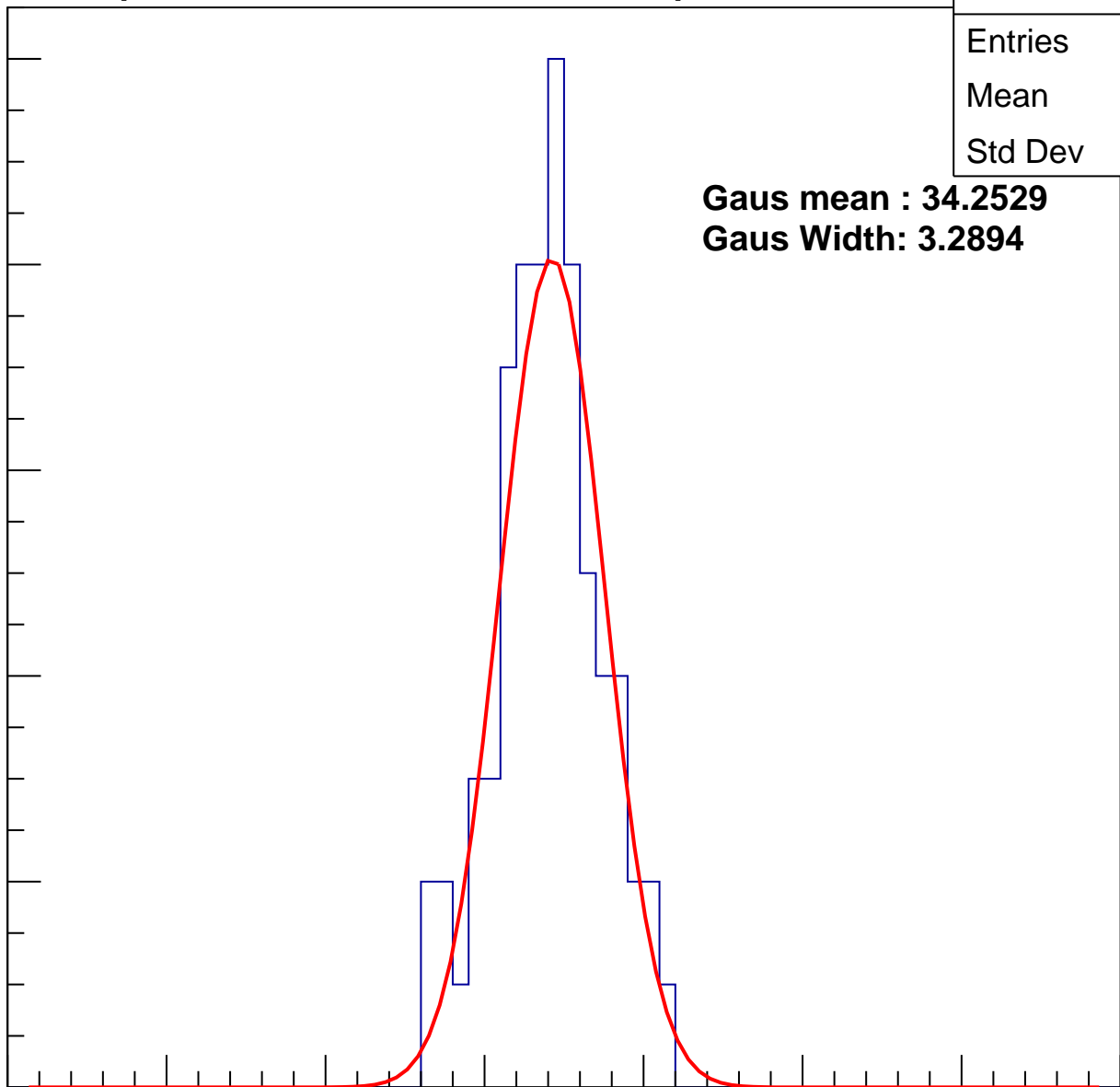
**Gaus Width: 3.2894**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch6, adc2

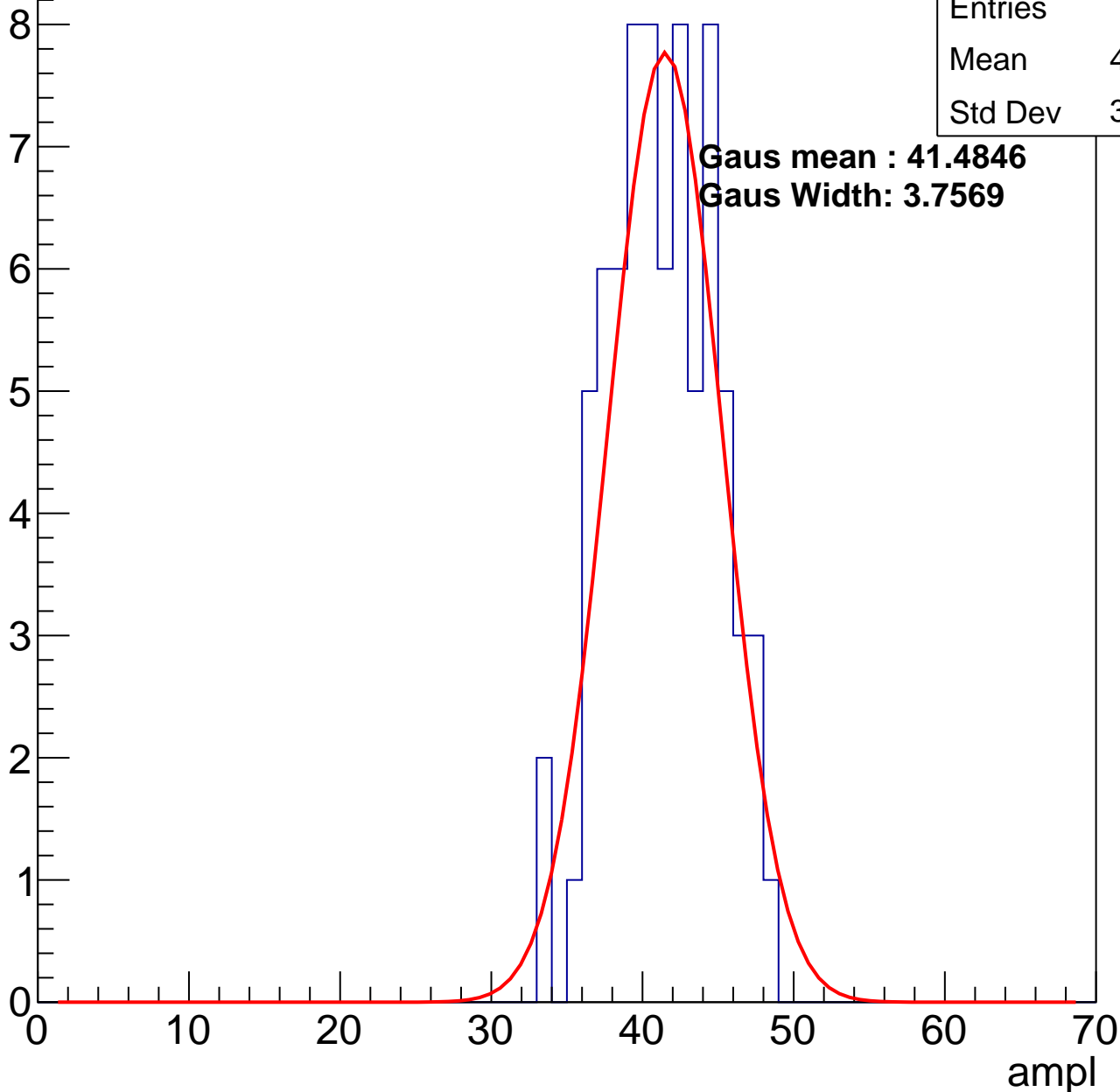
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	40.85
Std Dev	3.444

**Gaus mean : 41.4846**

**Gaus Width: 3.7569**



# B1L000S, U23-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

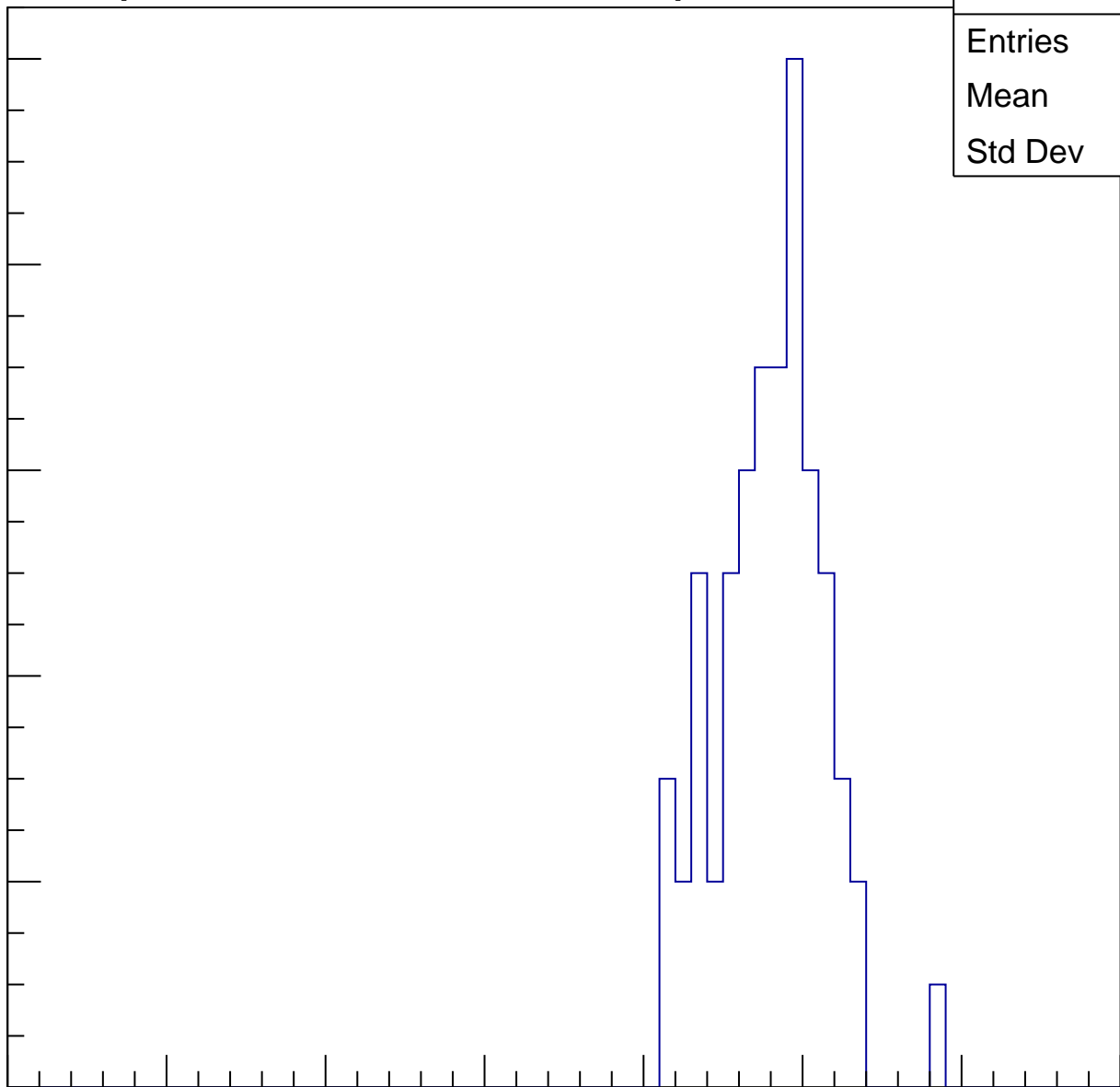
Entries	64
Mean	47.52
Std Dev	3.34

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

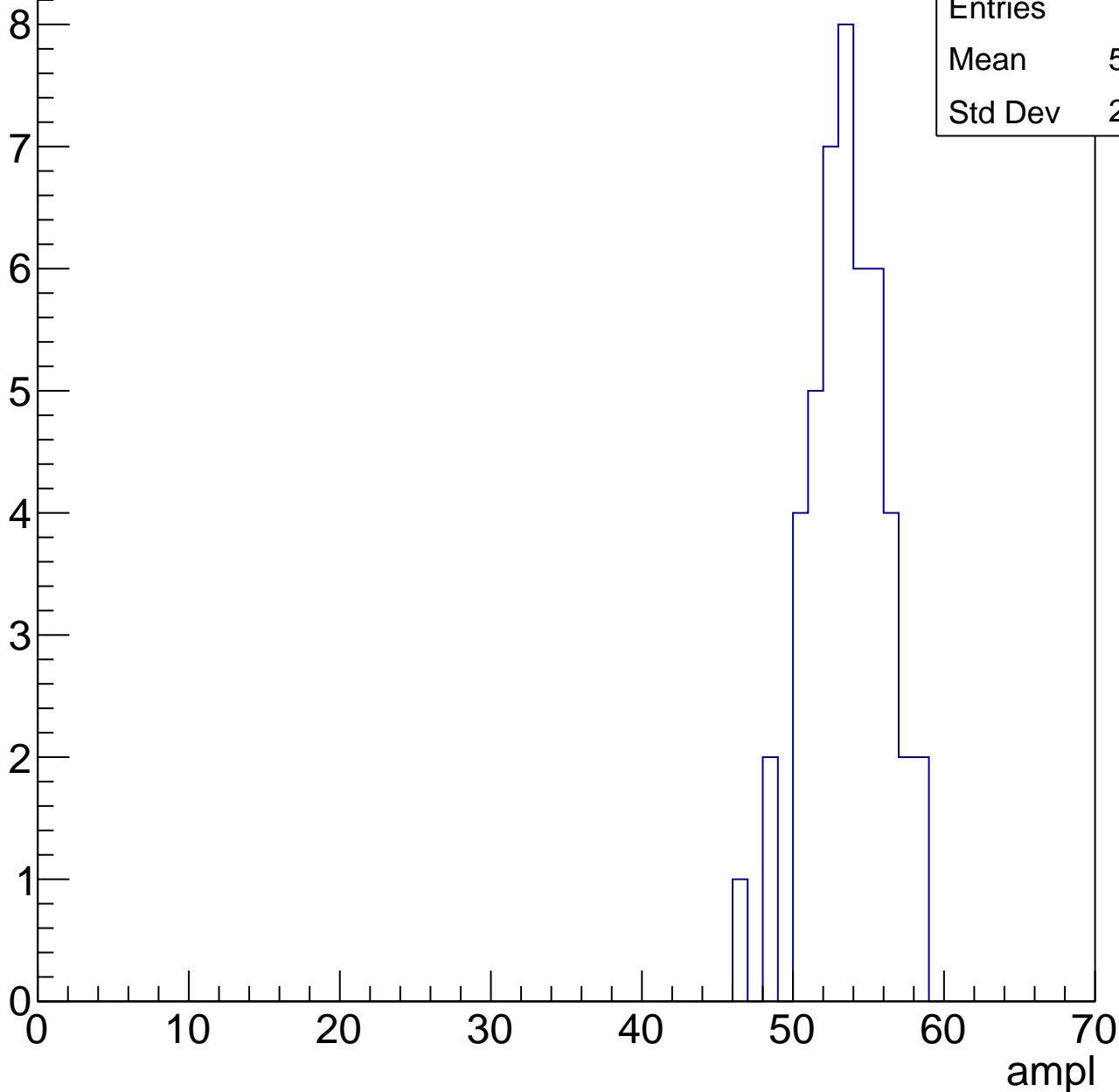


# B1L000S, U23-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

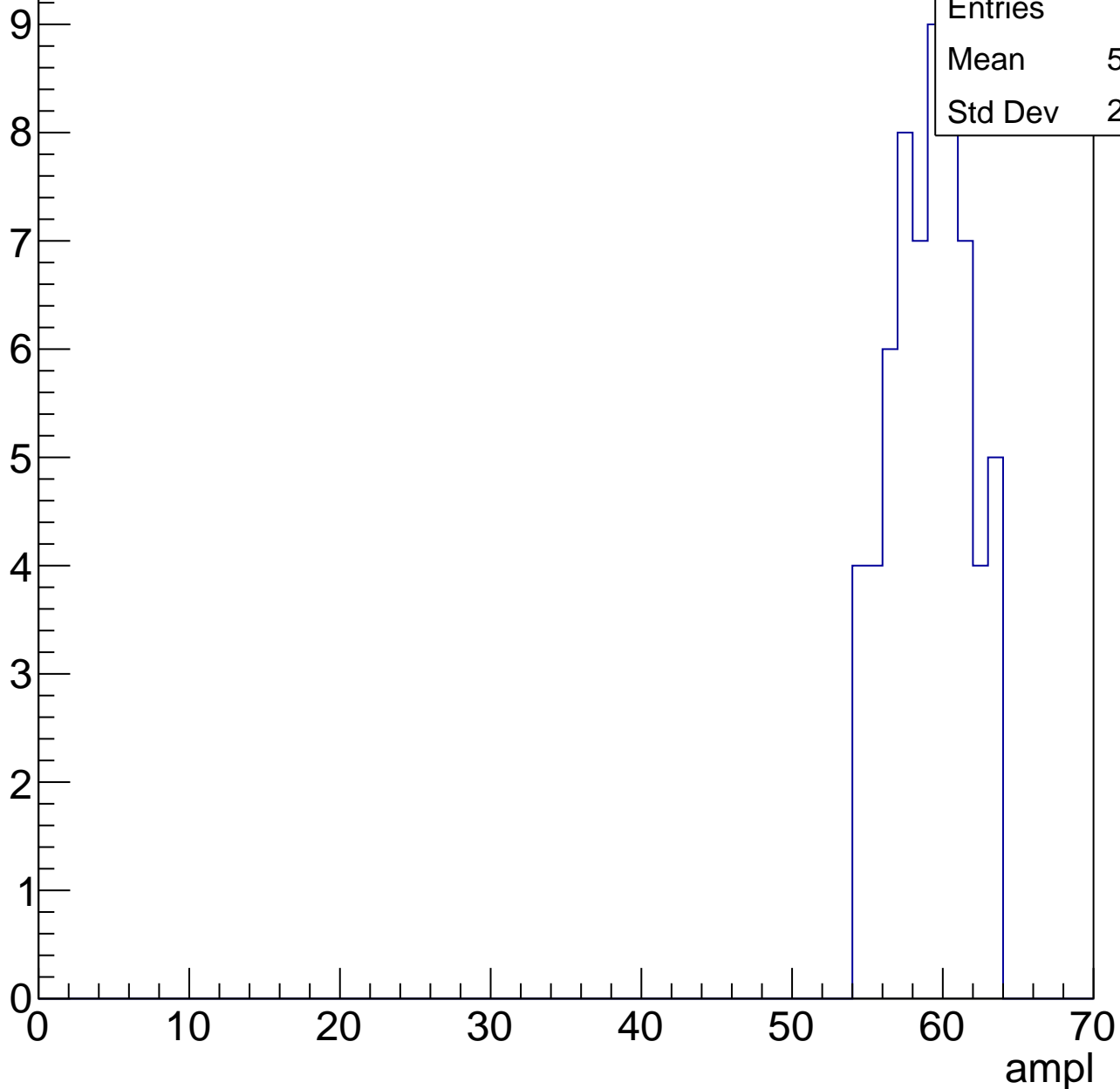
Entries	47
Mean	53.04
Std Dev	2.568



# B1L000S, U23-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

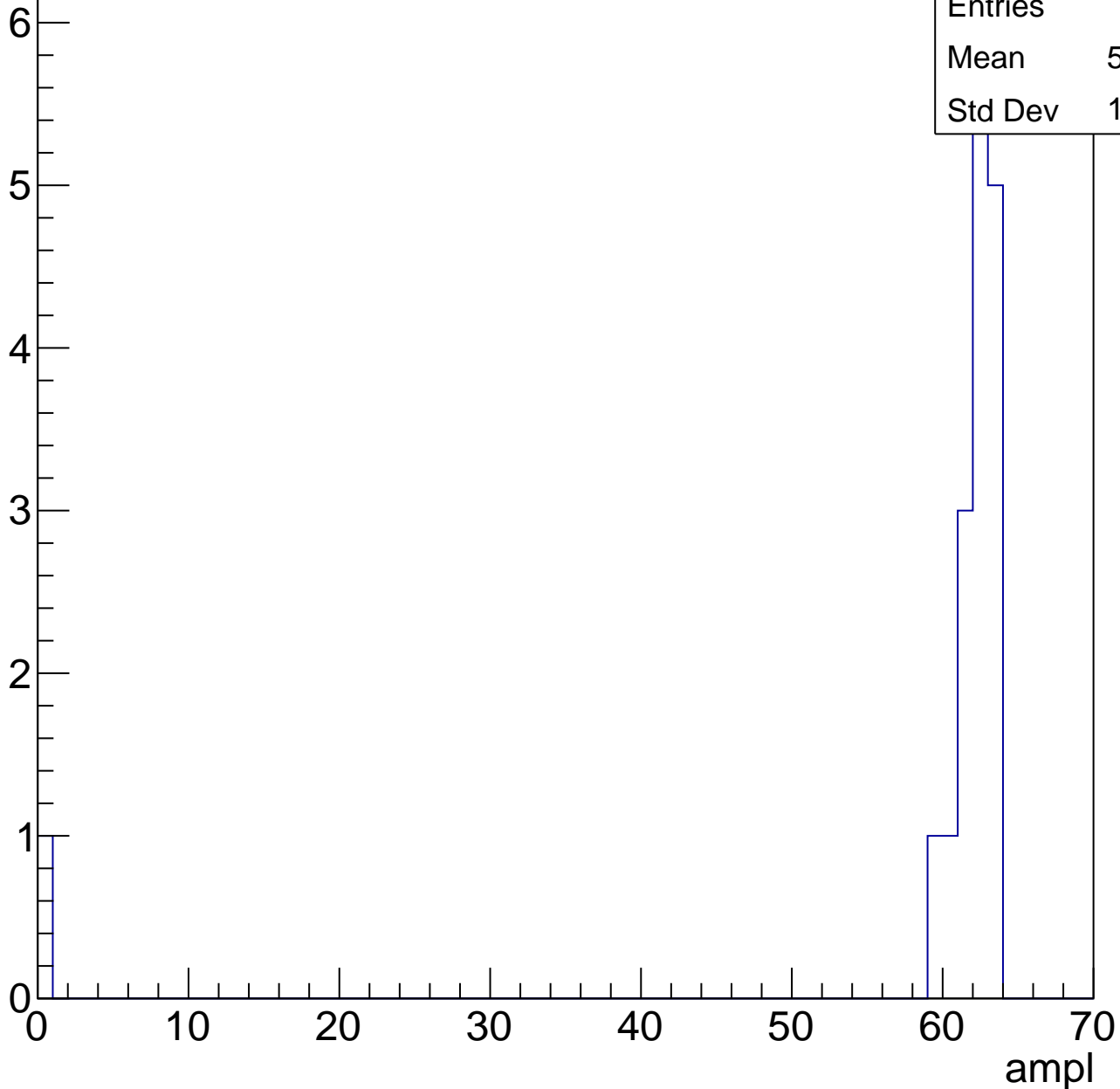


# B1L000S, U23-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.18
Std Dev	14.59





# B1L000S, U23-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

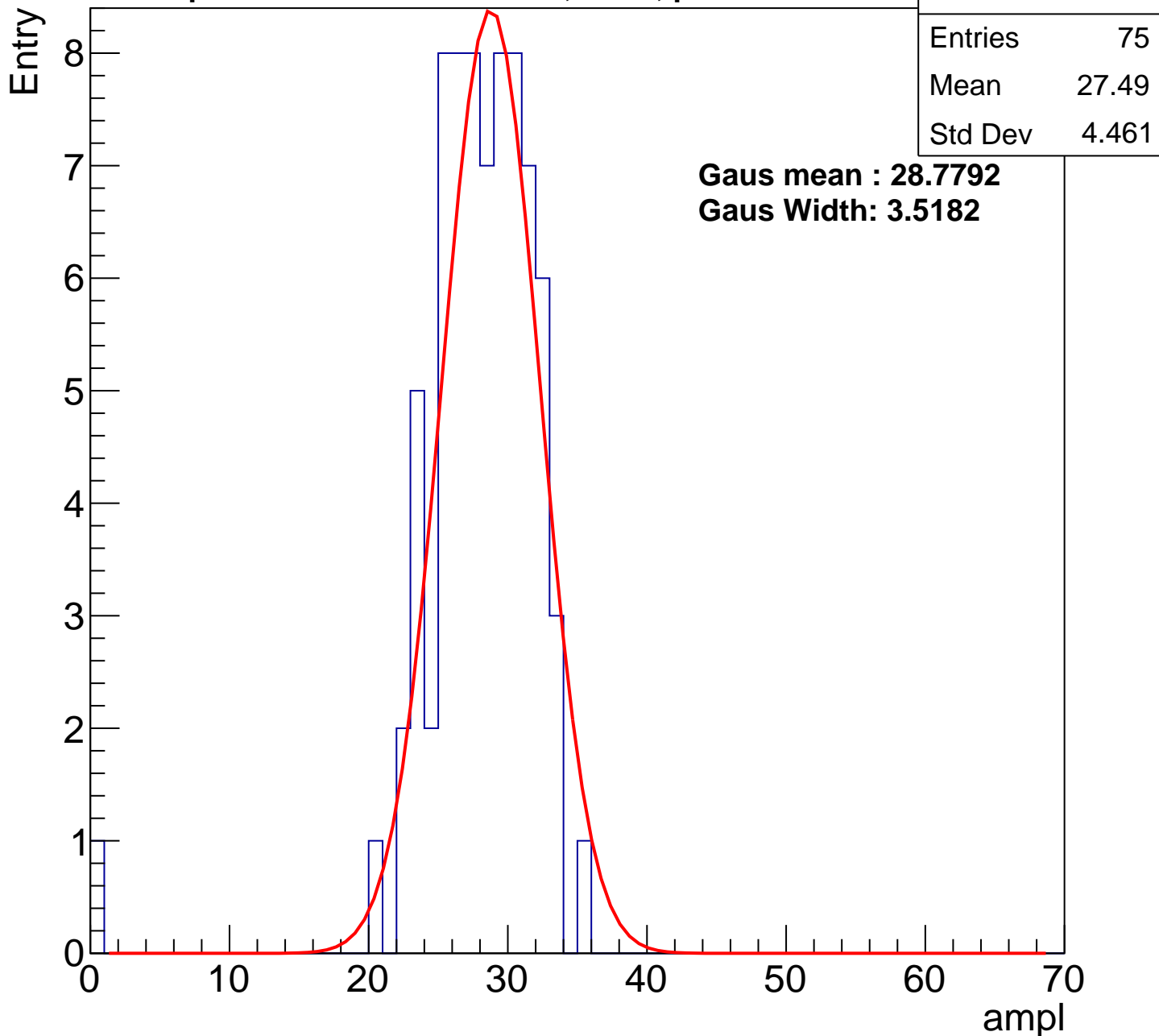
Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L000S, U23-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch7, adc1

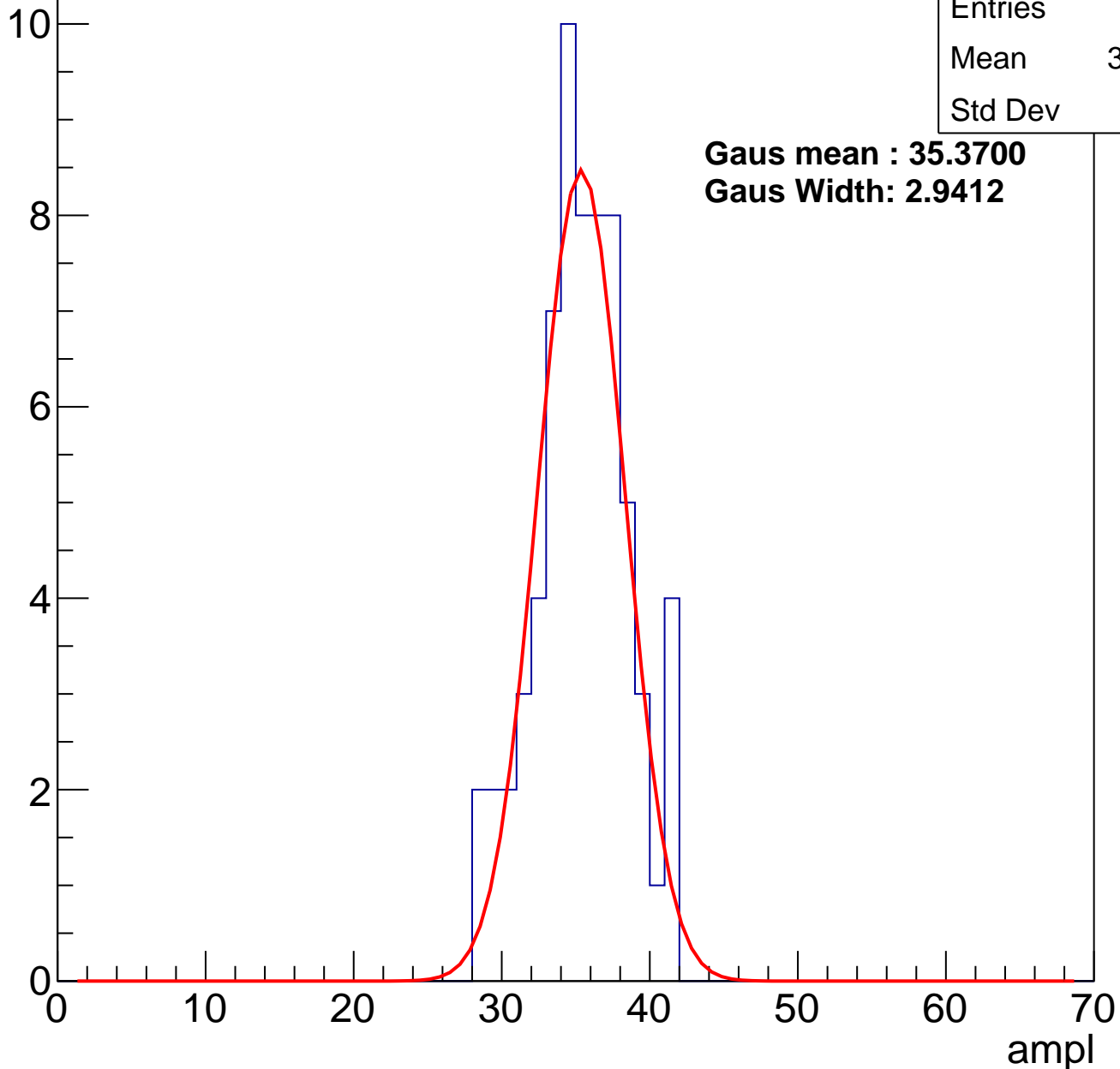
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	34.94
Std Dev	3.1

**Gaus mean : 35.3700**

**Gaus Width: 2.9412**

Entry



# B1L000S, U23-ch7, adc2

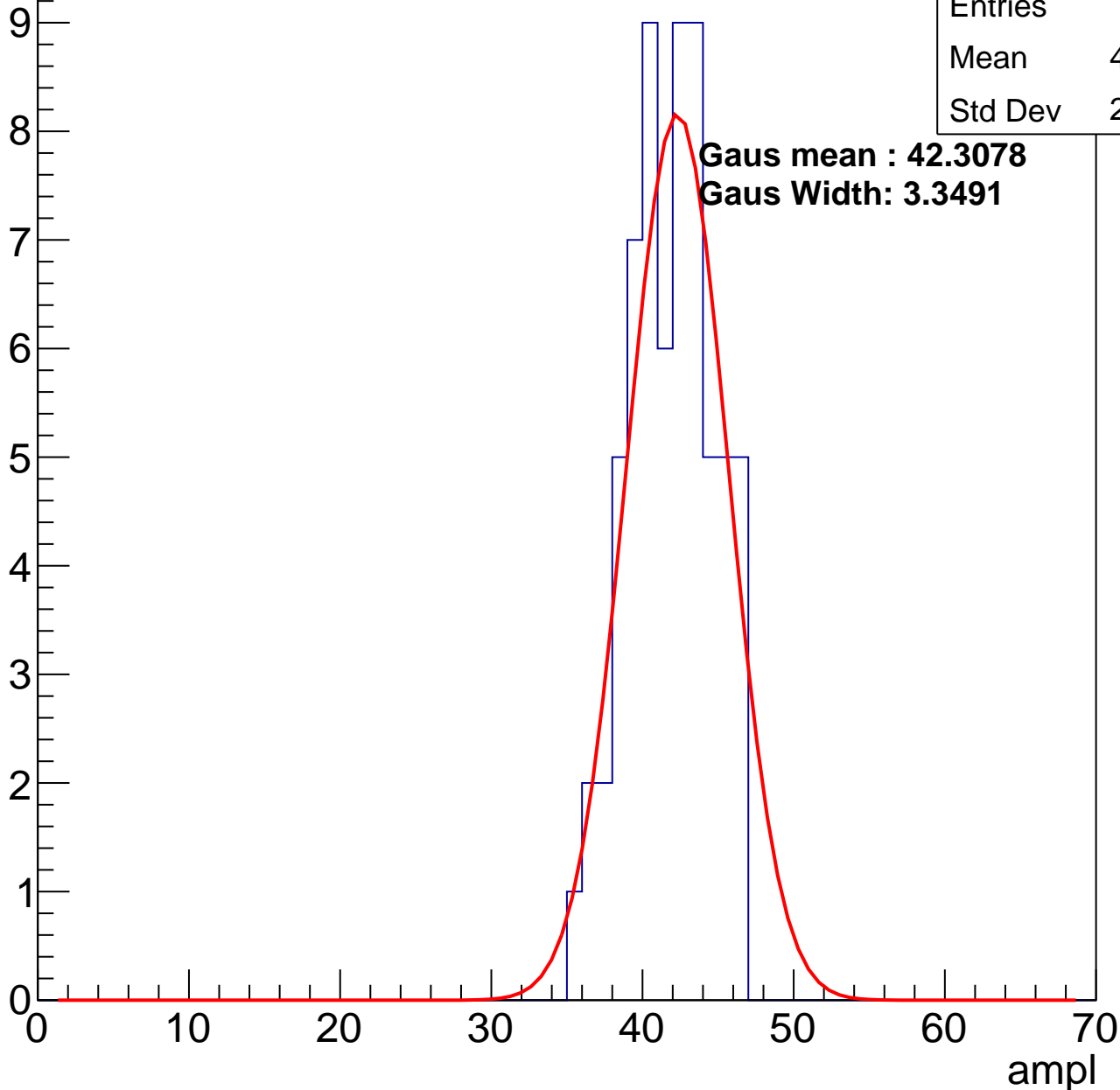
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.38
Std Dev	2.733

**Gaus mean : 42.3078**

**Gaus Width: 3.3491**

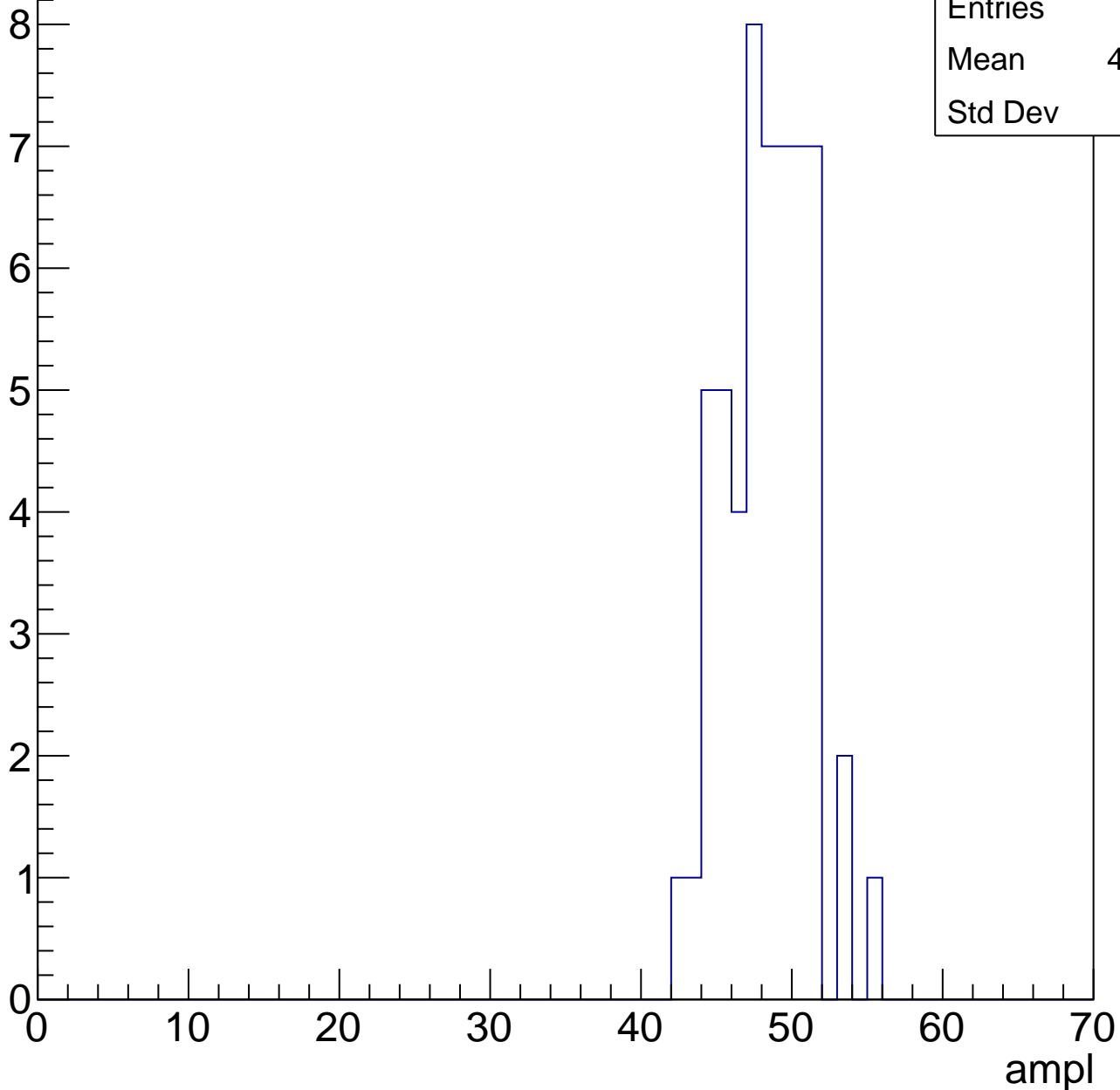


# B1L000S, U23-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	47.95
Std Dev	2.72

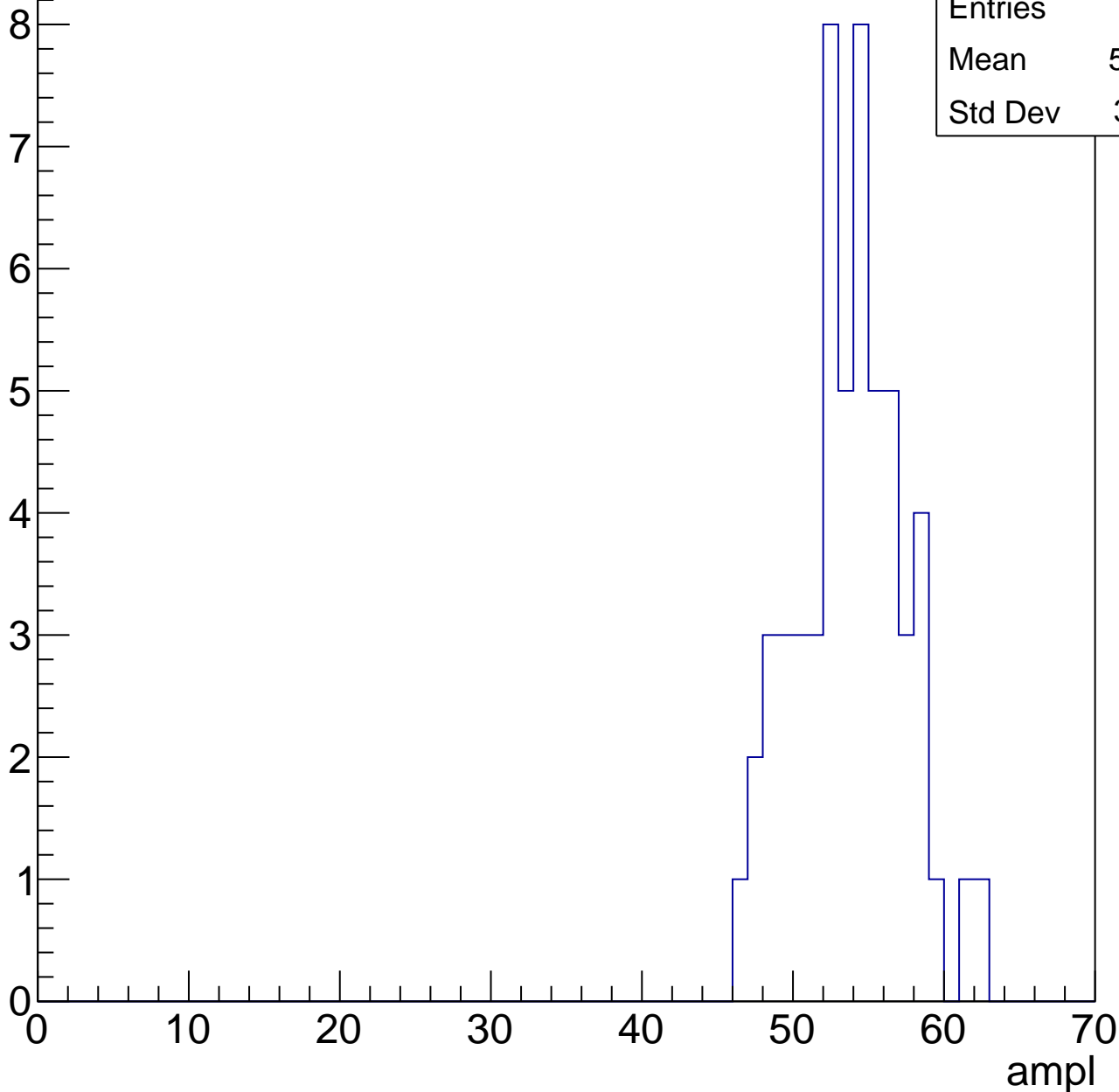


# B1L000S, U23-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

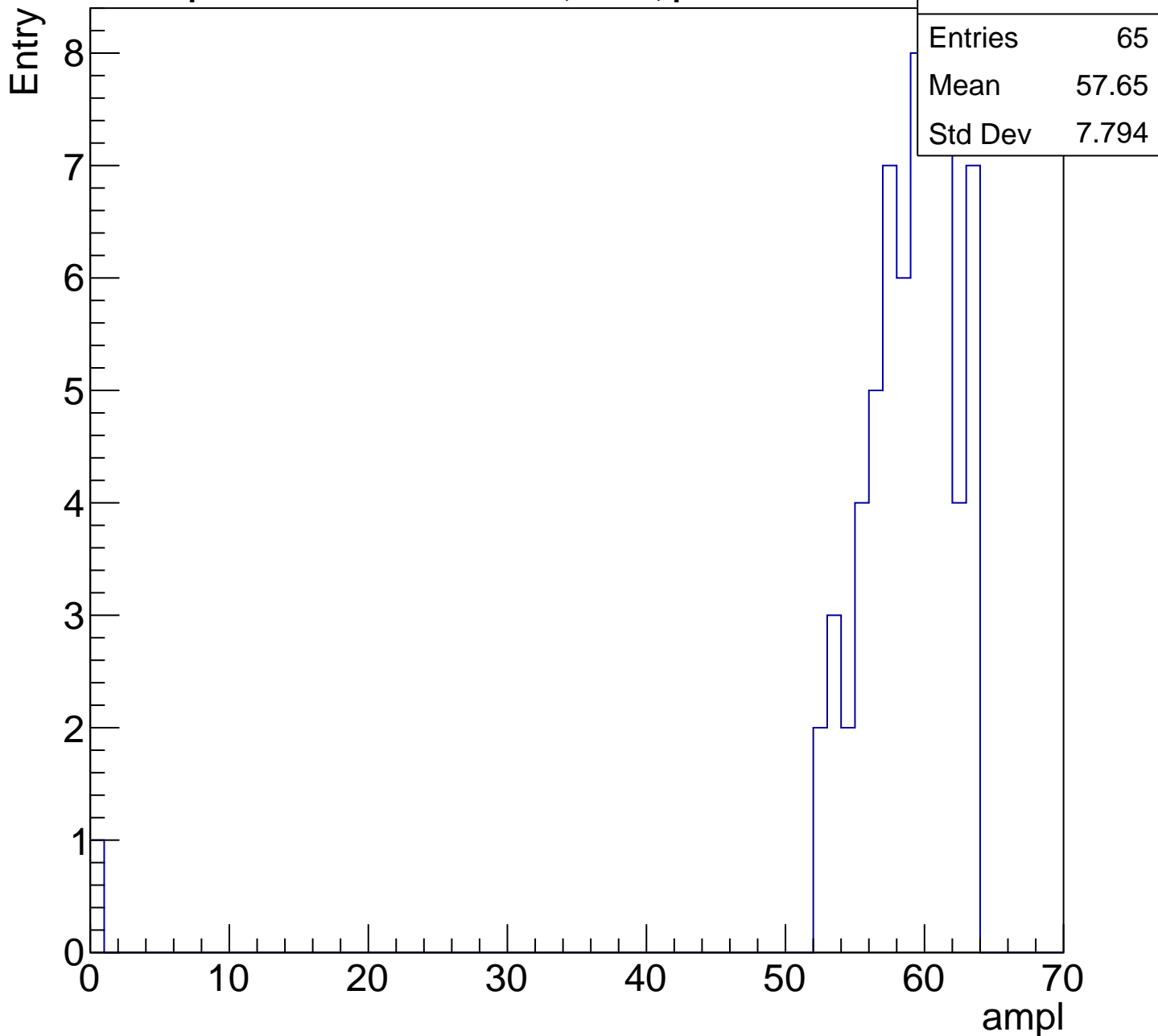
Entry

Entries	56
Mean	53.34
Std Dev	3.501



# B1L000S, U23-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

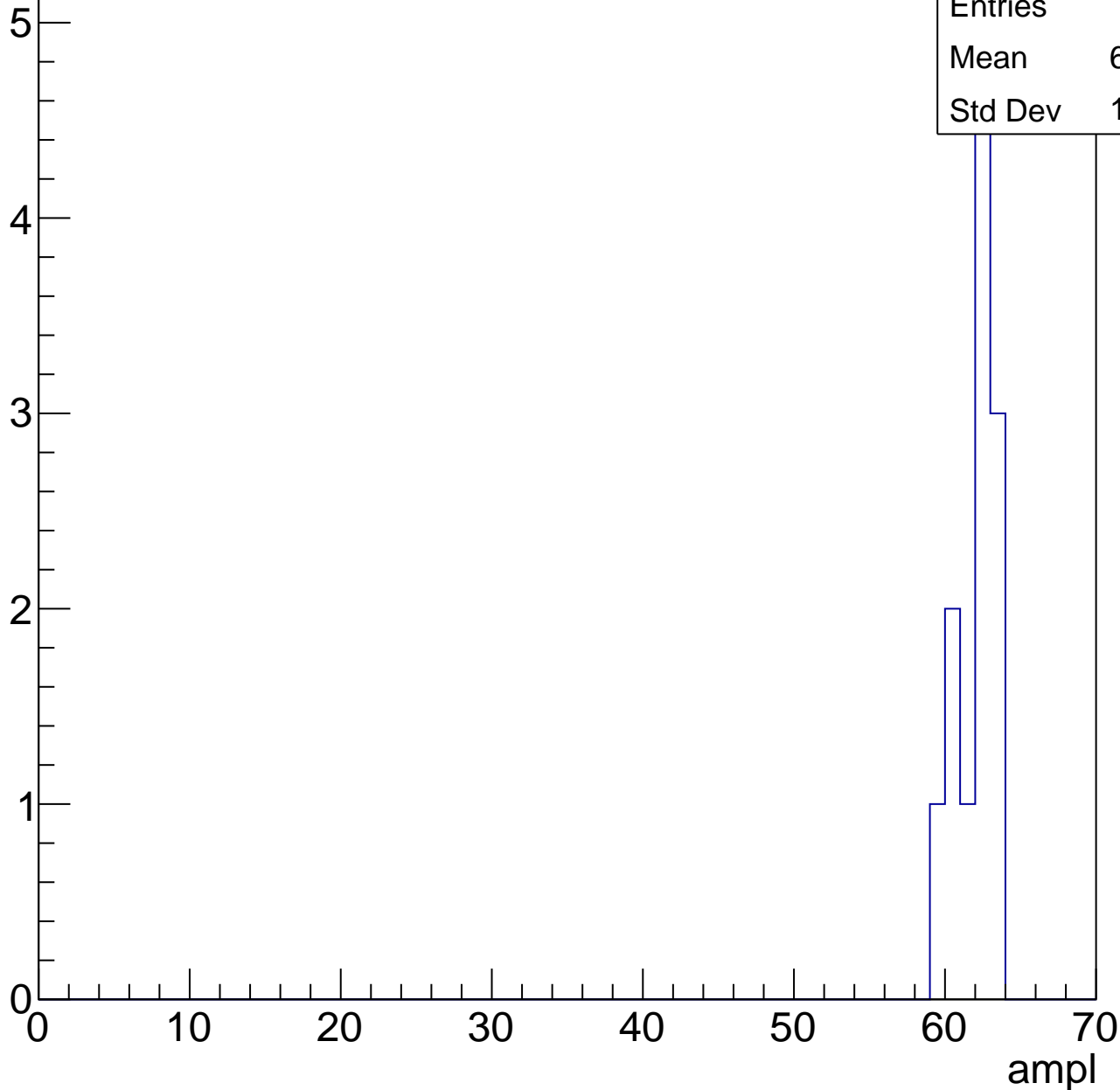


# B1L000S, U23-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.58
Std Dev	1.256





# B1L000S, U23-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U23-ch8, adc0

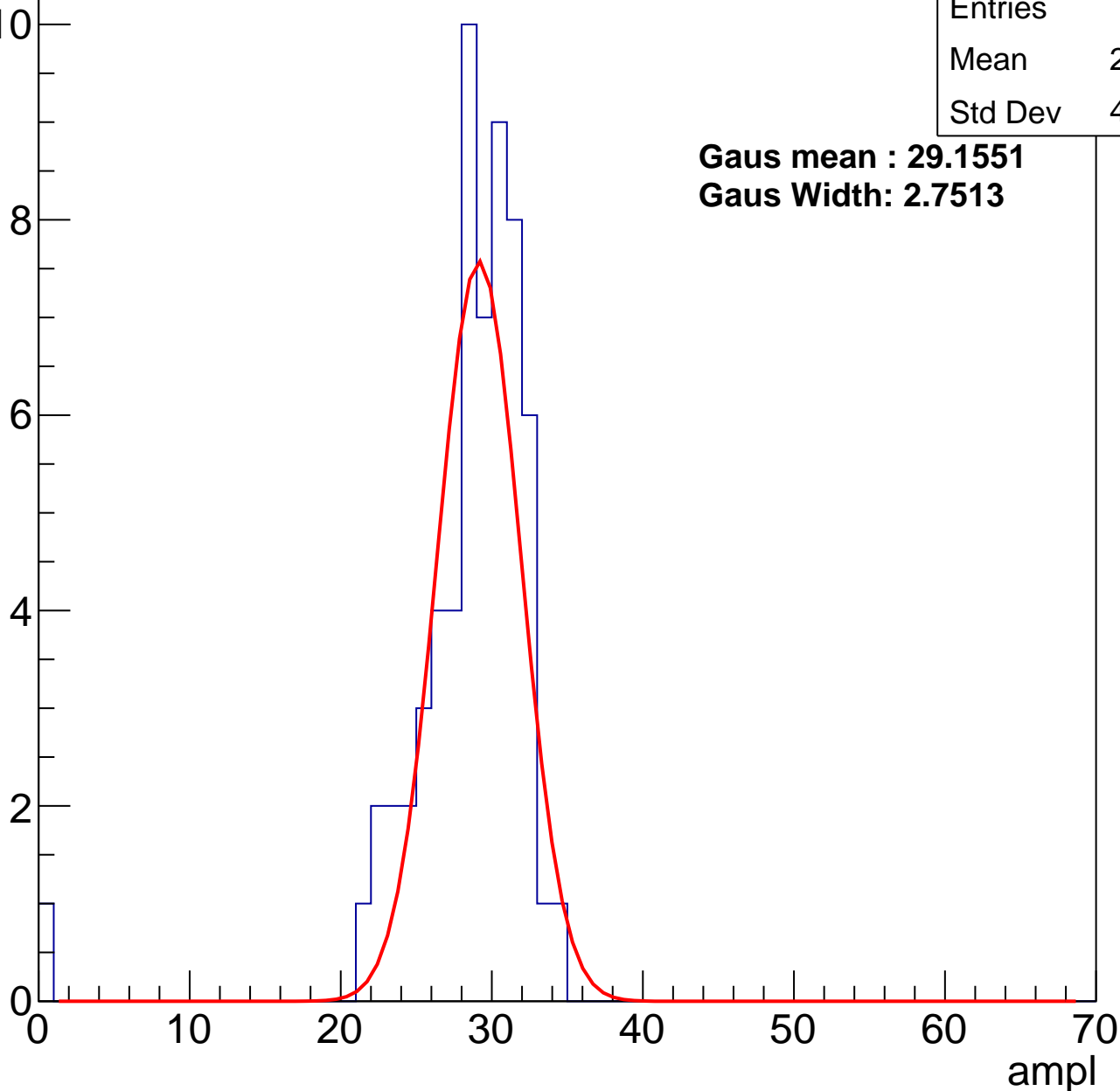
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.97
Std Dev	4.627

**Gaus mean : 29.1551**

**Gaus Width: 2.7513**



# B1L000S, U23-ch8, adc1

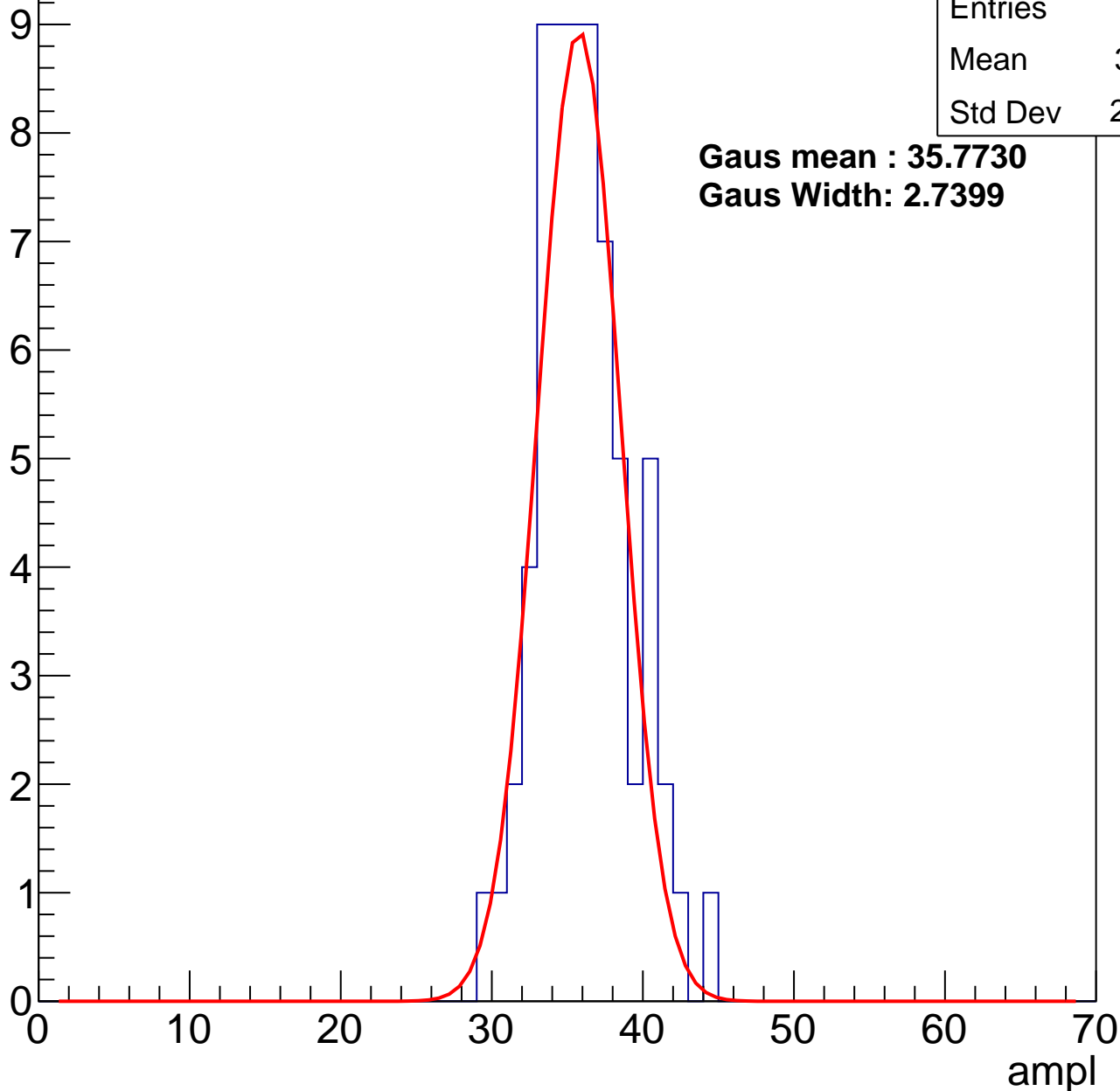
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.61
Std Dev	2.967

**Gaus mean : 35.7730**

**Gaus Width: 2.7399**



# B1L000S, U23-ch8, adc2

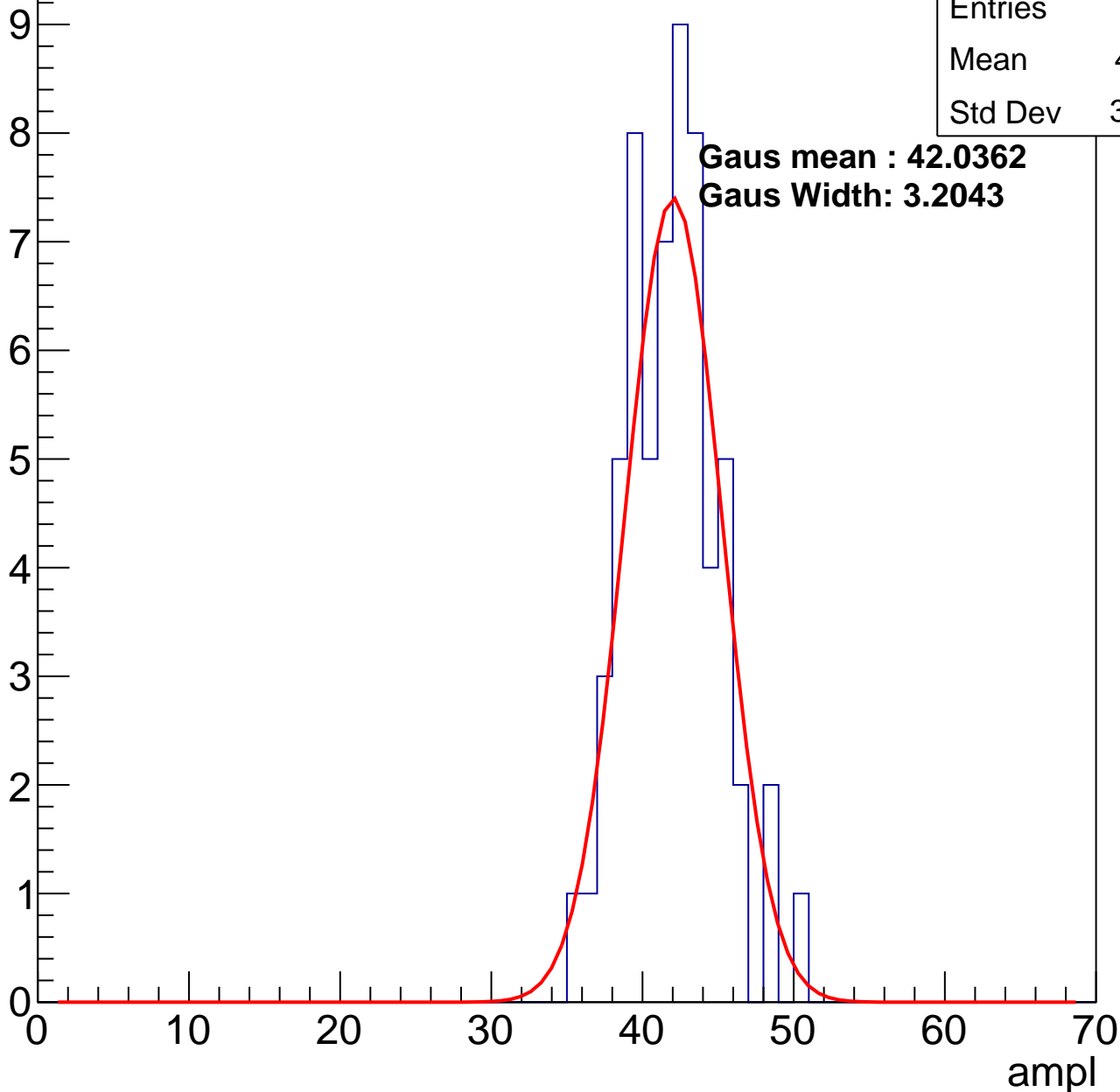
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.51
Std Dev	3.033

**Gaus mean : 42.0362**

**Gaus Width: 3.2043**



# B1L000S, U23-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

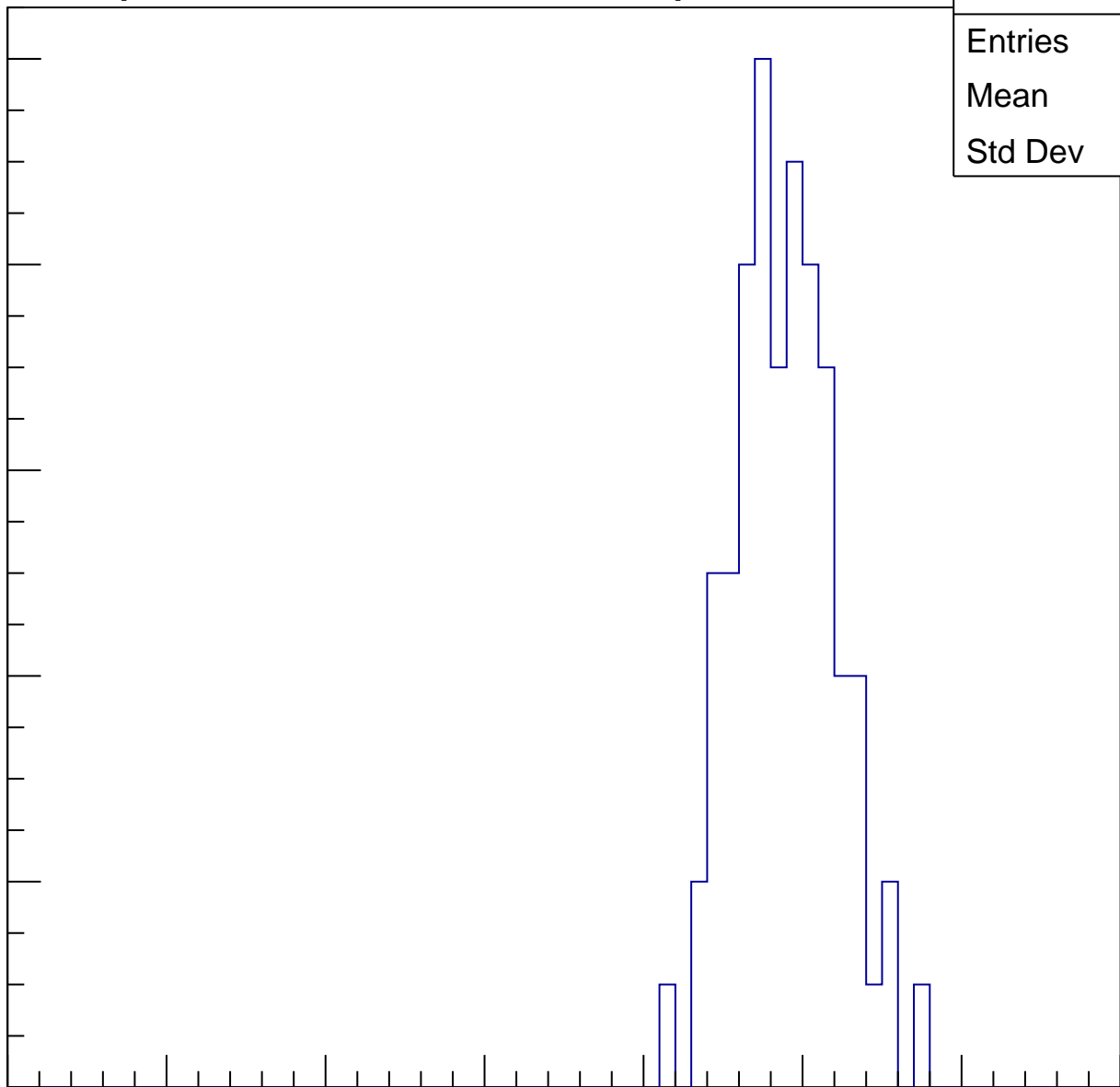
Entries	74
Mean	48.45
Std Dev	3.15

Entry

10  
8  
6  
4  
2  
0

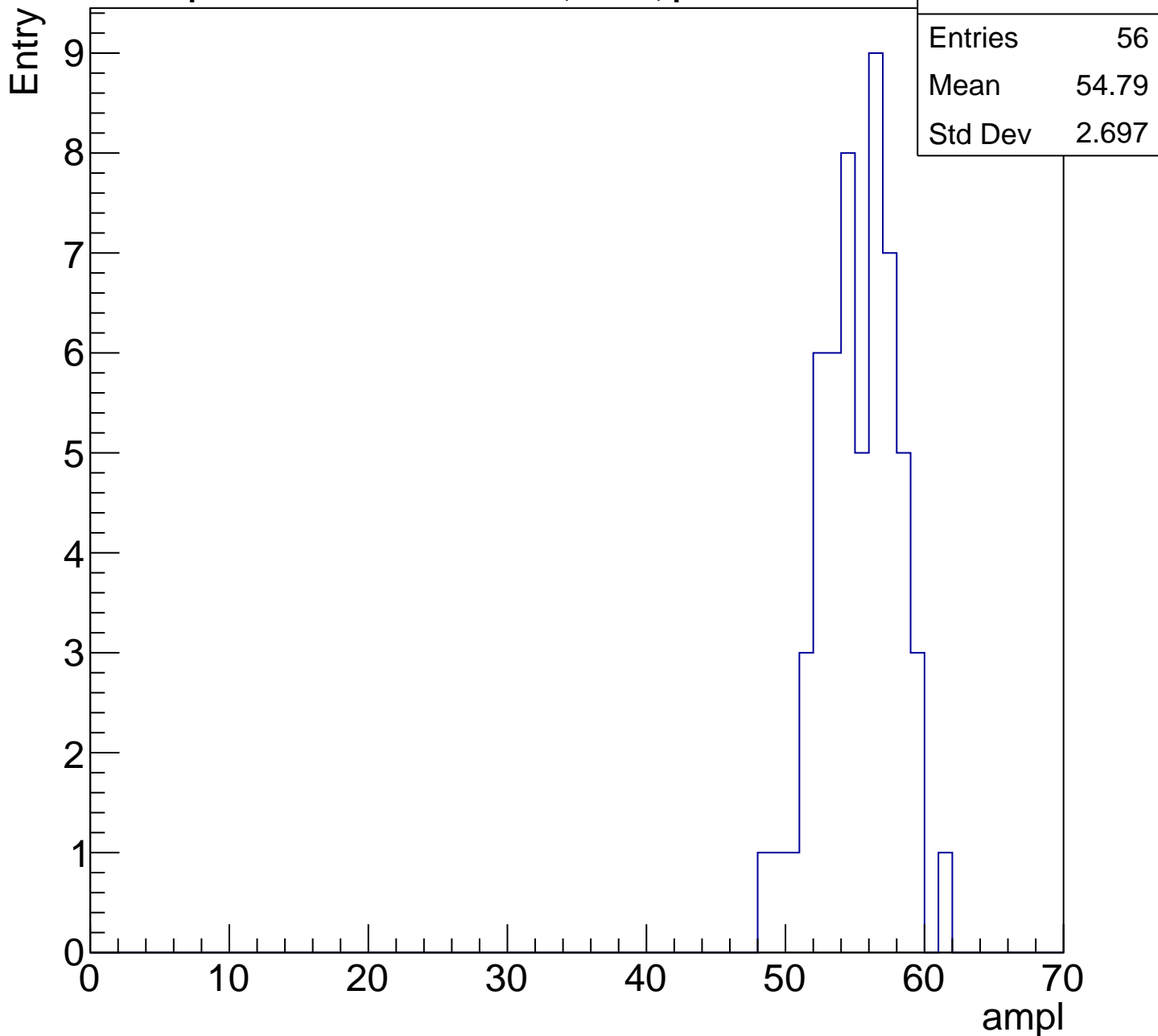
0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

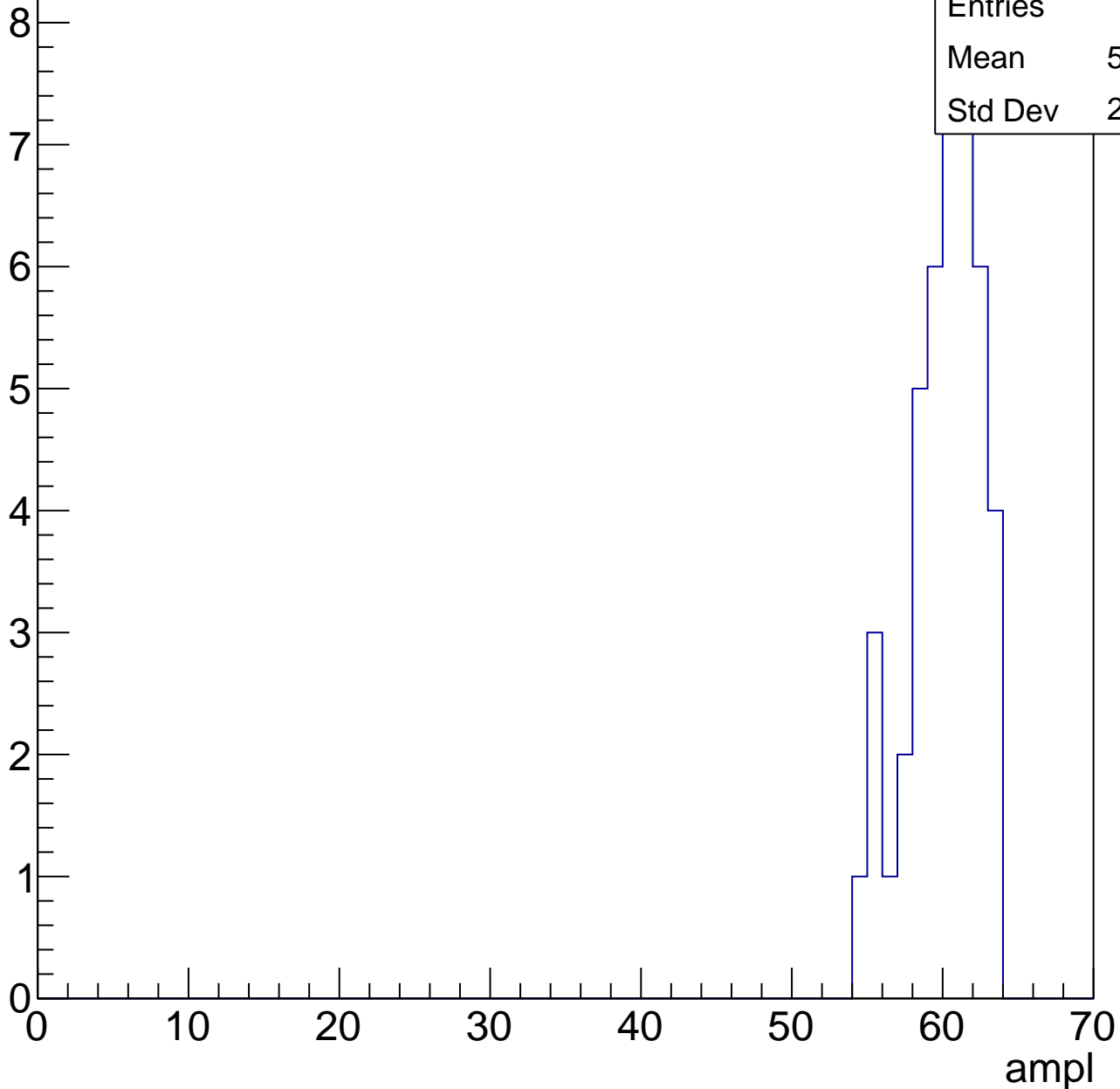


# B1L000S, U23-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	59.66
Std Dev	2.306

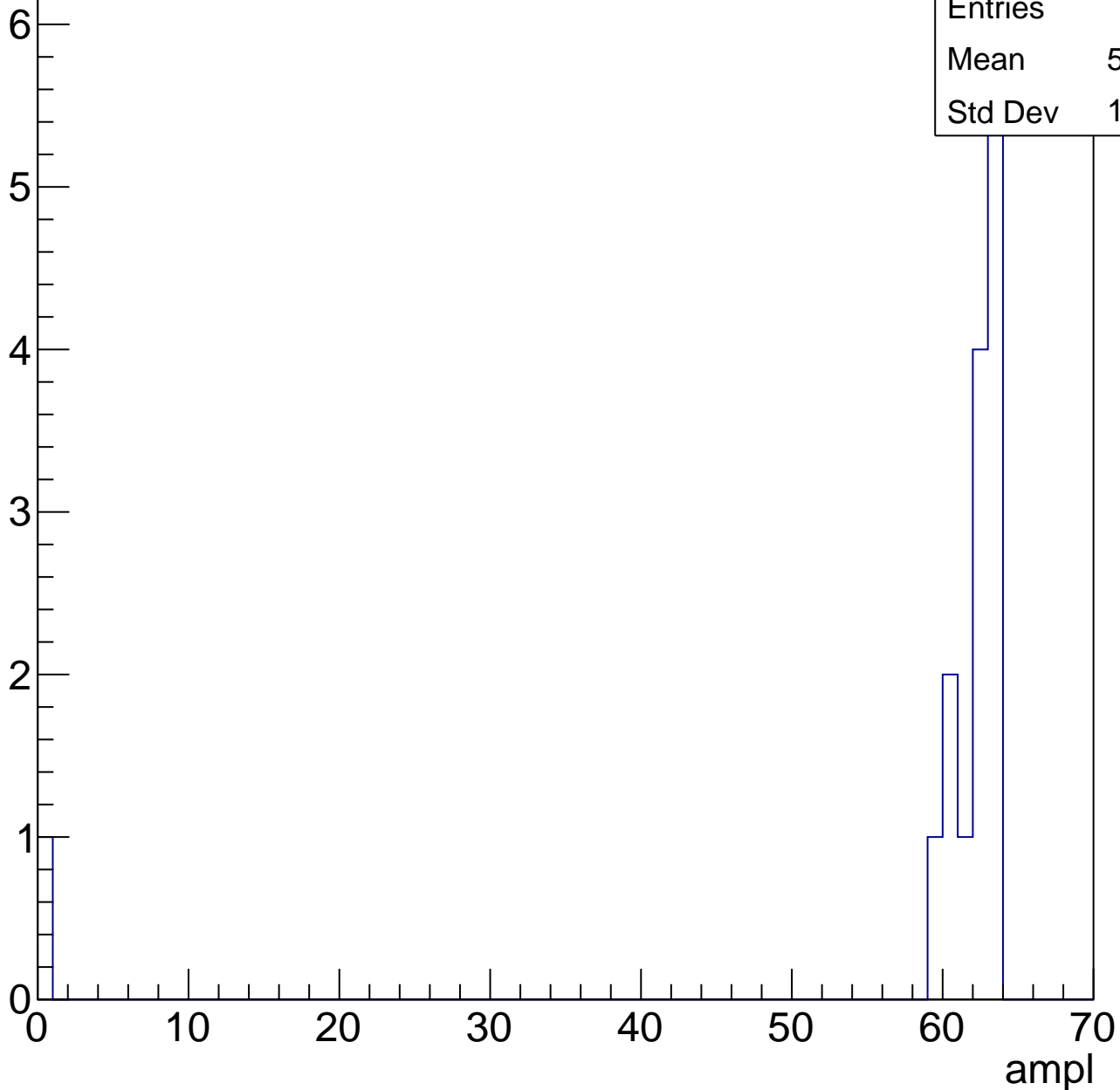


# B1L000S, U23-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.73
Std Dev	15.48





# B1L000S, U23-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	31.88
Std Dev	3.398

**Gaus mean : 31.7969**

**Gaus Width: 3.9302**

10

8

6

4

2

0

0

10

20

30

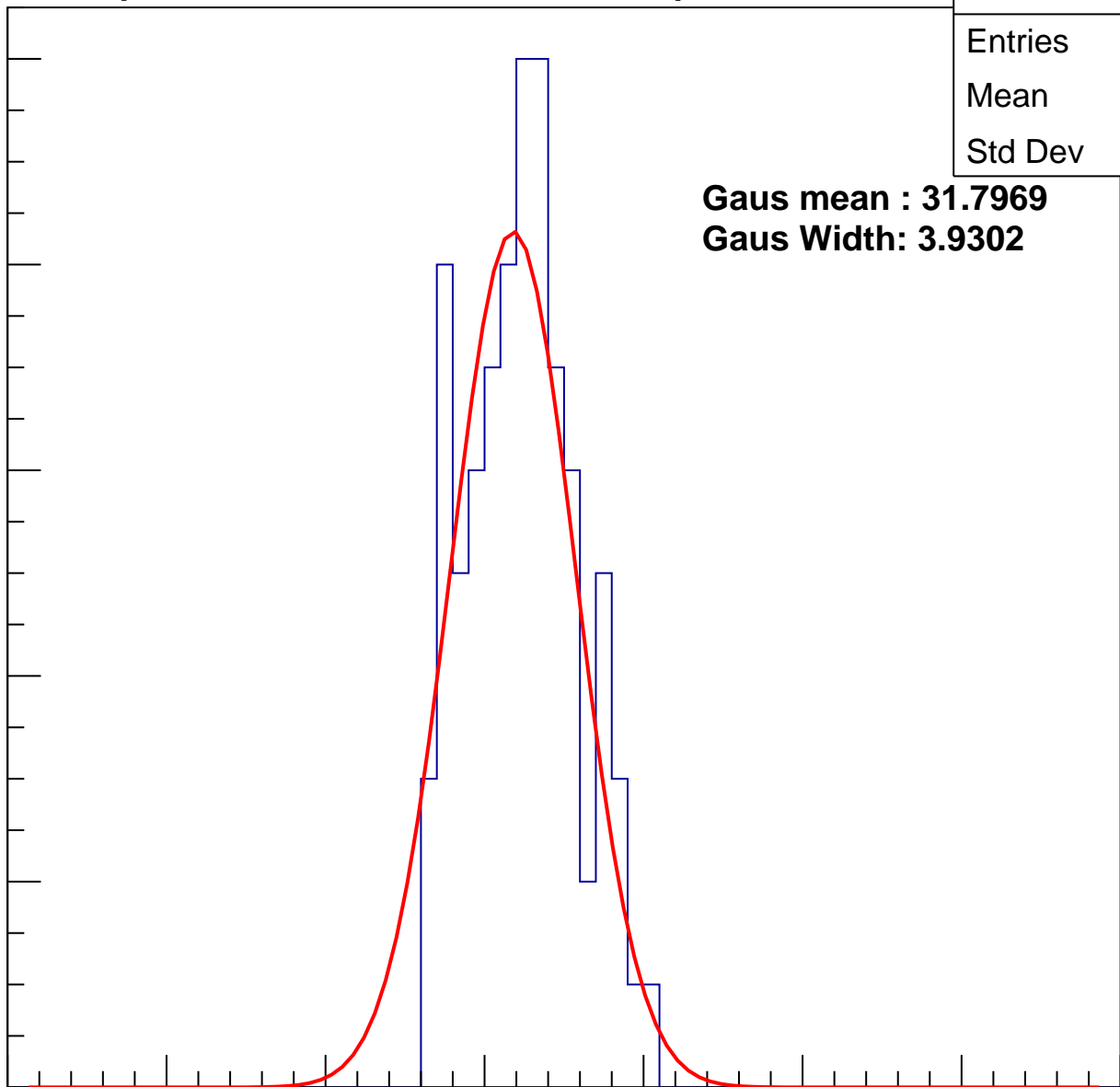
40

50

60

70

ampl



# B1L000S, U23-ch9, adc1

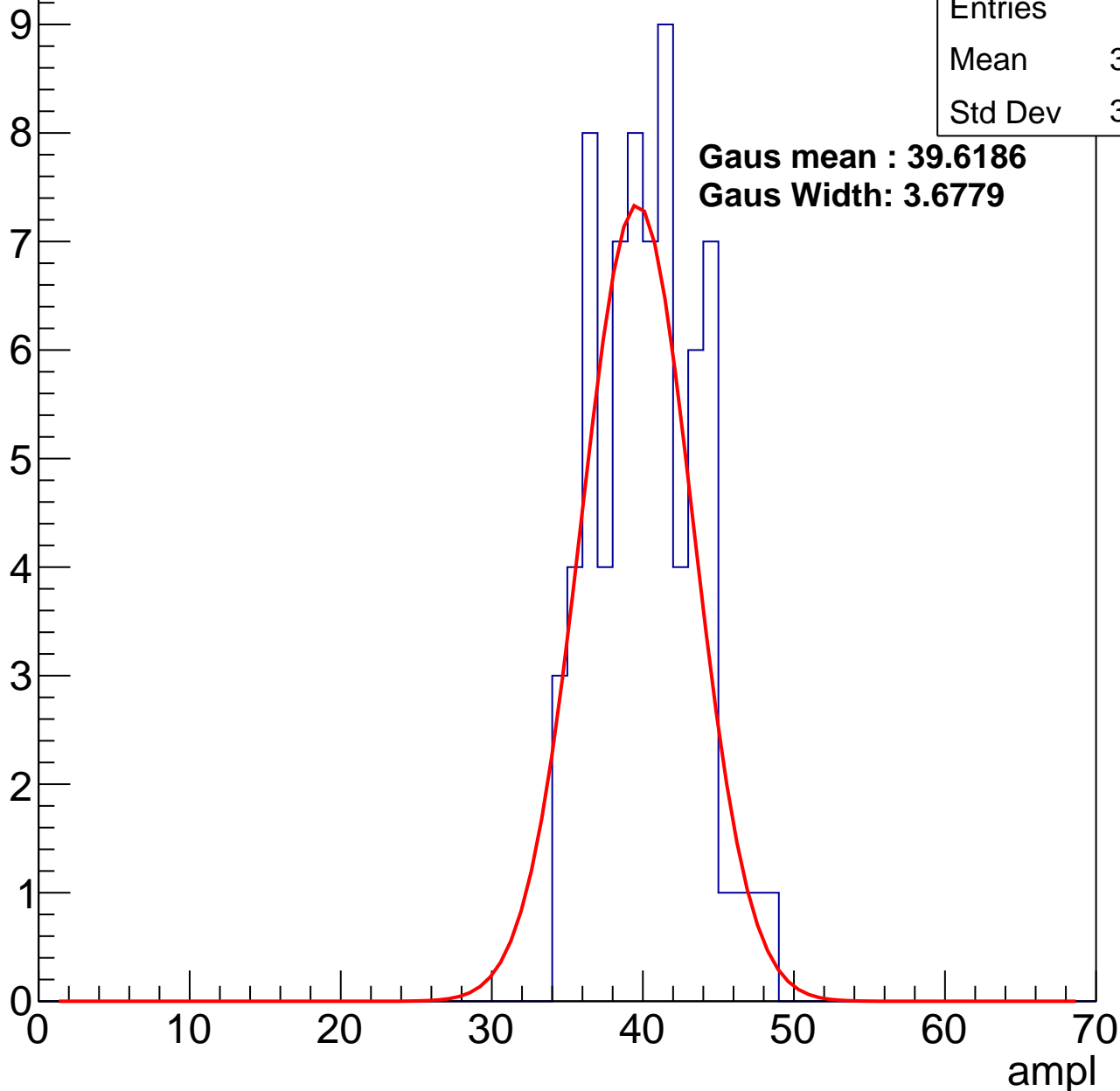
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	39.79
Std Dev	3.293

**Gaus mean : 39.6186**

**Gaus Width: 3.6779**



# B1L000S, U23-ch9, adc2

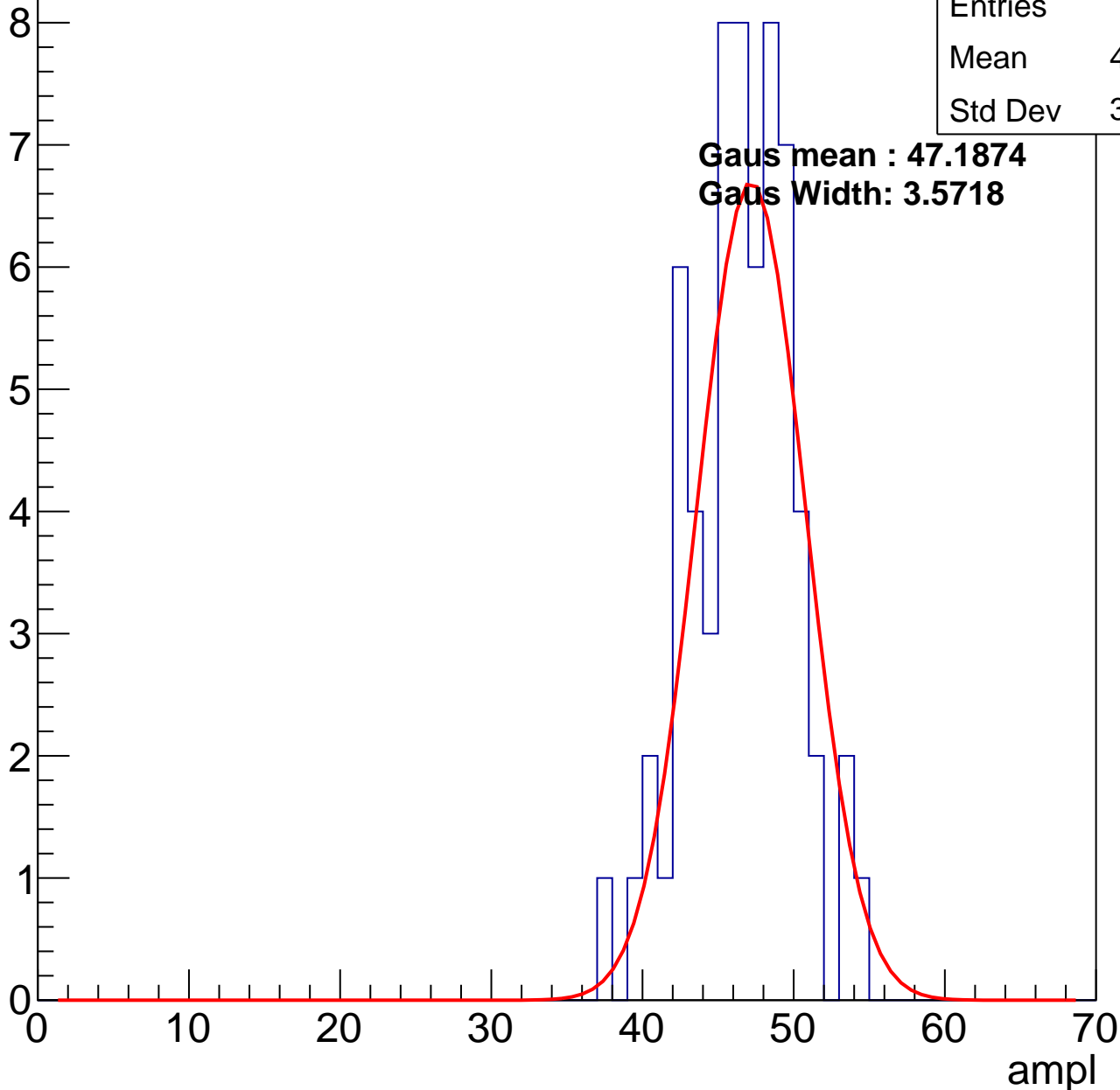
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.12
Std Dev	3.435

Gaus mean : 47.1874

Gaus Width: 3.5718

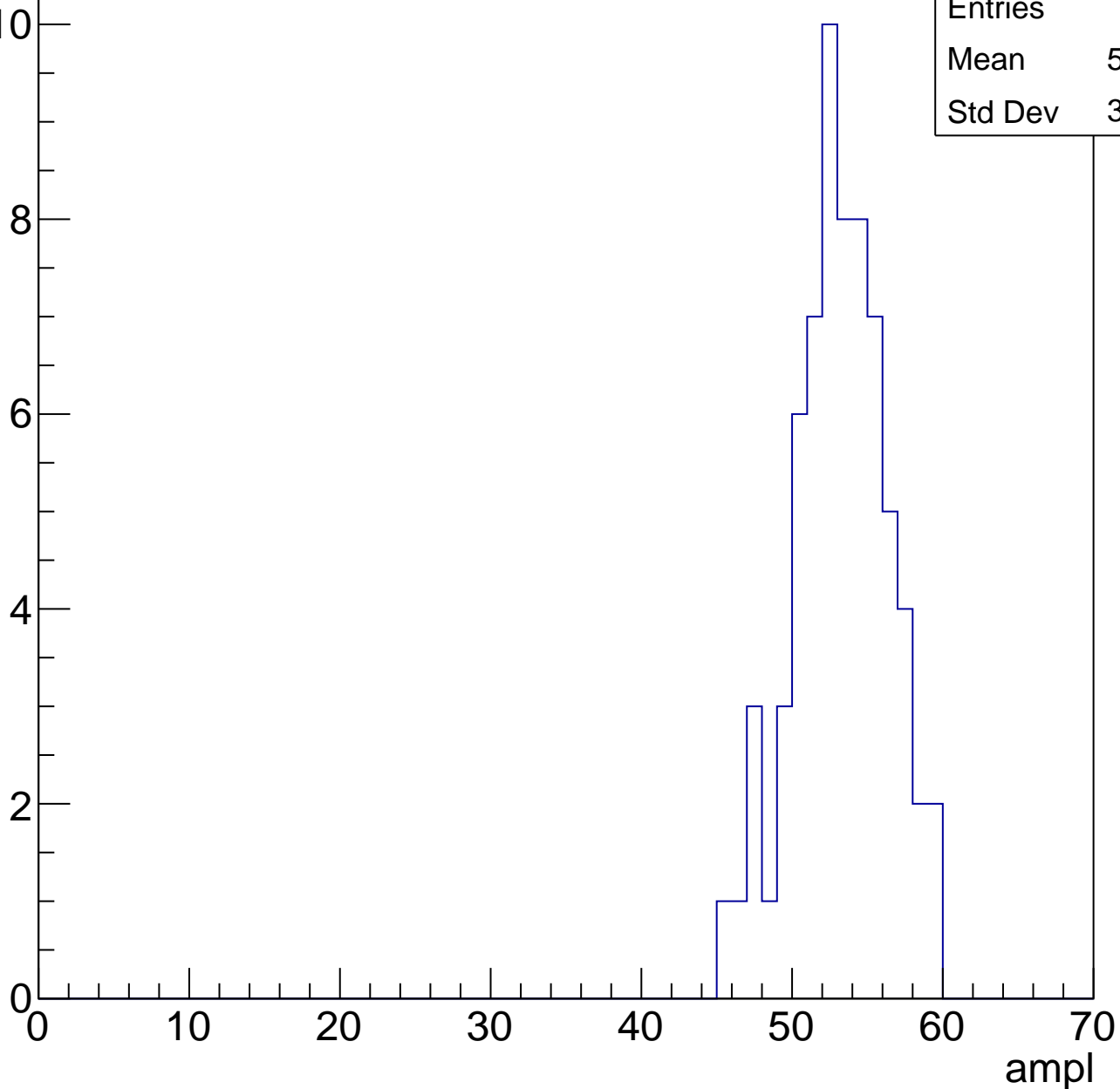


# B1L000S, U23-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	52.75
Std Dev	3.089

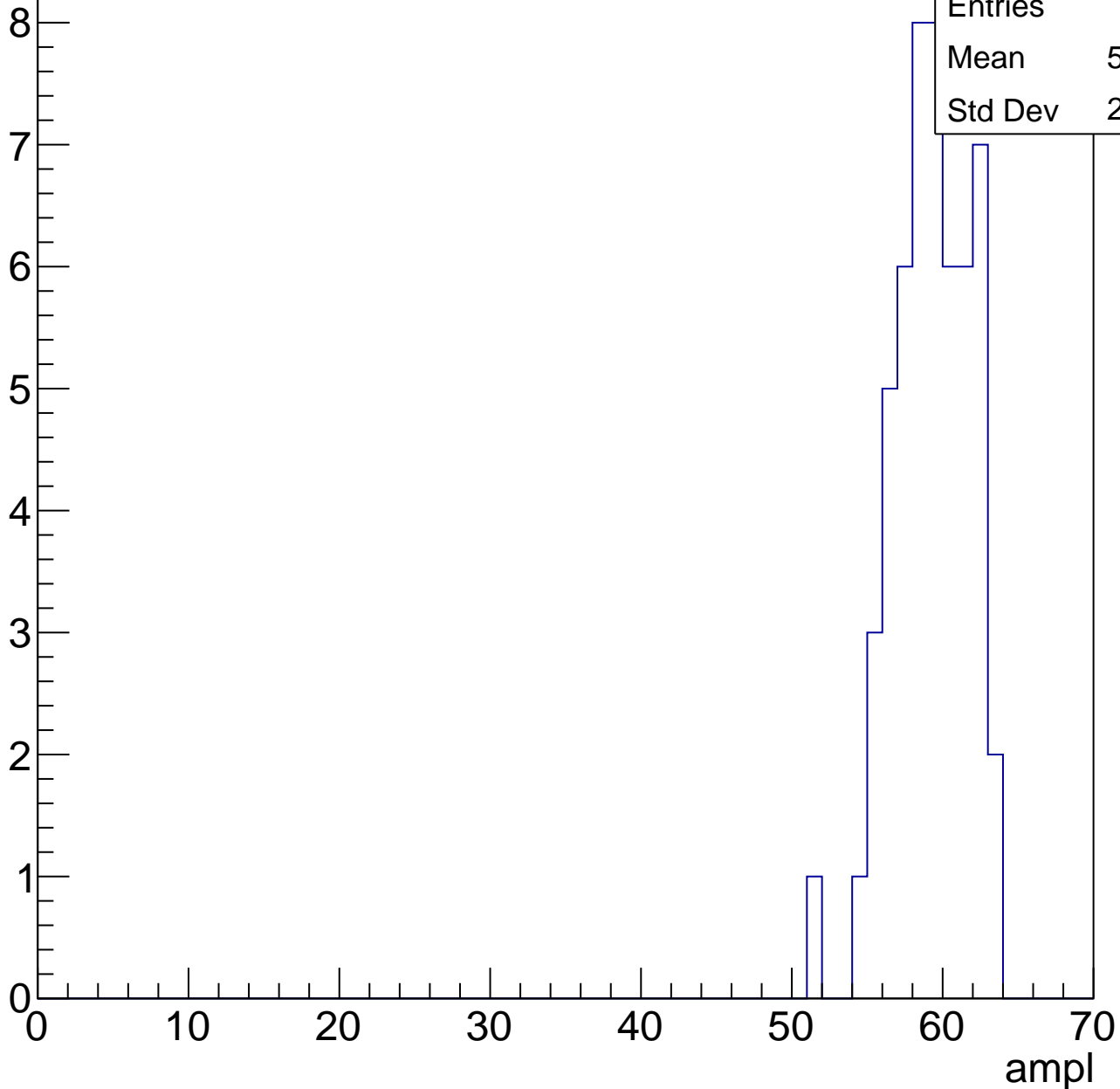


# B1L000S, U23-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.75
Std Dev	2.517



# B1L000S, U23-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

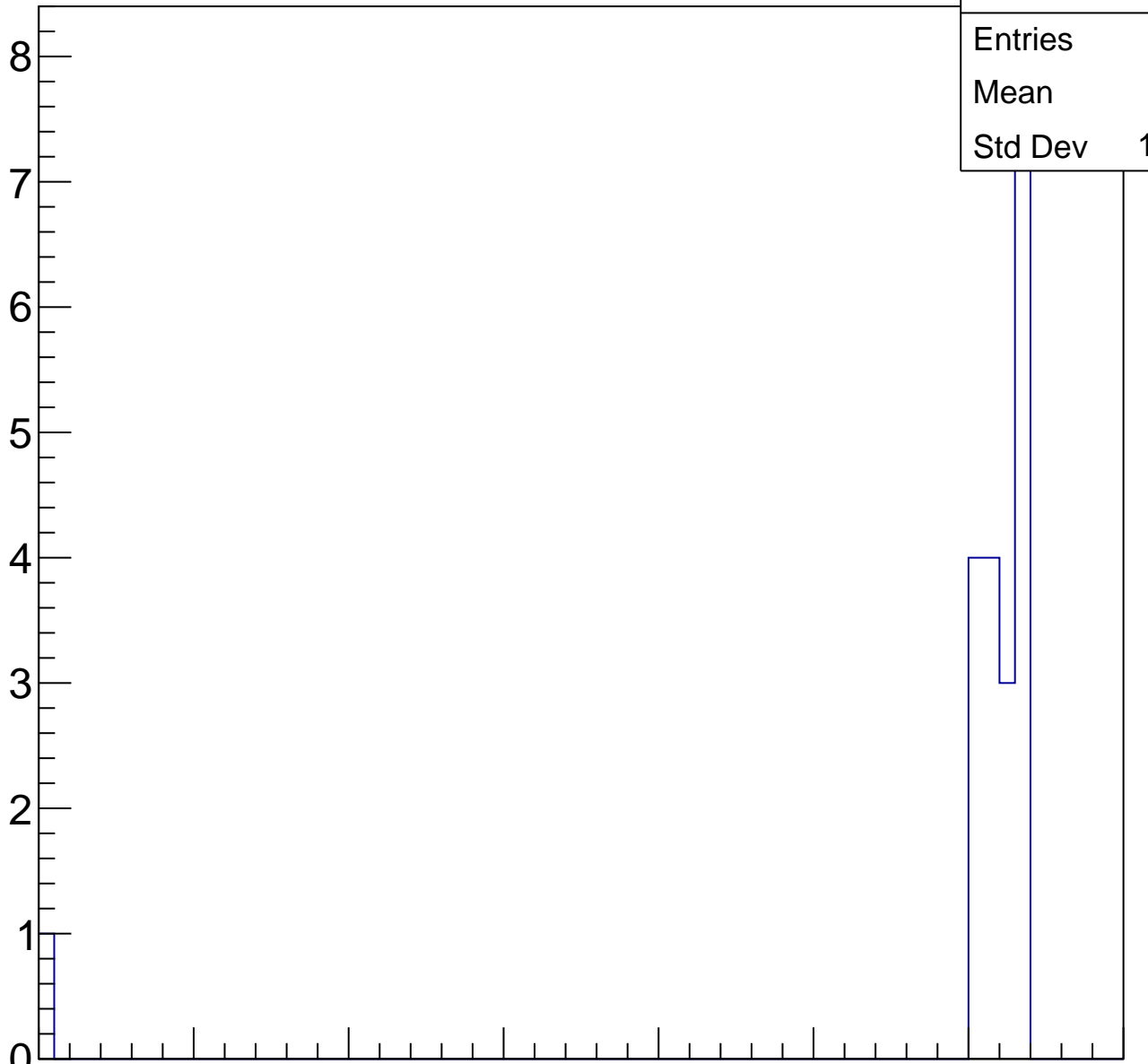
Entry

Entries	20
Mean	58.7
Std Dev	13.52

8  
7  
6  
5  
4  
3  
2  
1  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0



# B1L000S, U23-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	84
Mean	29.75
Std Dev	3.233

**Gaus mean : 30.5956**

**Gaus Width: 3.2535**

10

8

6

4

2

0

0

10

20

30

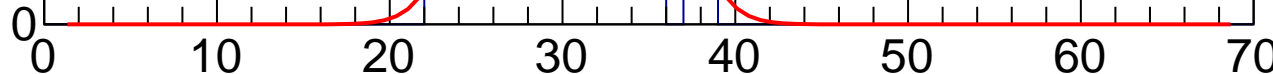
40

50

60

70

ampl



# B1L000S, U23-ch10, adc1

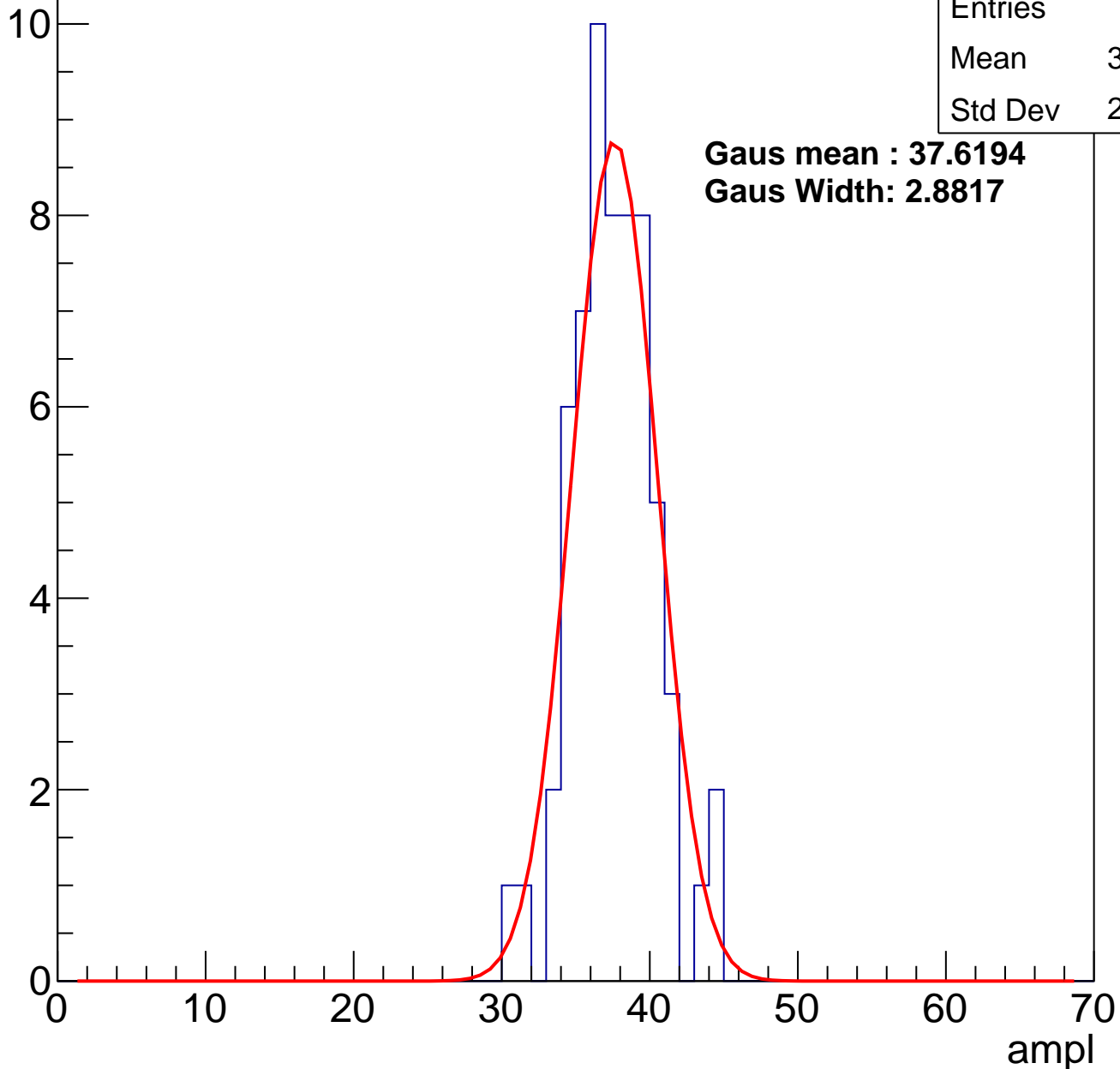
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	37.13
Std Dev	2.768

**Gaus mean : 37.6194**

**Gaus Width: 2.8817**

Entry



# B1L000S, U23-ch10, adc2

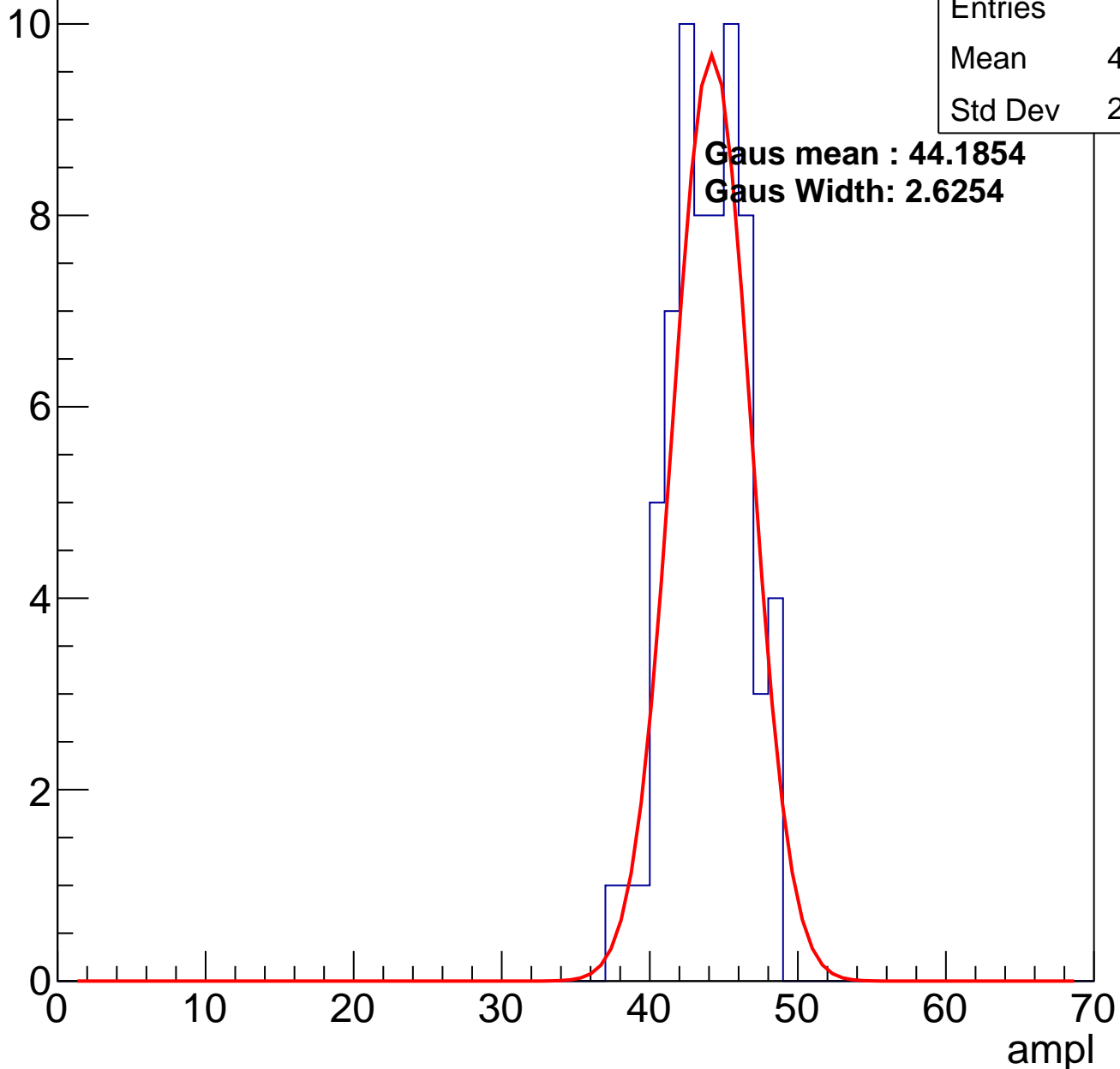
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	43.45
Std Dev	2.506

**Gaus mean : 44.1854**

**Gaus Width: 2.6254**

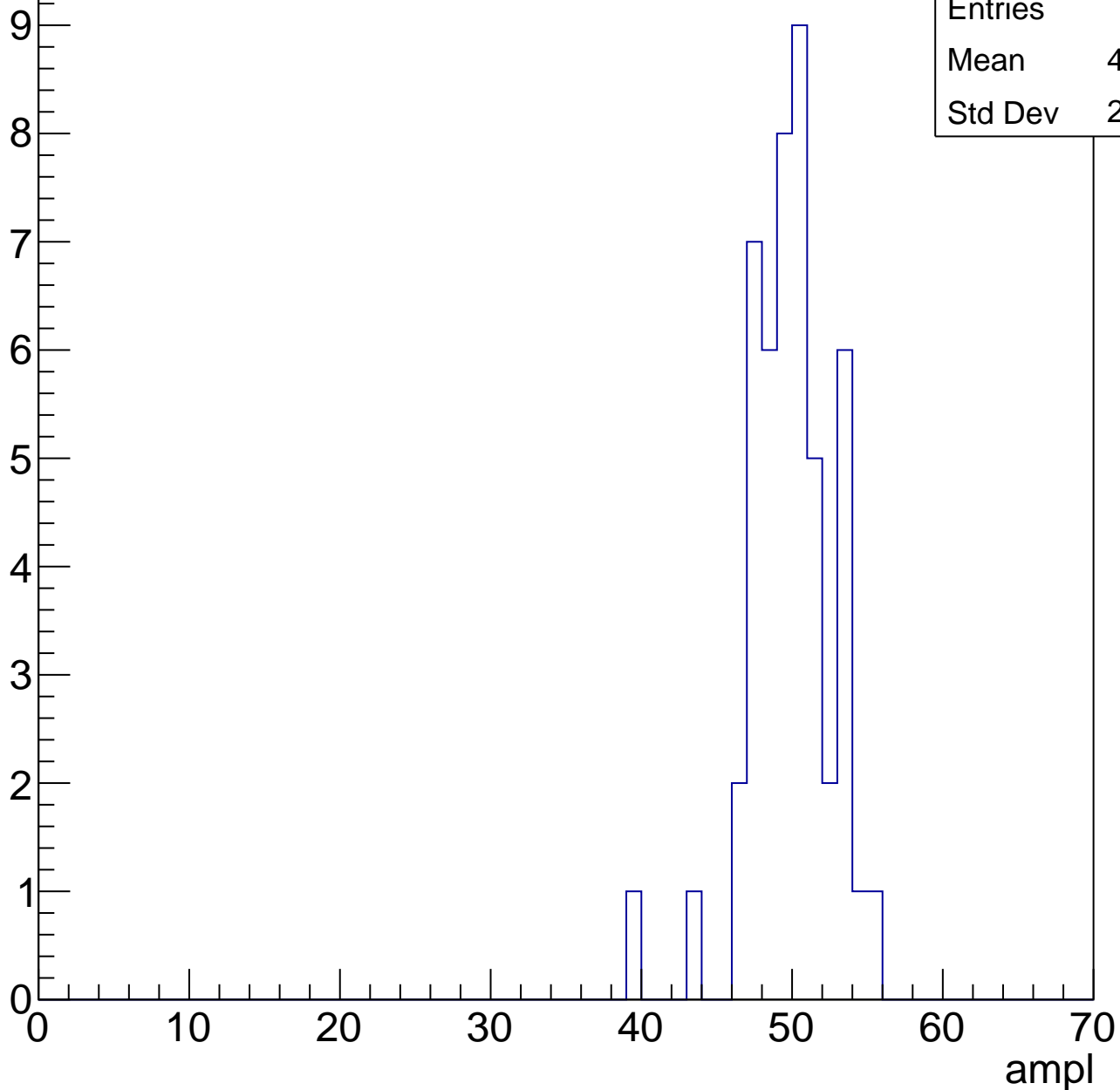
Entry



# B1L000S, U23-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

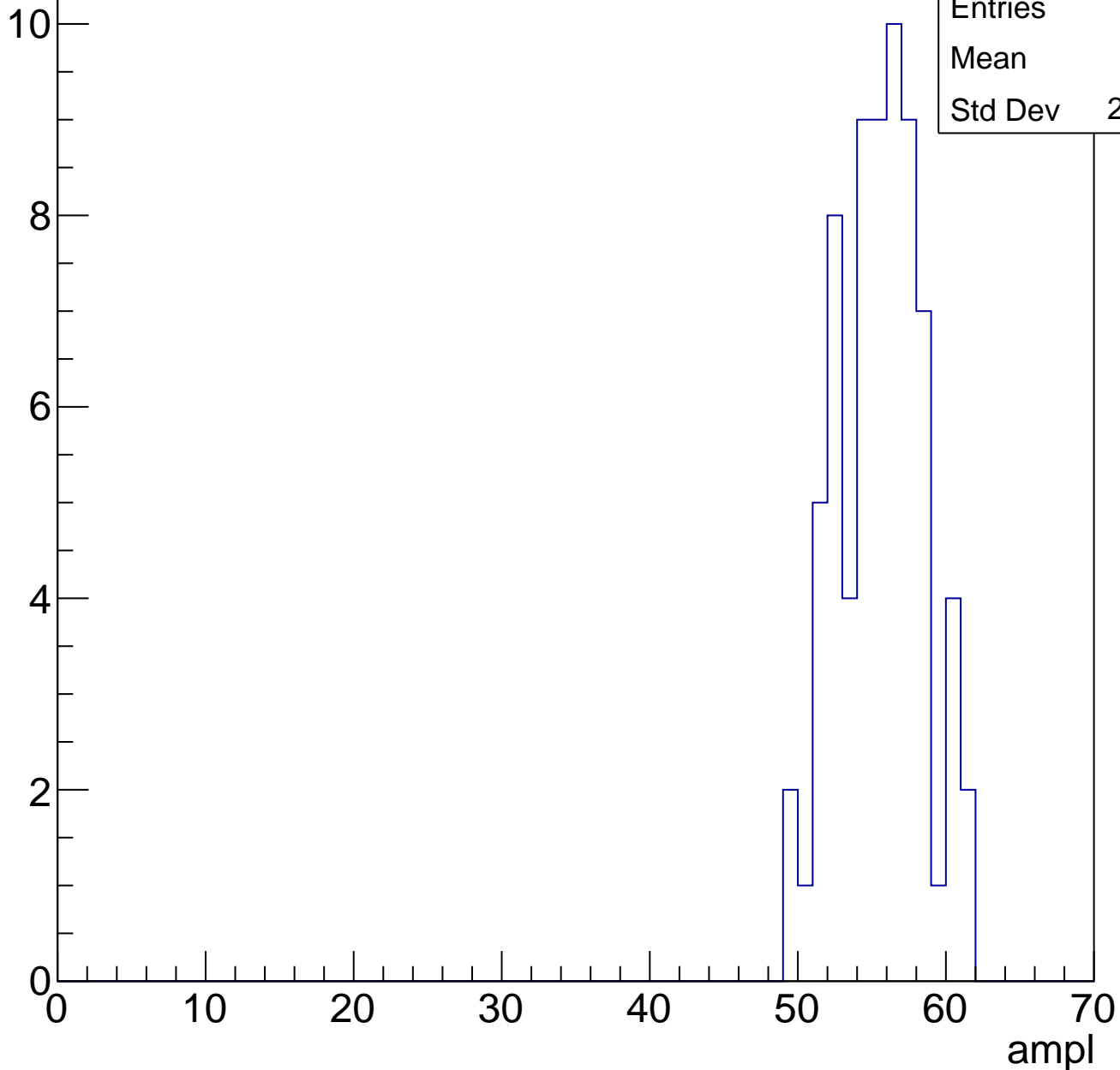


# B1L000S, U23-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	55.1
Std Dev	2.834

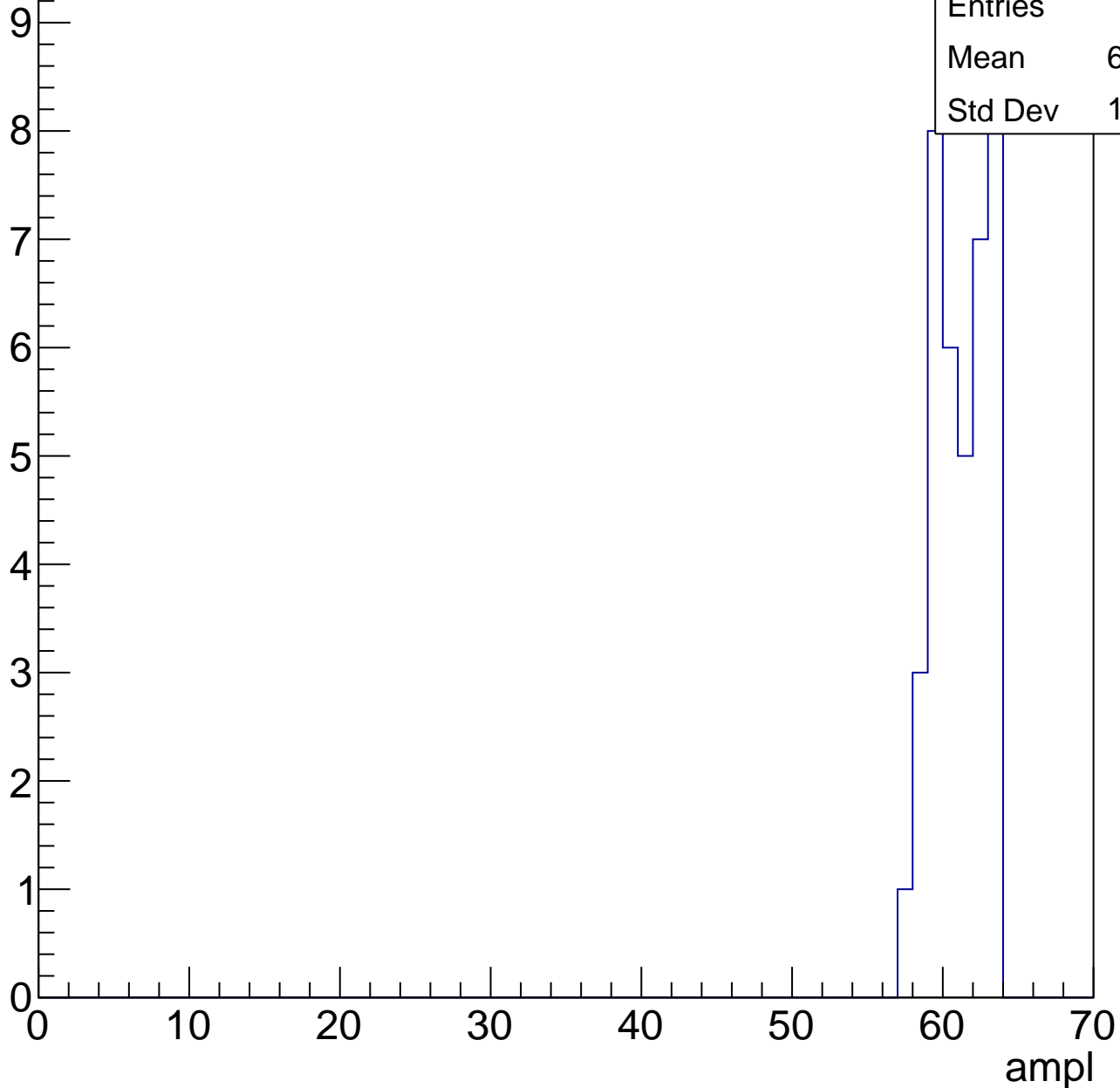
Entry



# B1L000S, U23-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

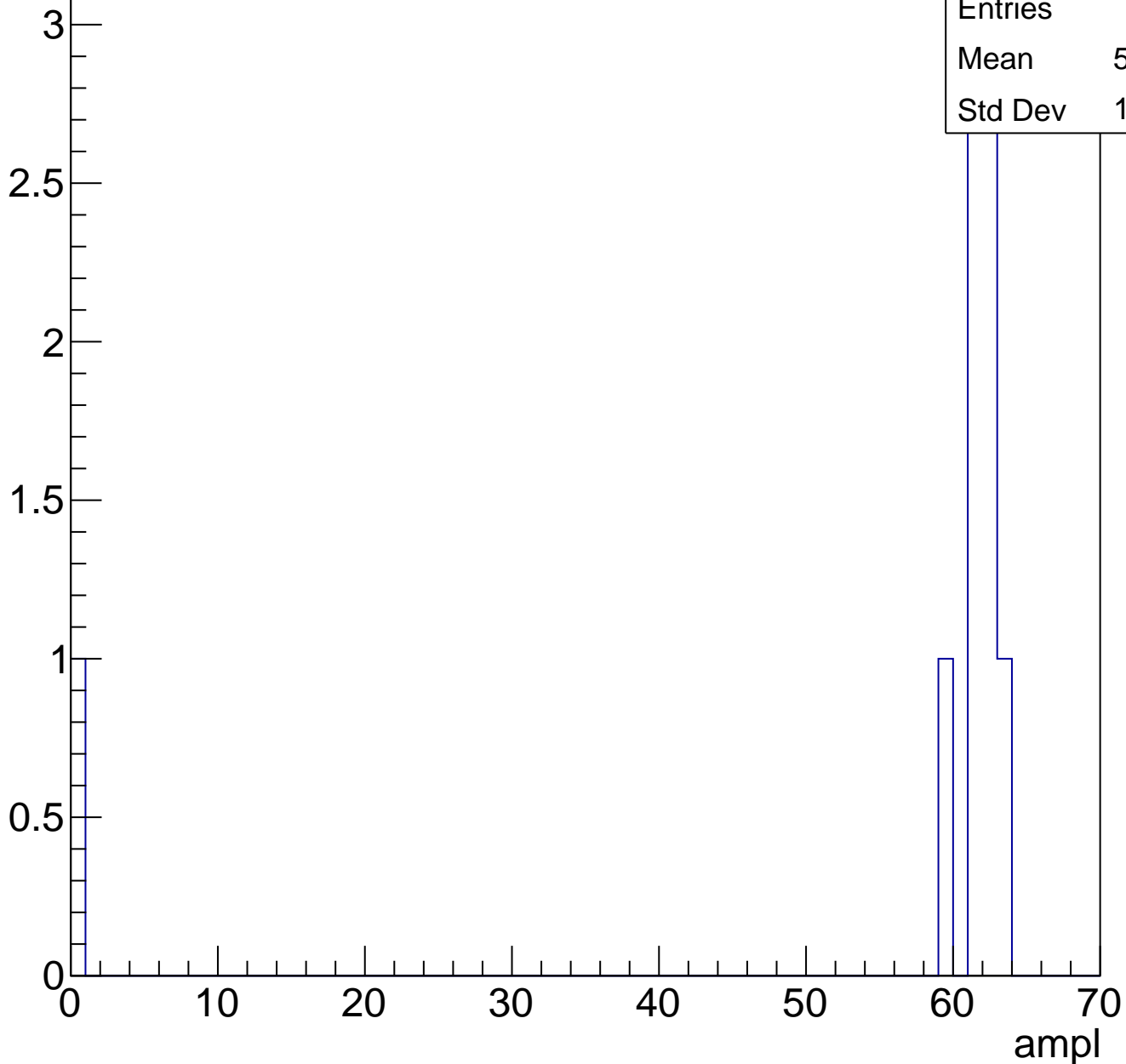
Entry



# B1L000S, U23-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch11, adc0

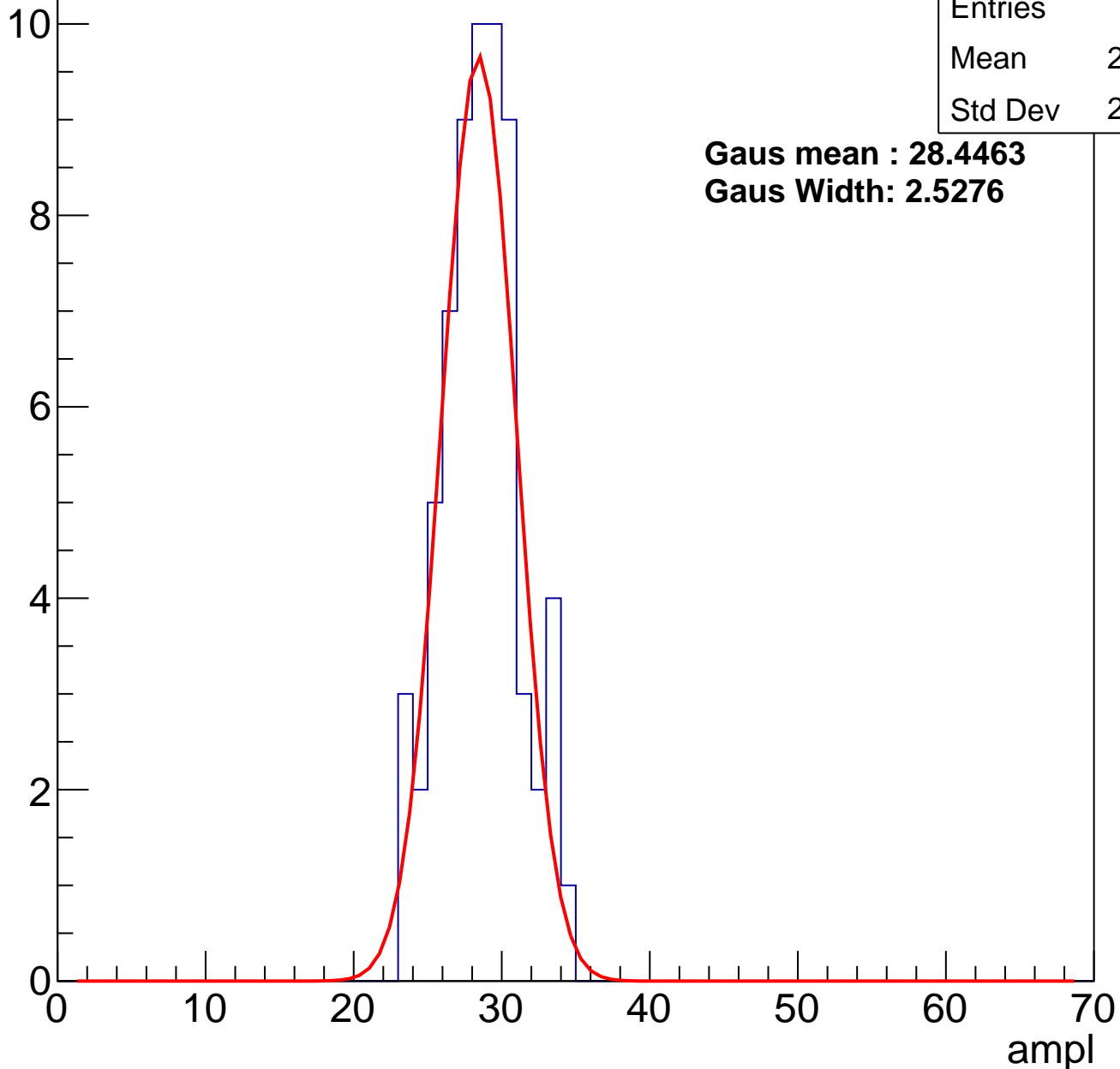
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.15
Std Dev	2.567

**Gaus mean : 28.4463**

**Gaus Width: 2.5276**

Entry



# B1L000S, U23-ch11, adc1

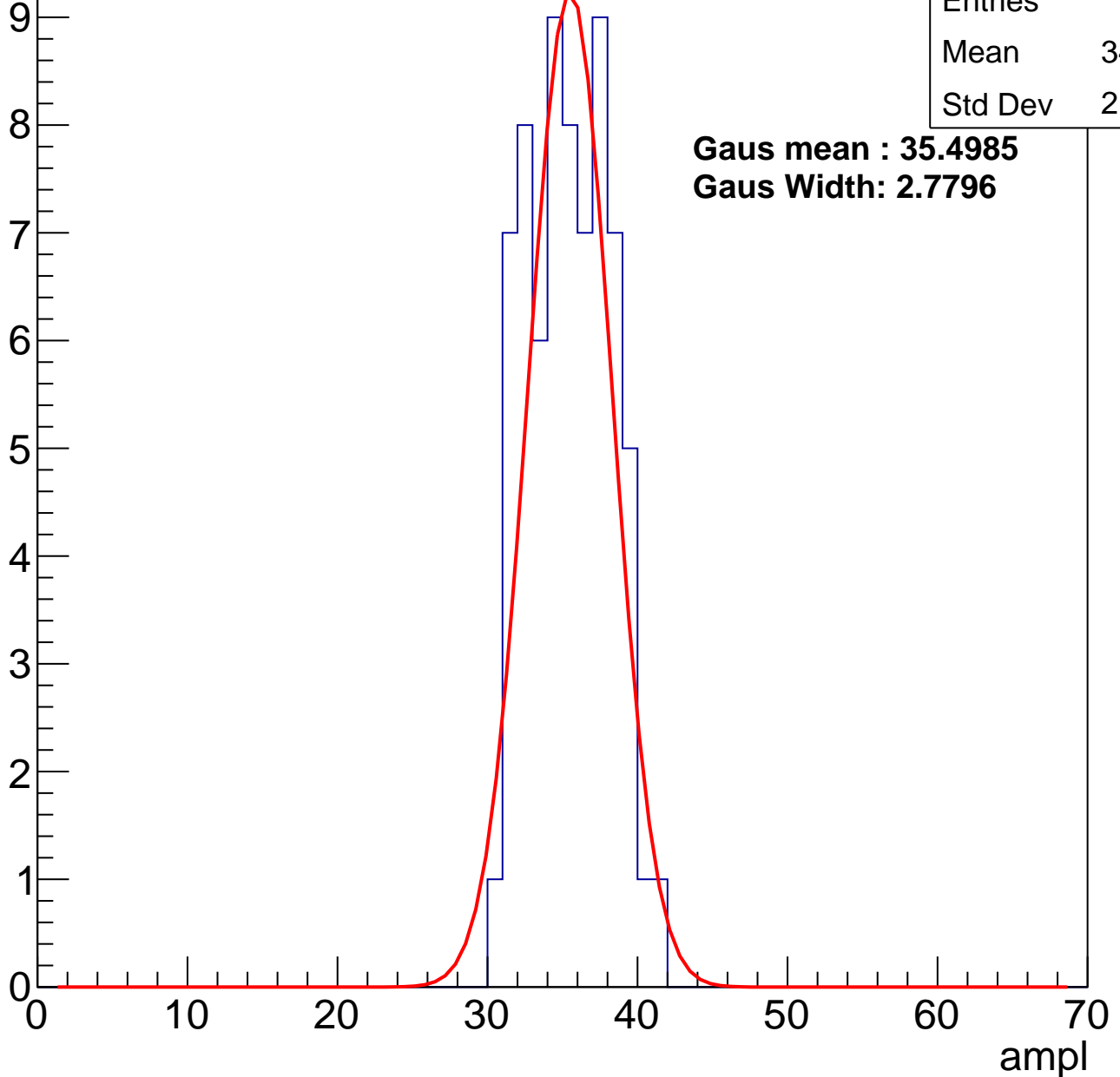
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.99
Std Dev	2.662

**Gaus mean : 35.4985**

**Gaus Width: 2.7796**



# B1L000S, U23-ch11, adc2

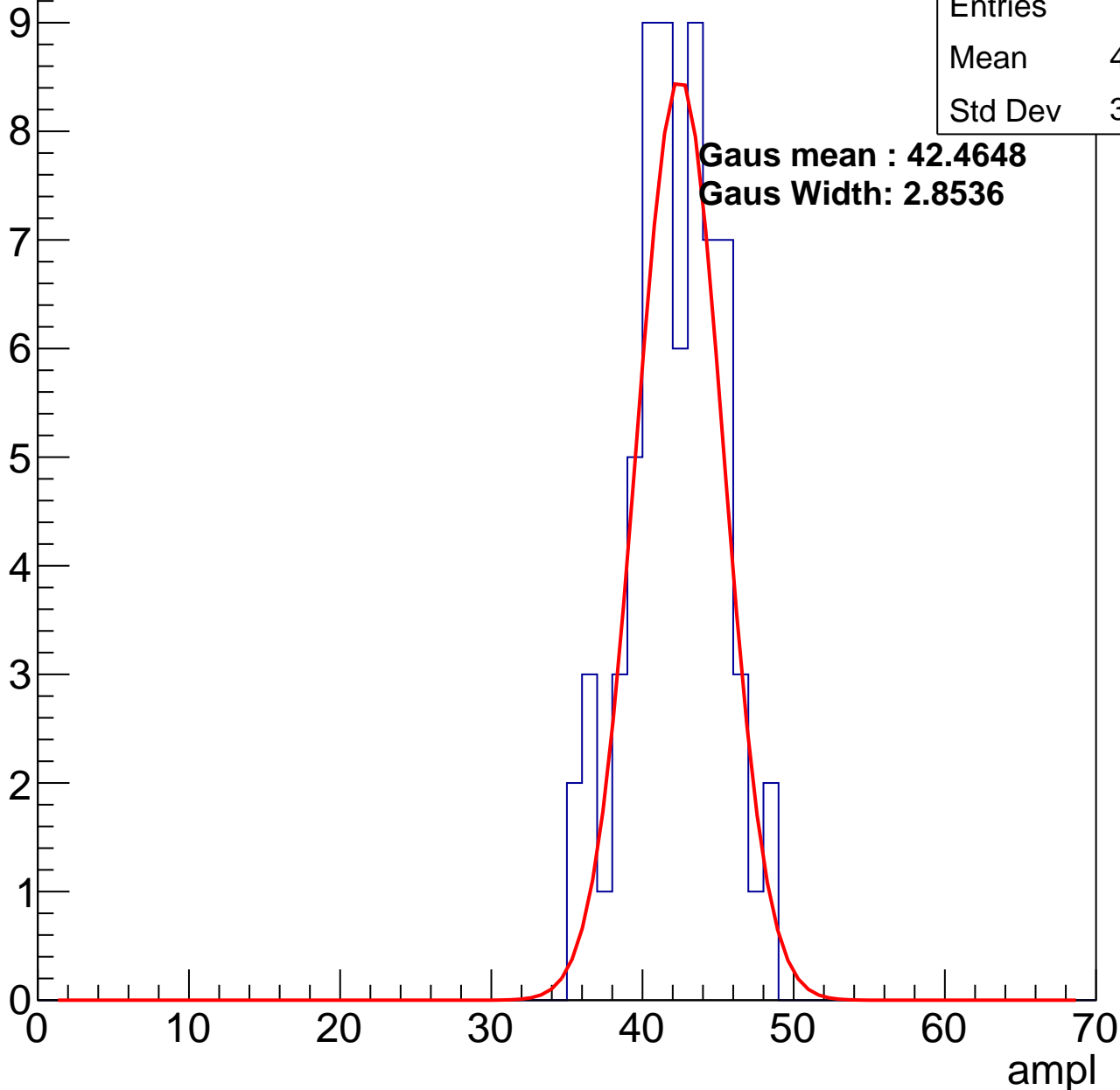
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.73
Std Dev	3.015

**Gaus mean : 42.4648**

**Gaus Width: 2.8536**

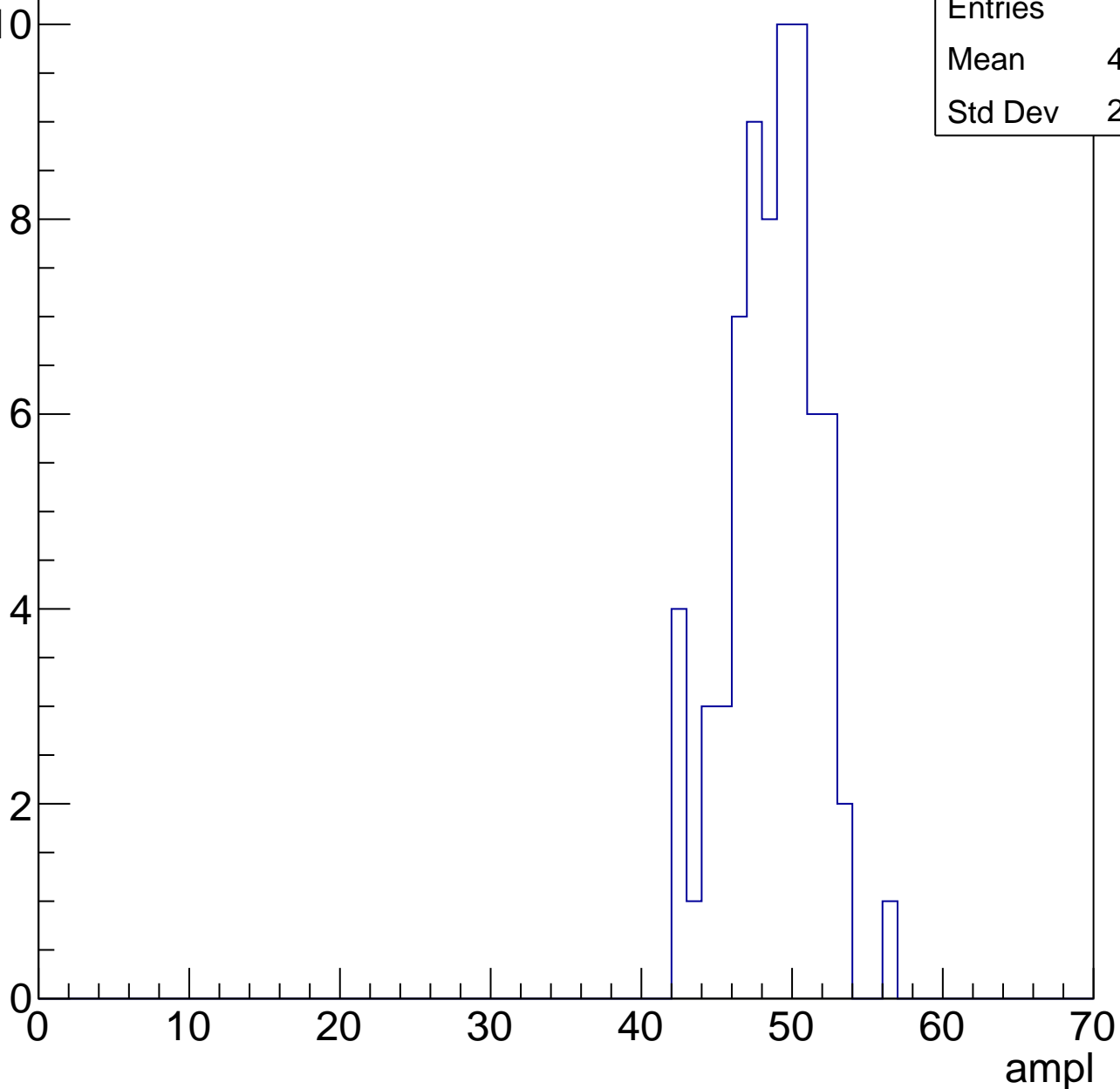


# B1L000S, U23-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

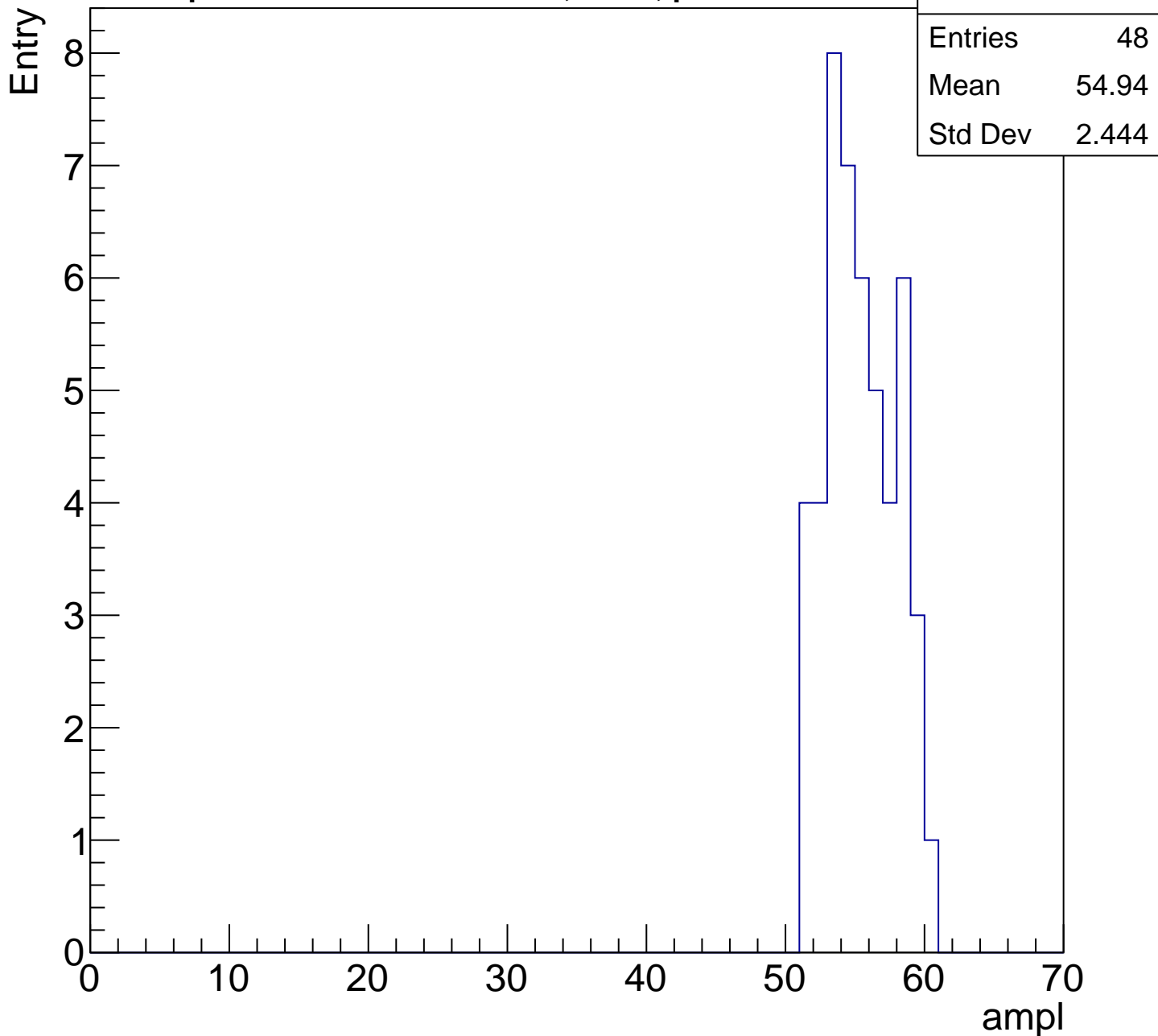
Entry

Entries	70
Mean	48.24
Std Dev	2.905



# B1L000S, U23-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

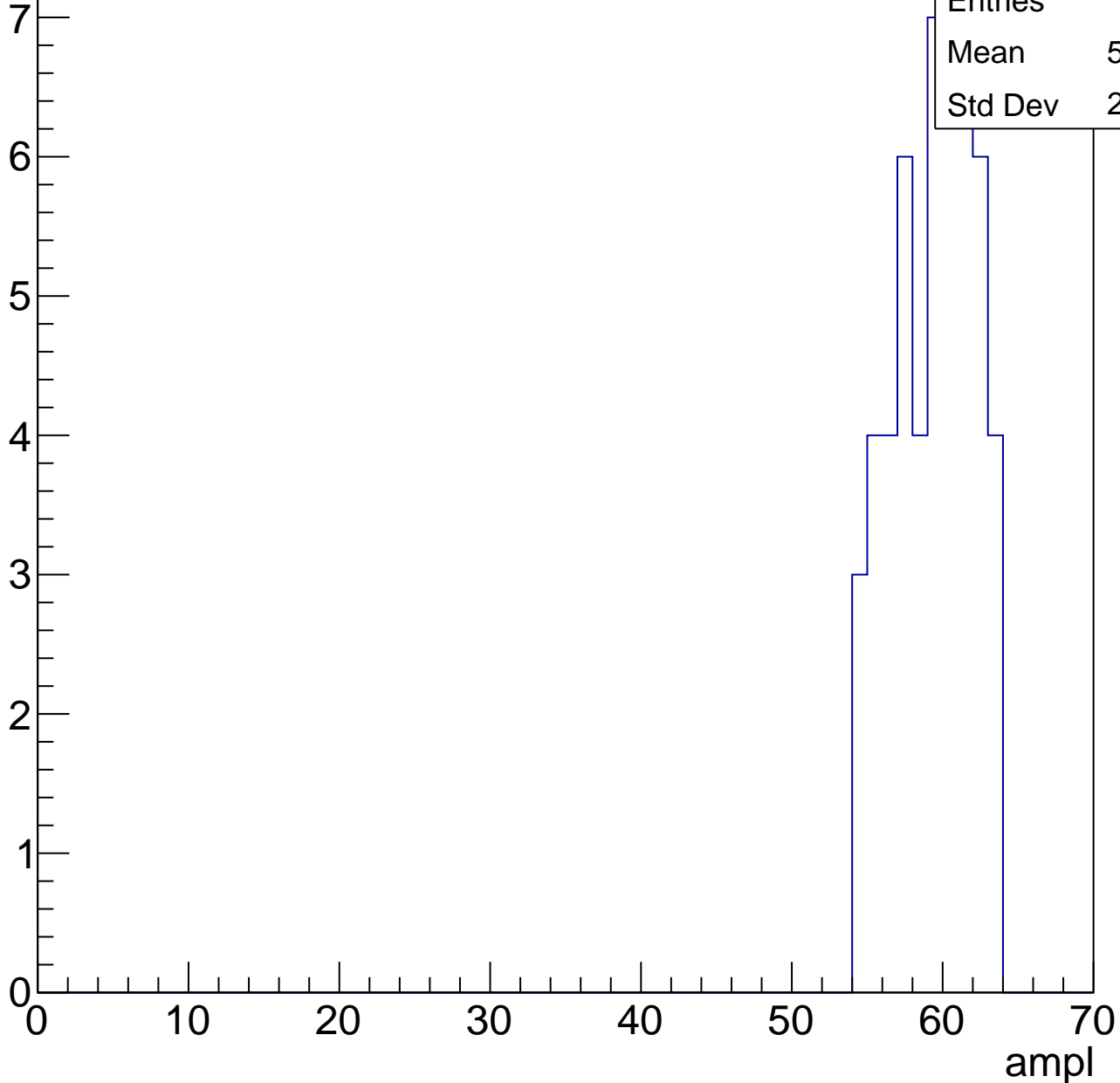


# B1L000S, U23-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	58.92
Std Dev	2.615

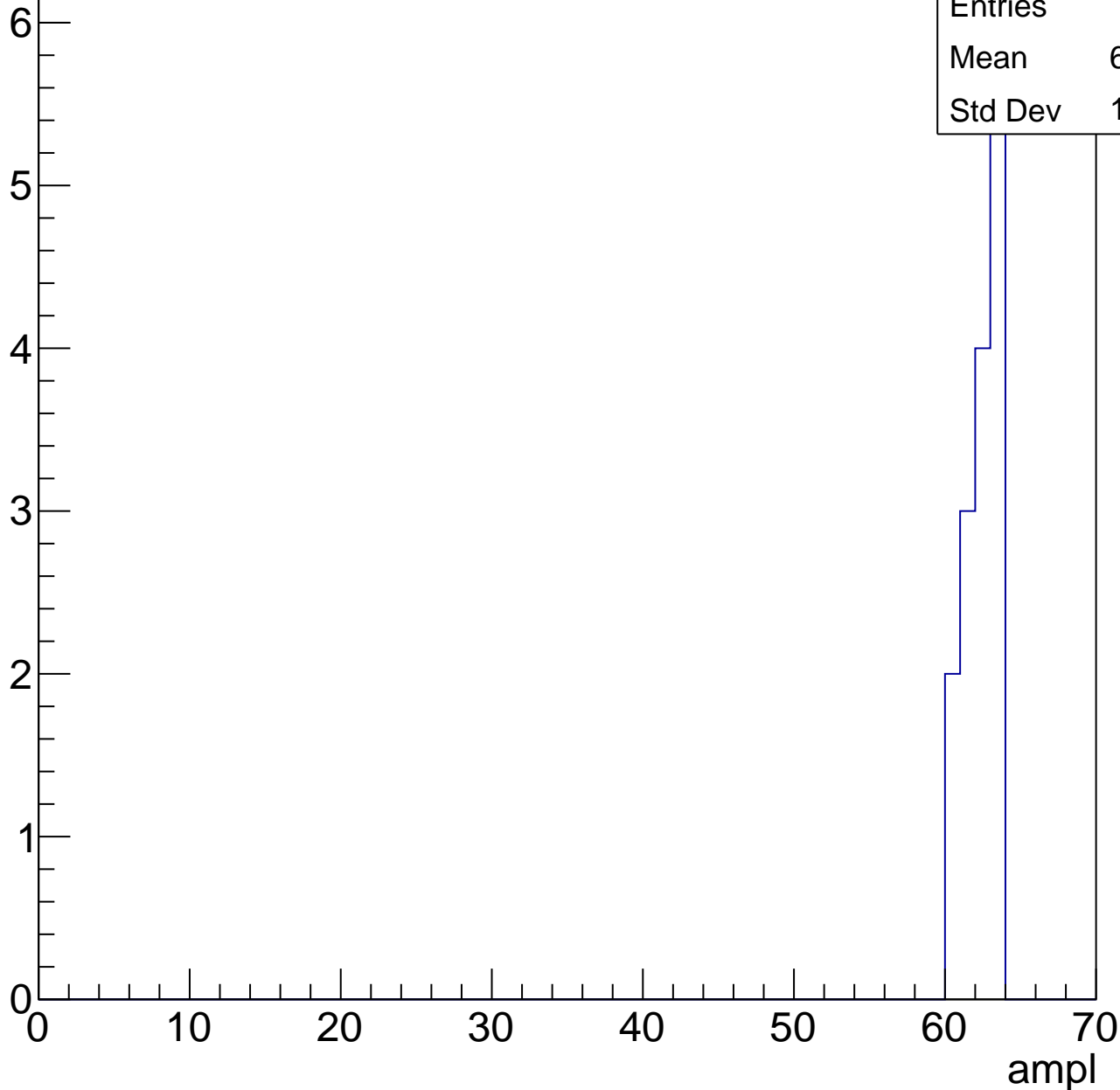


# B1L000S, U23-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.93
Std Dev	1.062





# B1L000S, U23-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch12, adc0

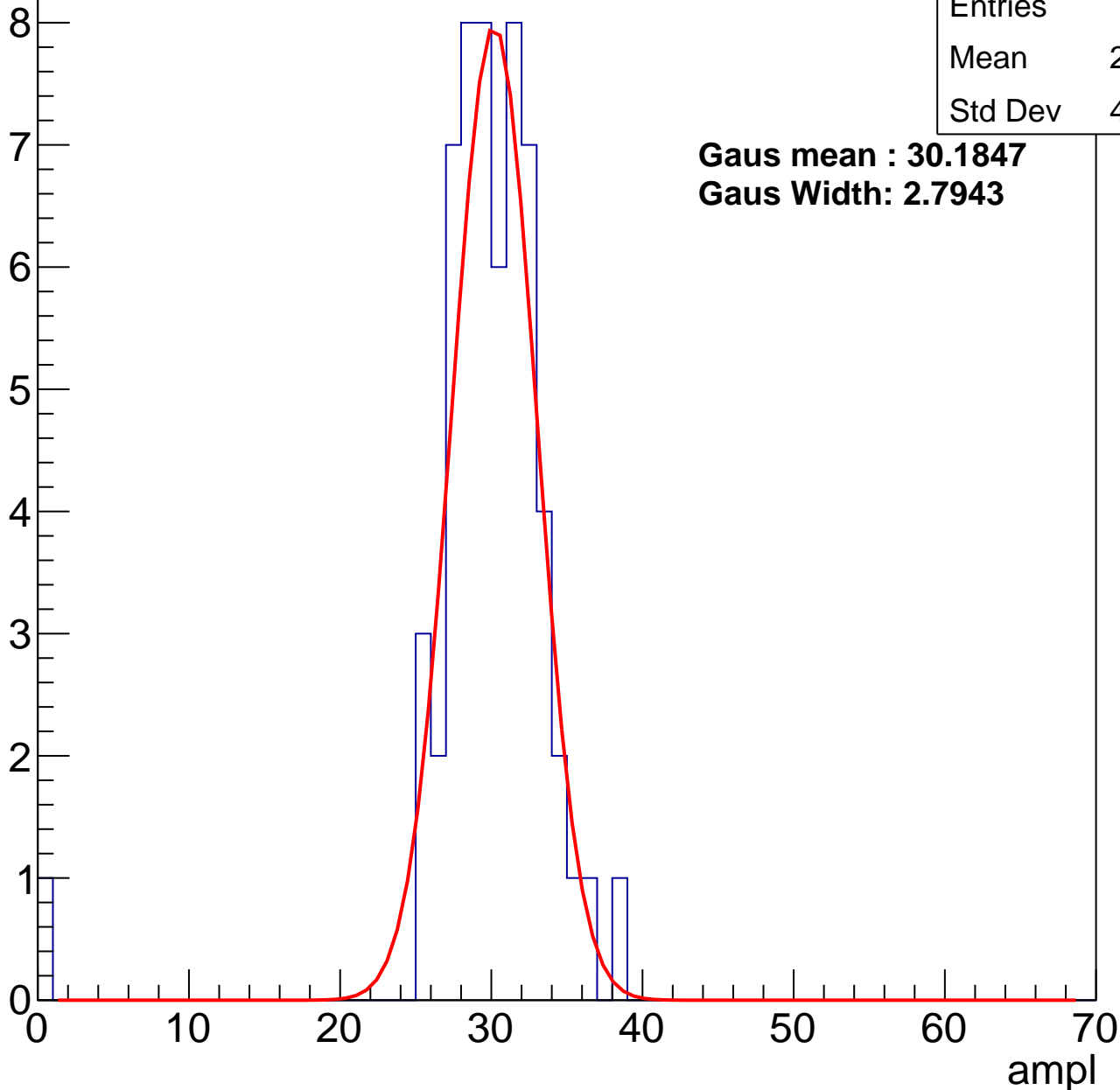
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.37
Std Dev	4.722

**Gaus mean : 30.1847**

**Gaus Width: 2.7943**



# B1L000S, U23-ch12, adc1

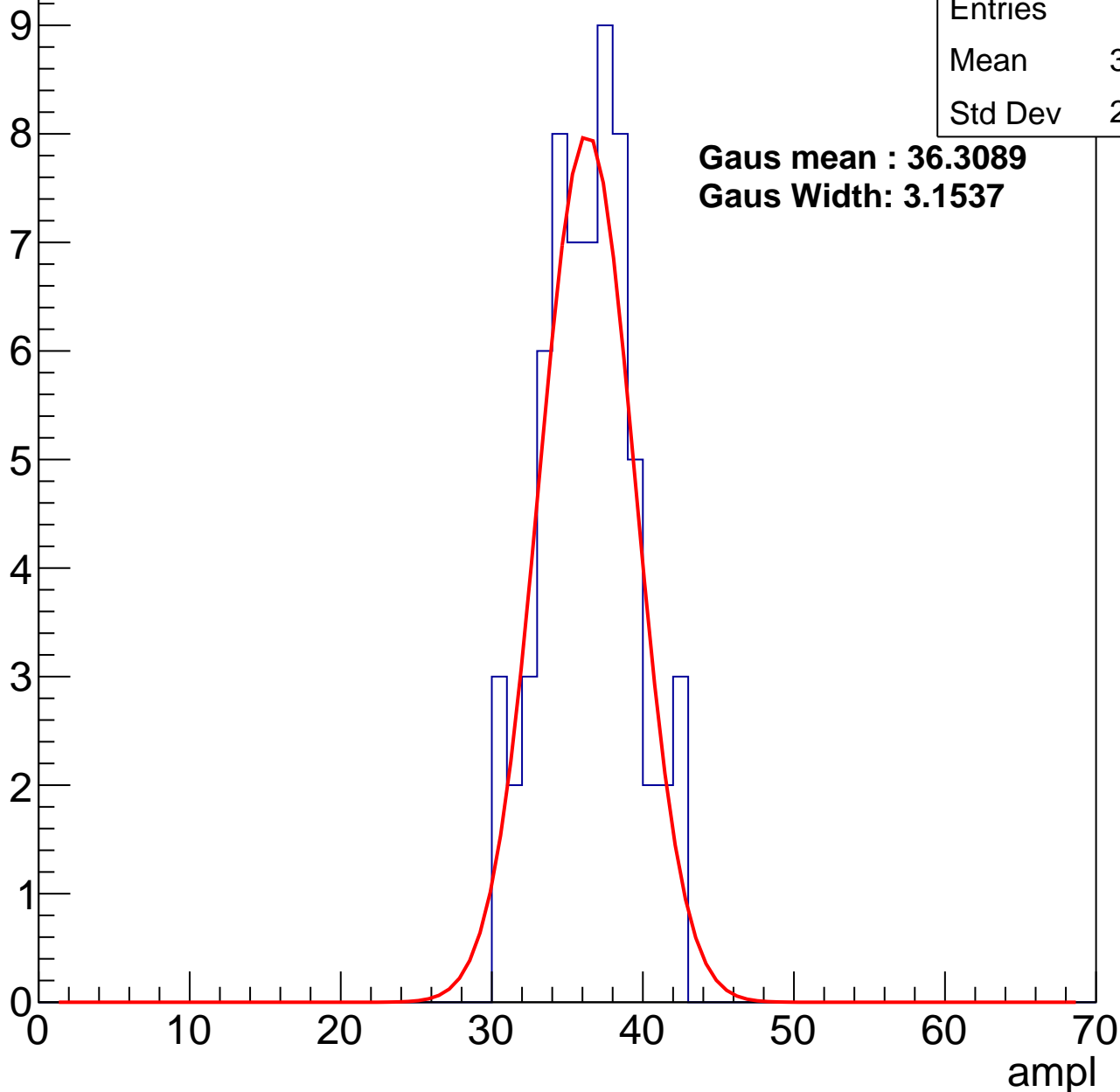
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.92
Std Dev	2.973

**Gaus mean : 36.3089**

**Gaus Width: 3.1537**



# B1L000S, U23-ch12, adc2

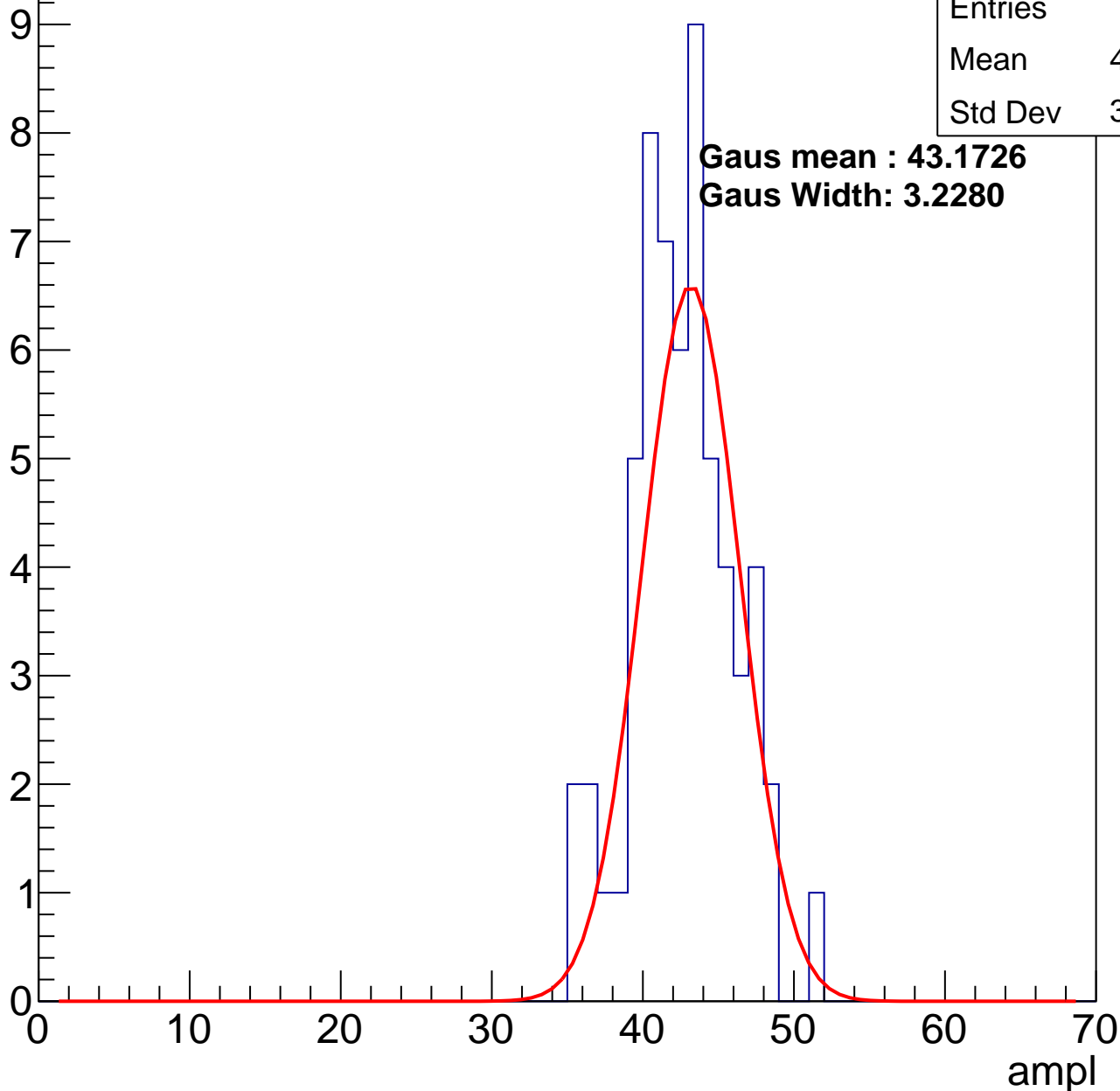
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.18
Std Dev	3.314

**Gaus mean : 43.1726**

**Gaus Width: 3.2280**

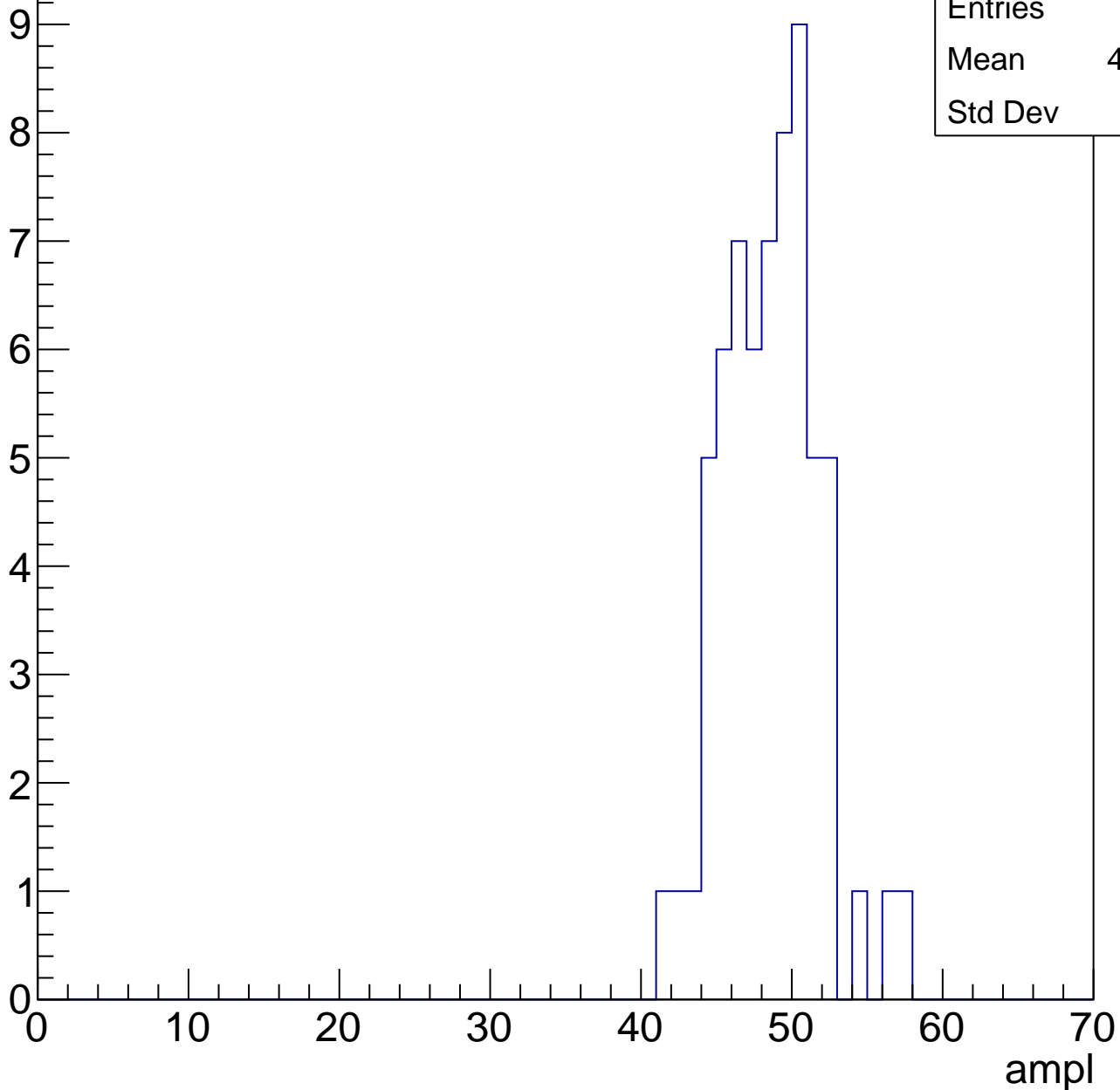


# B1L000S, U23-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.12
Std Dev	3.13

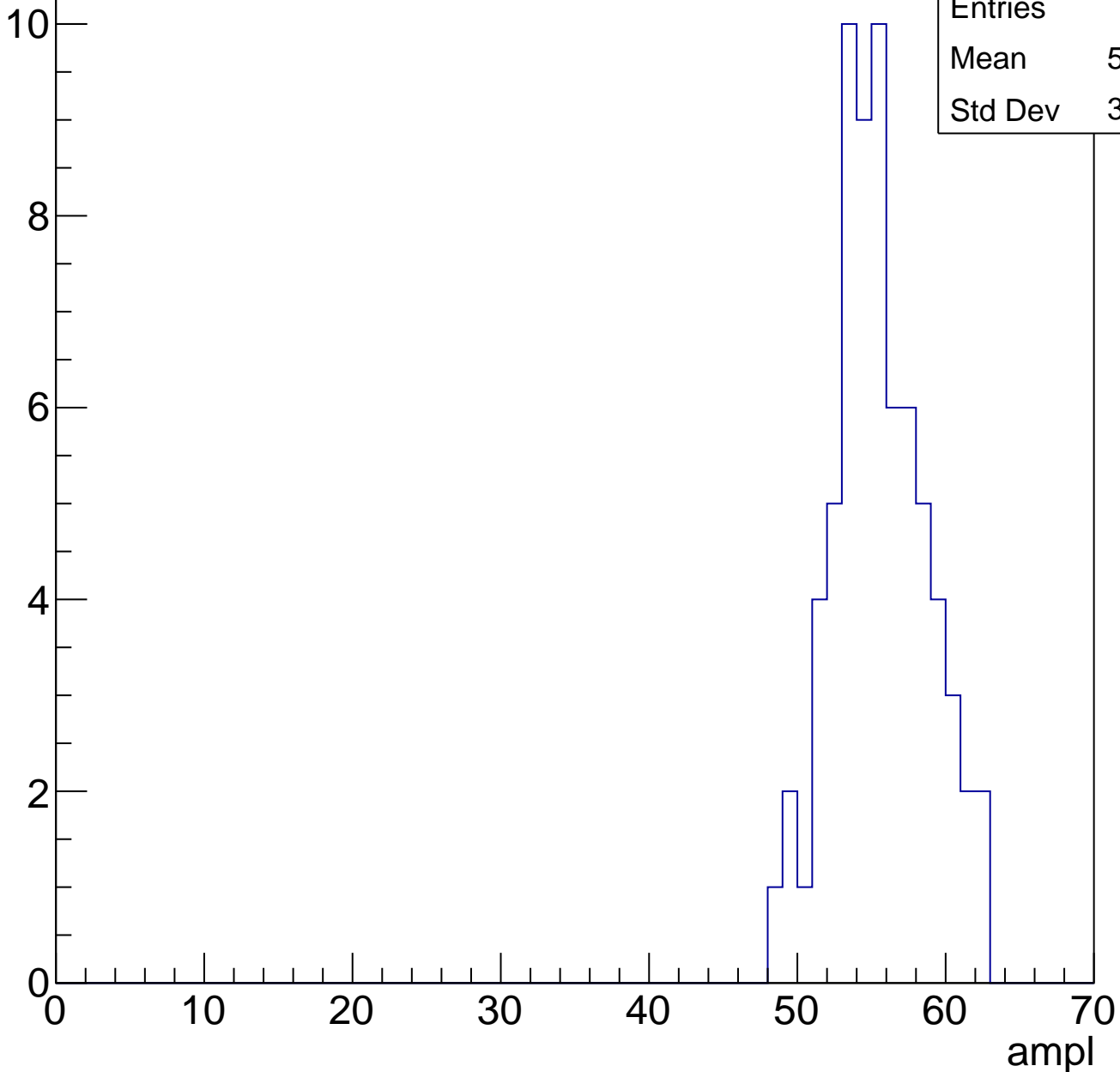


# B1L000S, U23-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	55.09
Std Dev	3.134

Entry



# B1L000S, U23-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 58.58

Std Dev 9.277

ampl

0

10

20

30

40

50

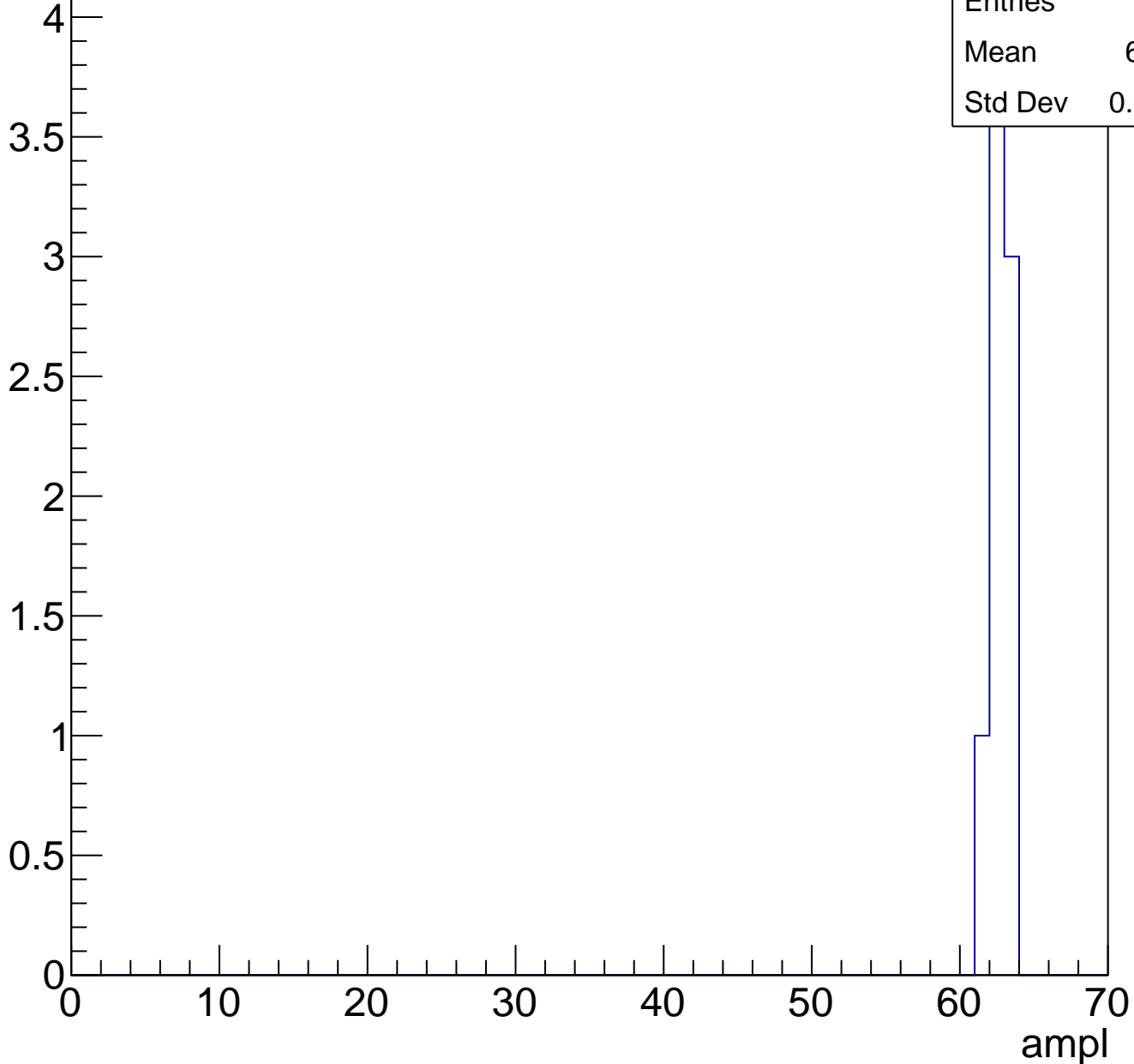
60

70

# B1L000S, U23-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	24
Std Dev	0

# B1L000S, U23-ch13, adc0

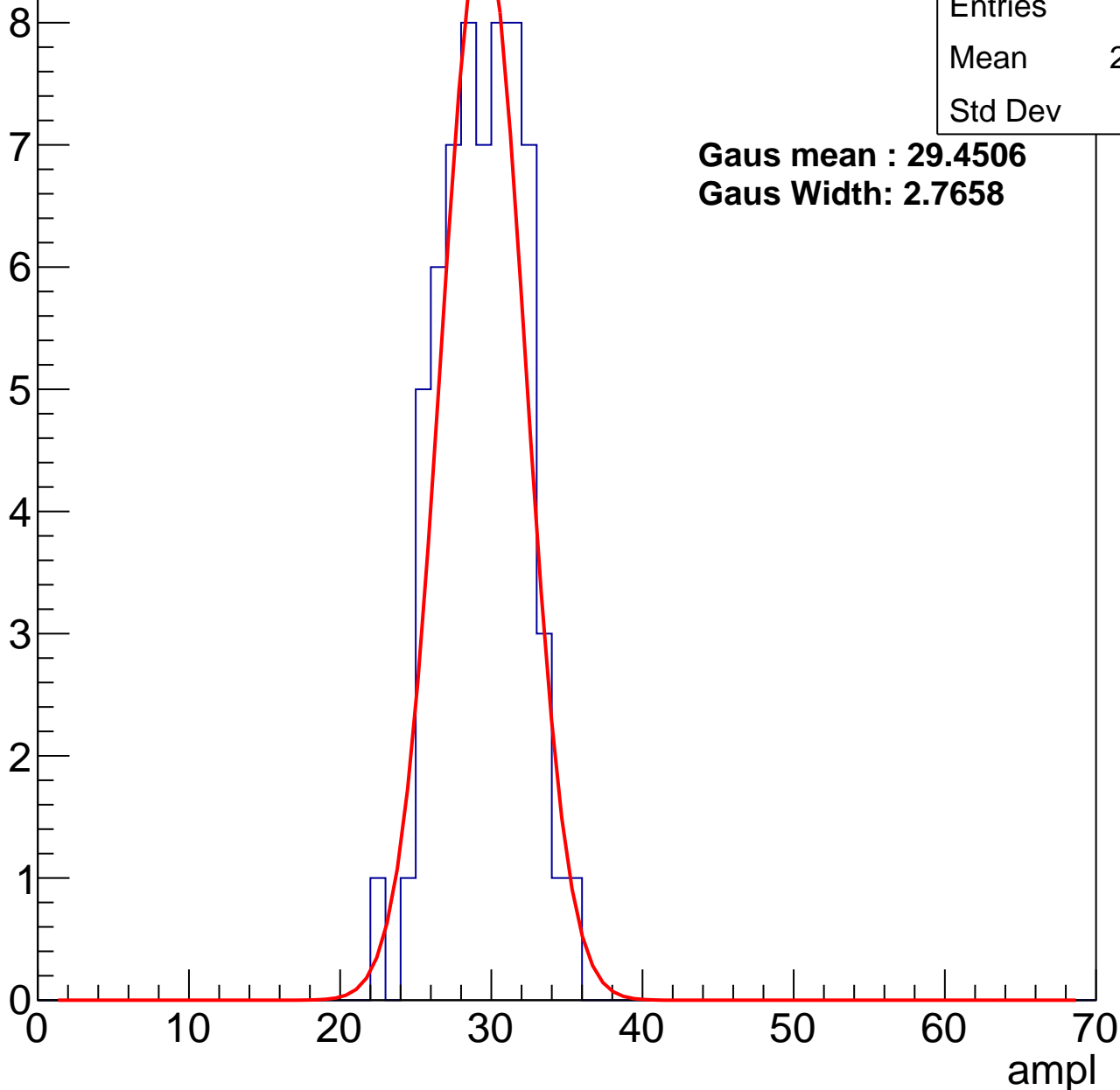
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.94
Std Dev	2.69

**Gaus mean : 29.4506**

**Gaus Width: 2.7658**



# B1L000S, U23-ch13, adc1

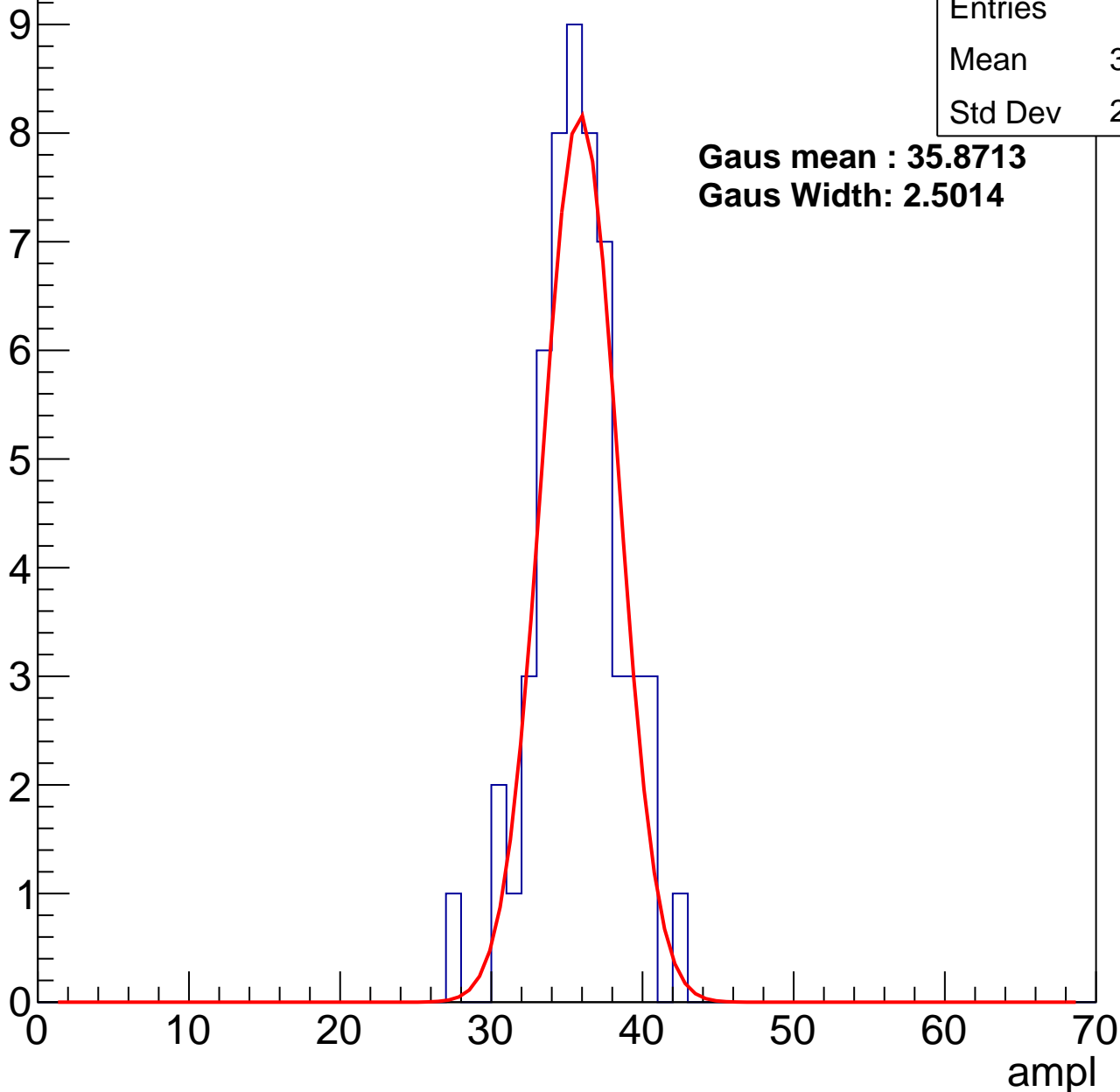
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	35.25
Std Dev	2.765

**Gaus mean : 35.8713**

**Gaus Width: 2.5014**



# B1L000S, U23-ch13, adc2

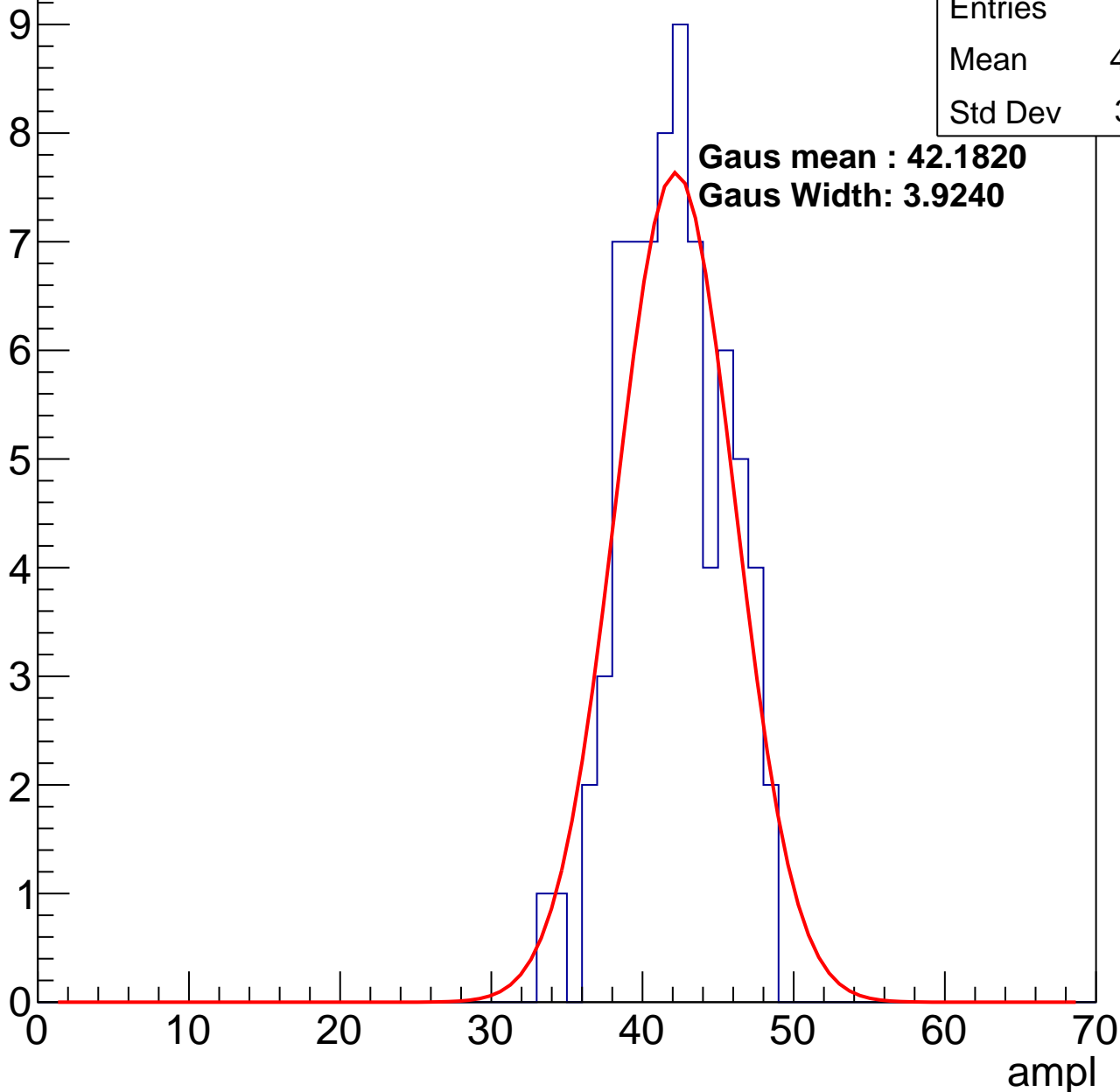
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	41.59
Std Dev	3.351

**Gaus mean : 42.1820**

**Gaus Width: 3.9240**

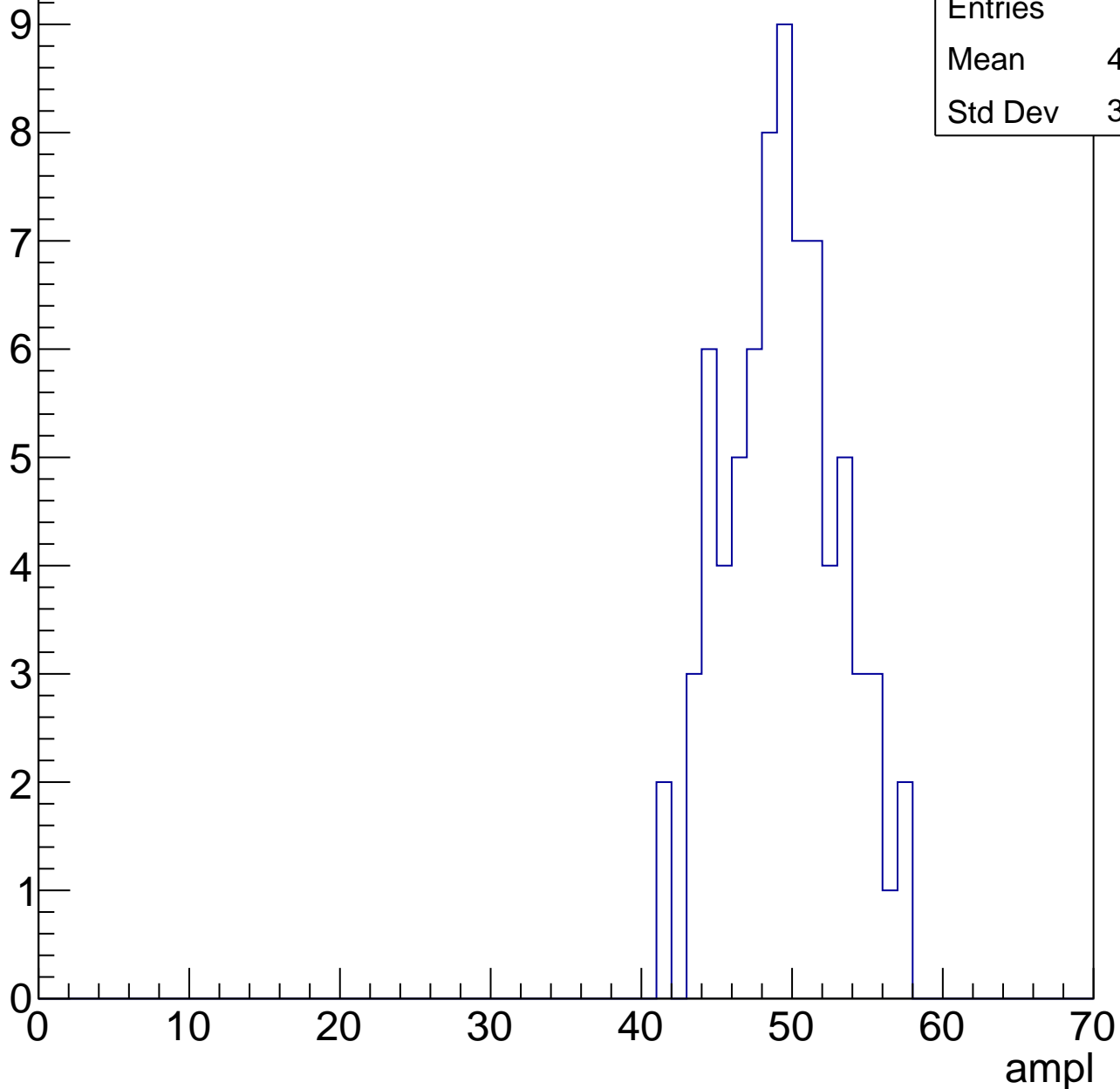


# B1L000S, U23-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	48.92
Std Dev	3.719



# B1L000S, U23-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 54.9

Std Dev 3.084

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

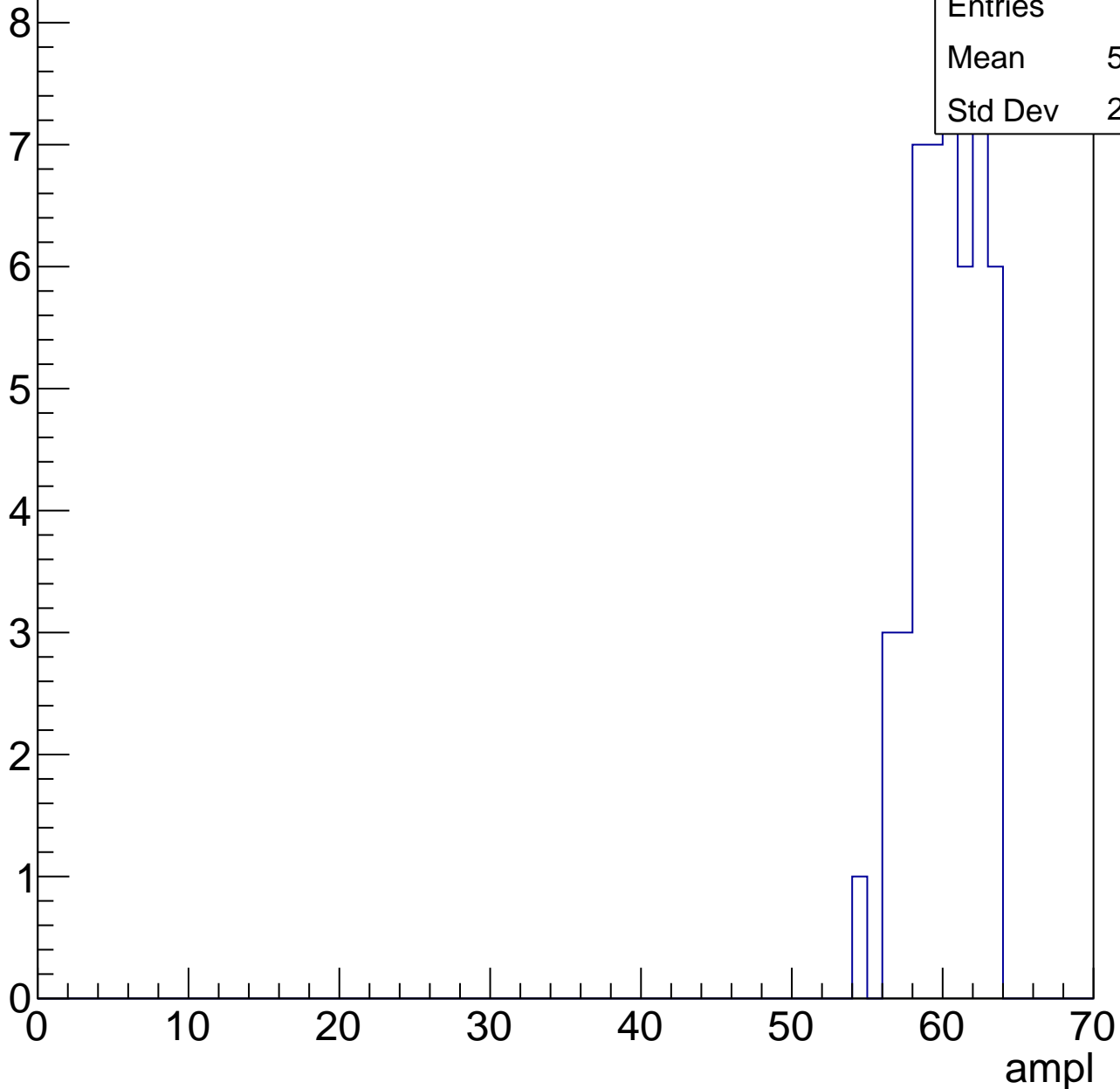
7

# B1L000S, U23-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

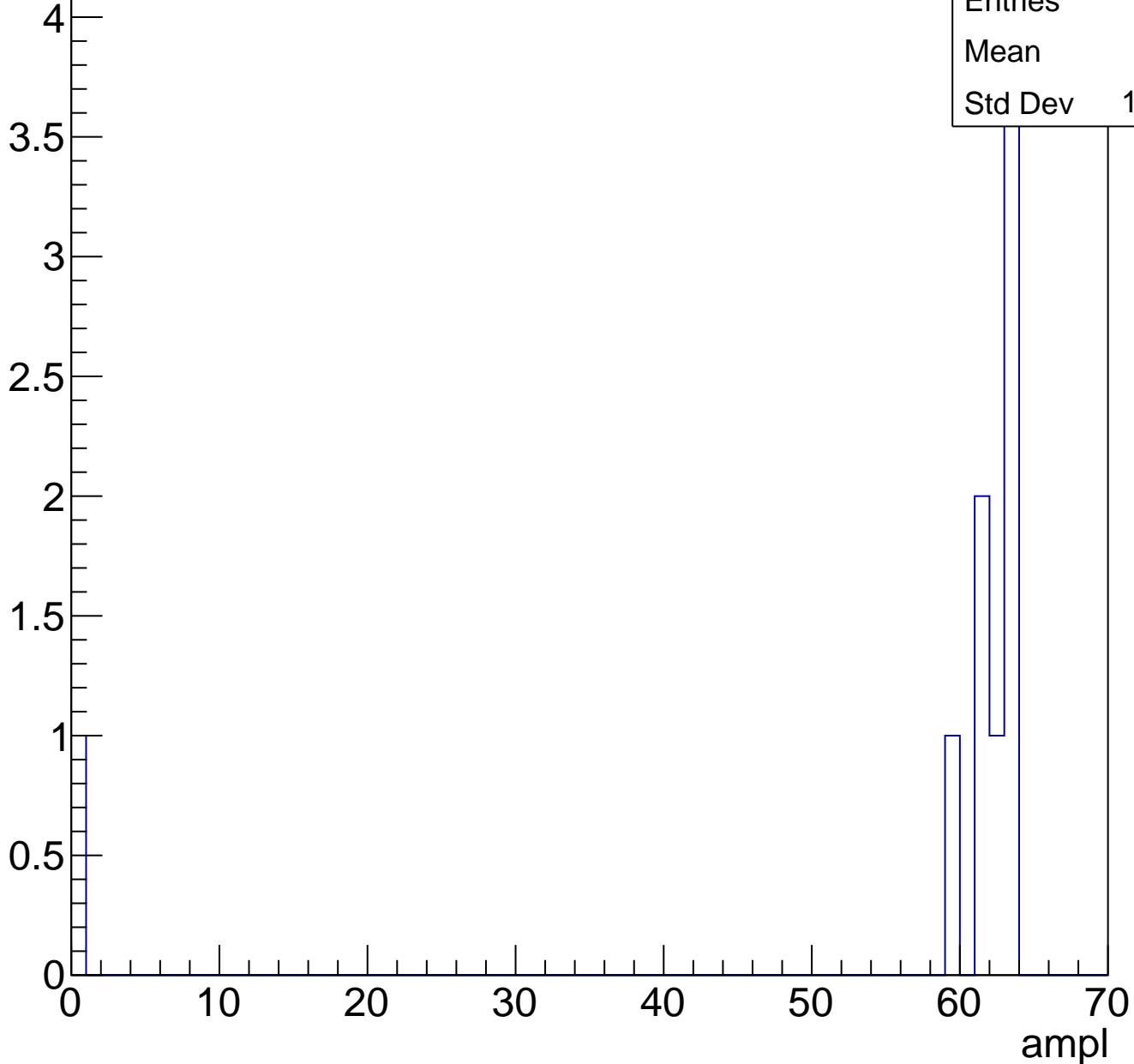
Entries	49
Mean	59.84
Std Dev	2.198



# B1L000S, U23-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch14, adc0

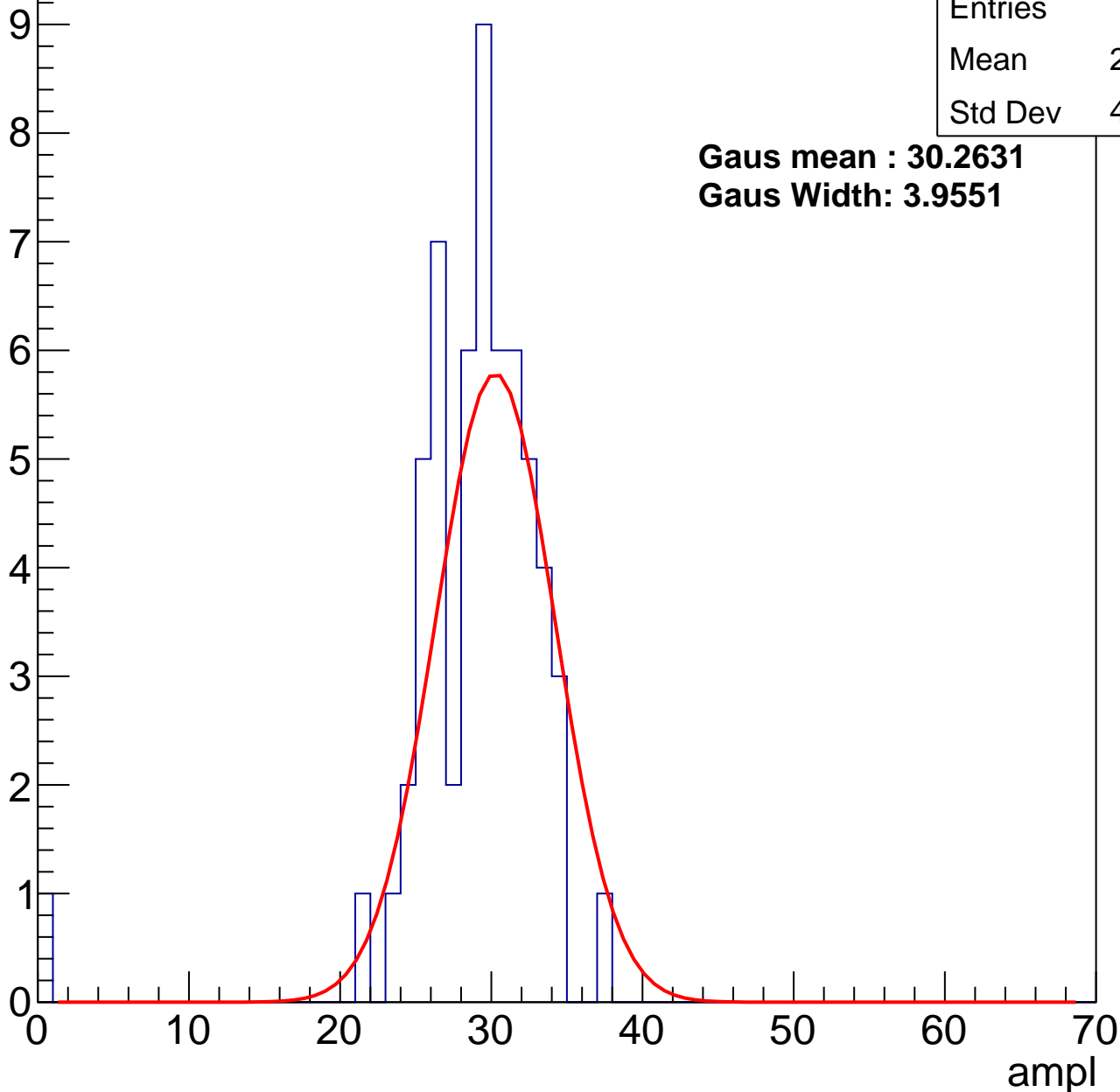
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.46
Std Dev	4.886

**Gaus mean : 30.2631**

**Gaus Width: 3.9551**



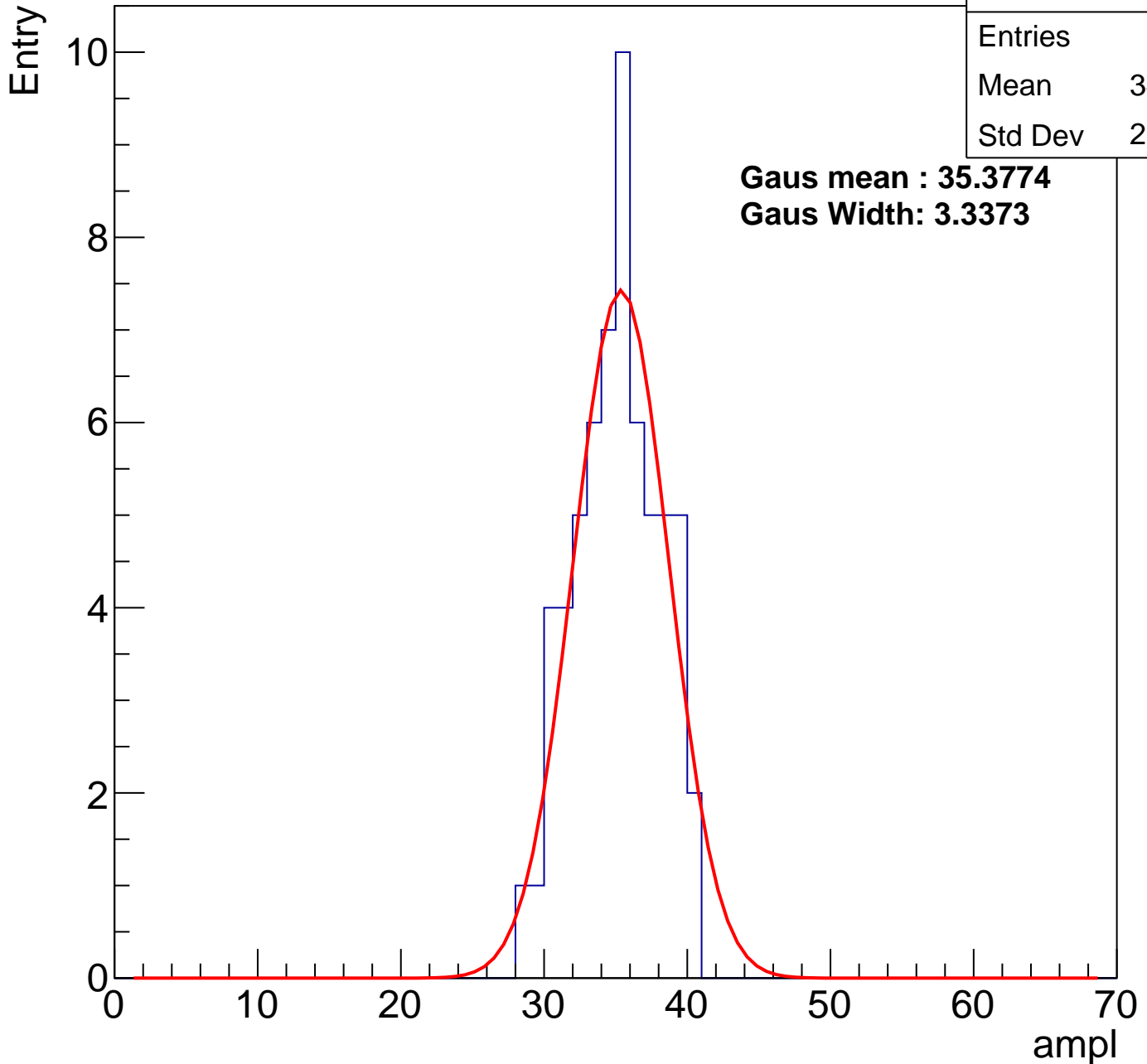
# B1L000S, U23-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	34.64
Std Dev	2.914

**Gaus mean : 35.3774**

**Gaus Width: 3.3373**



# B1L000S, U23-ch14, adc2

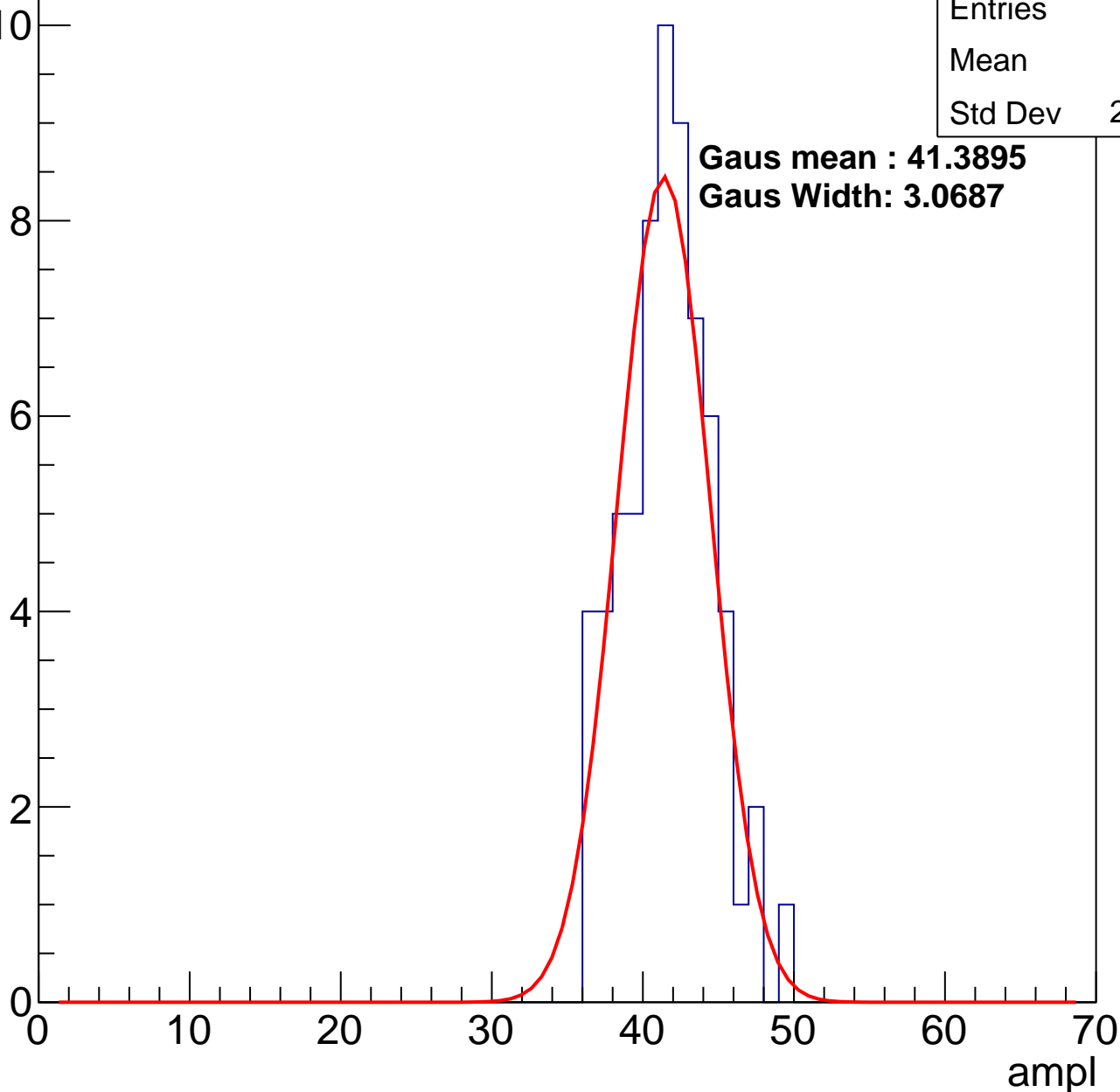
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	41.2
Std Dev	2.888

**Gaus mean : 41.3895**

**Gaus Width: 3.0687**



# B1L000S, U23-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	47.84
Std Dev	2.869

Entry

10

8

6

4

2

0

0

10

20

30

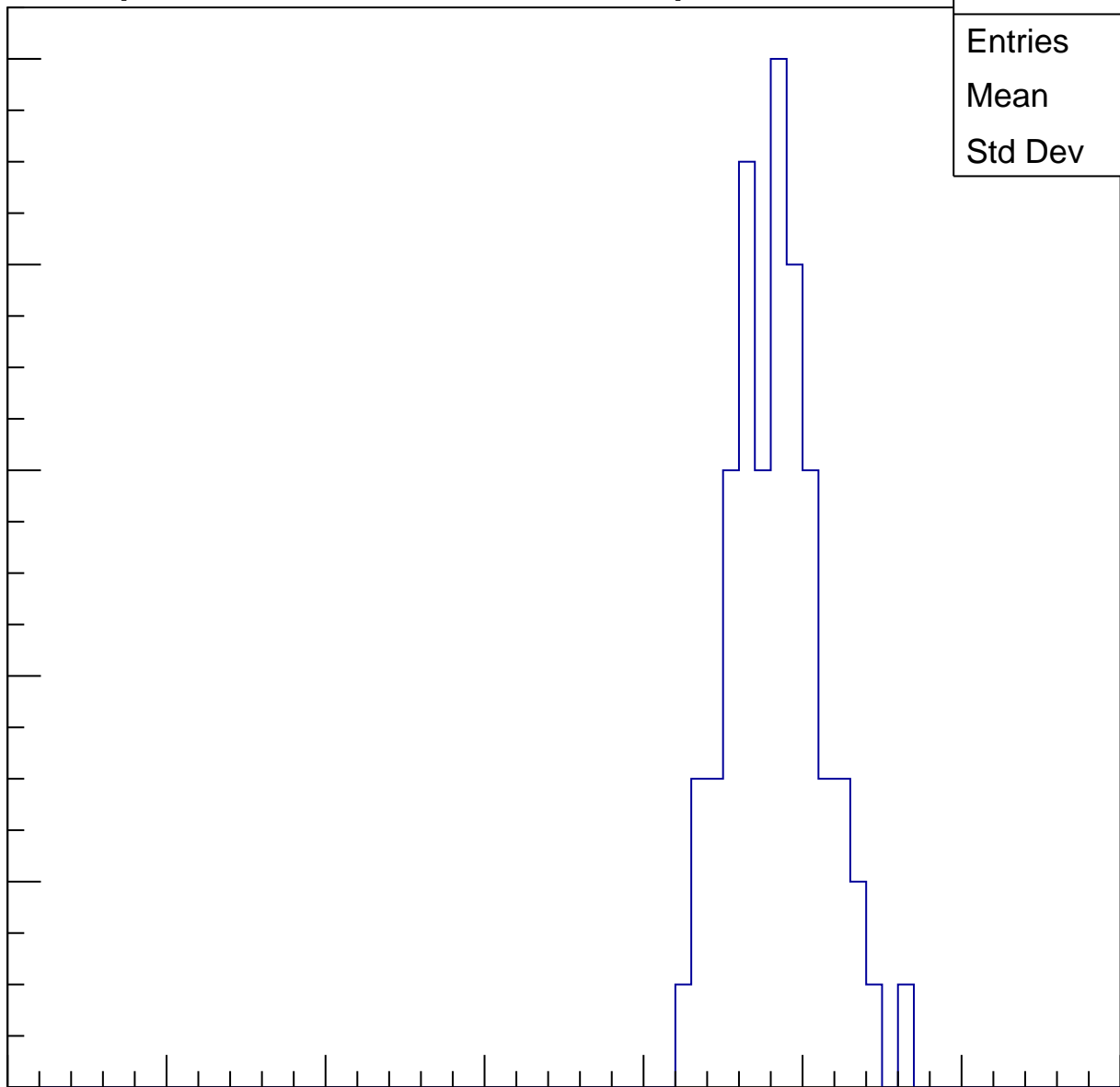
40

50

60

70

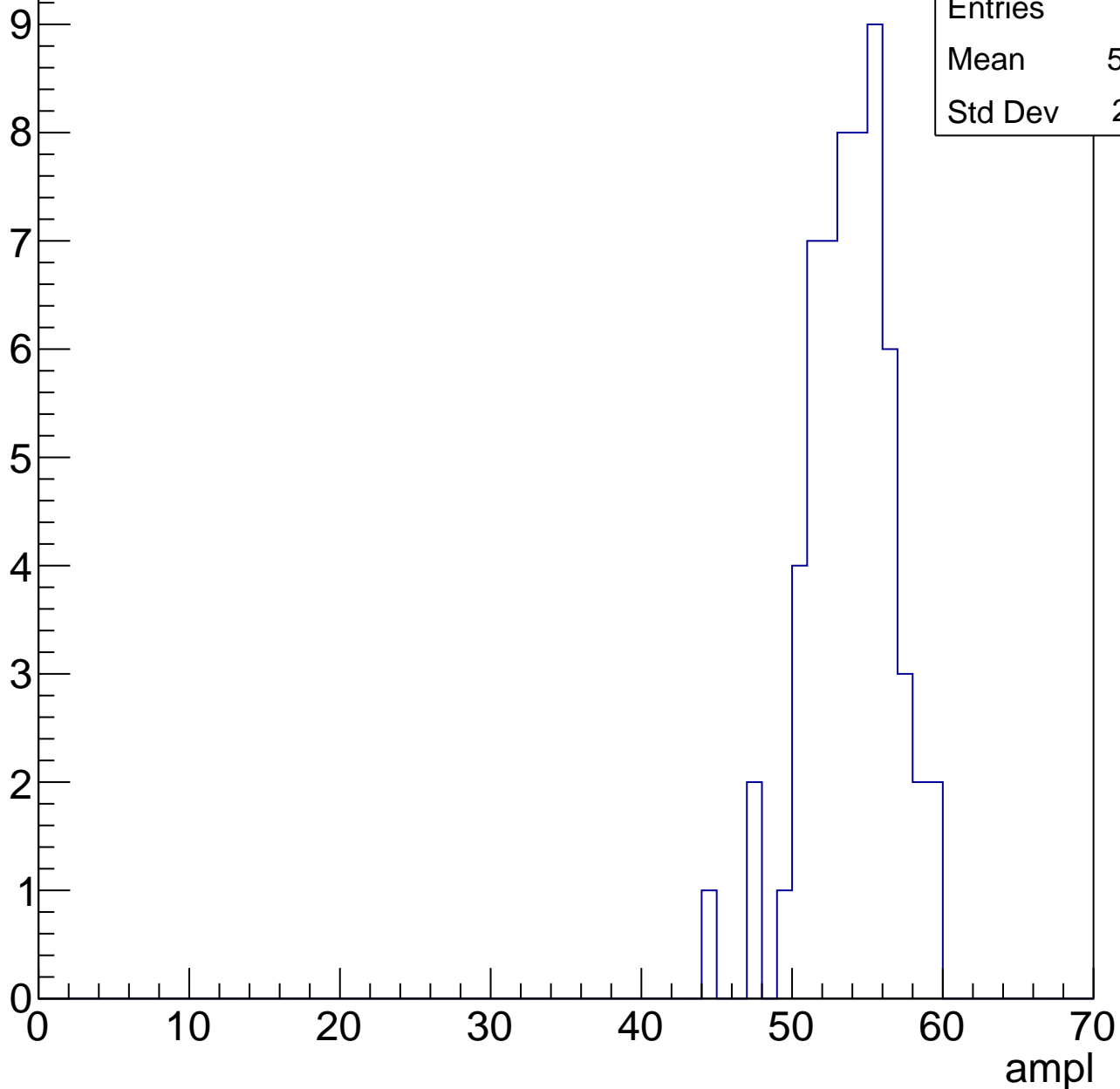
ampl



# B1L000S, U23-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



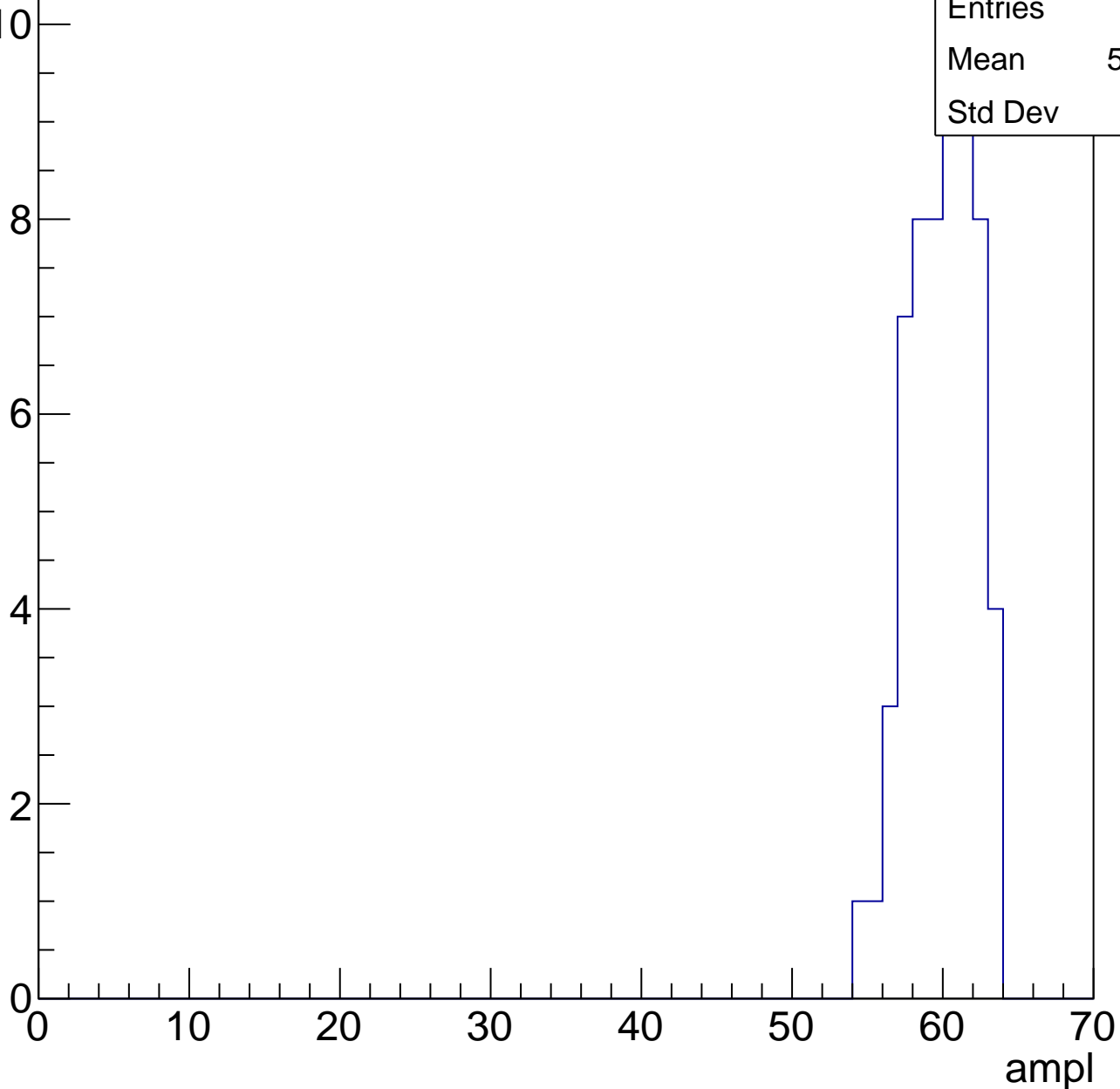
Entries	60
Mean	53.33
Std Dev	2.891

# B1L000S, U23-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	59.47
Std Dev	2.15

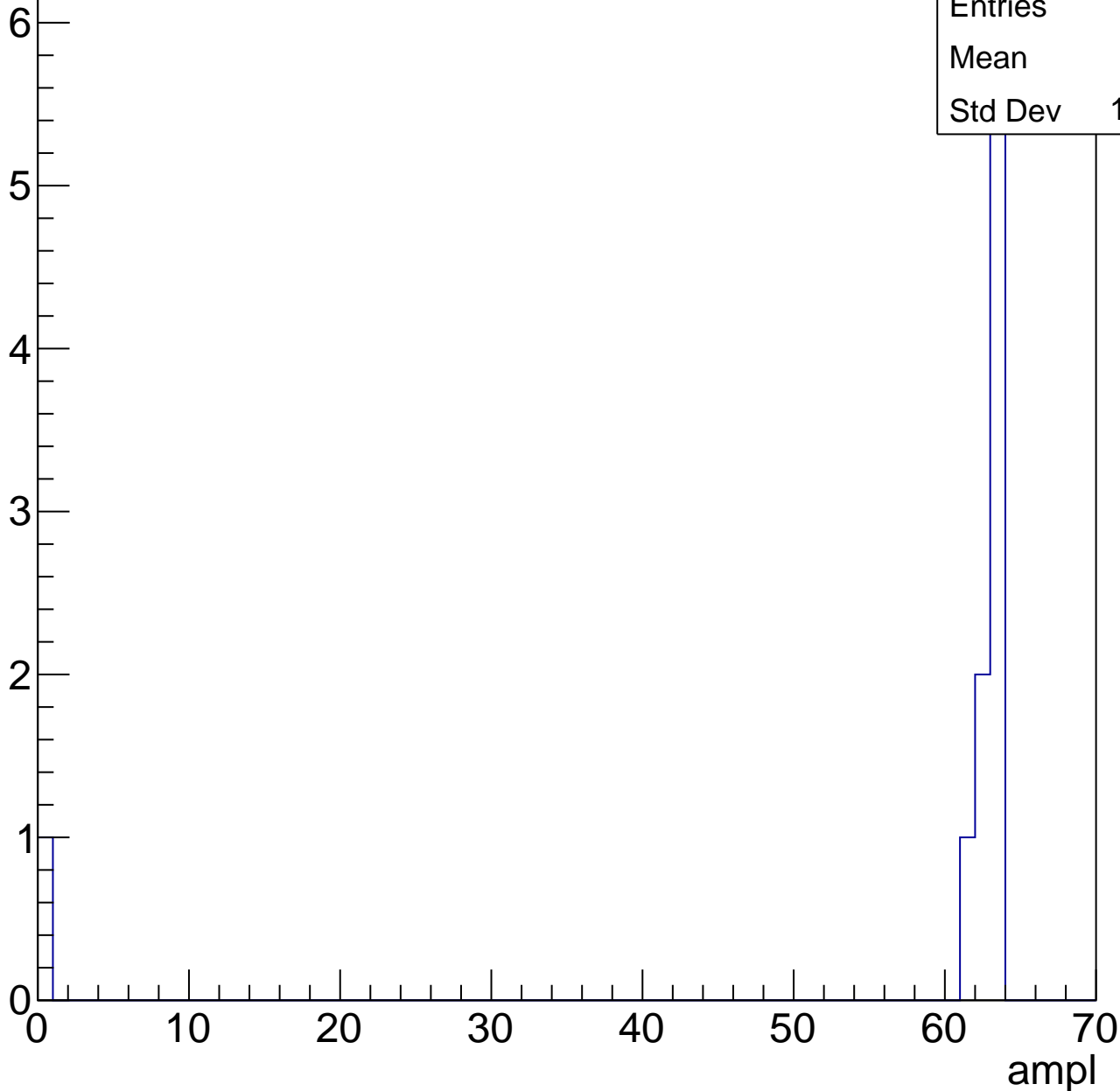


# B1L000S, U23-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	10
Mean	56.3
Std Dev	18.78





# B1L000S, U23-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	30.02
Std Dev	4.938

**Gaus mean : 29.7283**

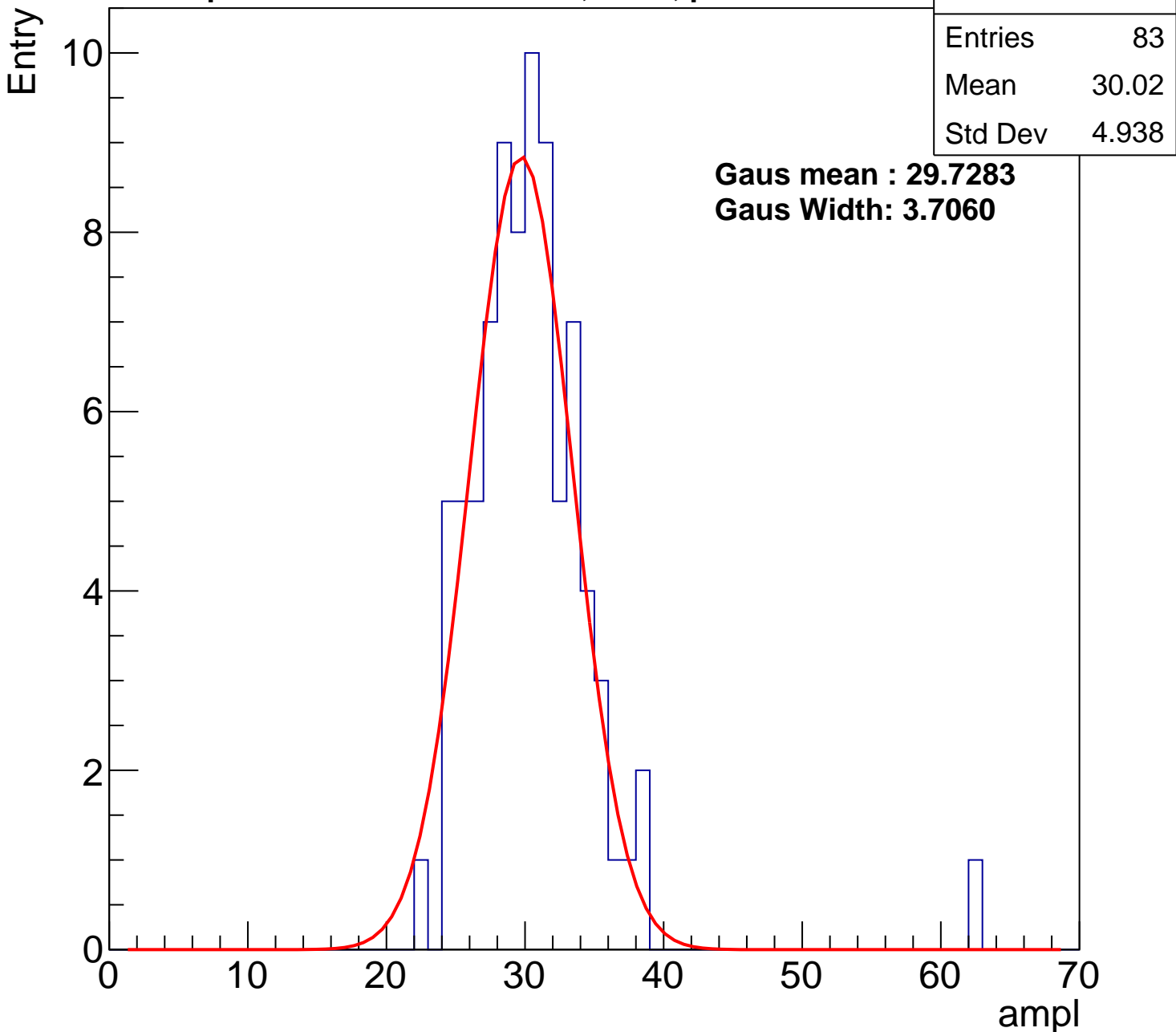
**Gaus Width: 3.7060**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch15, adc1

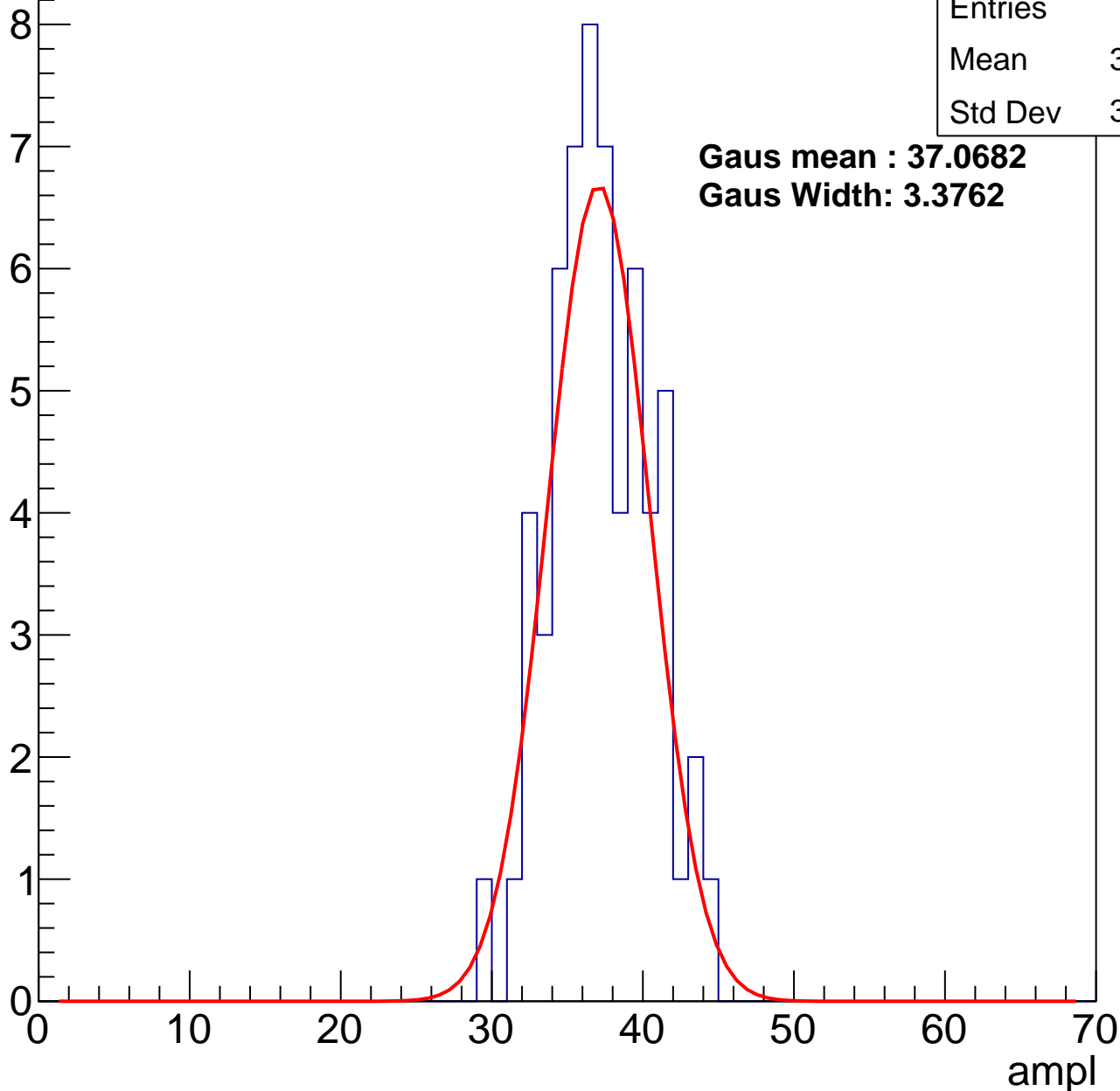
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	36.77
Std Dev	3.227

**Gaus mean : 37.0682**

**Gaus Width: 3.3762**



# B1L000S, U23-ch15, adc2

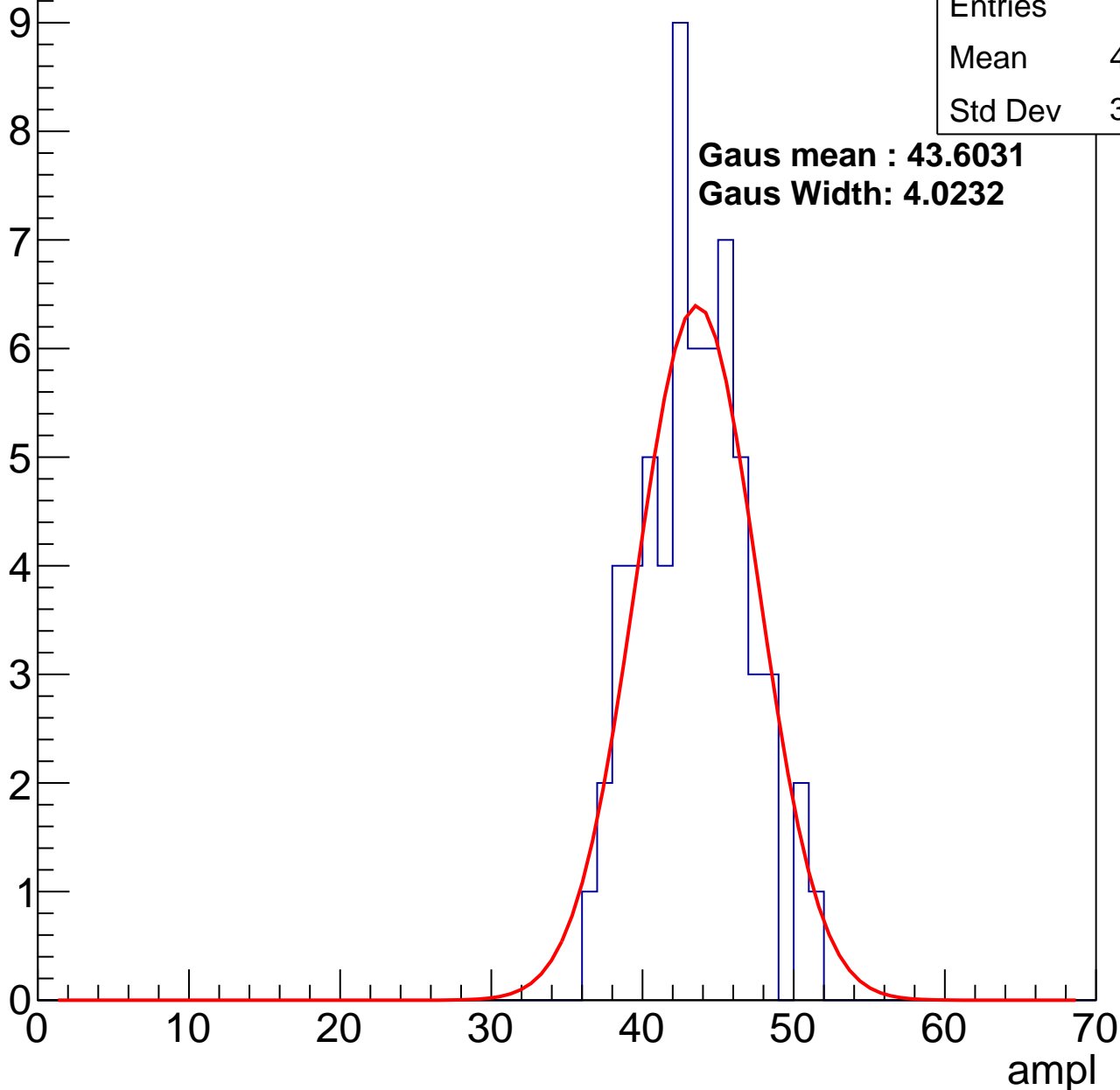
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.95
Std Dev	3.405

**Gaus mean : 43.6031**

**Gaus Width: 4.0232**



# B1L000S, U23-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	49.26
Std Dev	3.528

Entry

10

8

6

4

2

0

0

10

20

30

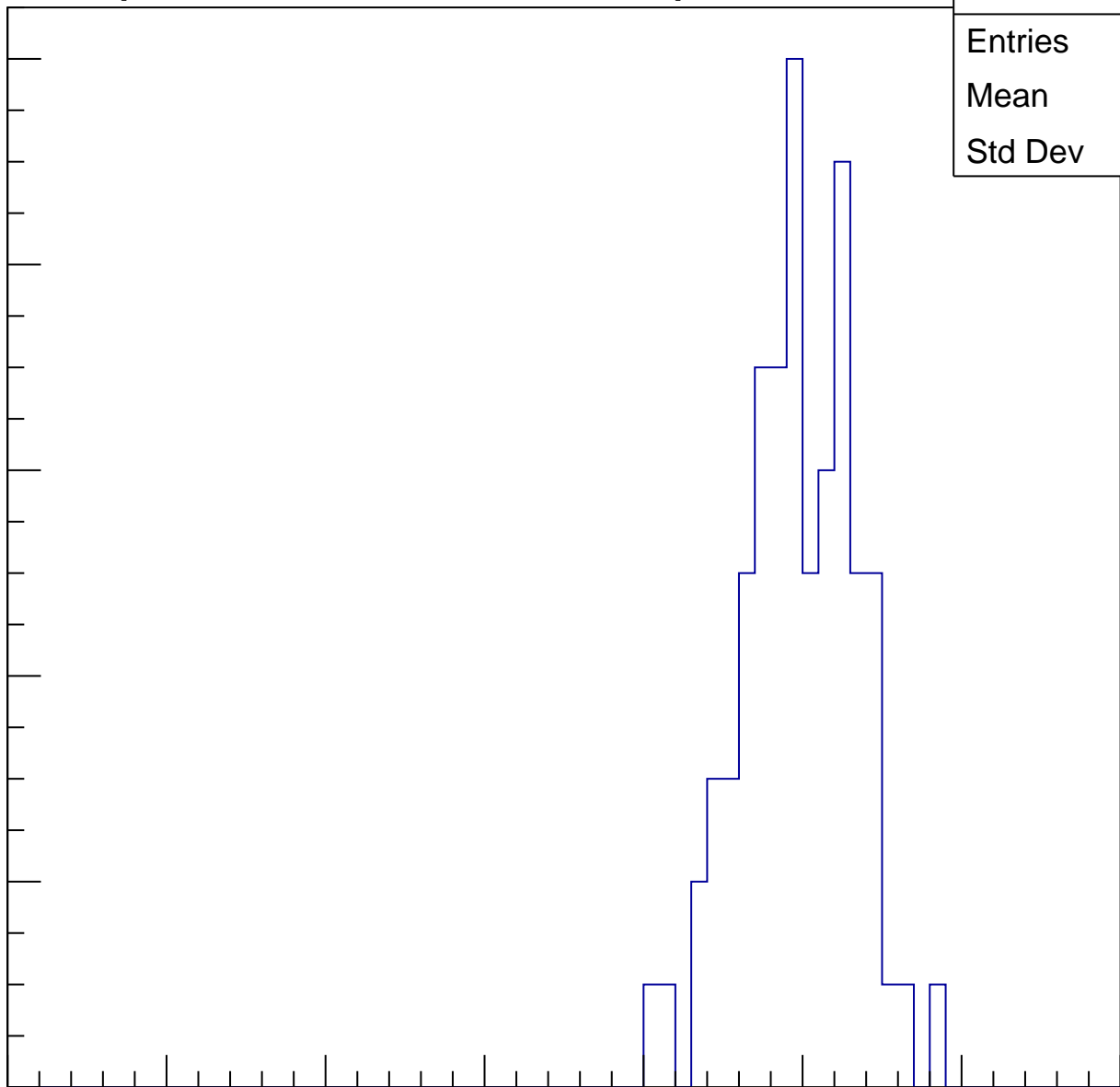
40

50

60

ampl

70



# B1L000S, U23-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries	51
Mean	55.86
Std Dev	3.169

ampl

0

10

20

30

40

50

60

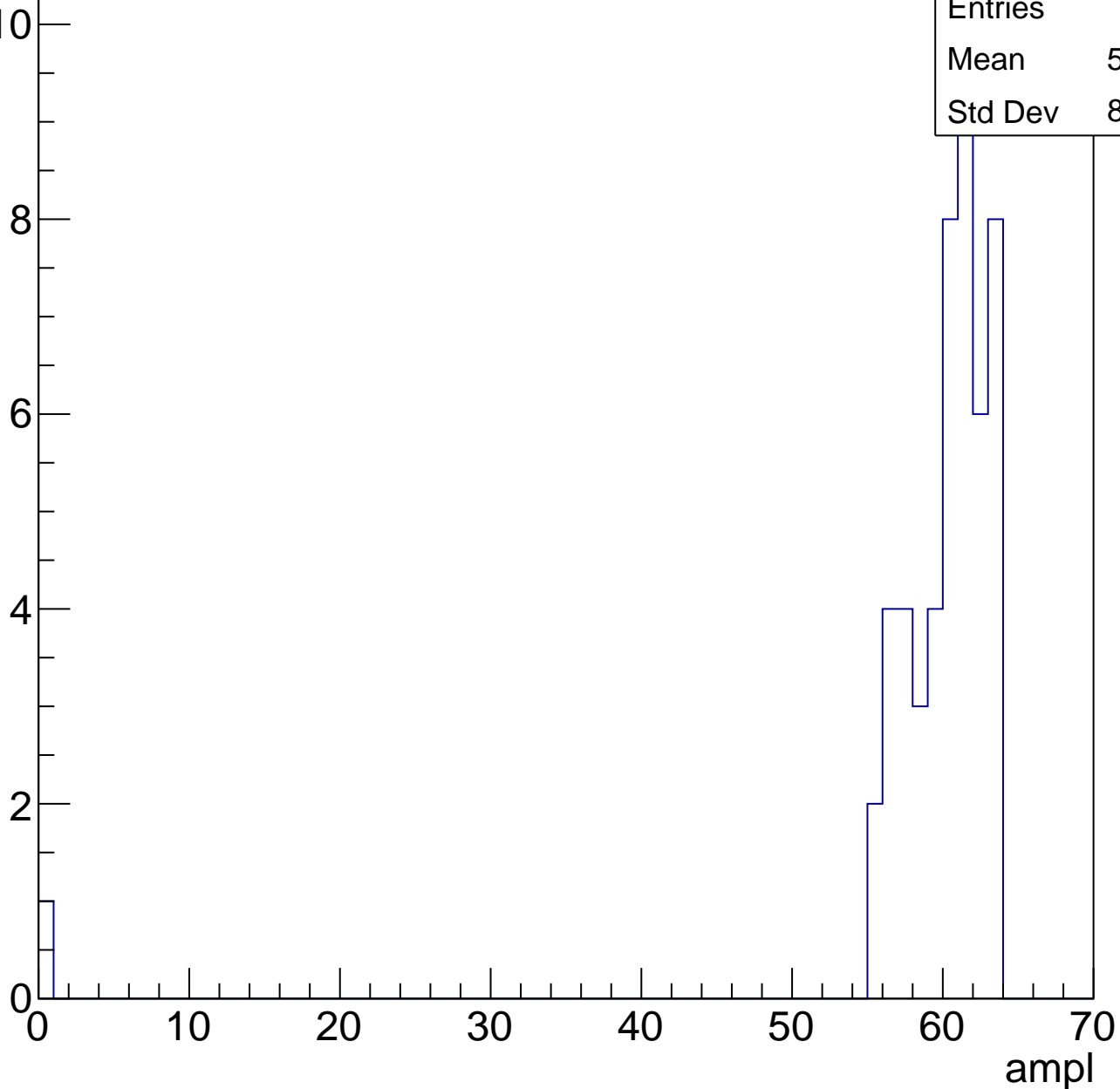
70

# B1L000S, U23-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.76
Std Dev	8.712



# B1L000S, U23-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	61
Std Dev	2



# B1L000S, U23-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U23-ch16, adc0

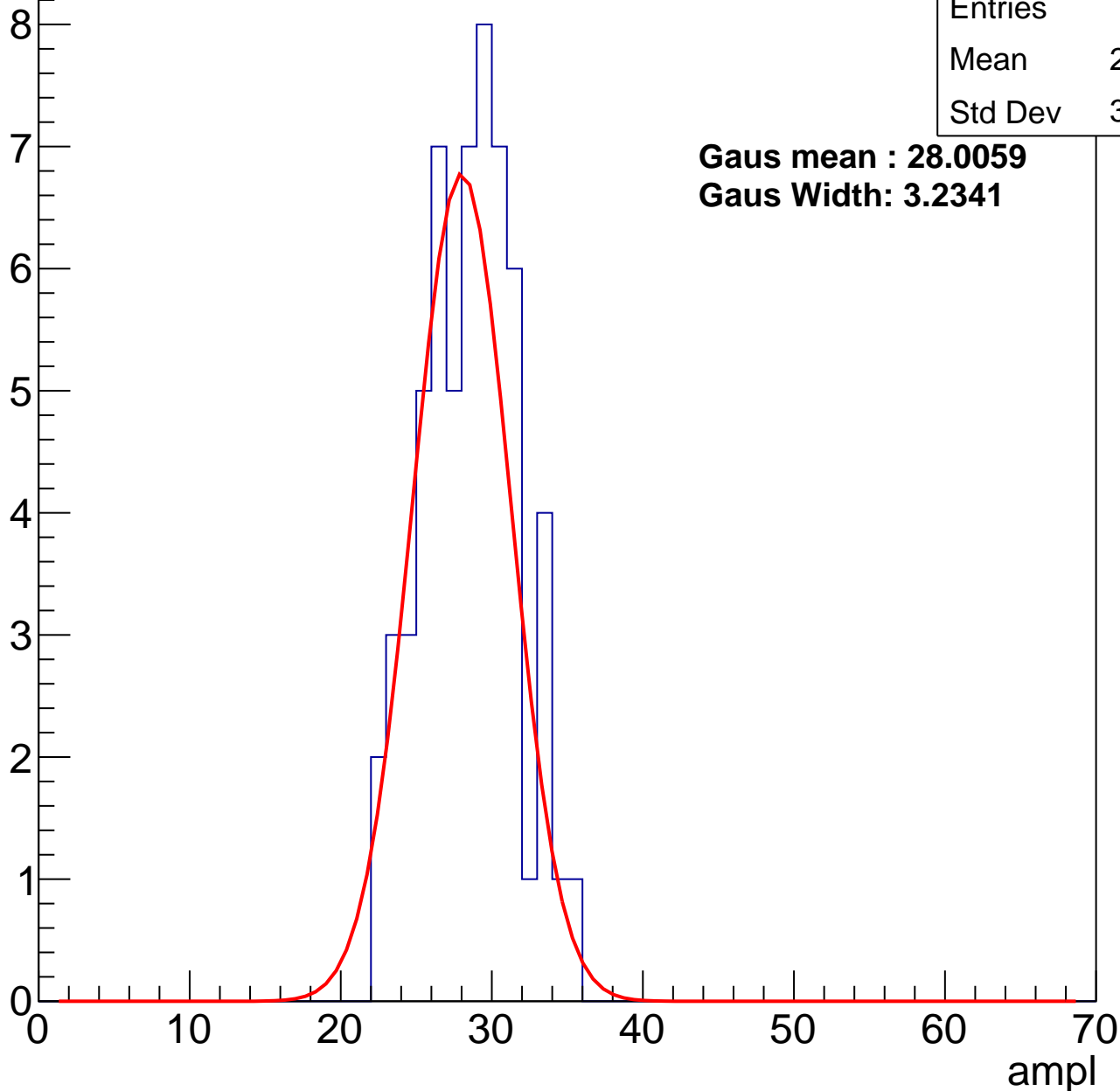
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.07
Std Dev	3.065

**Gaus mean : 28.0059**

**Gaus Width: 3.2341**



# B1L000S, U23-ch16, adc1

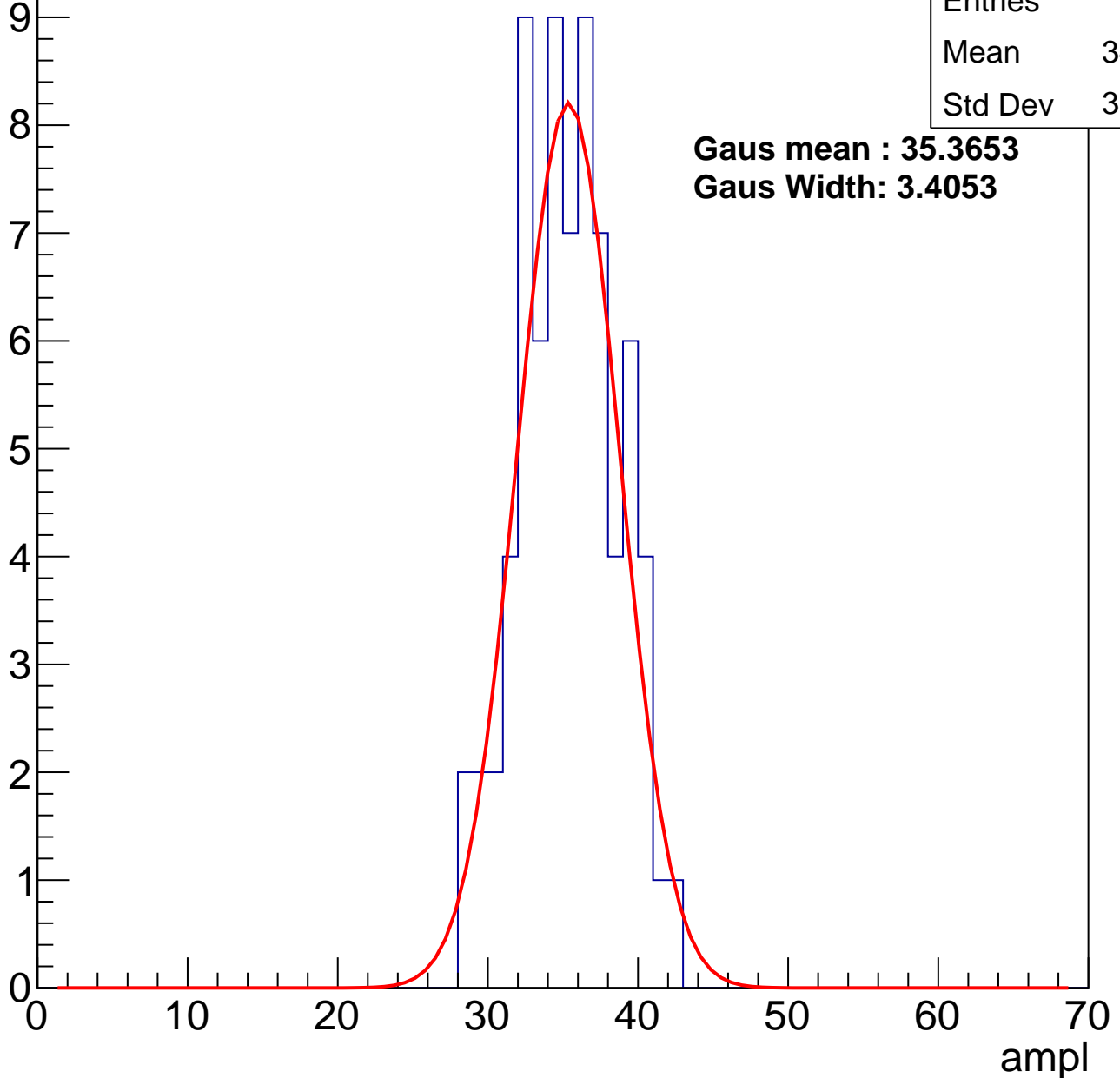
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	34.89
Std Dev	3.208

**Gaus mean : 35.3653**

**Gaus Width: 3.4053**



# B1L000S, U23-ch16, adc2

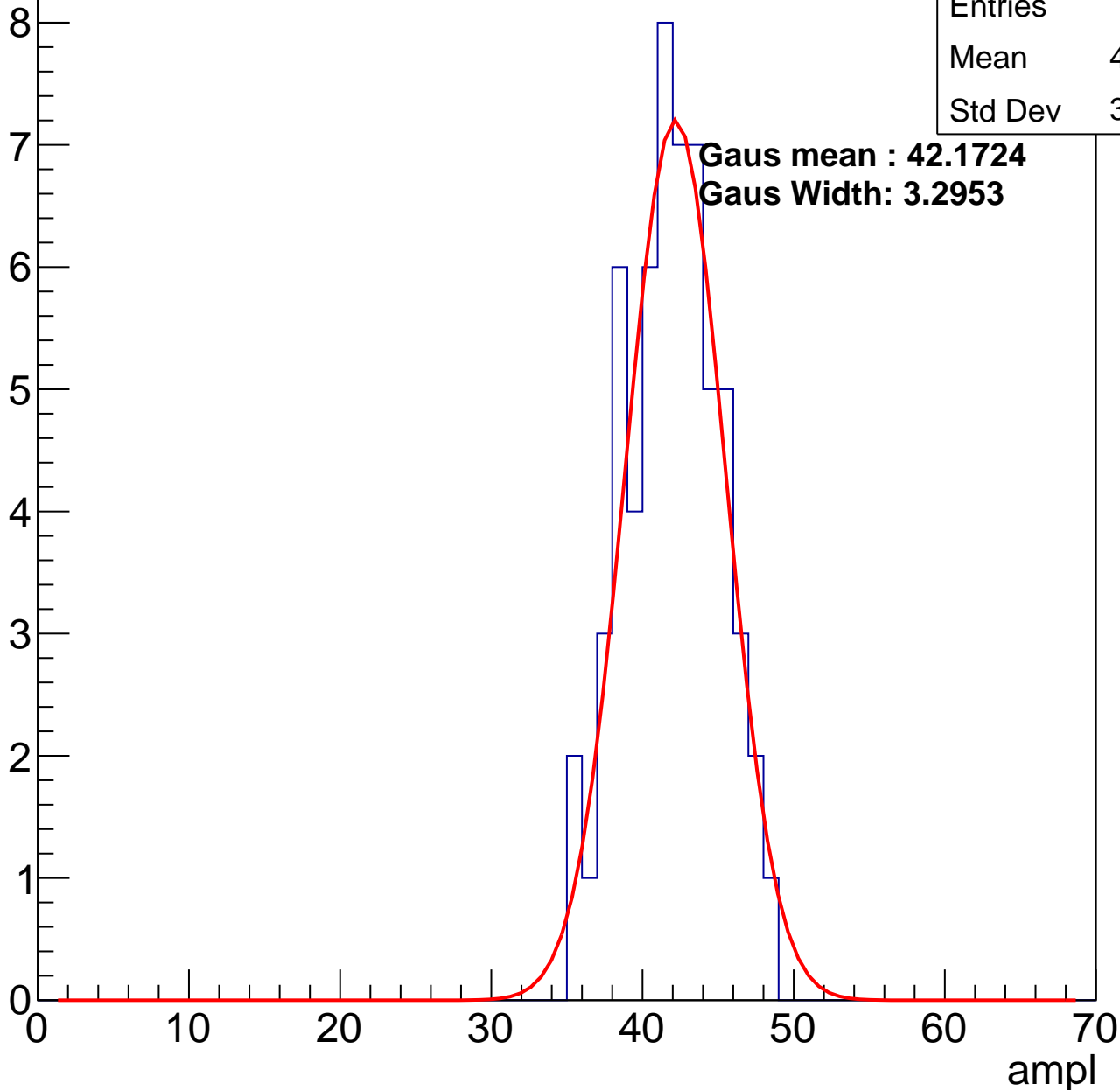
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.48
Std Dev	3.063

Gaus mean : 42.1724

Gaus Width: 3.2953

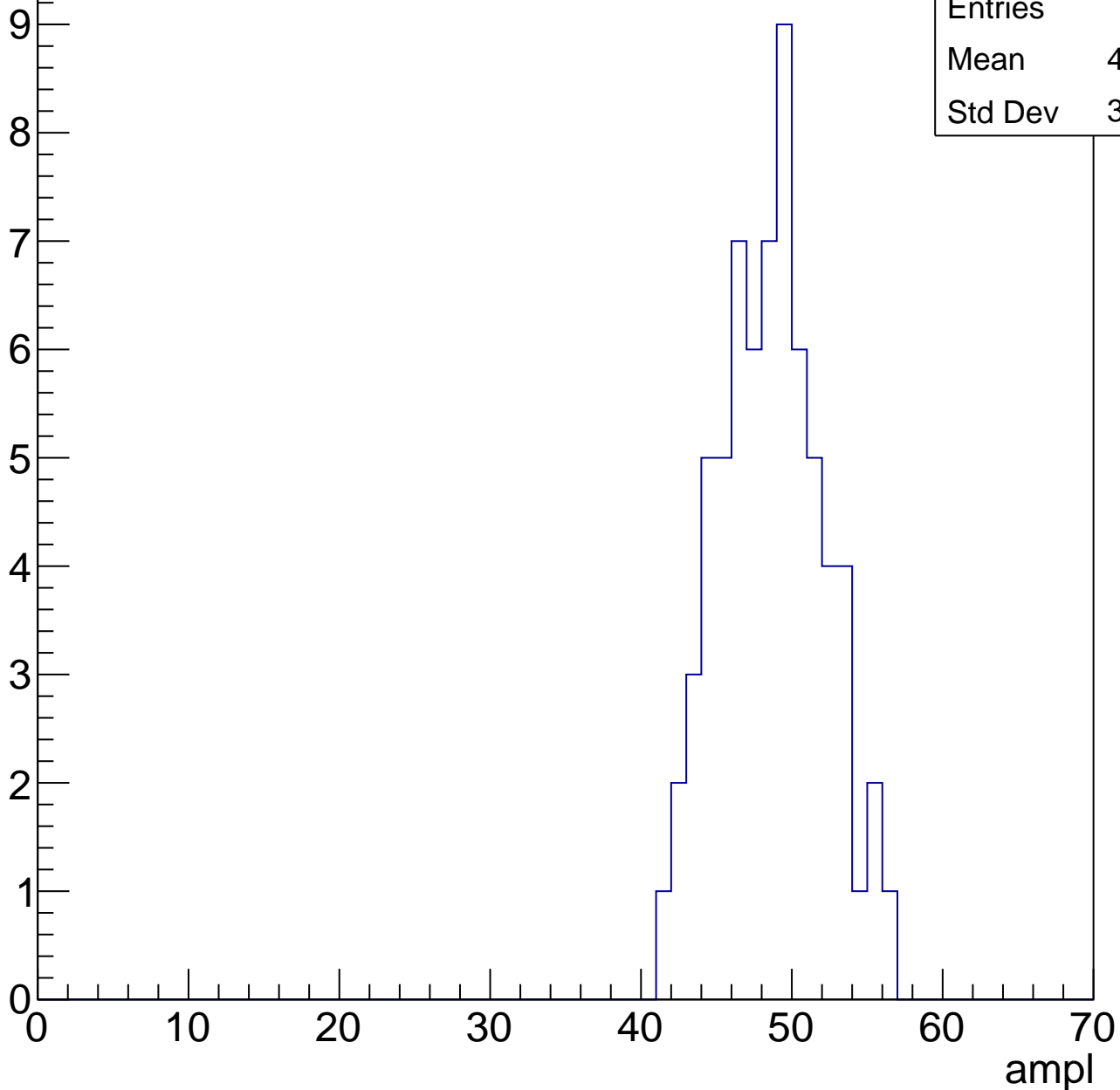


# B1L000S, U23-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

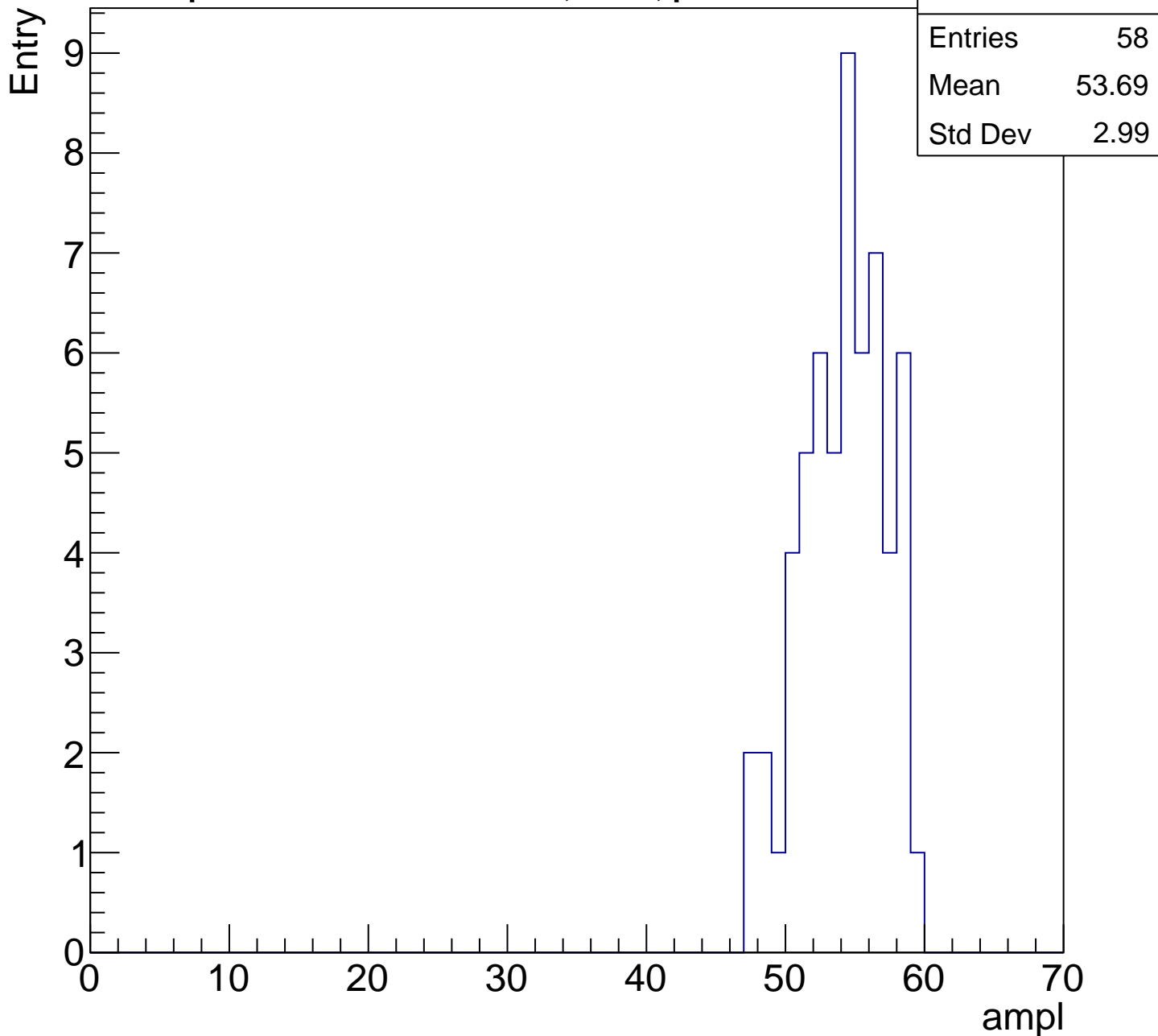
Entry

Entries	68
Mean	48.16
Std Dev	3.415



# B1L000S, U23-ch16, adc4

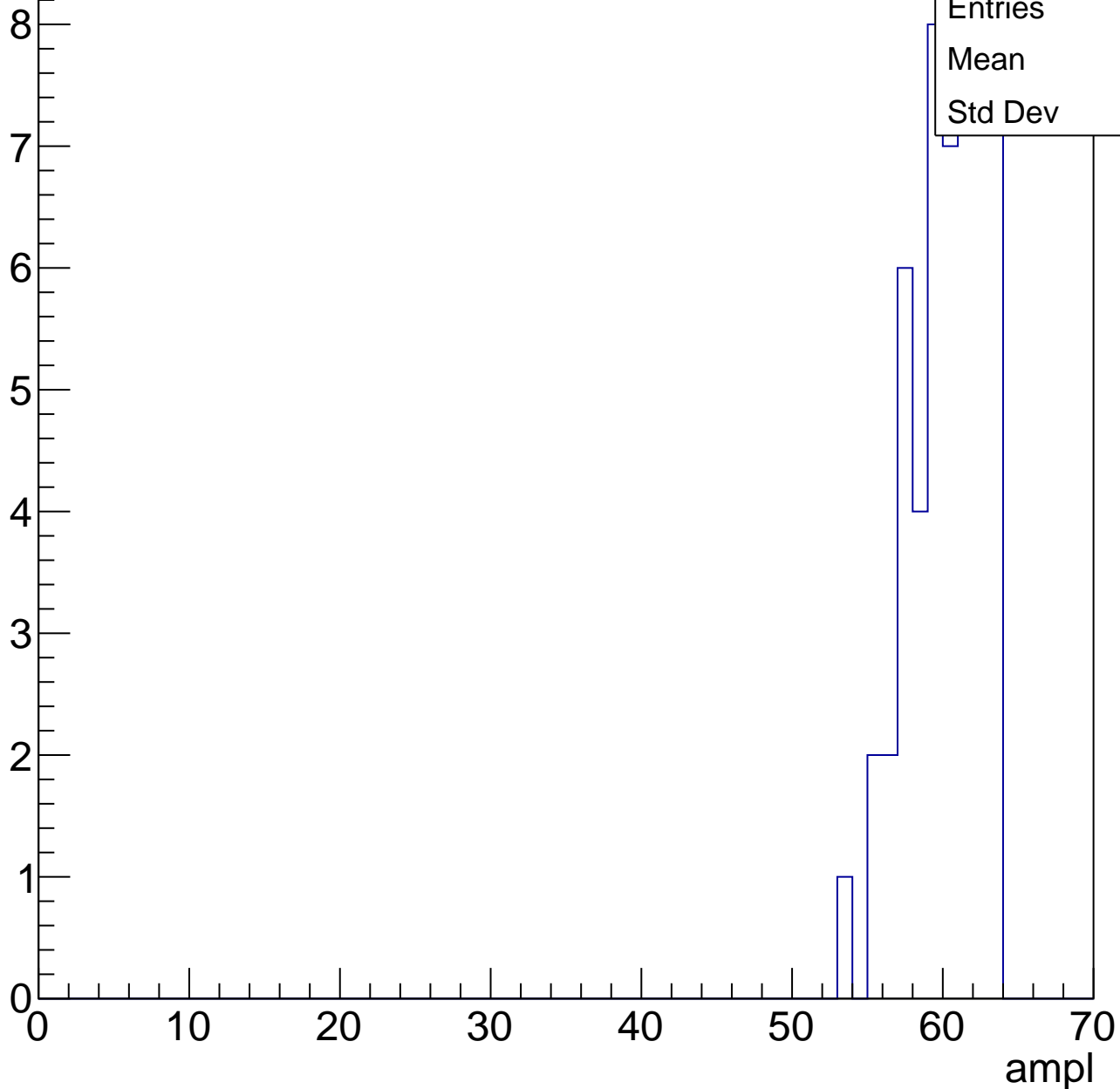
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	54
Mean	59.8
Std Dev	2.43

# B1L000S, U23-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

3

2.5

2

1.5

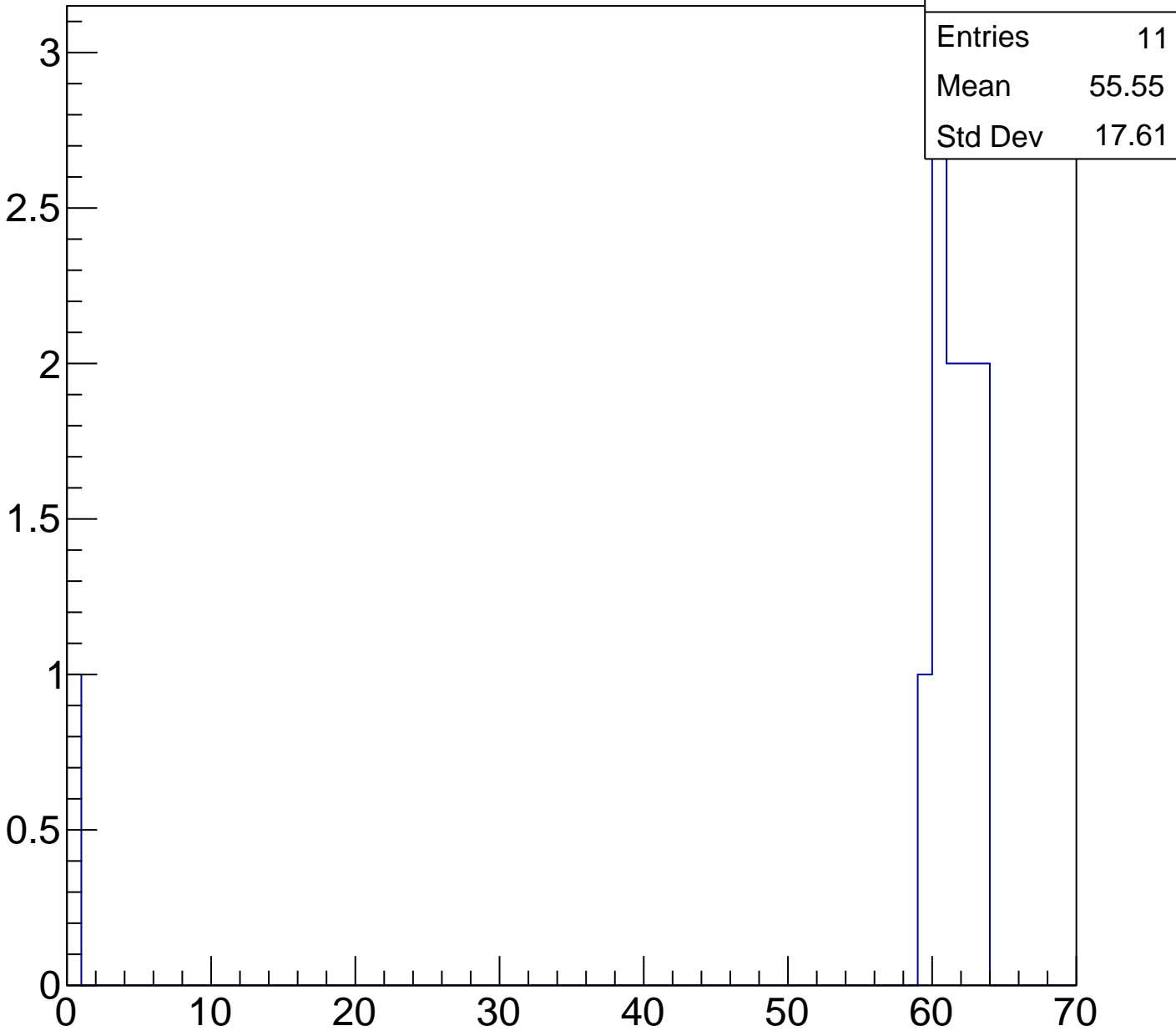
1

0.5

0

ampl

Entries	11
Mean	55.55
Std Dev	17.61





# B1L000S, U23-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U23-ch17, adc0

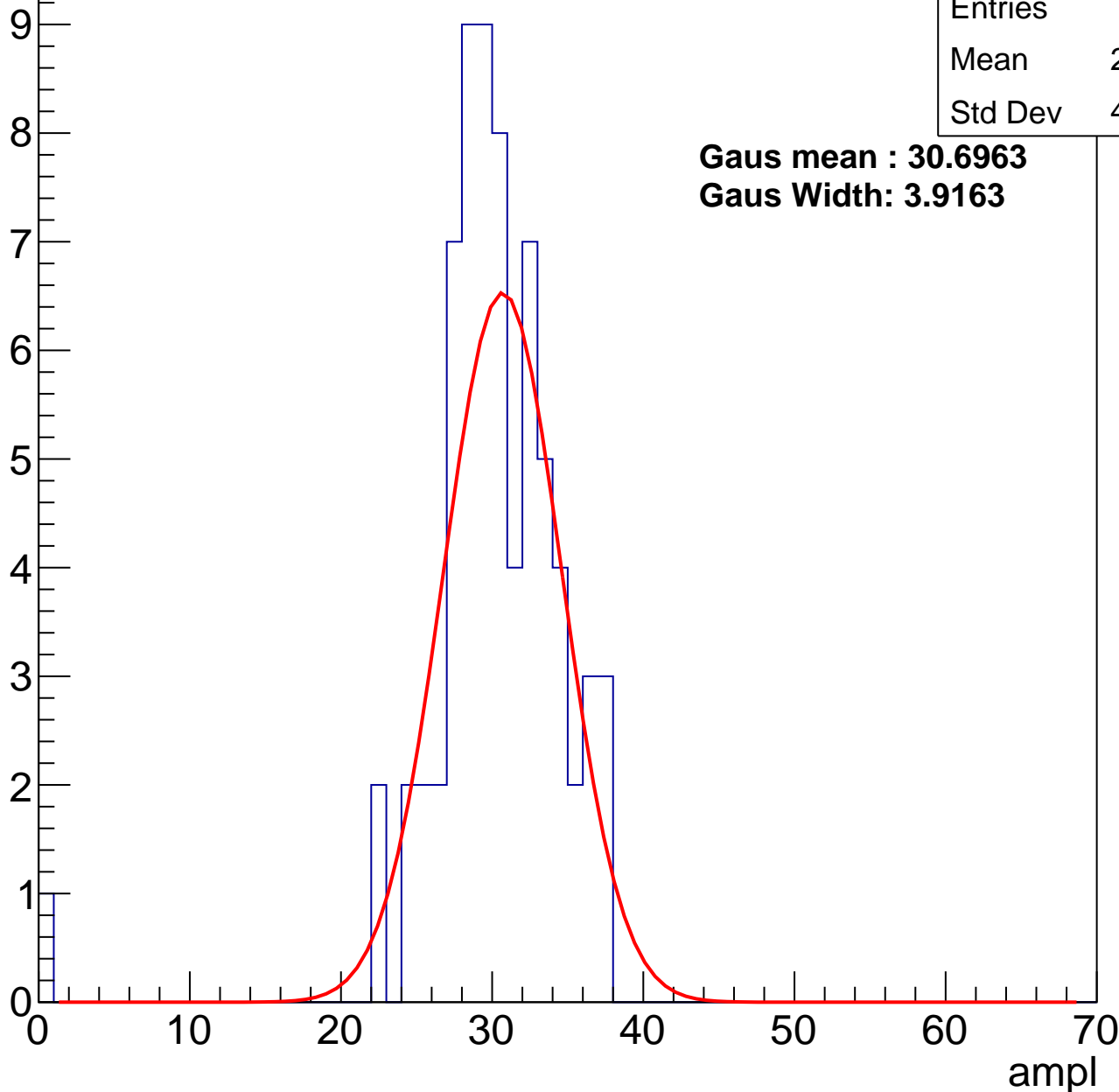
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	29.63
Std Dev	4.966

**Gaus mean : 30.6963**

**Gaus Width: 3.9163**



# B1L000S, U23-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	36.51
Std Dev	3.651

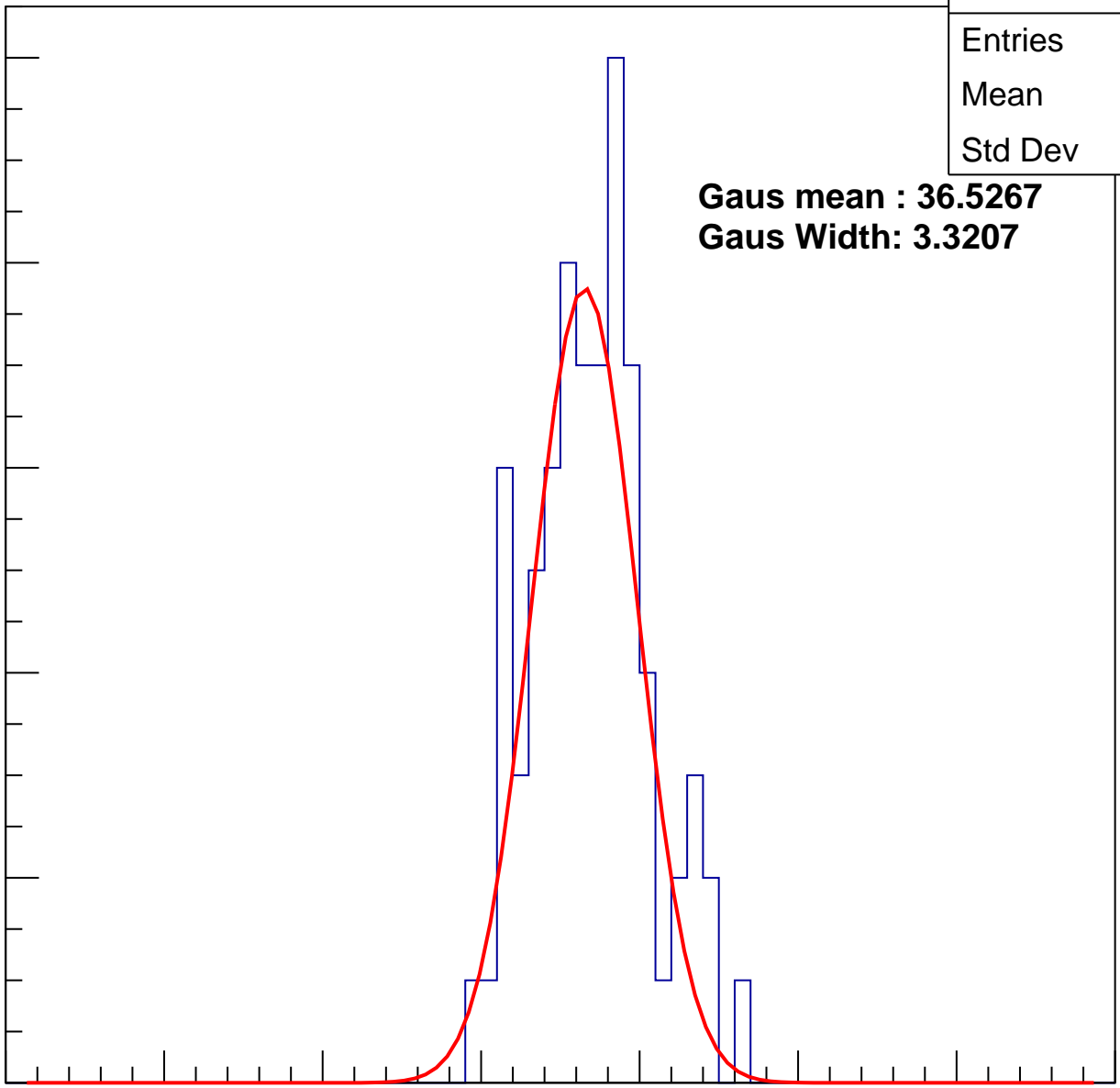
**Gaus mean : 36.5267**

**Gaus Width: 3.3207**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch17, adc2

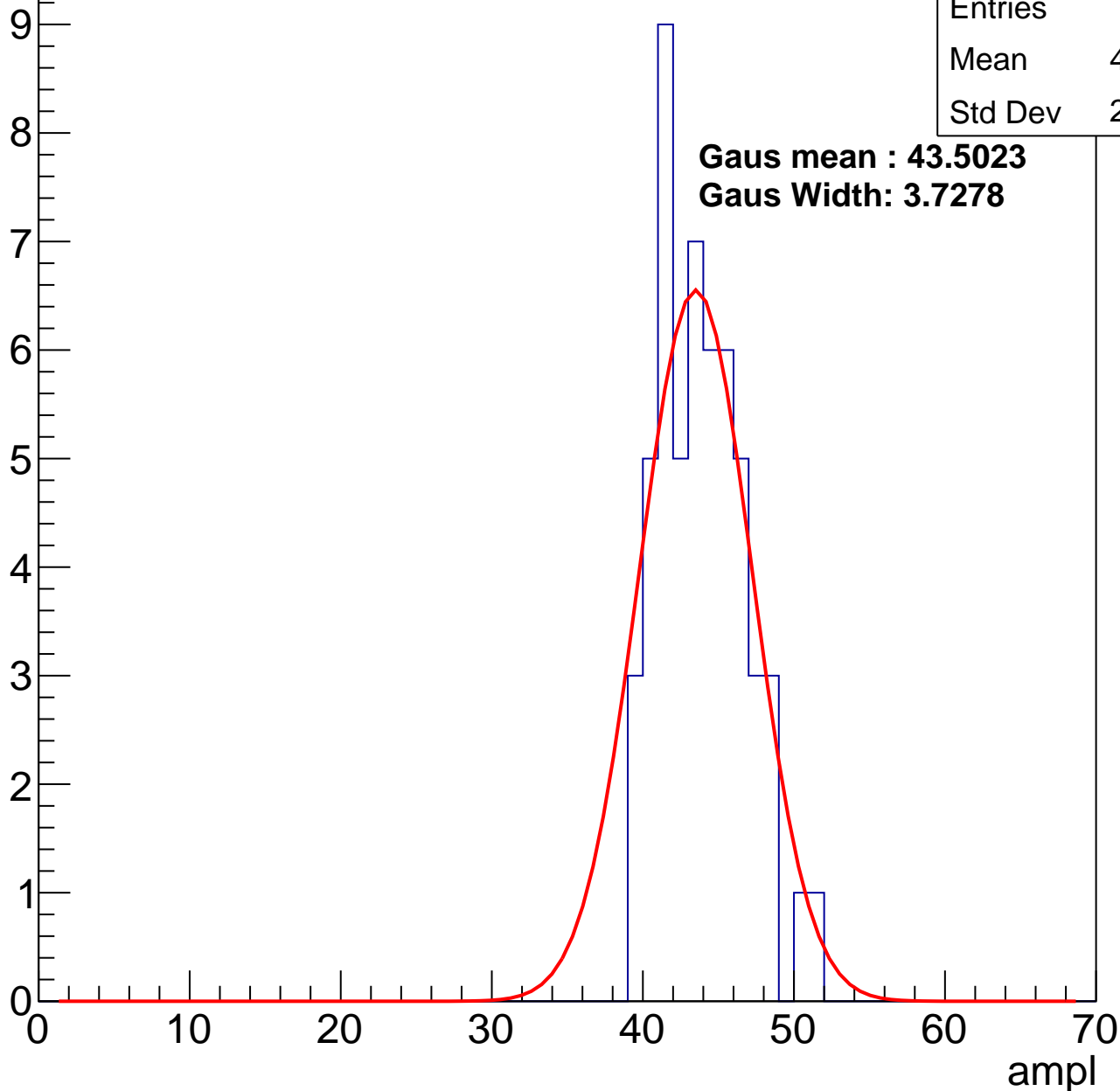
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	43.46
Std Dev	2.833

**Gaus mean : 43.5023**

**Gaus Width: 3.7278**

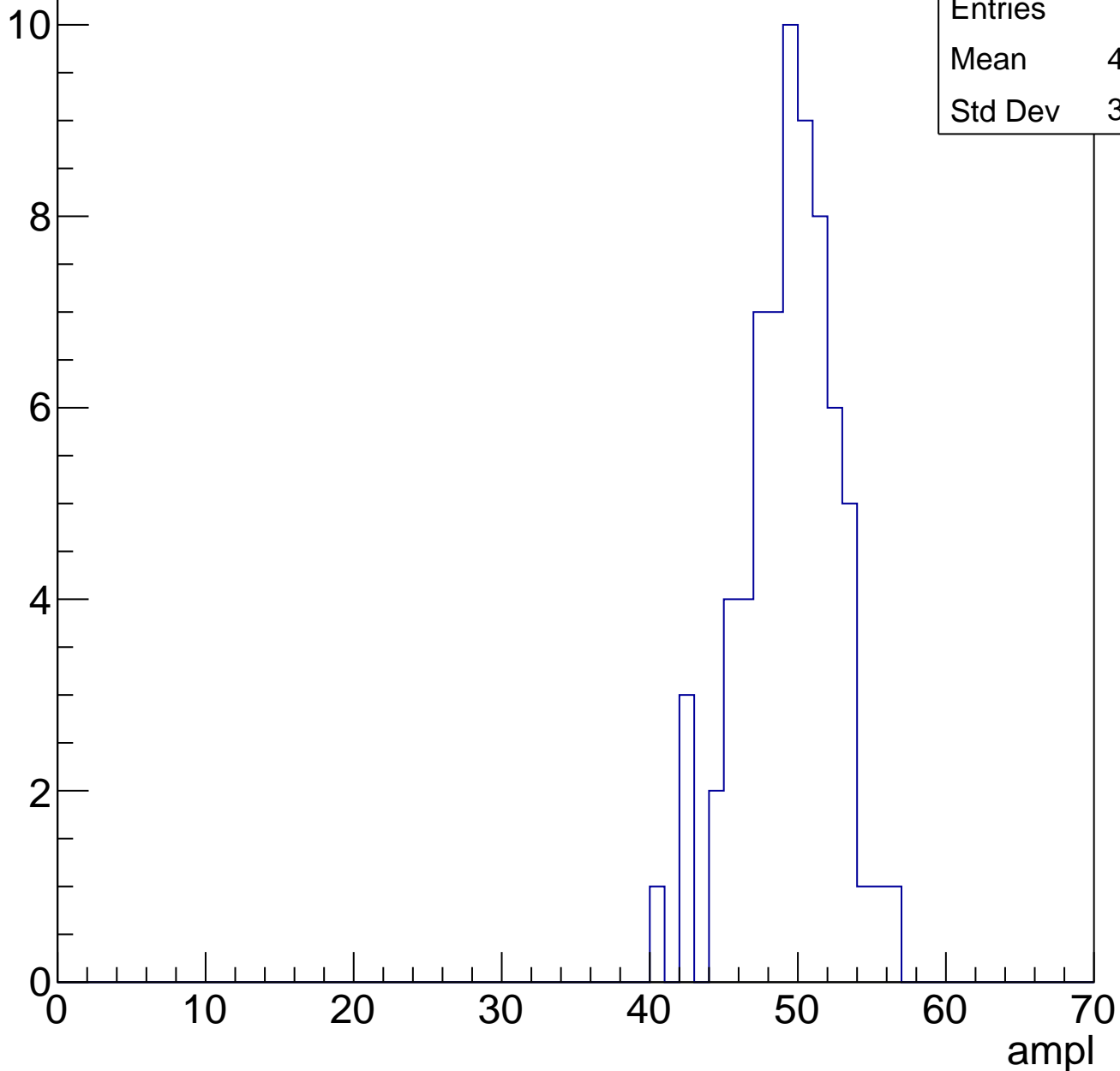


# B1L000S, U23-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

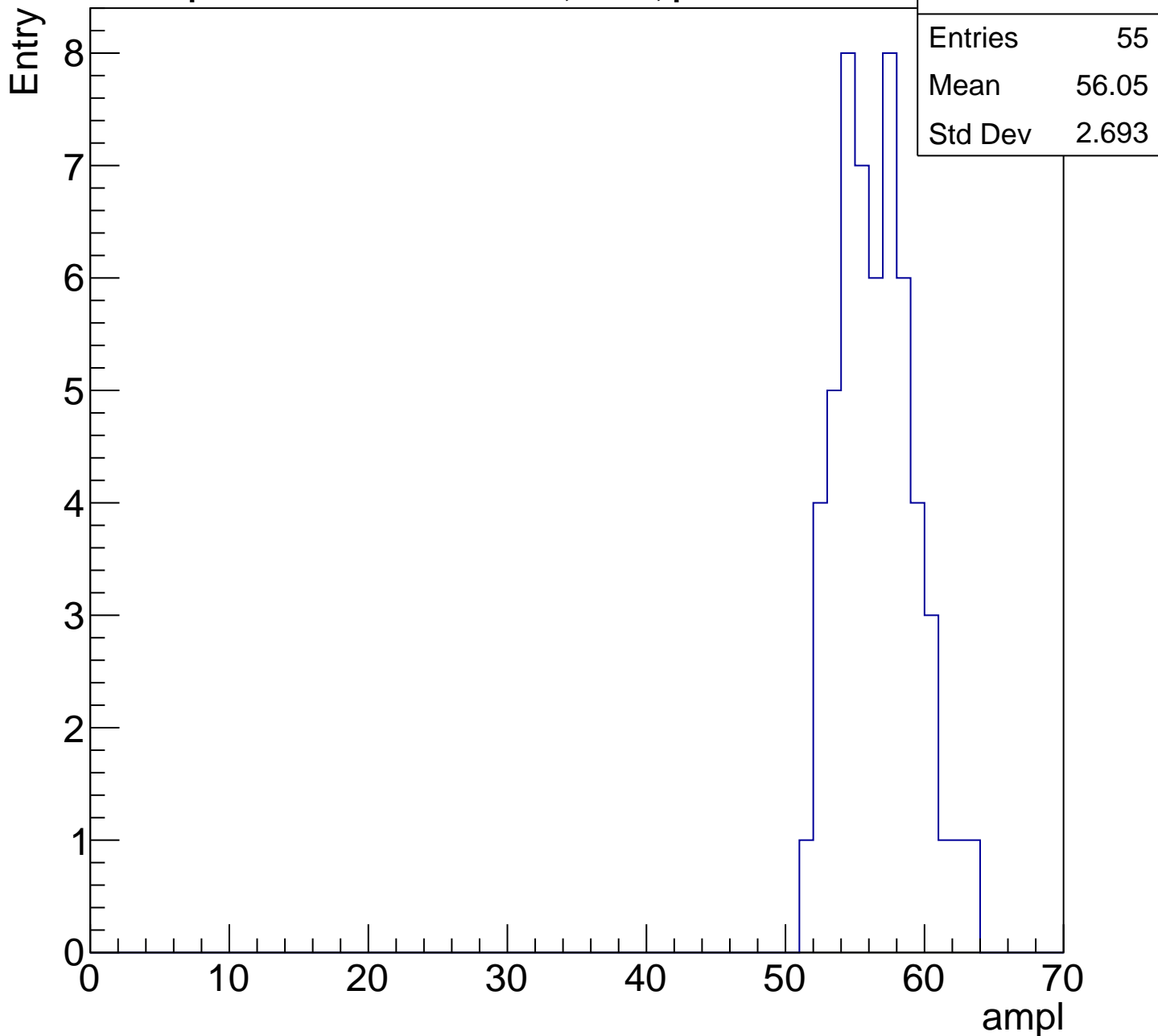
Entries	69
Mean	48.88
Std Dev	3.178

Entry



# B1L000S, U23-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

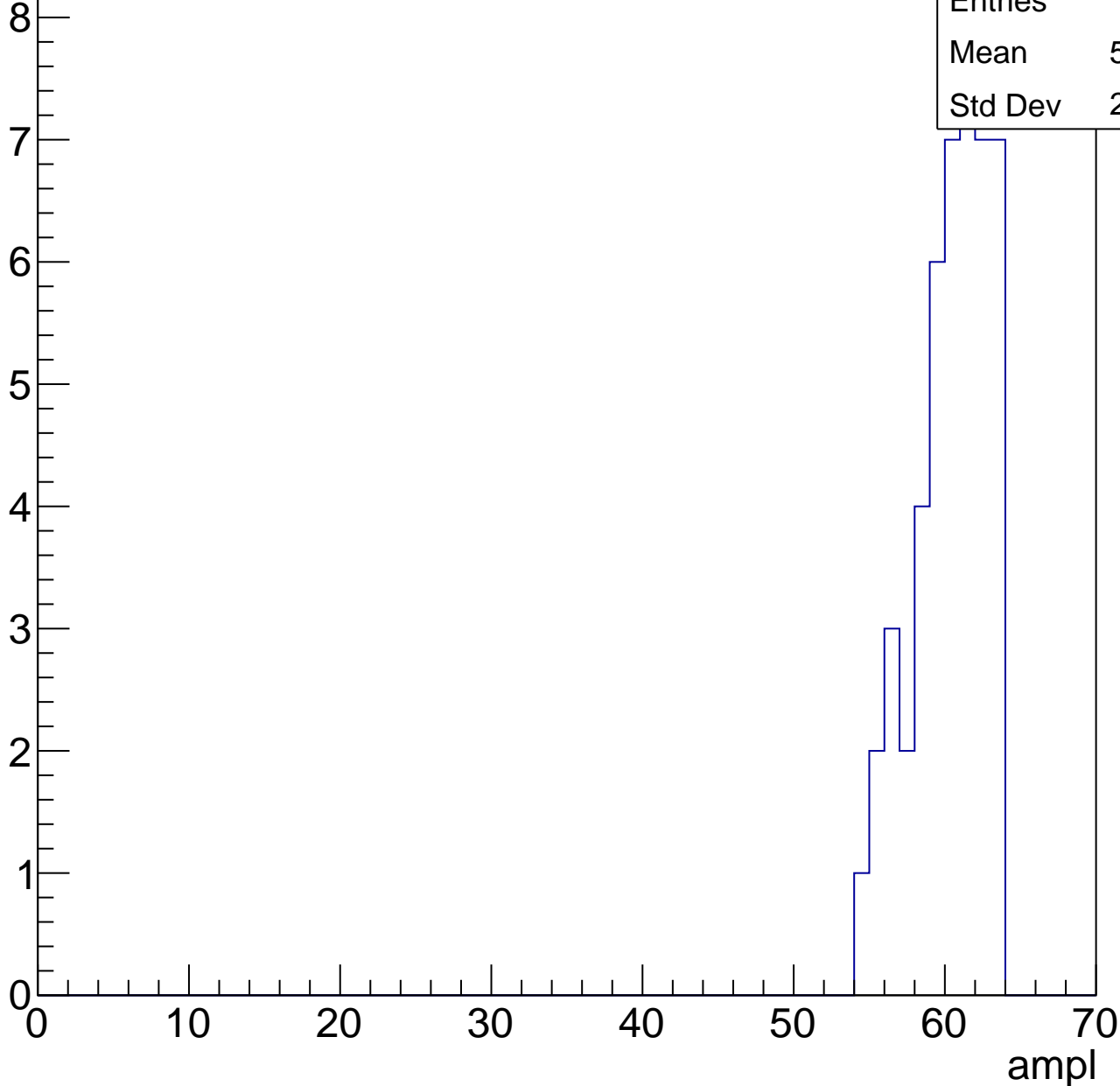


# B1L000S, U23-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	59.89
Std Dev	2.408



# B1L000S, U23-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch18, adc0

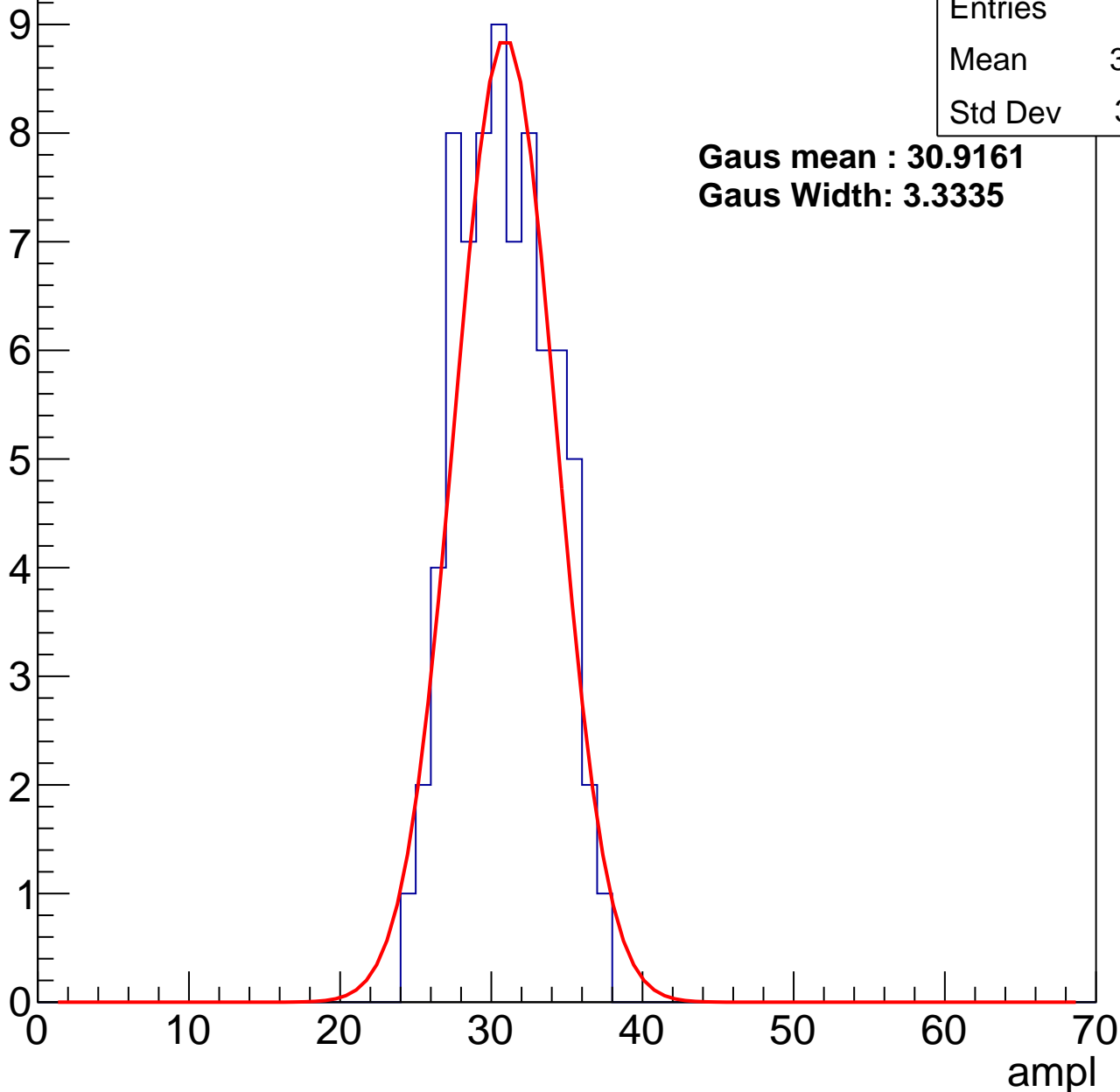
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	30.42
Std Dev	3.031

**Gaus mean : 30.9161**

**Gaus Width: 3.3335**



# B1L000S, U23-ch18, adc1

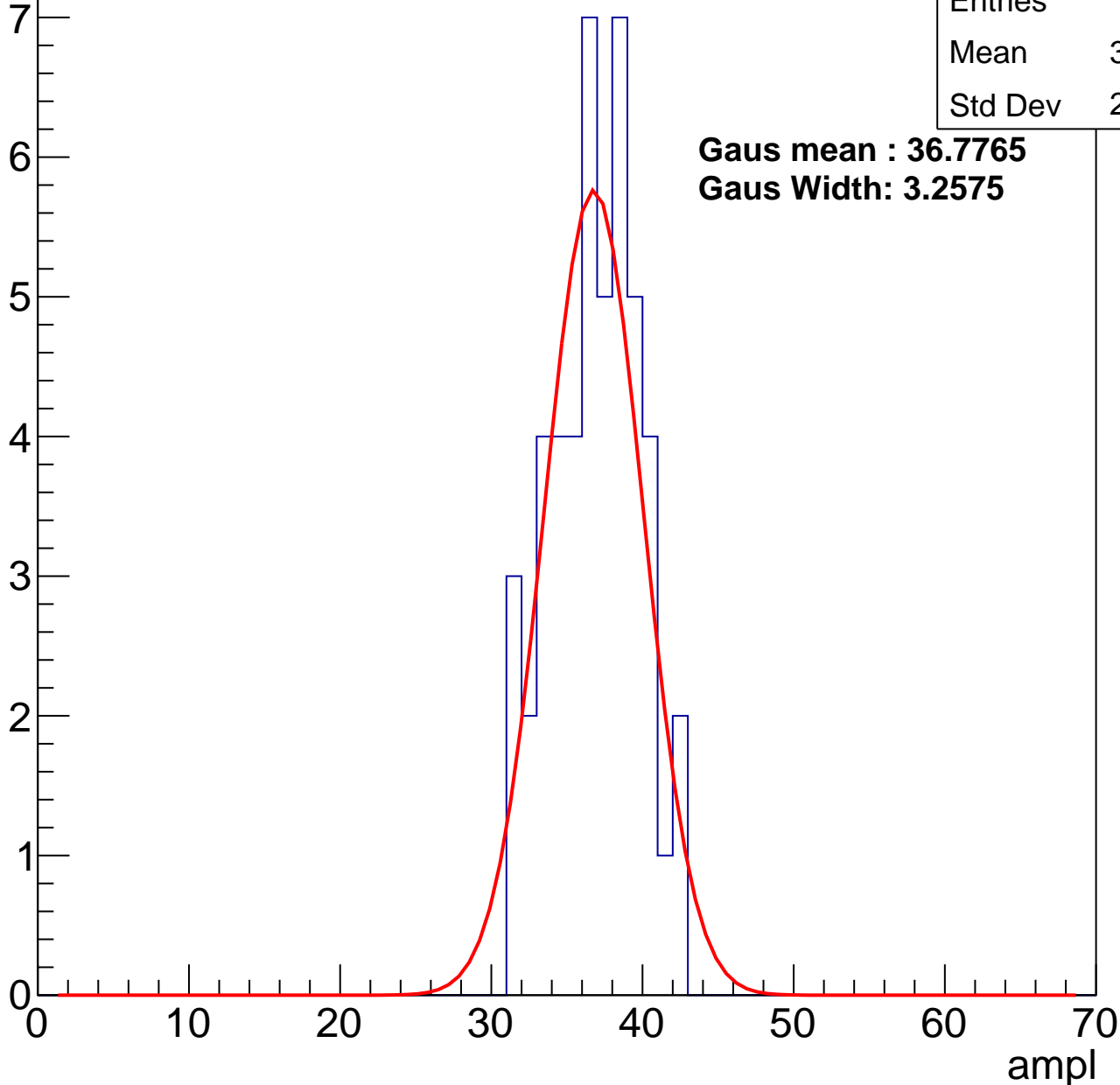
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	36.42
Std Dev	2.864

**Gaus mean : 36.7765**

**Gaus Width: 3.2575**



# B1L000S, U23-ch18, adc2

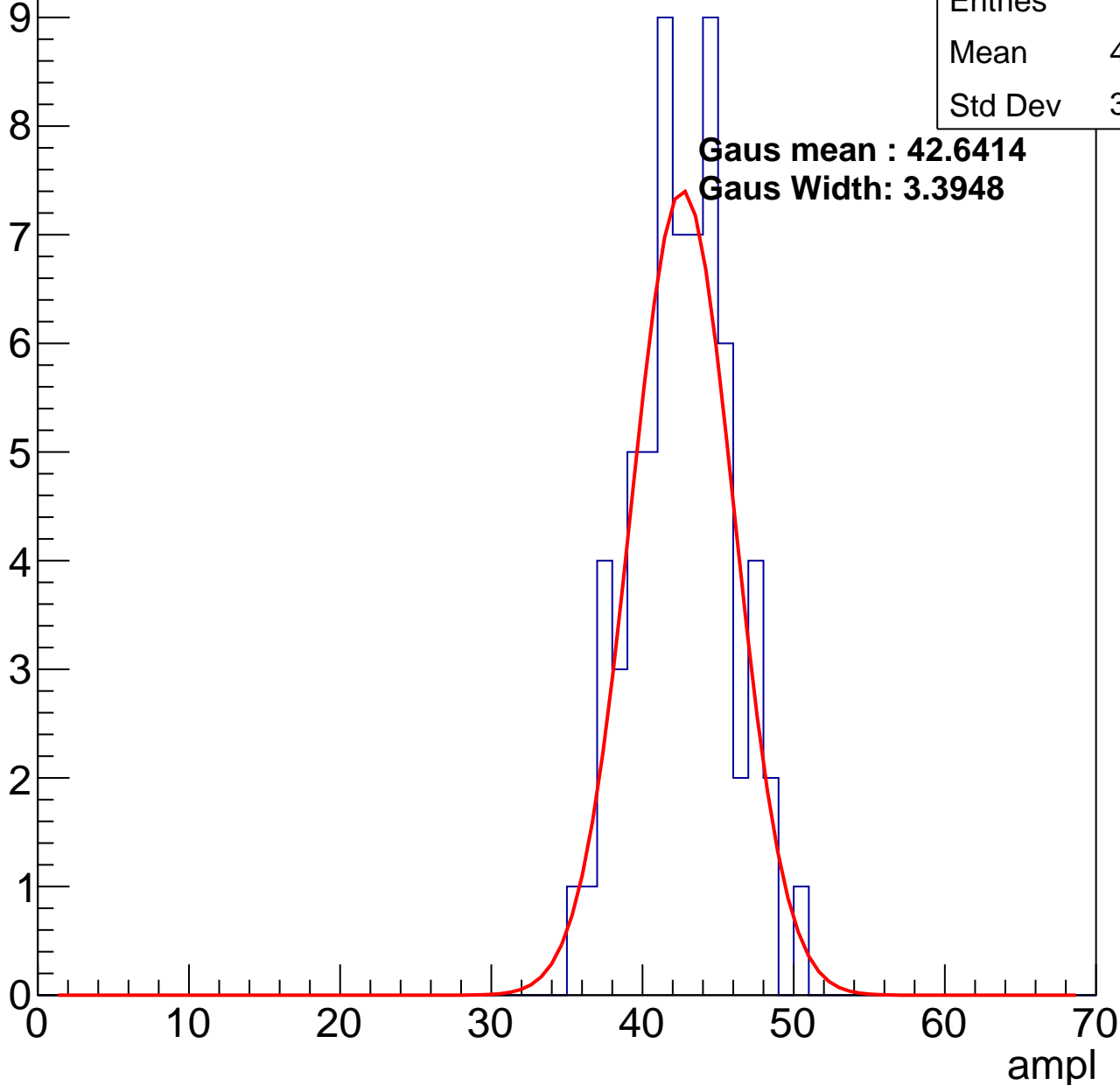
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	42.18
Std Dev	3.186

**Gaus mean : 42.6414**

**Gaus Width: 3.3948**

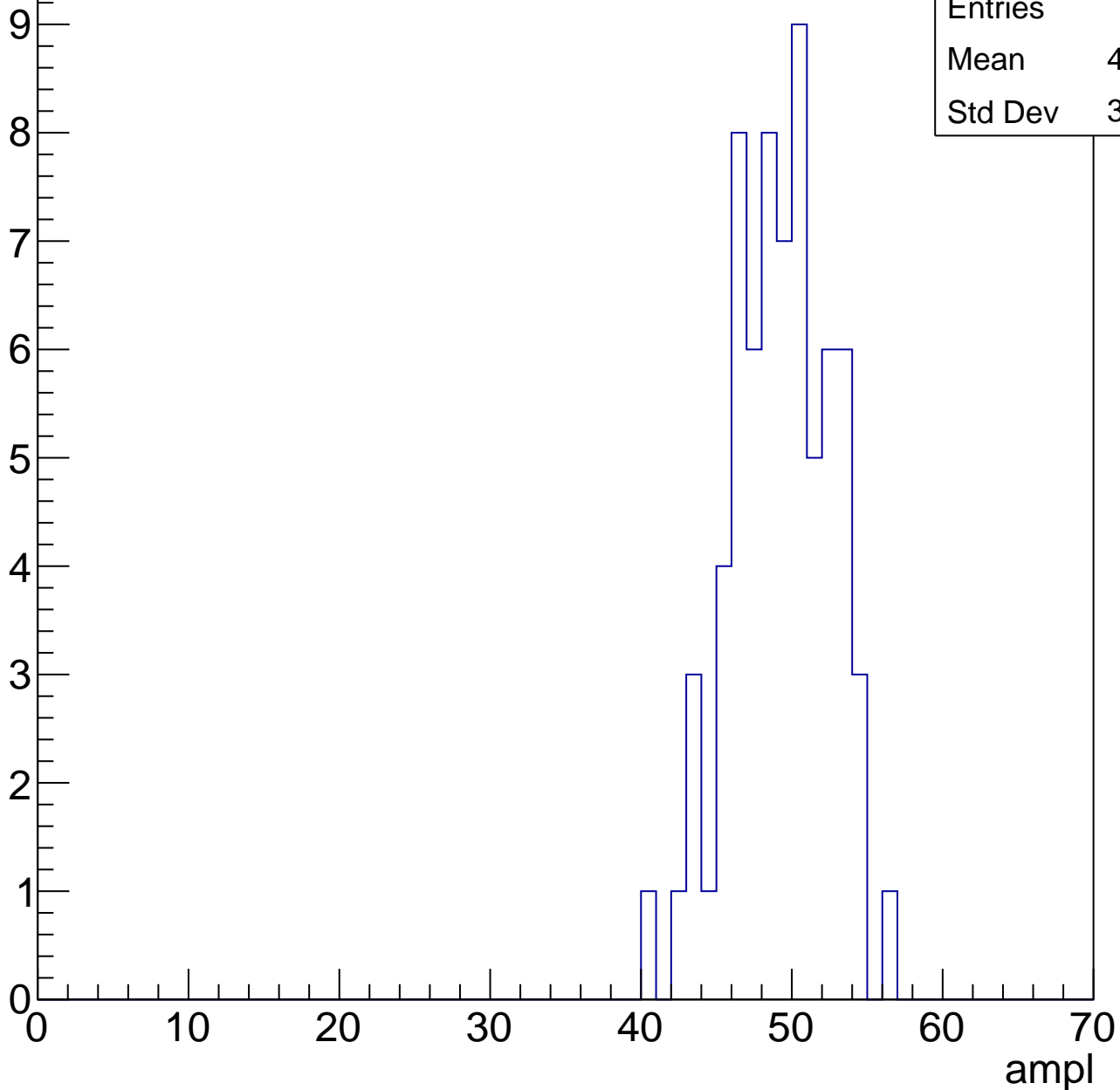


# B1L000S, U23-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.77
Std Dev	3.253

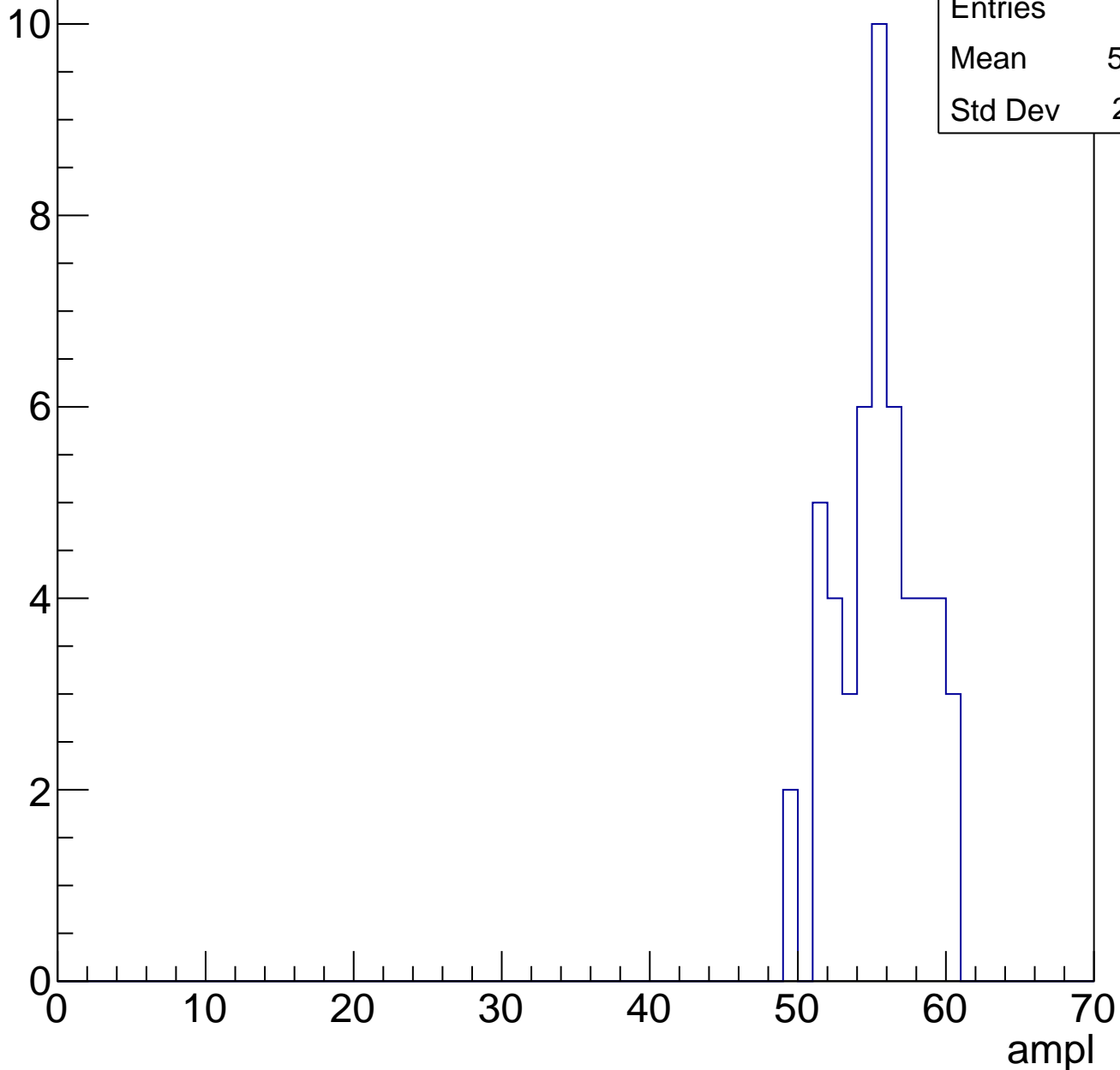


# B1L000S, U23-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	51
Mean	55.02
Std Dev	2.811

Entry

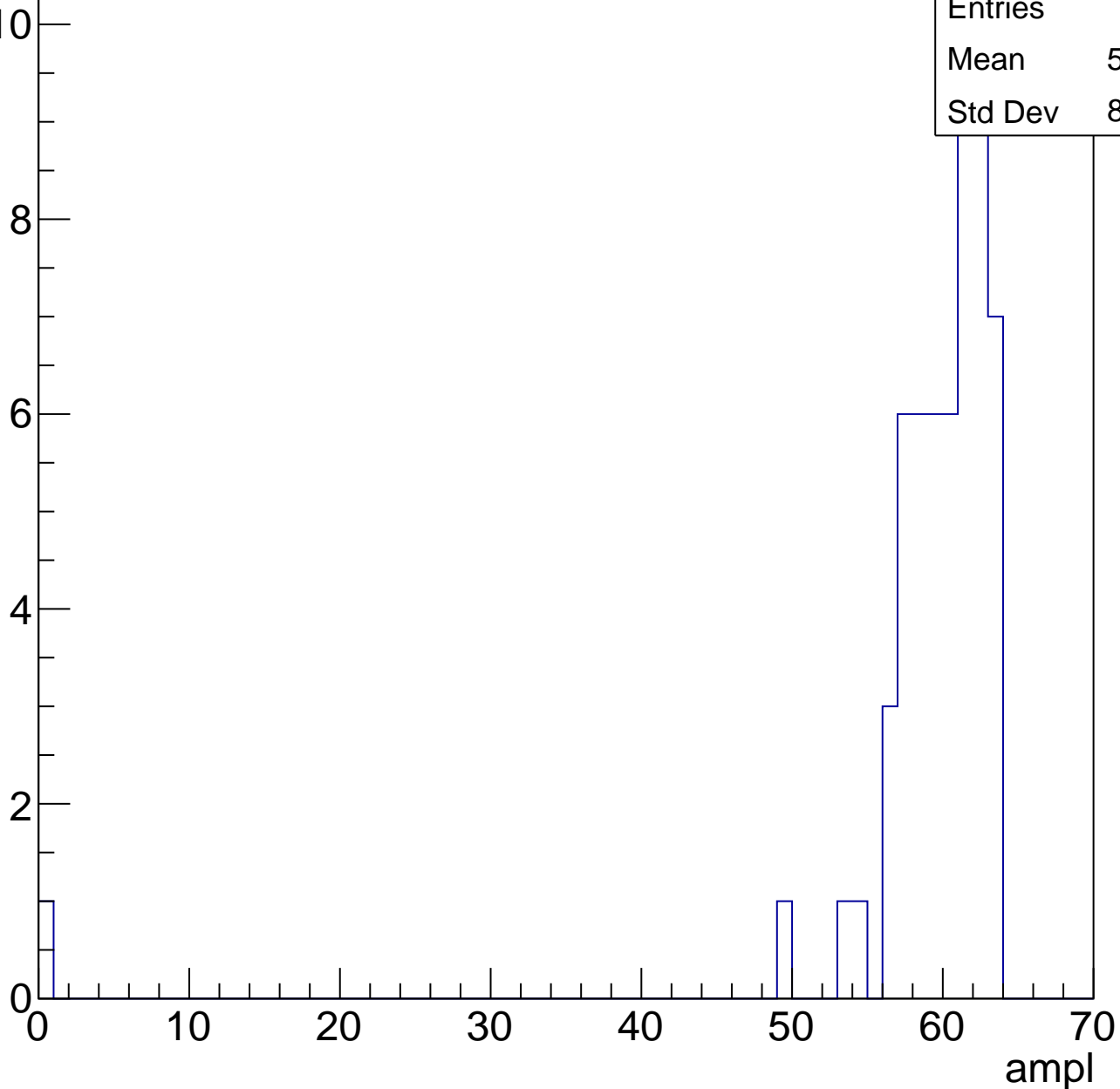


# B1L000S, U23-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	58.54
Std Dev	8.299



# B1L000S, U23-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U23-ch19, adc0

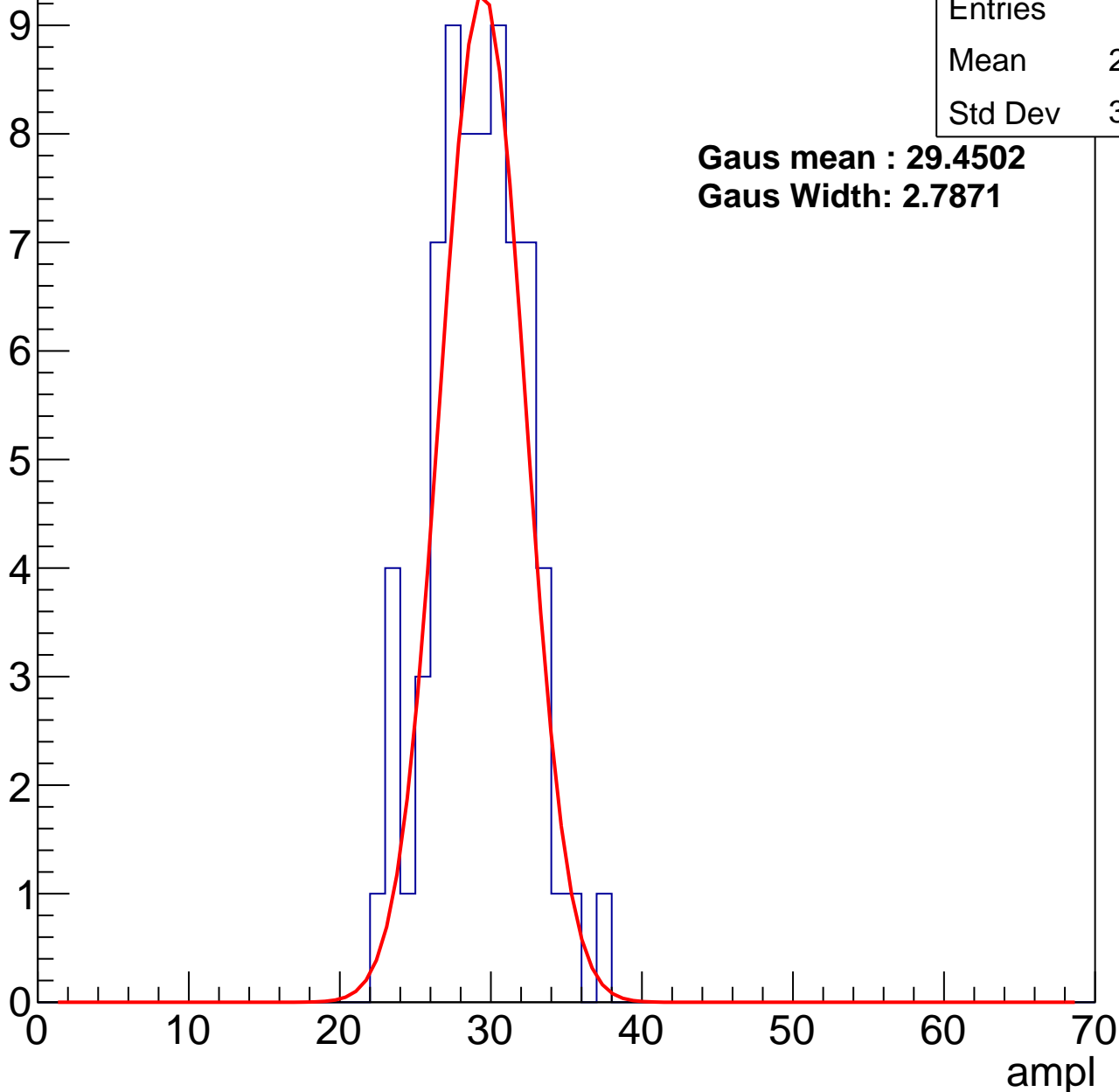
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	28.77
Std Dev	3.045

**Gaus mean : 29.4502**

**Gaus Width: 2.7871**



# B1L000S, U23-ch19, adc1

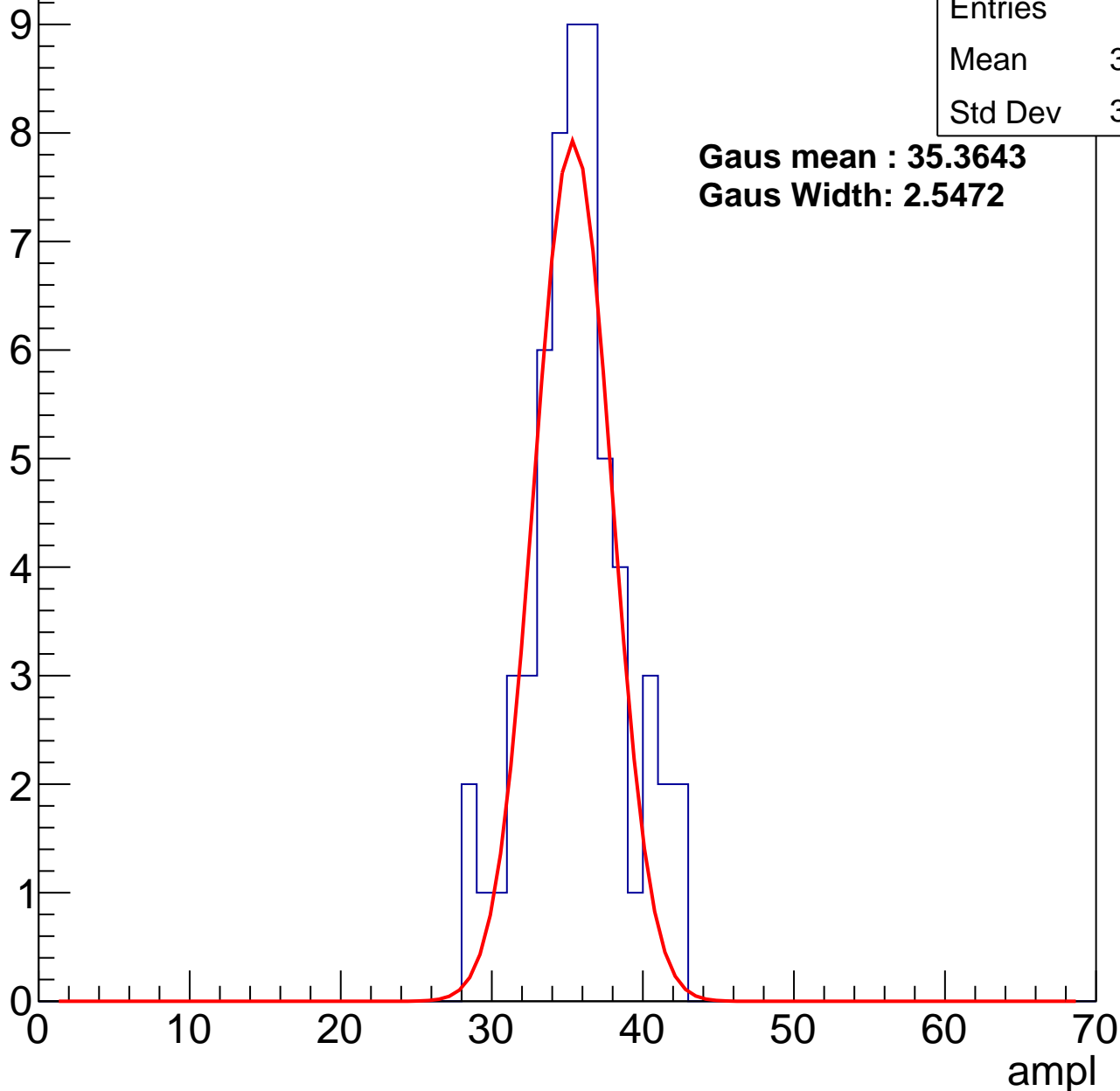
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	35.17
Std Dev	3.163

**Gaus mean : 35.3643**

**Gaus Width: 2.5472**



# B1L000S, U23-ch19, adc2

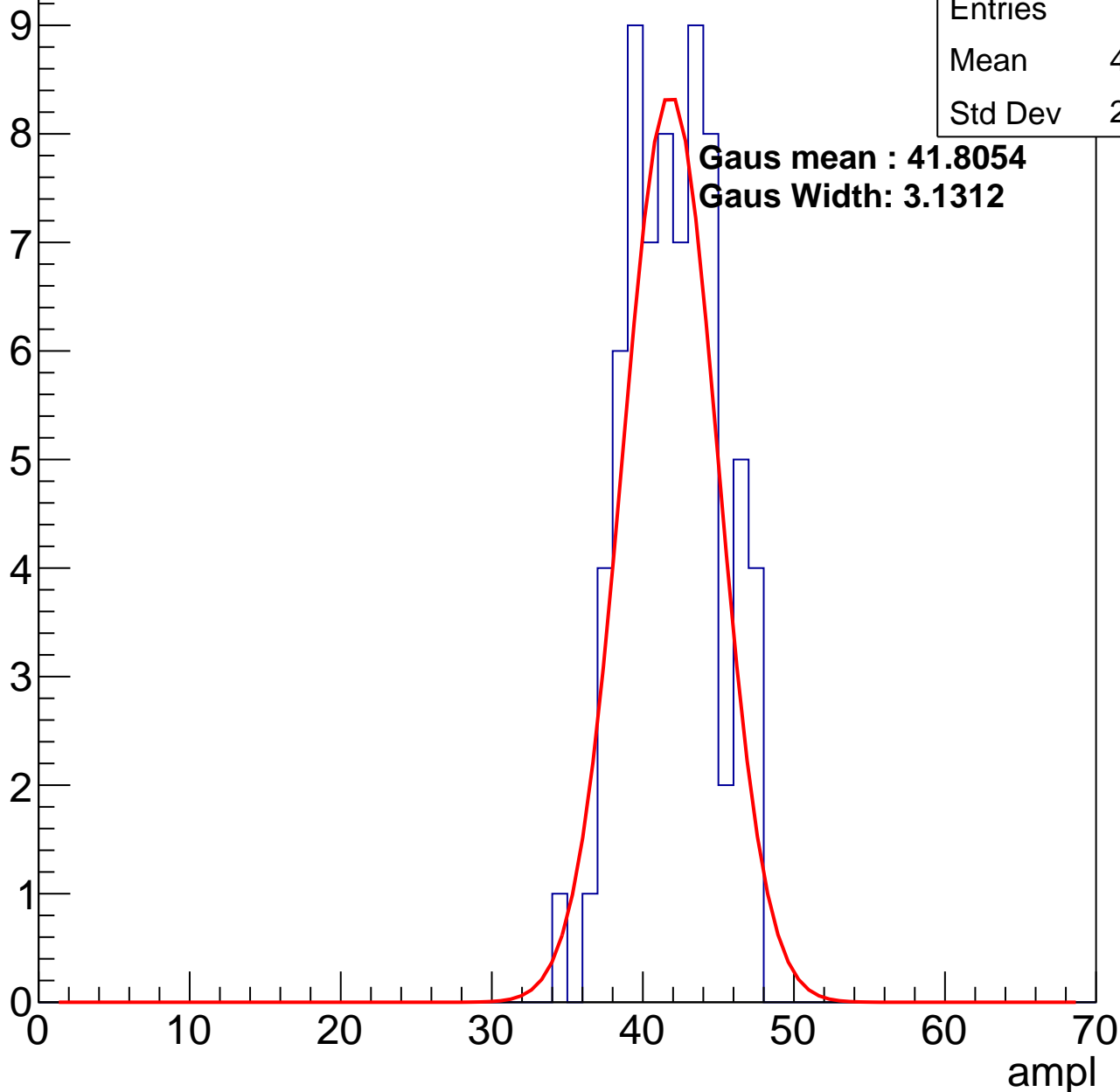
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	41.49
Std Dev	2.988

**Gaus mean : 41.8054**

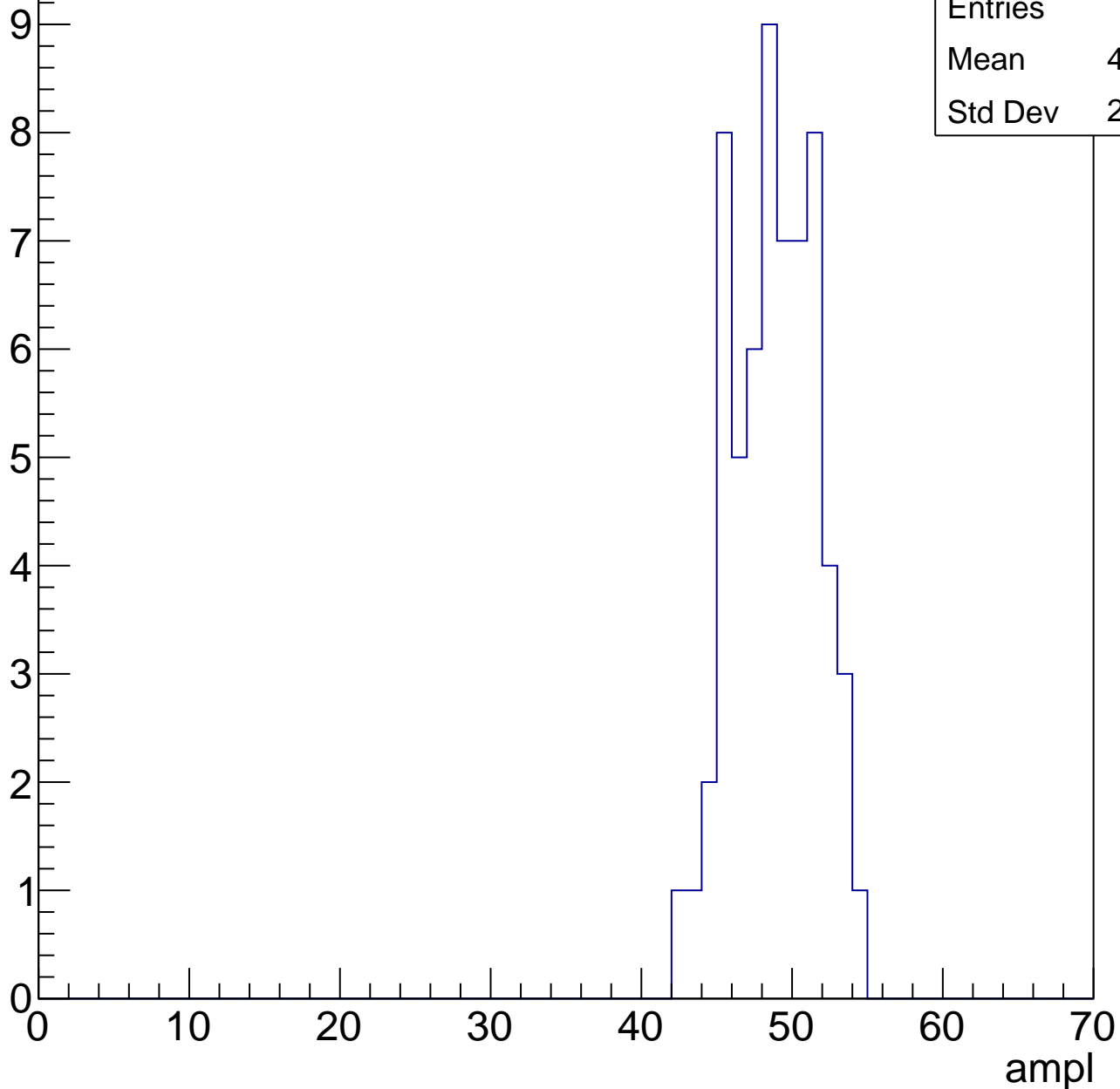
**Gaus Width: 3.1312**



# B1L000S, U23-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

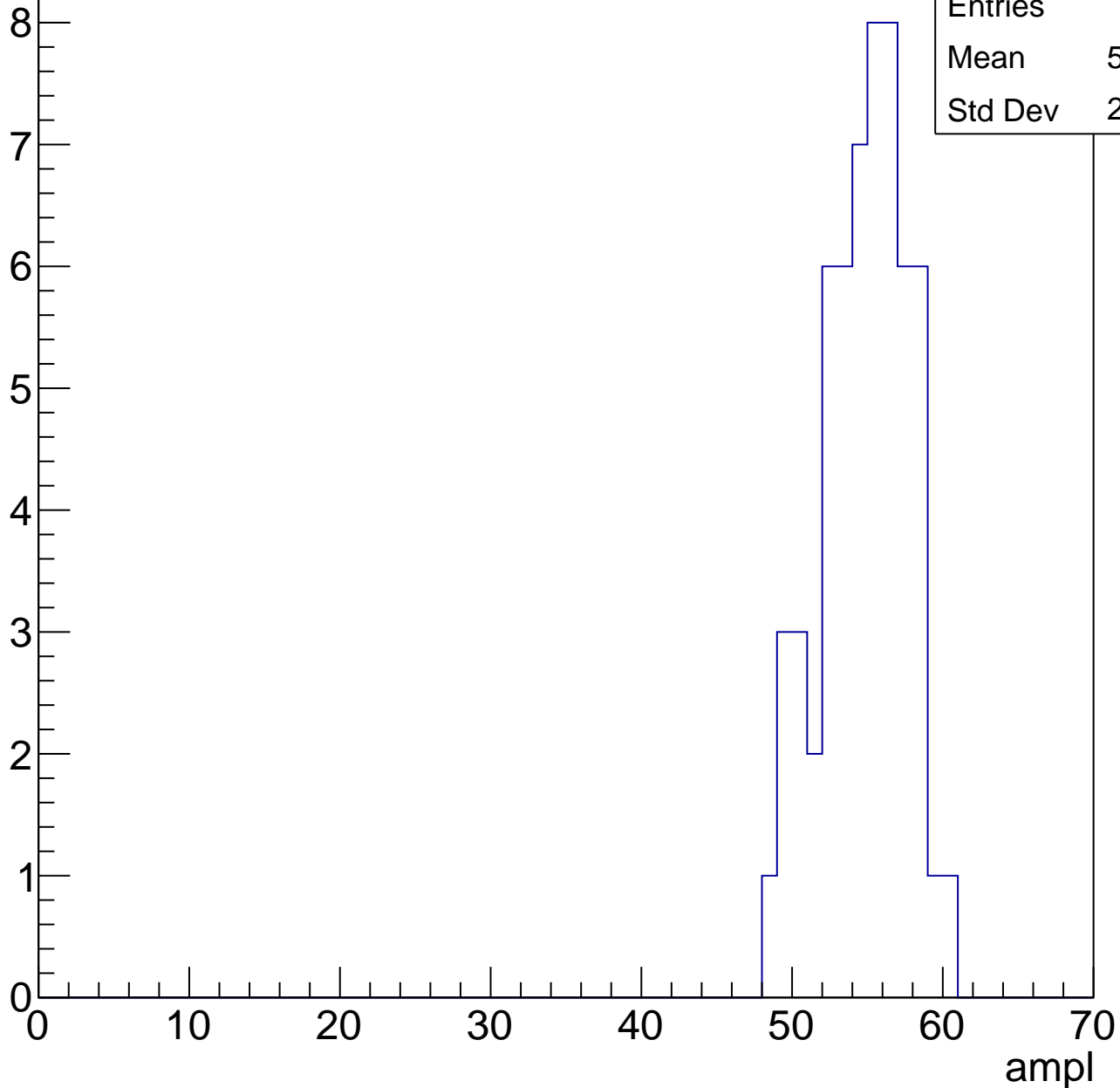


Entries	62
Mean	48.37
Std Dev	2.737

# B1L000S, U23-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

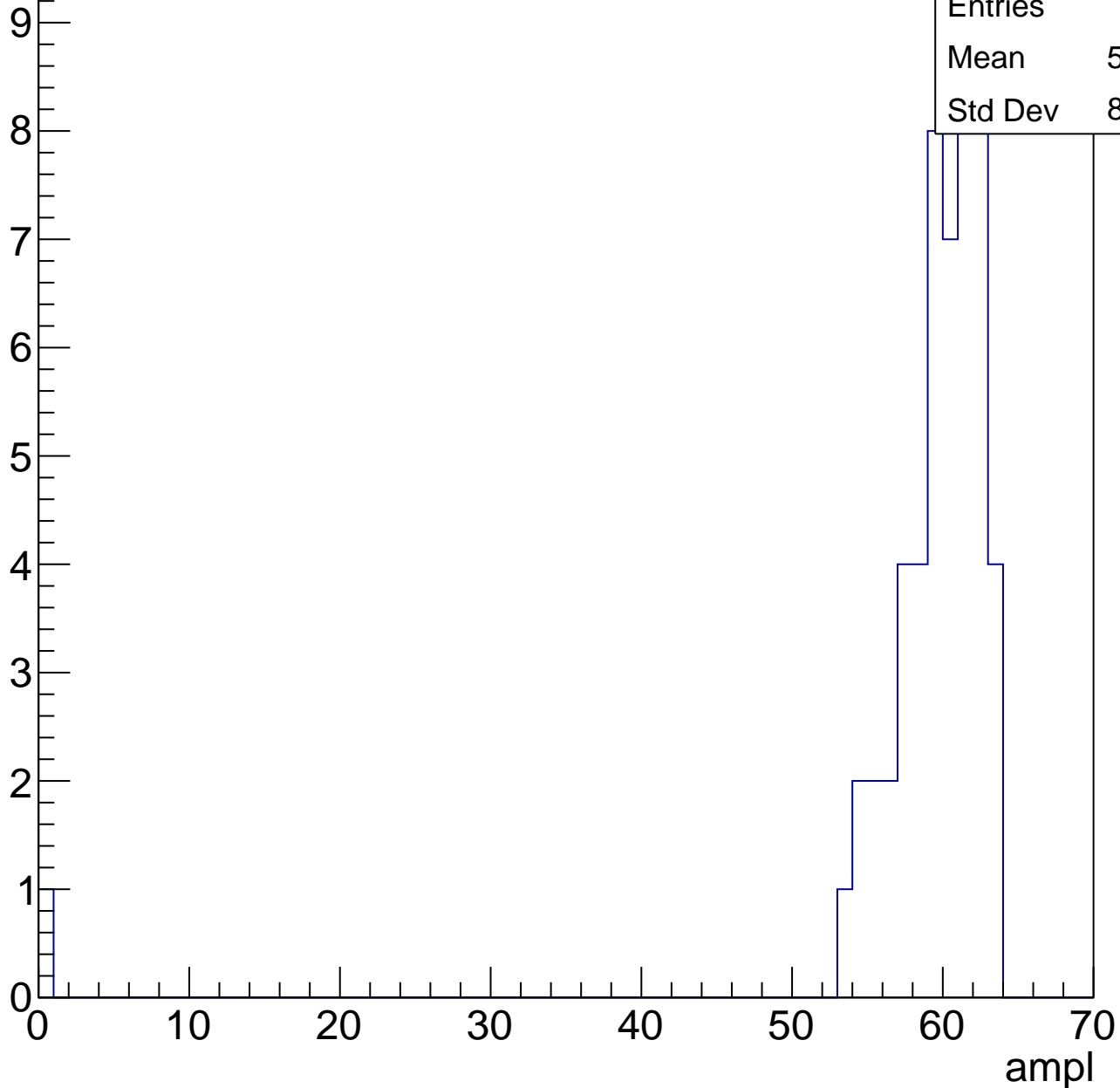
Entry



# B1L000S, U23-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

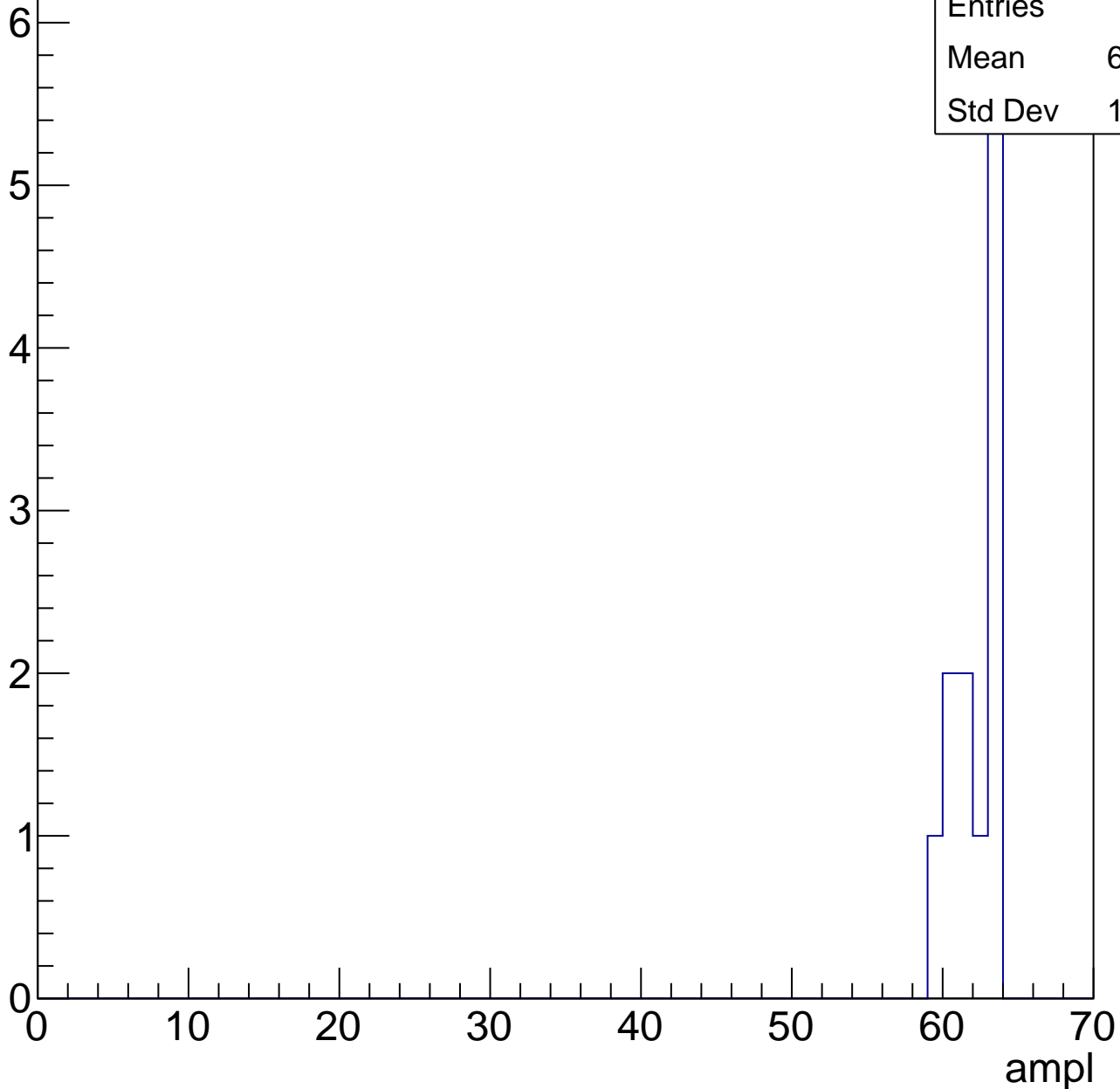


# B1L000S, U23-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.75
Std Dev	1.422

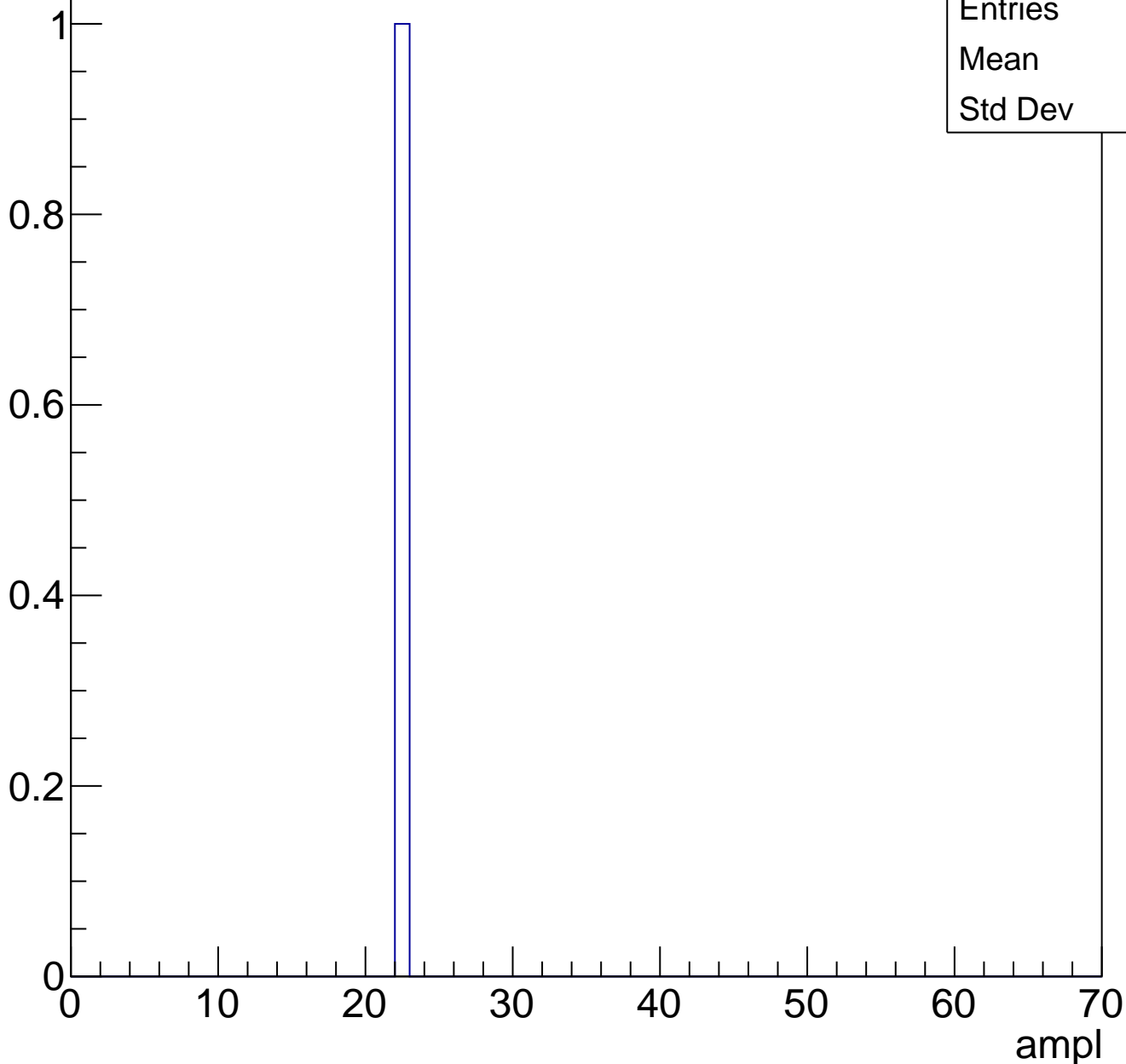




# B1L000S, U23-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U23-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	27.59
Std Dev	2.77

**Gaus mean : 28.3083**

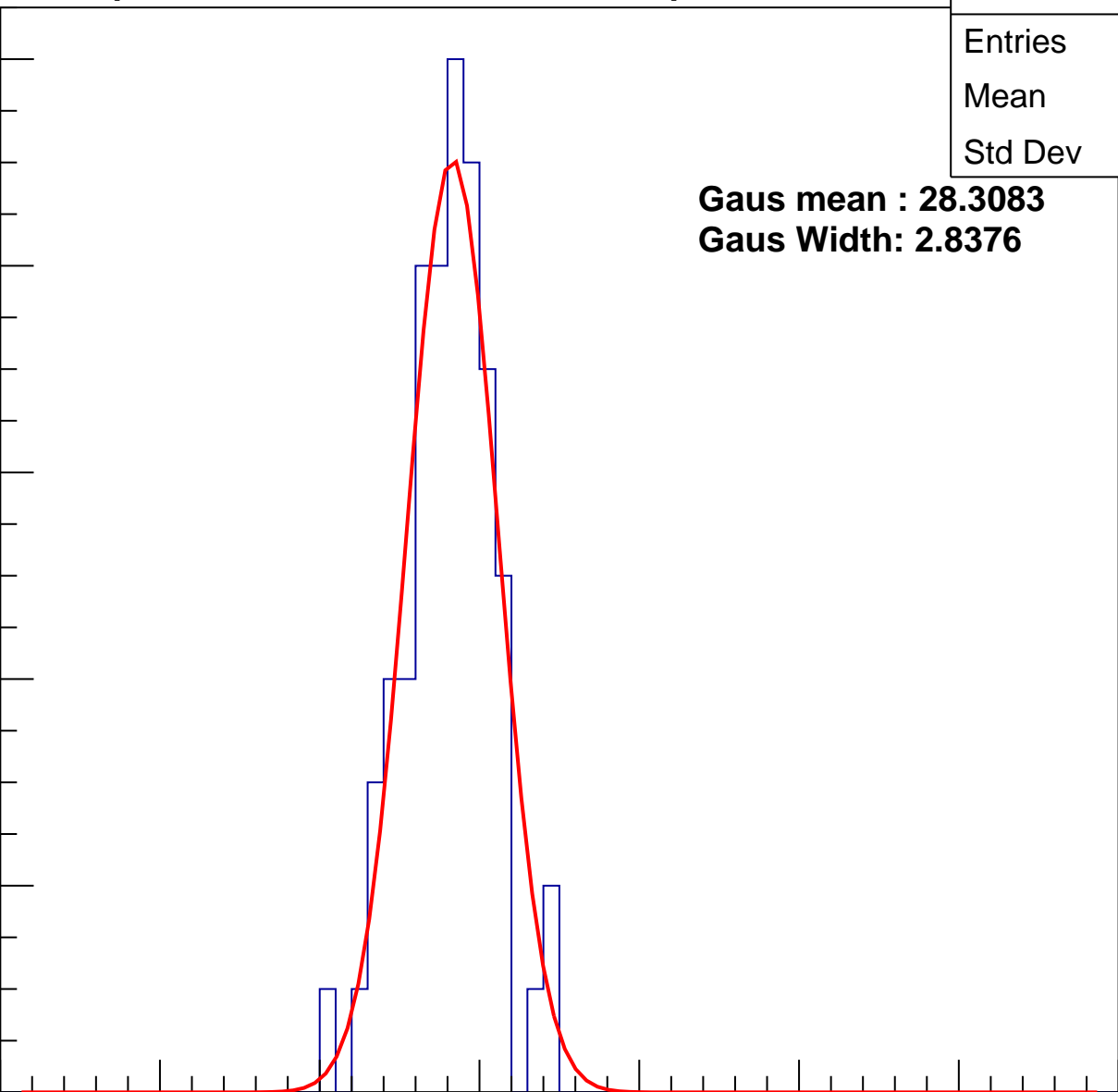
**Gaus Width: 2.8376**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch20, adc1

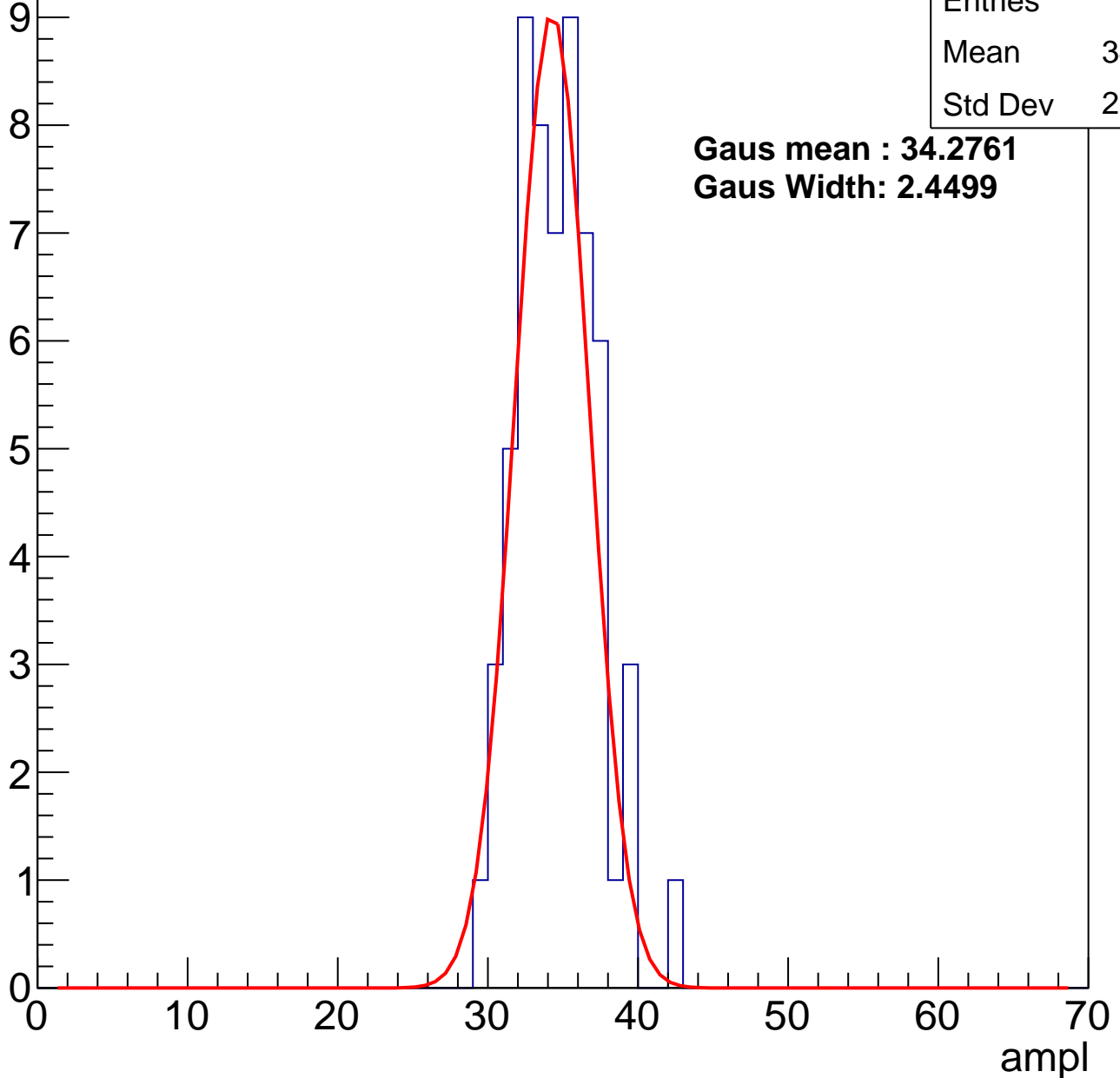
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.17
Std Dev	2.602

**Gaus mean : 34.2761**

**Gaus Width: 2.4499**



# B1L000S, U23-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	40.91
Std Dev	3.635

**Gaus mean : 41.3737**

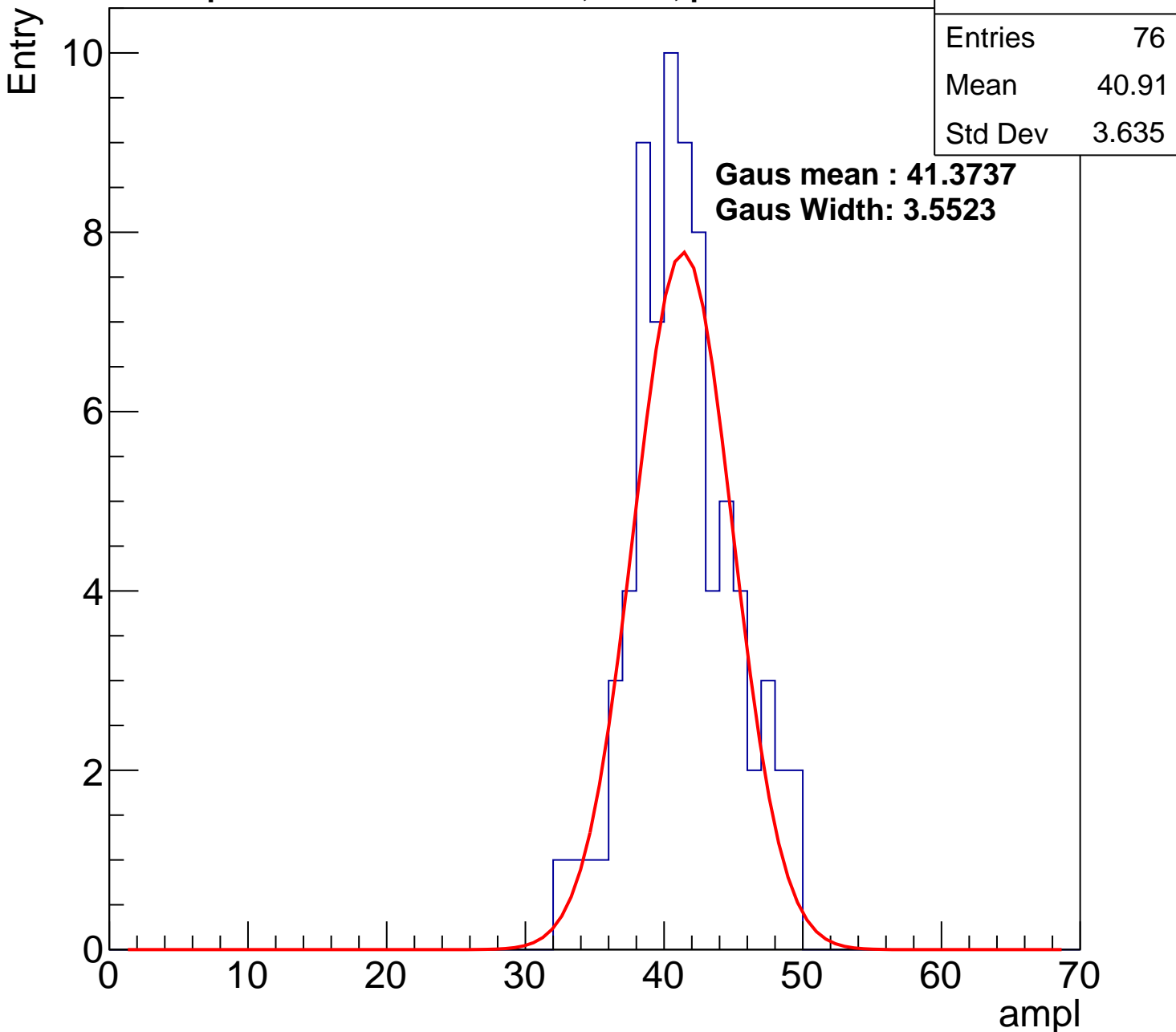
**Gaus Width: 3.5523**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

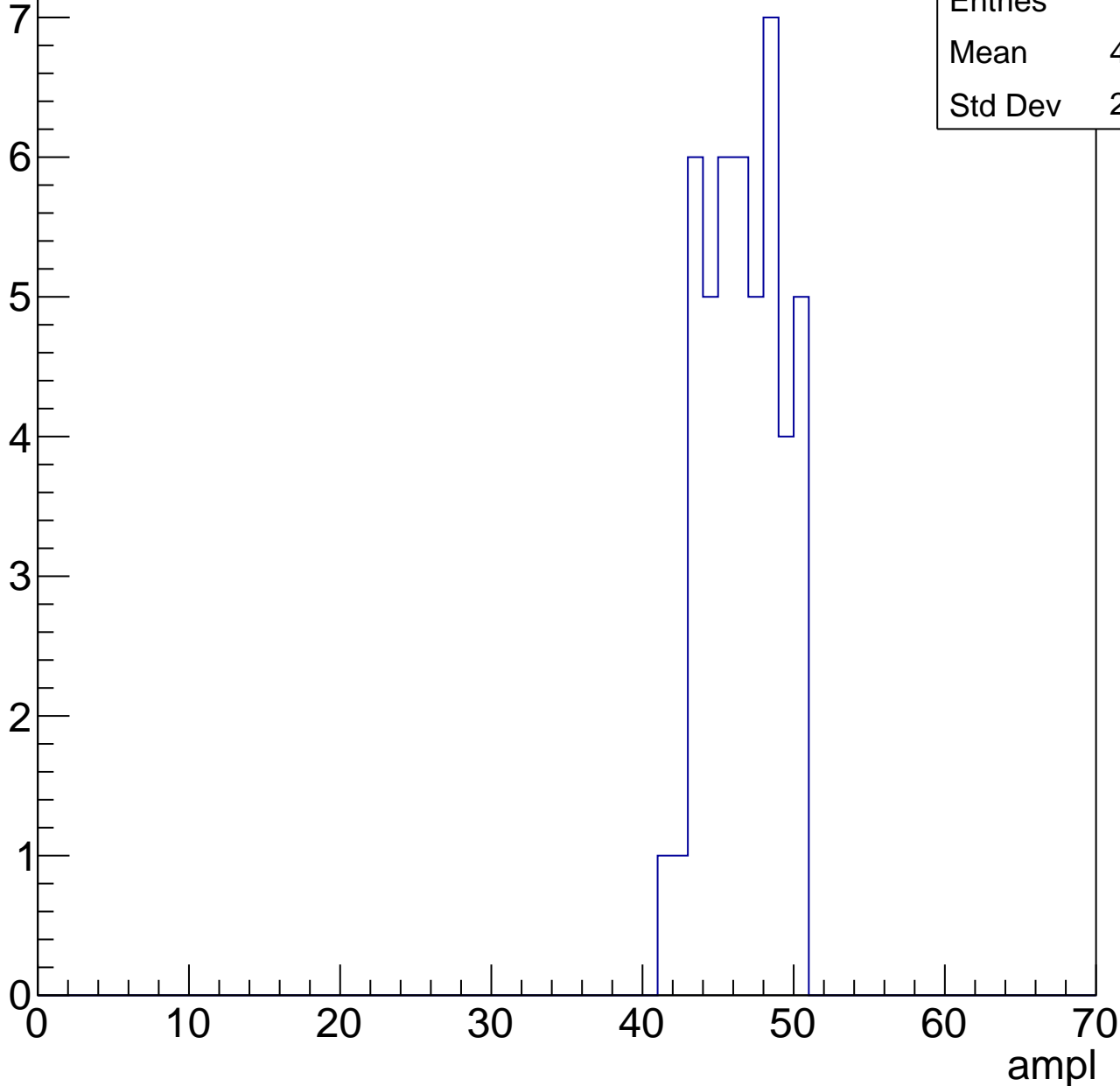


# B1L000S, U23-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	46.17
Std Dev	2.416



# B1L000S, U23-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

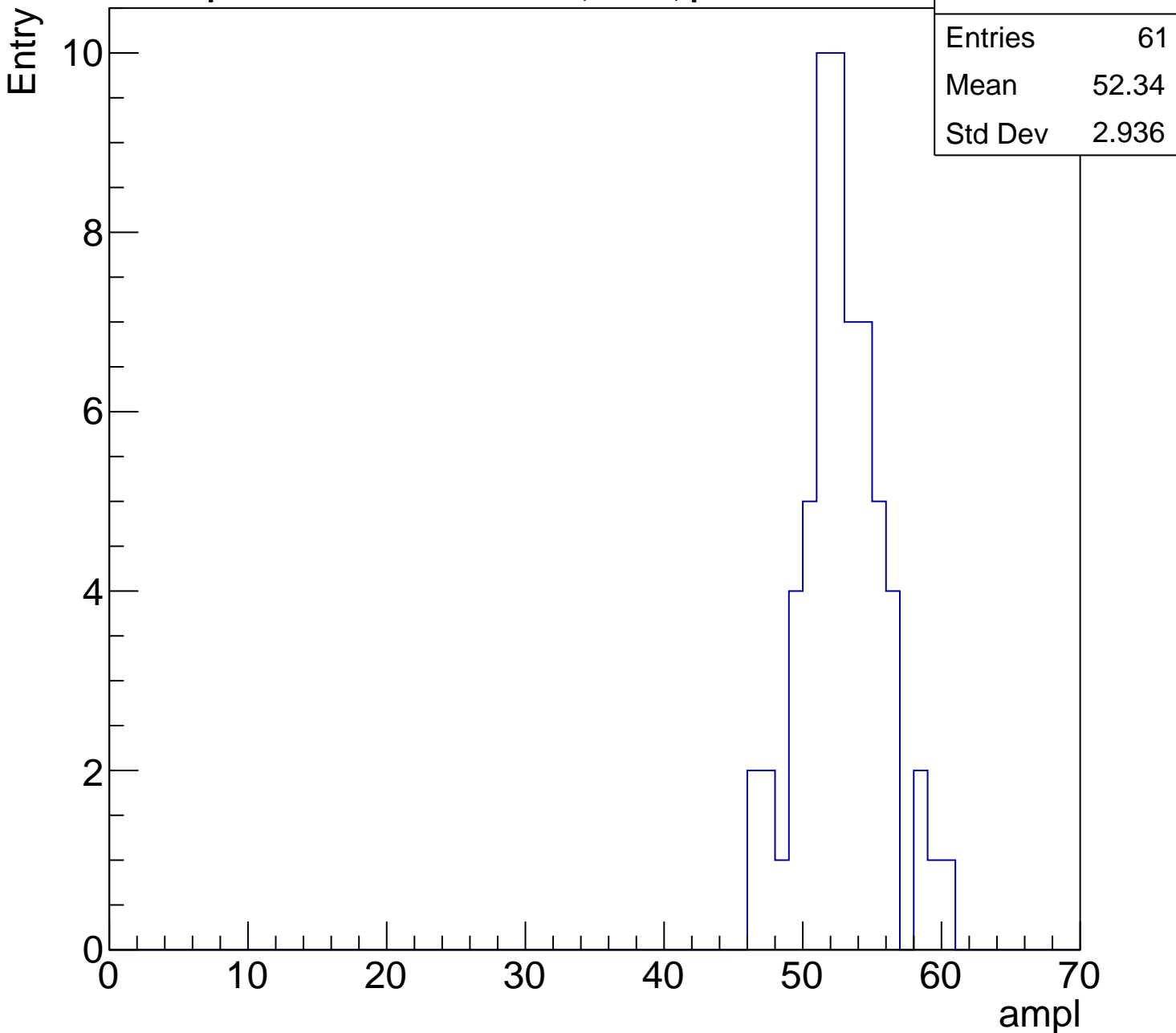
Entries	61
Mean	52.34
Std Dev	2.936

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 73

Mean 58.62

Std Dev 2.783

8

6

4

2

0

0

10

20

30

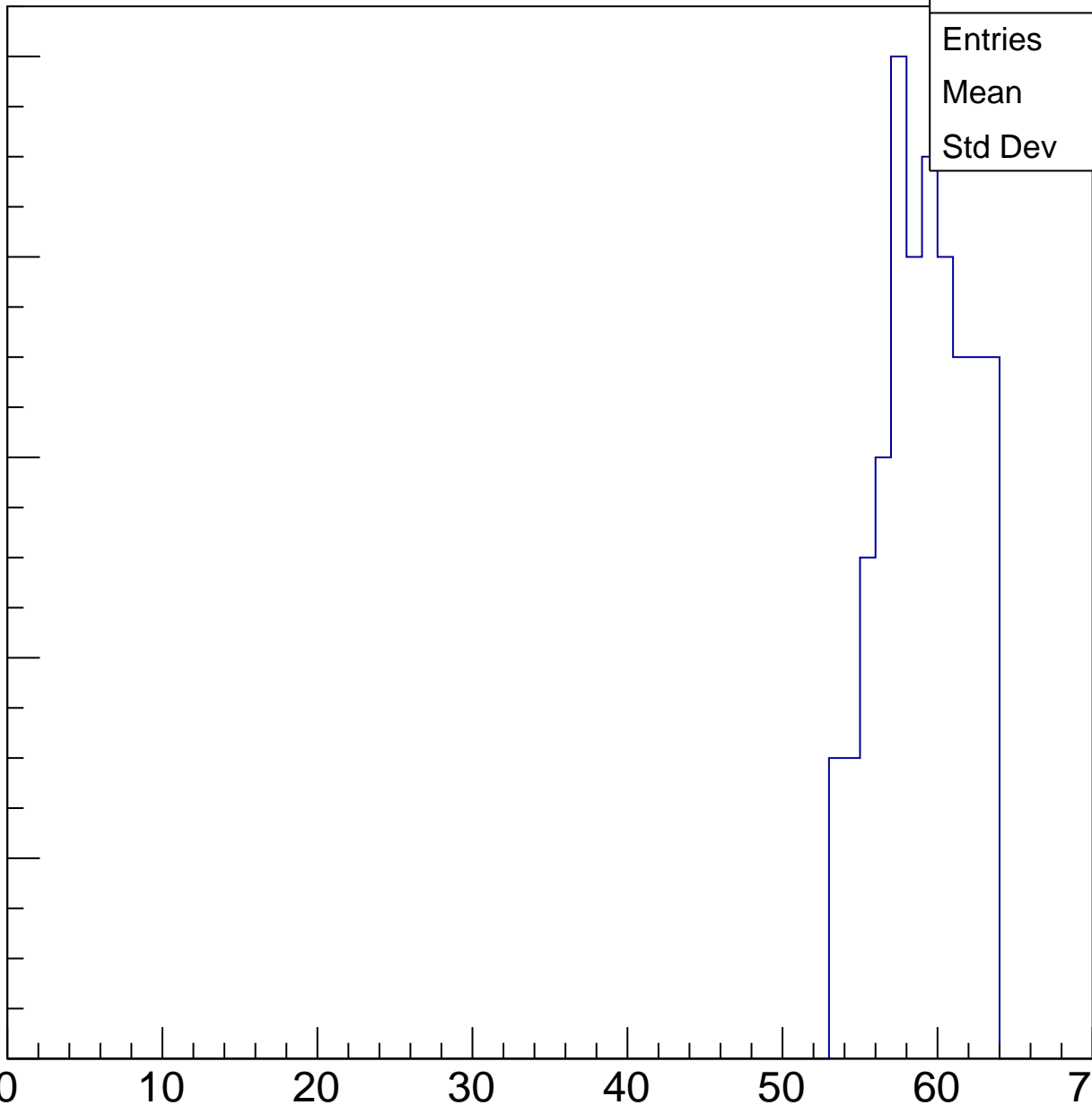
40

50

60

70

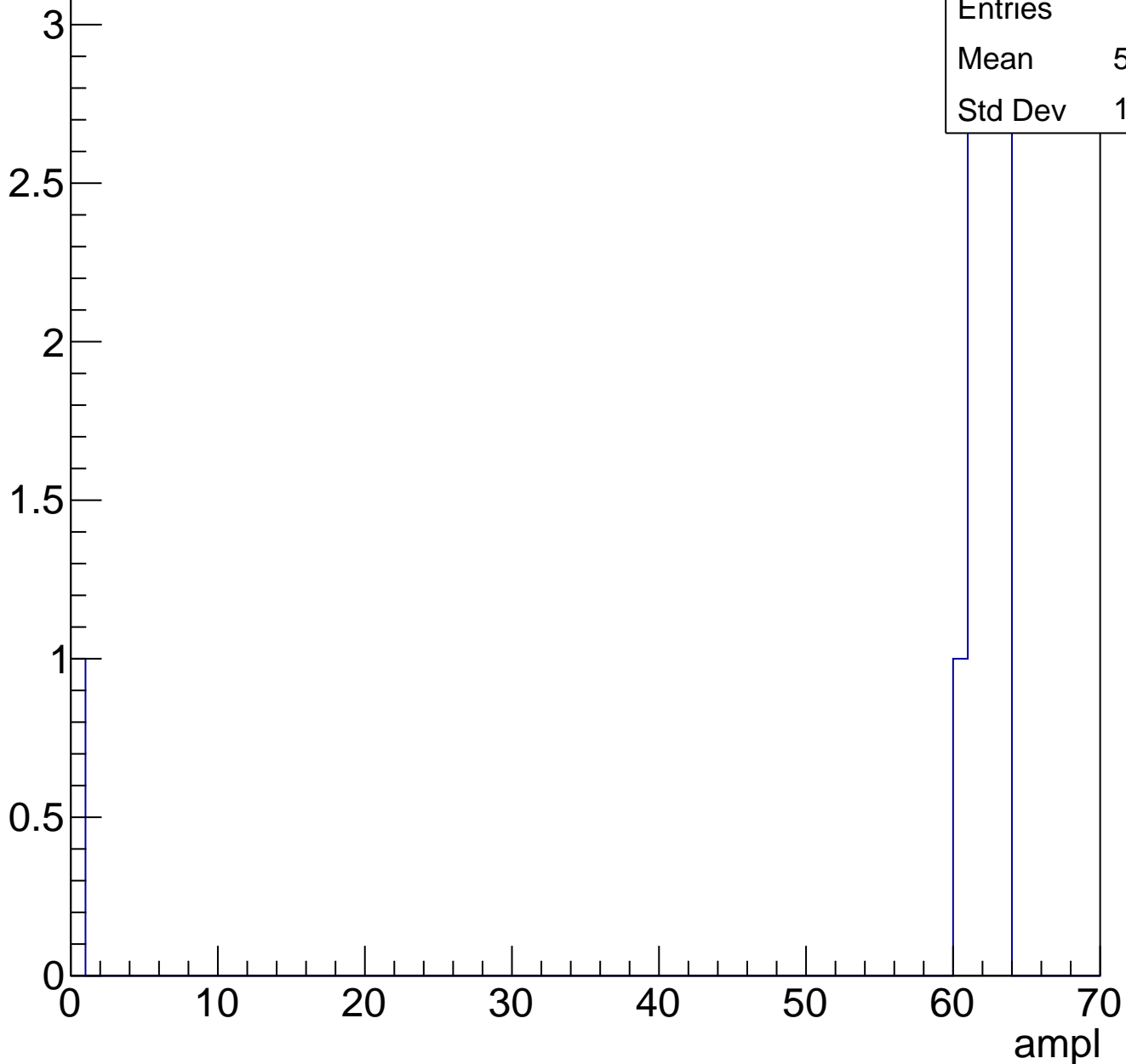
ampl



# B1L000S, U23-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch21, adc0

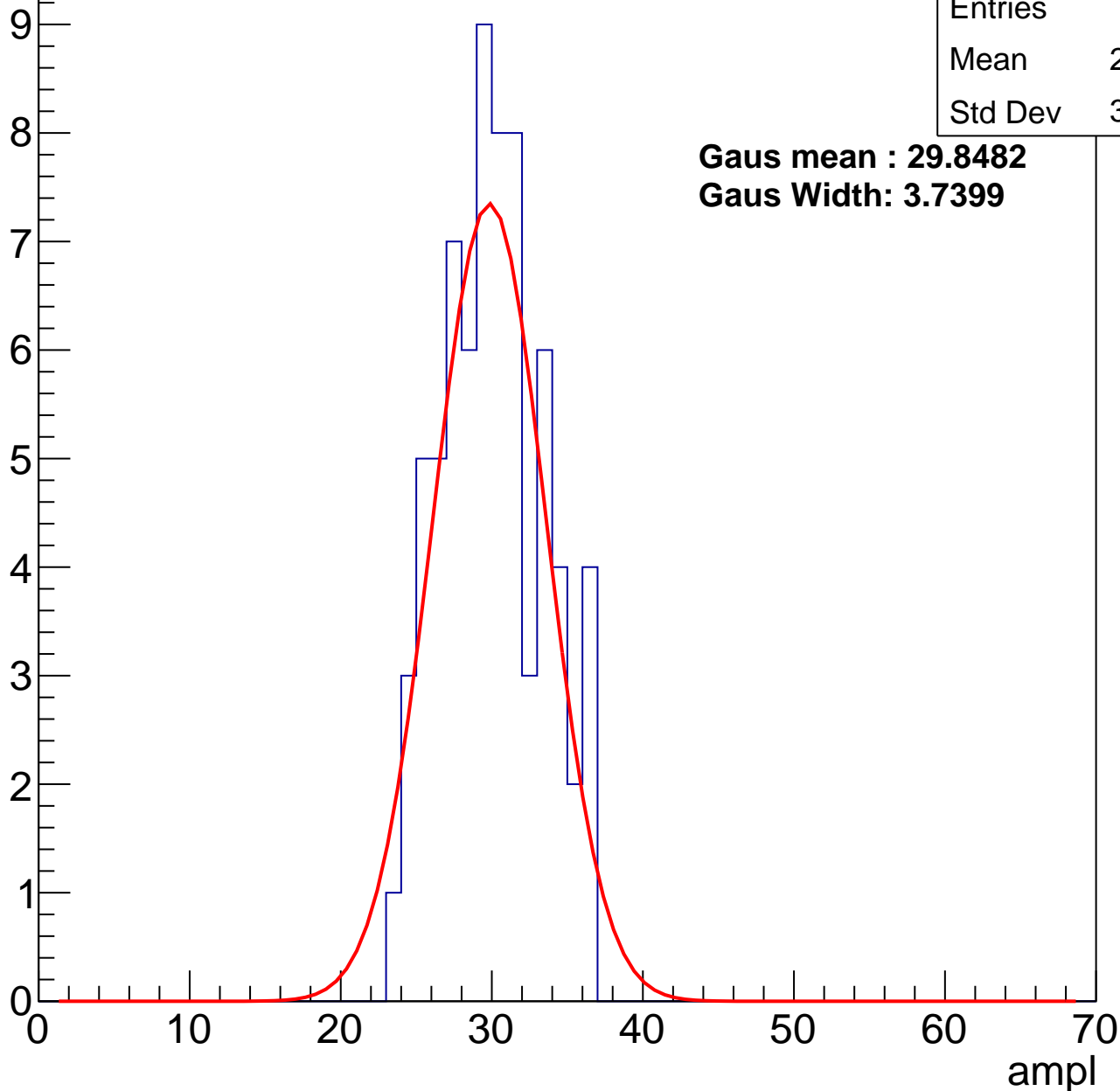
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.58
Std Dev	3.309

**Gaus mean : 29.8482**

**Gaus Width: 3.7399**



# B1L000S, U23-ch21, adc1

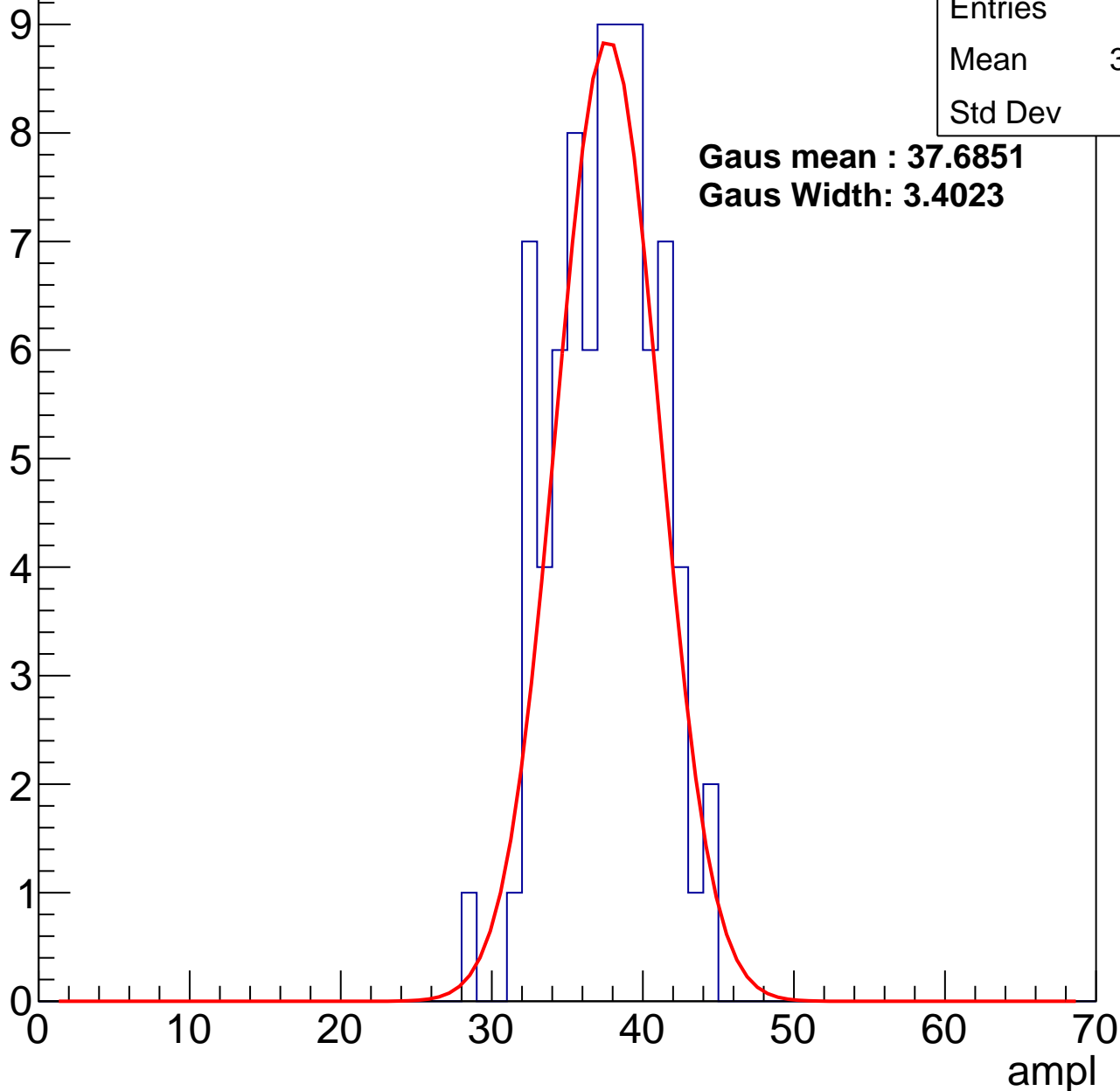
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	37.09
Std Dev	3.34

**Gaus mean : 37.6851**

**Gaus Width: 3.4023**



# B1L000S, U23-ch21, adc2

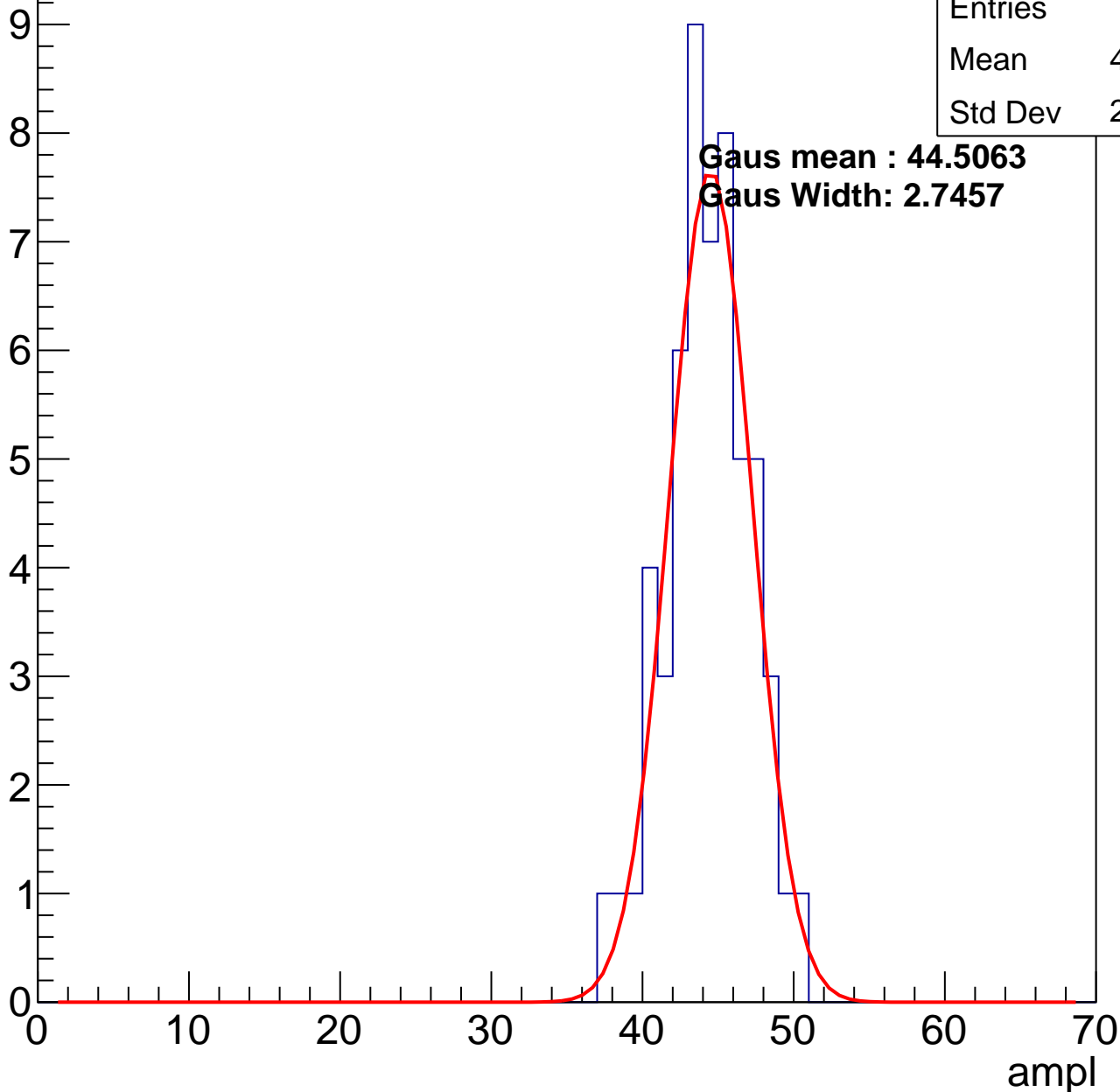
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	43.85
Std Dev	2.746

**Gaus mean : 44.5063**

**Gaus Width: 2.7457**

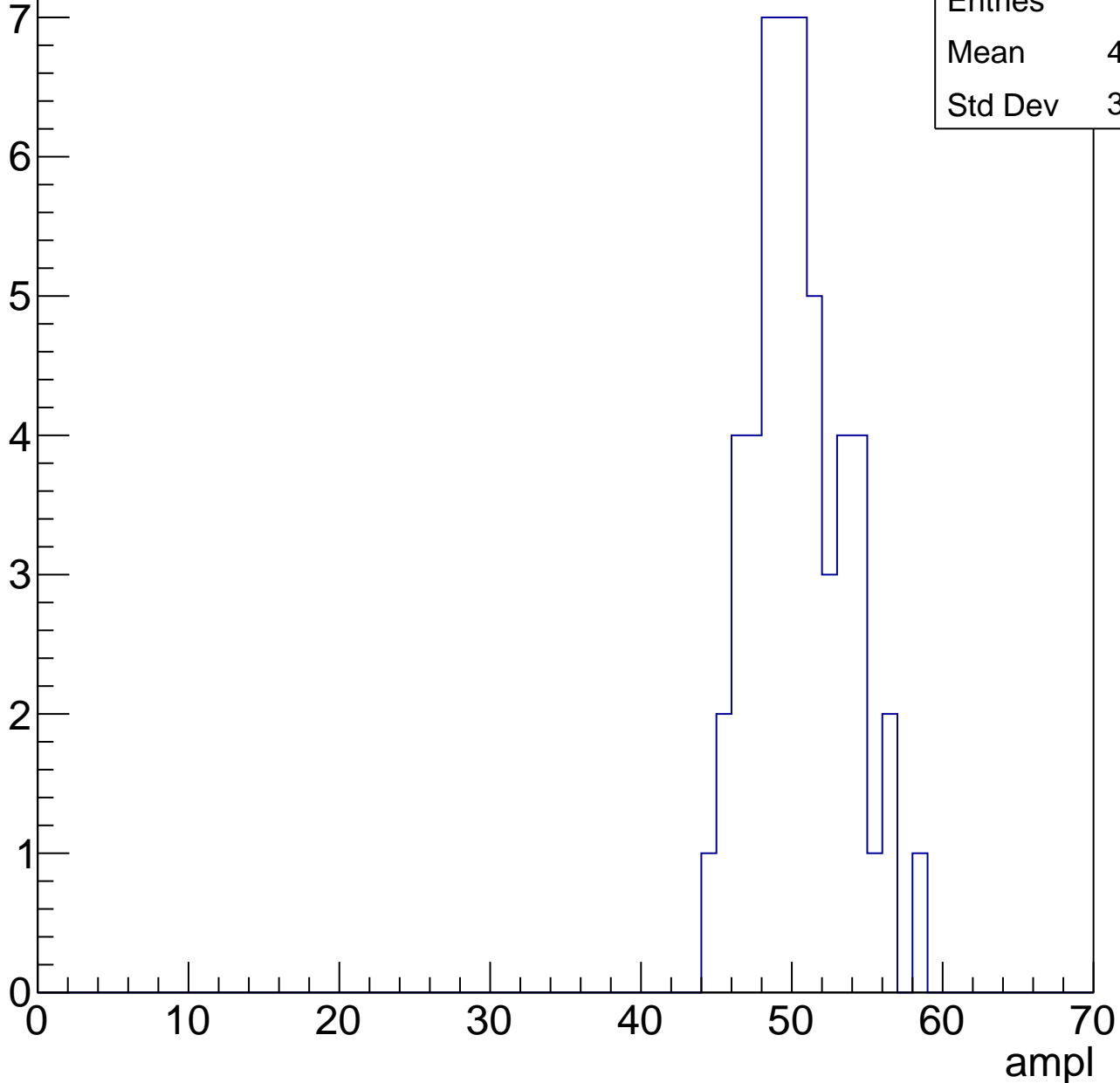


# B1L000S, U23-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

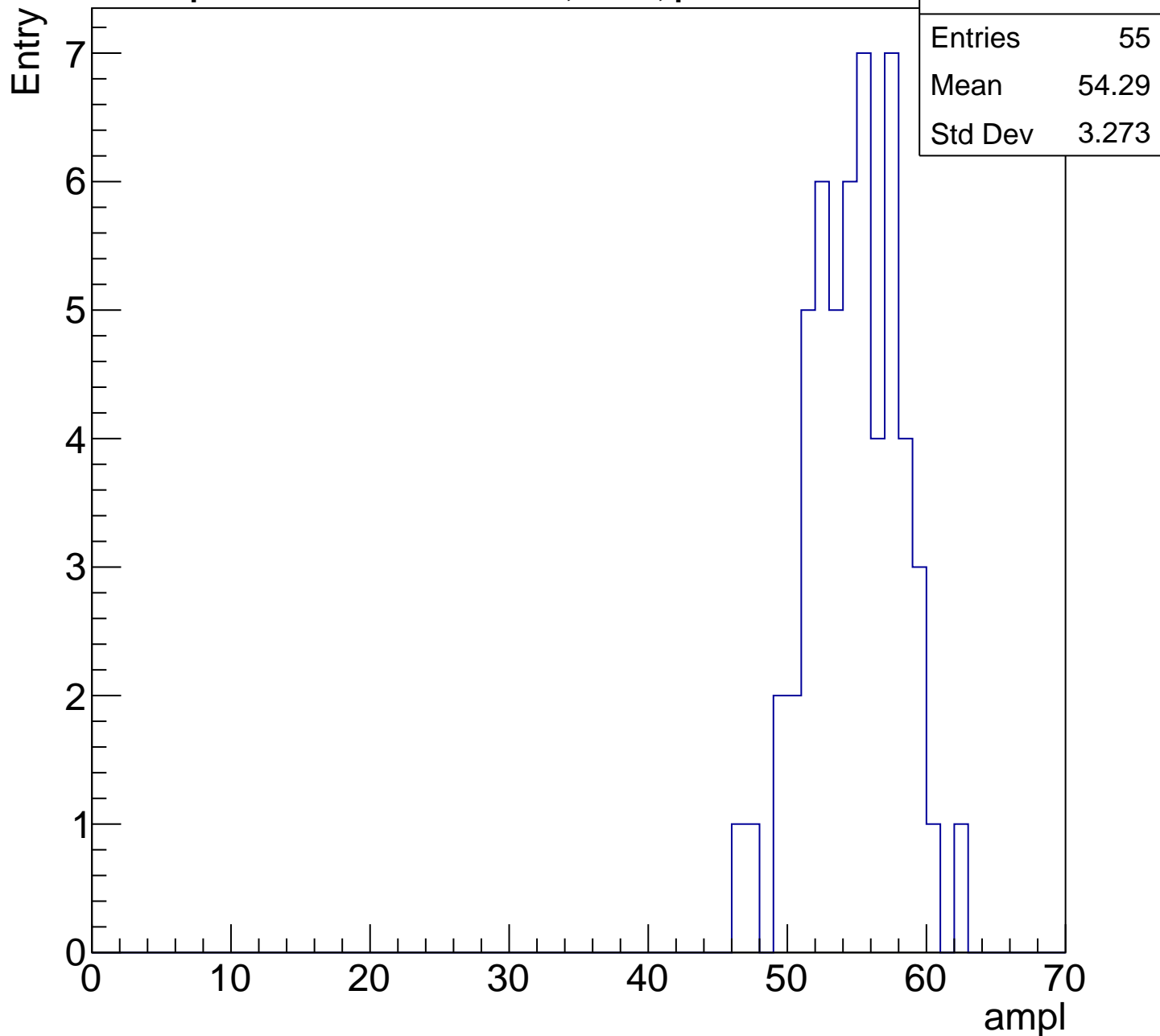
Entry

Entries	52
Mean	49.98
Std Dev	3.098



# B1L000S, U23-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

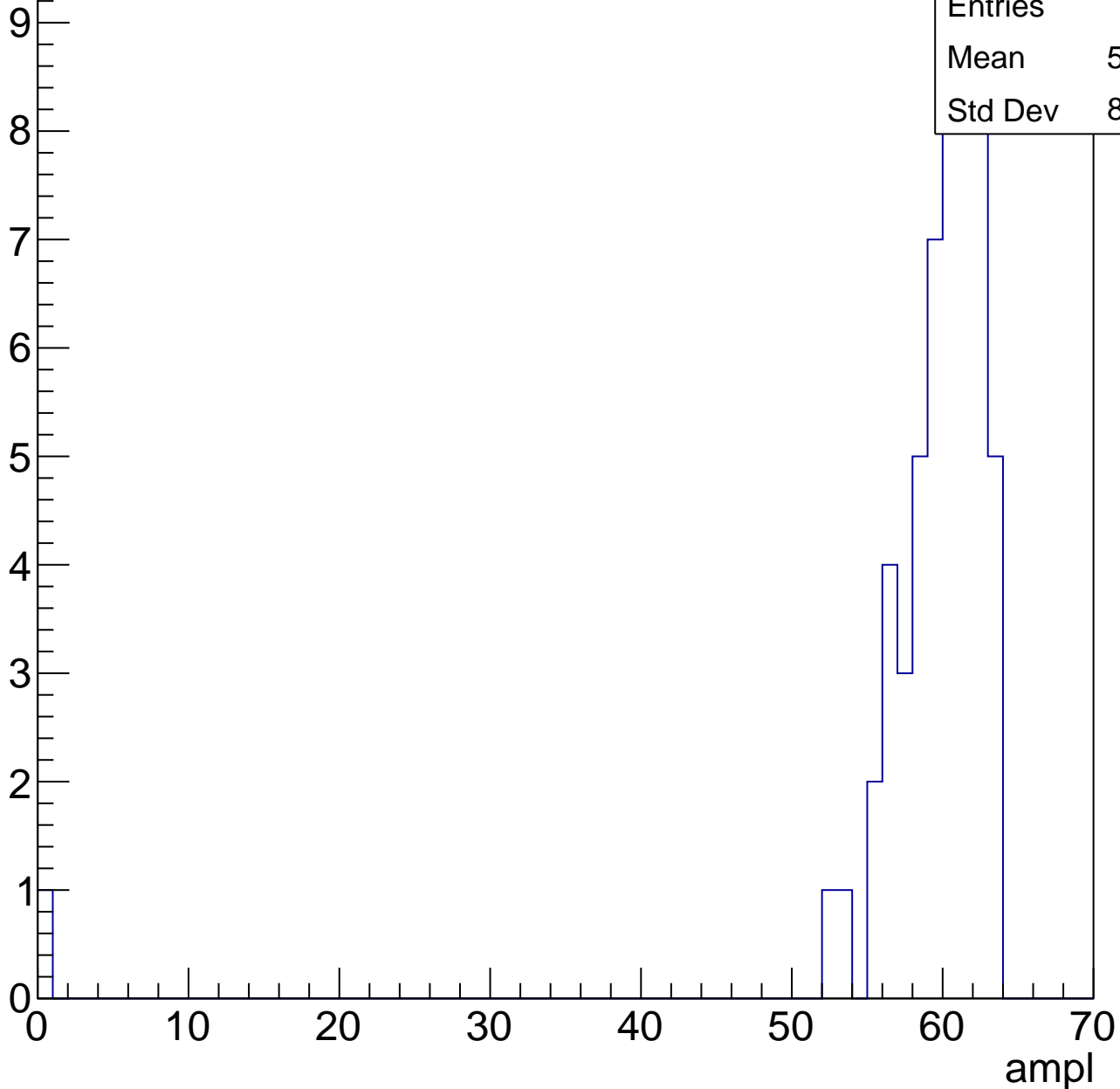


# B1L000S, U23-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	58.39
Std Dev	8.418

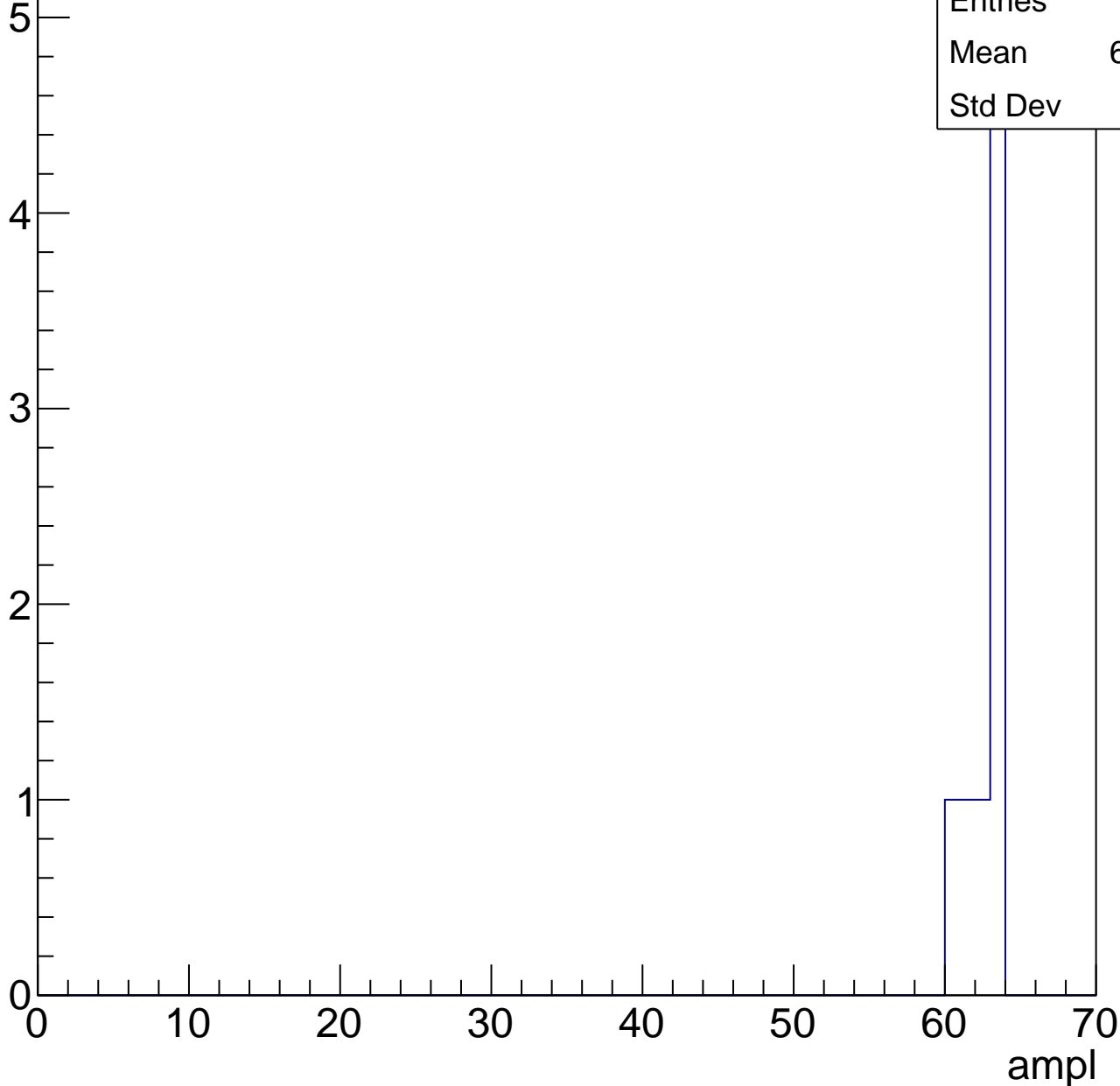


# B1L000S, U23-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.25
Std Dev	1.09





# B1L000S, U23-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch22, adc0

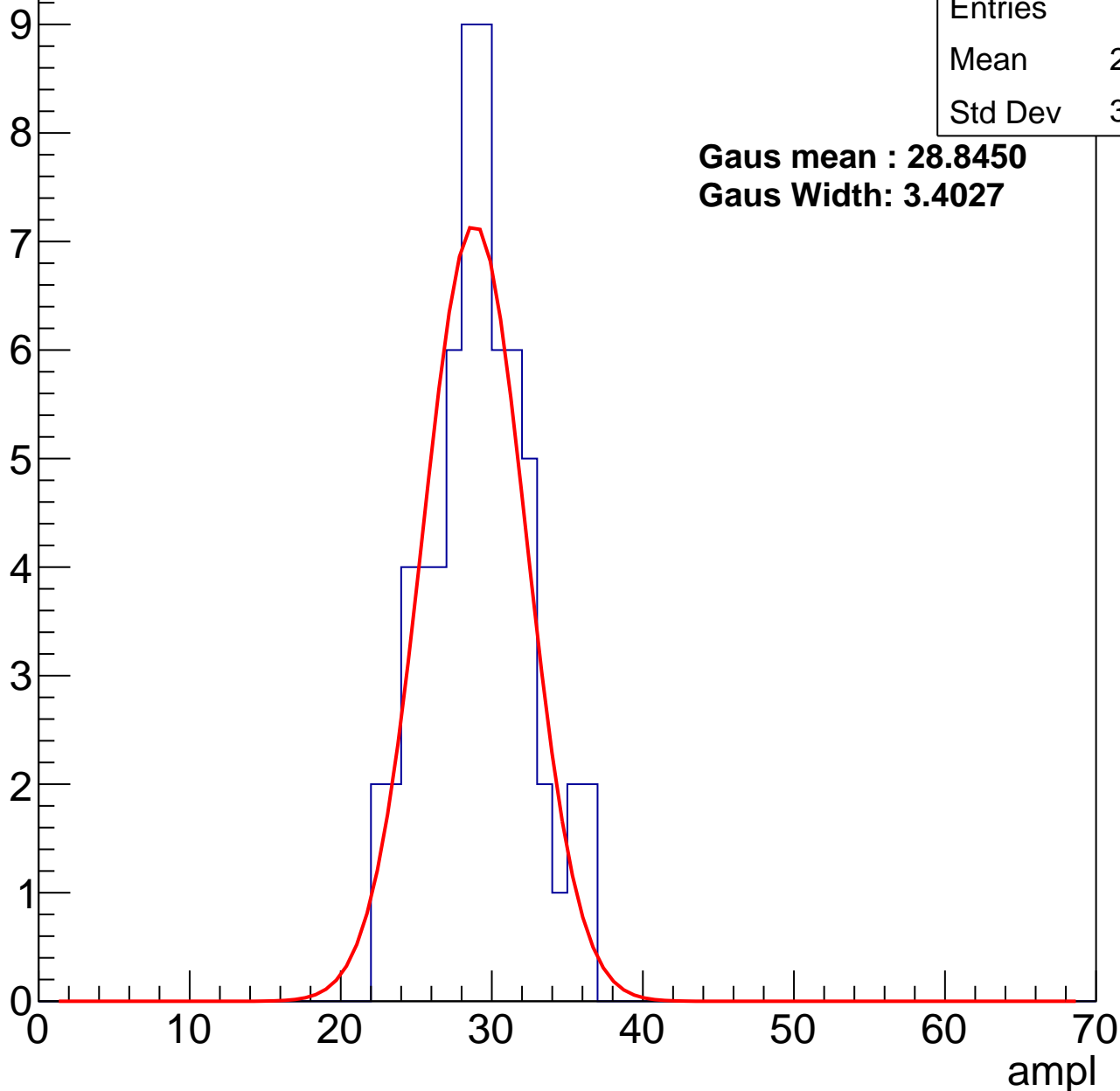
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.64
Std Dev	3.299

**Gaus mean : 28.8450**

**Gaus Width: 3.4027**



# B1L000S, U23-ch22, adc1

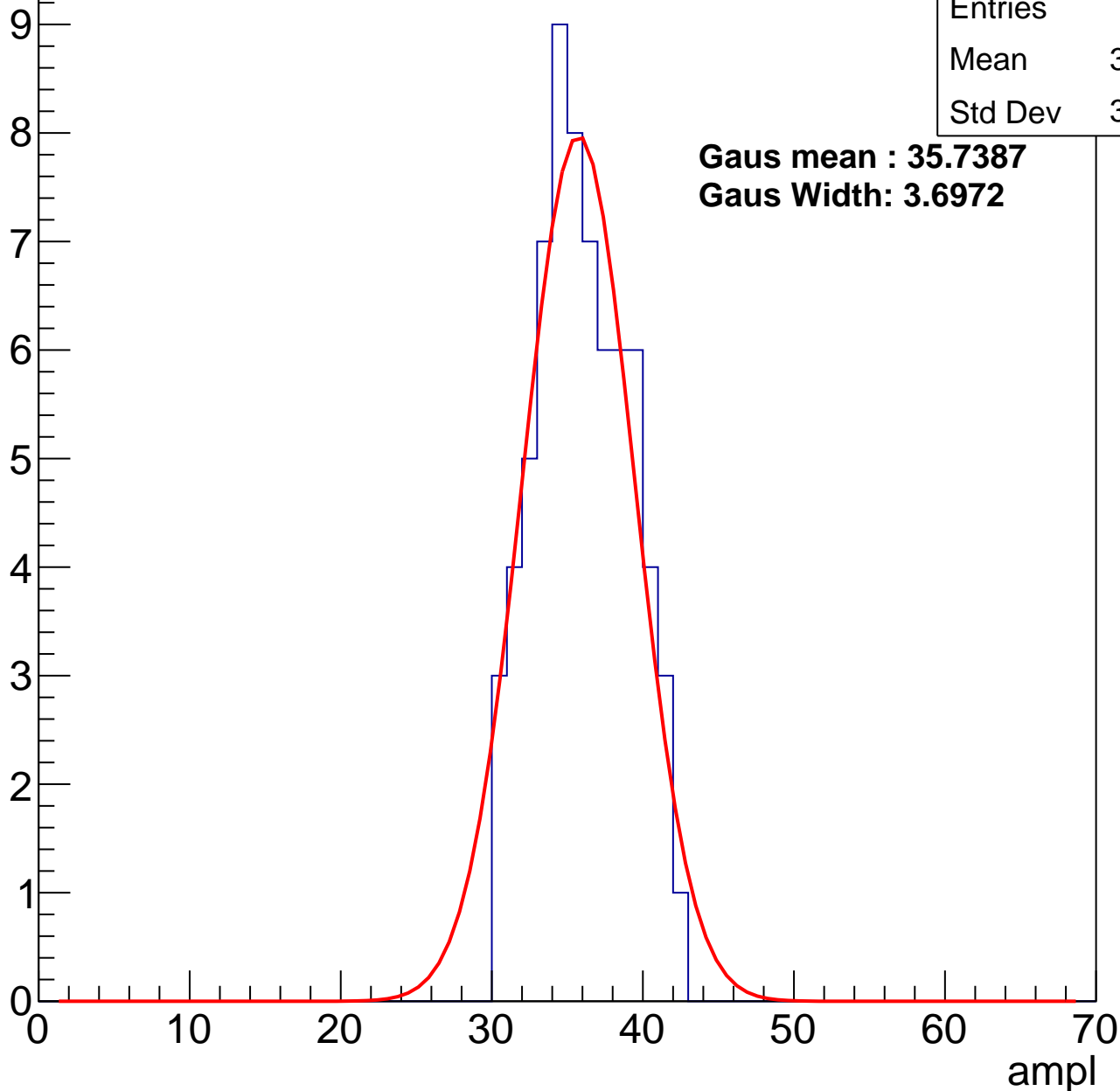
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.54
Std Dev	3.044

**Gaus mean : 35.7387**

**Gaus Width: 3.6972**



# B1L000S, U23-ch22, adc2

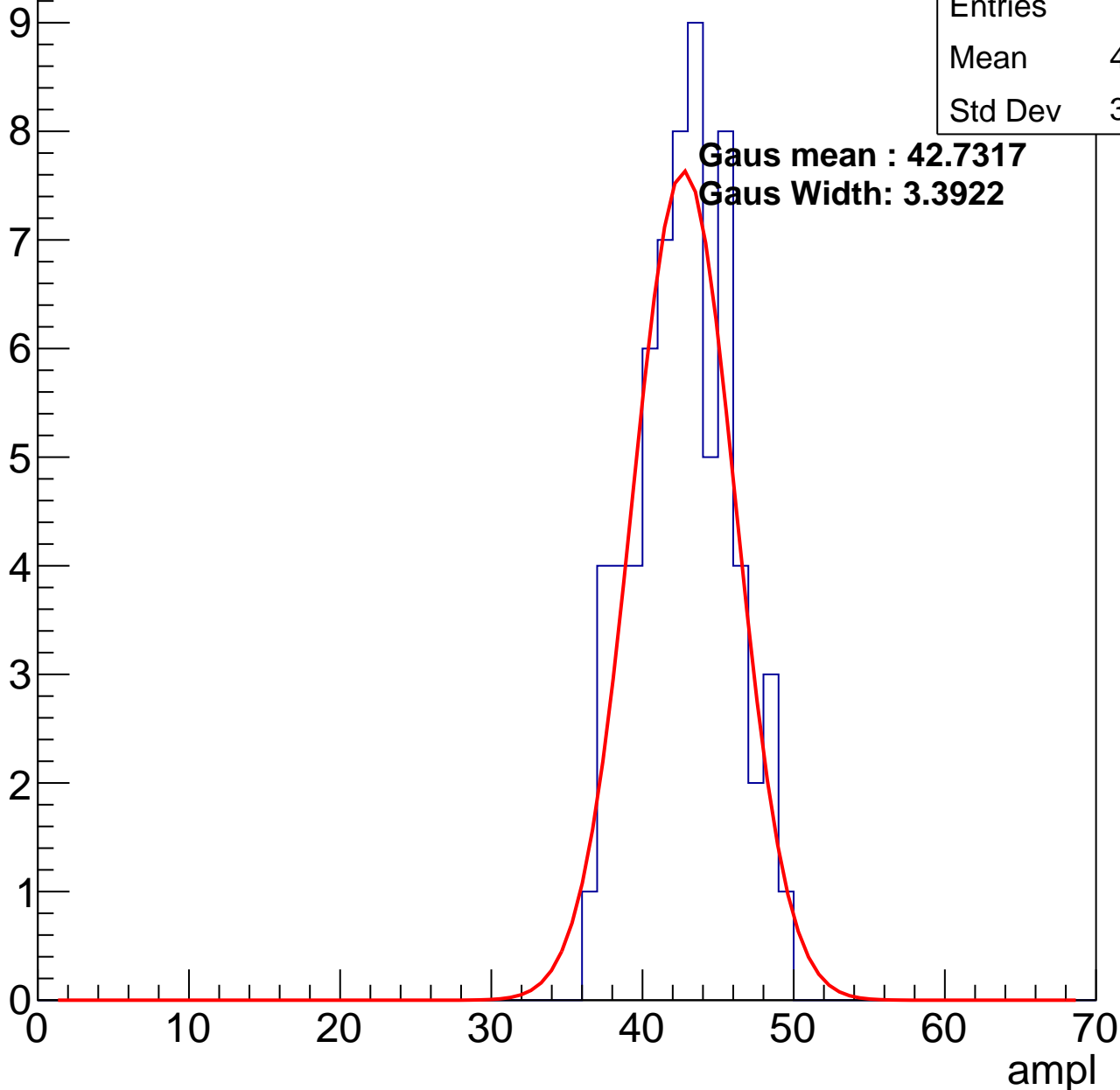
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	42.32
Std Dev	3.095

**Gaus mean : 42.7317**

**Gaus Width: 3.3922**

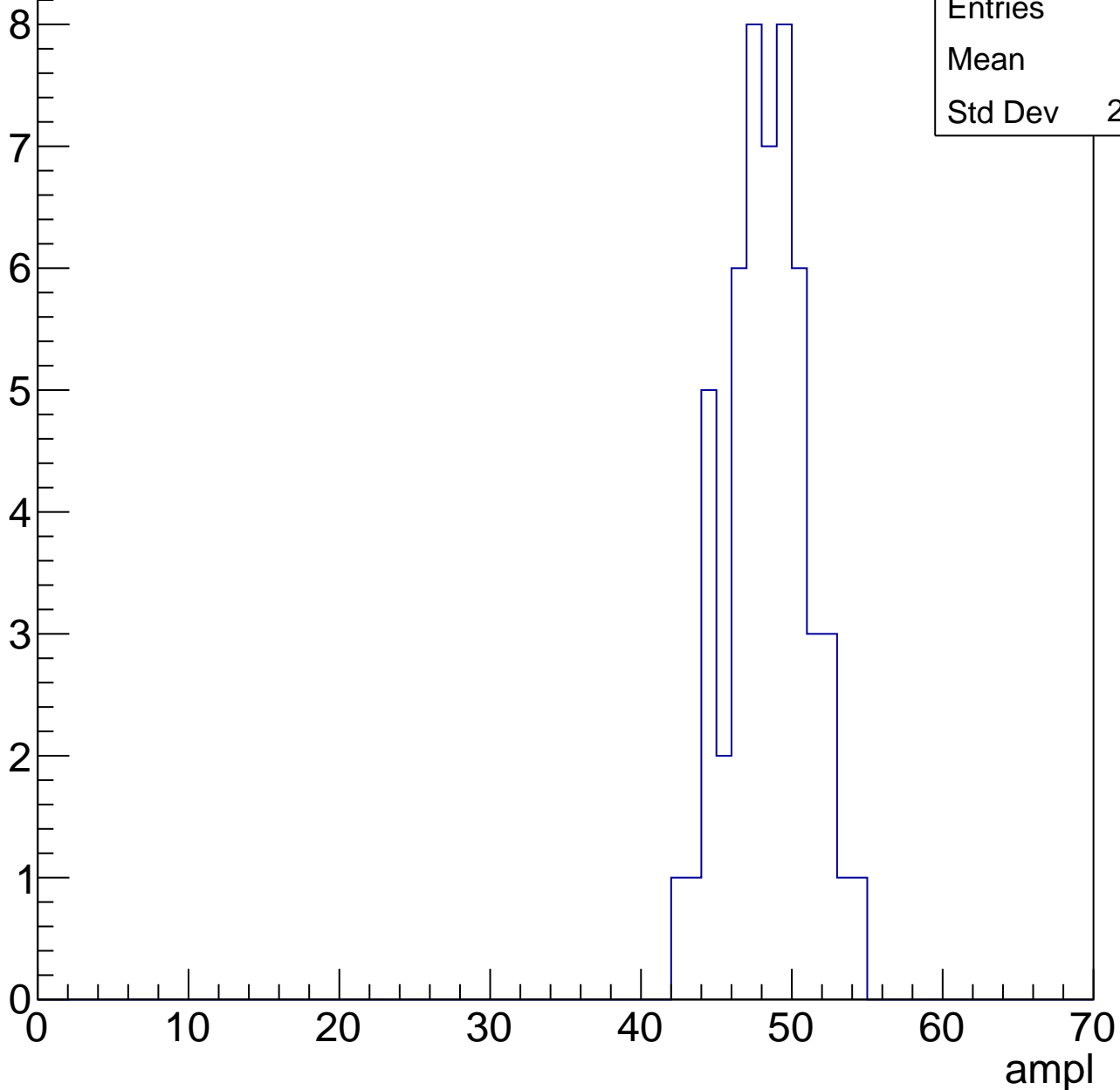


# B1L000S, U23-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

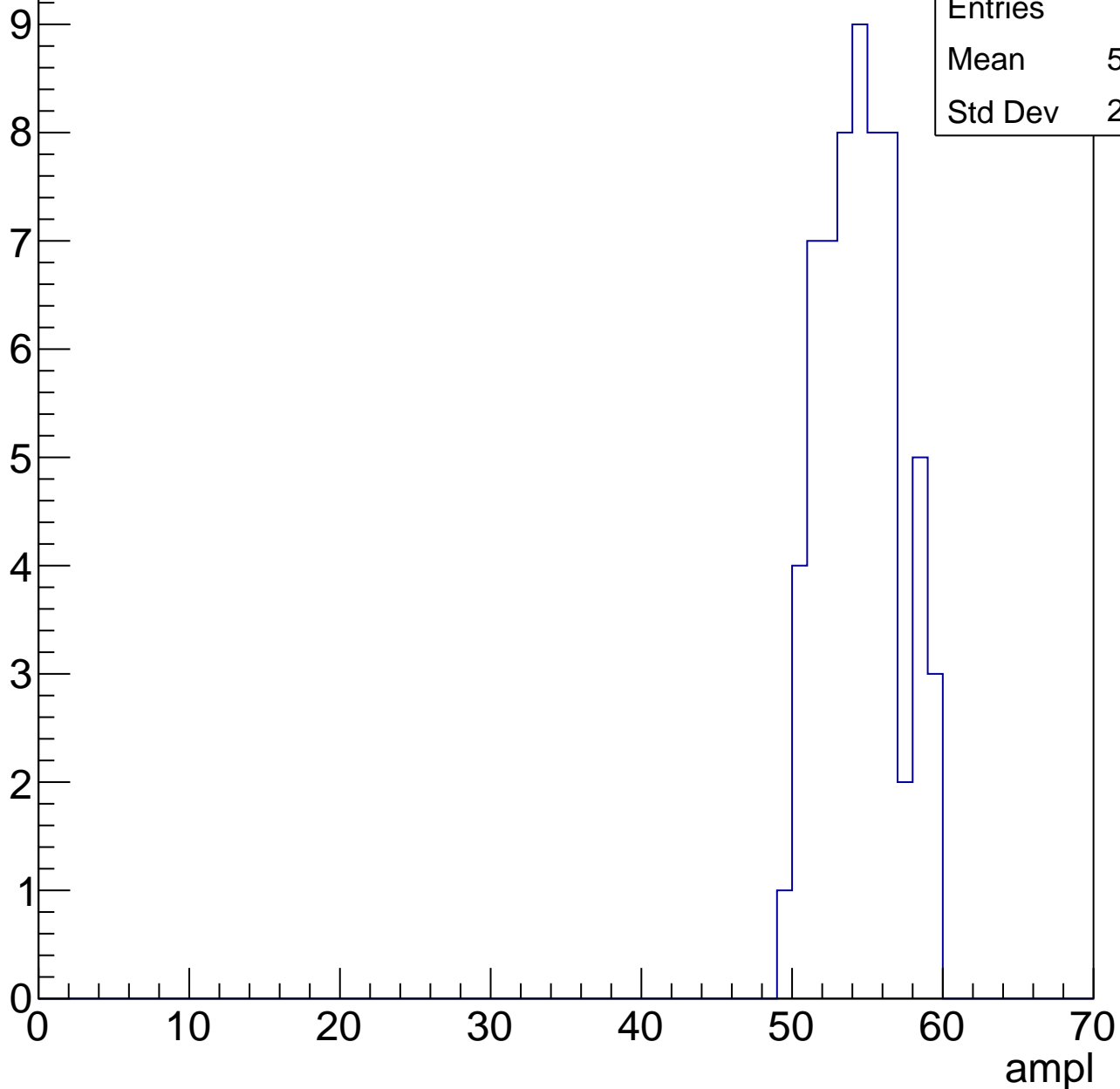
Entries	52
Mean	47.9
Std Dev	2.626



# B1L000S, U23-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

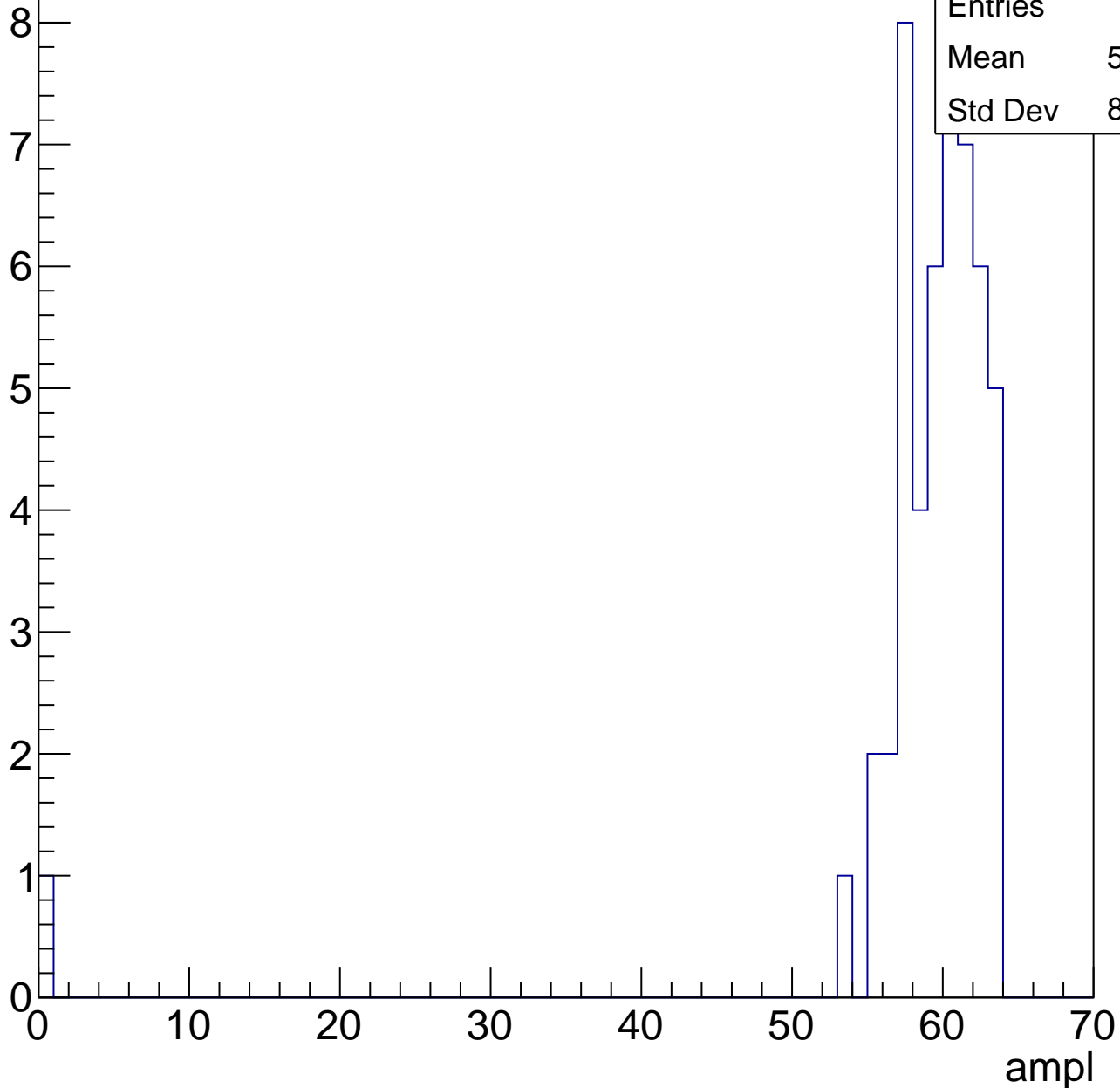


Entries	62
Mean	54.02
Std Dev	2.543

# B1L000S, U23-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

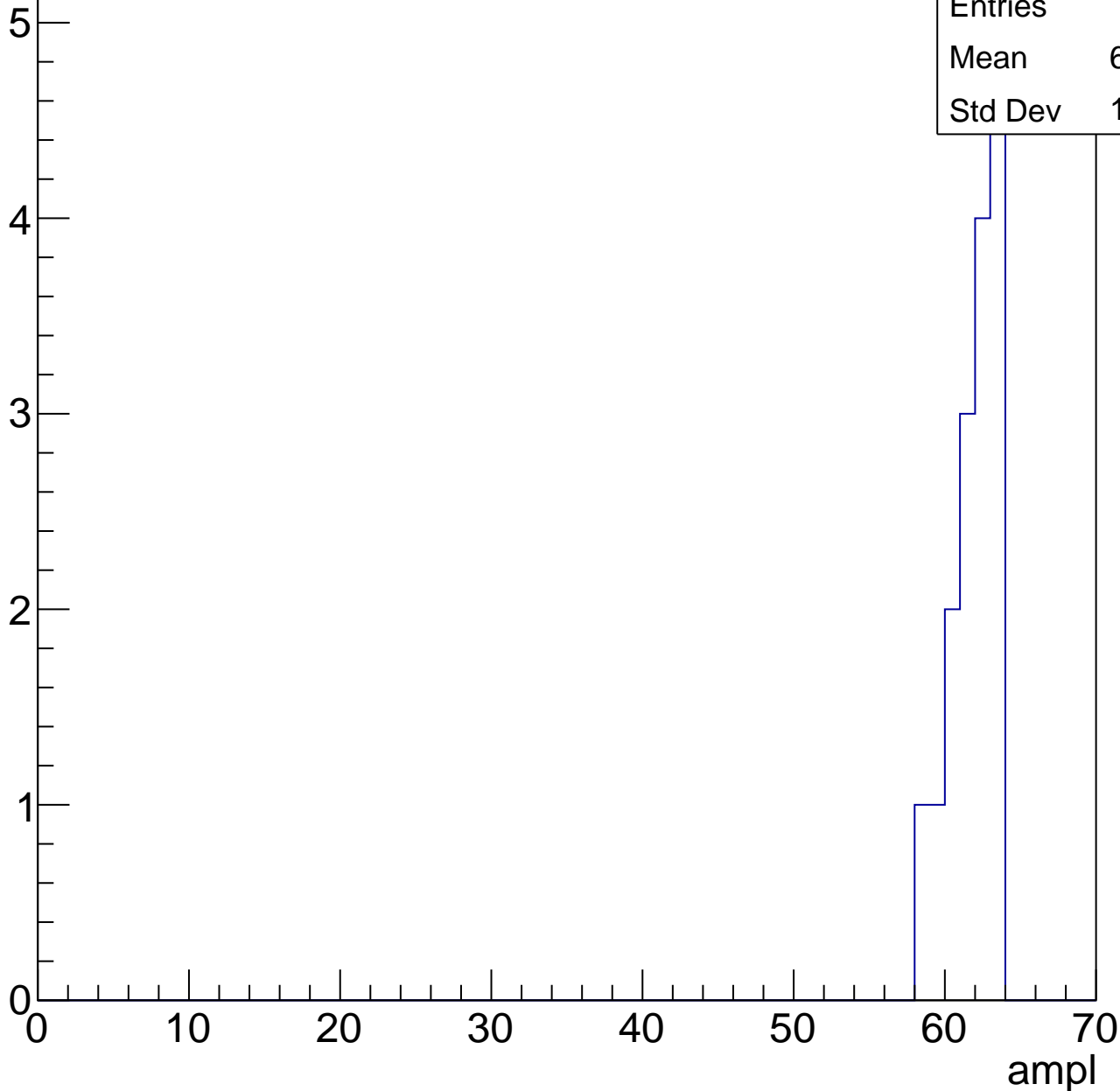


# B1L000S, U23-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.44
Std Dev	1.499





# B1L000S, U23-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch23, adc0

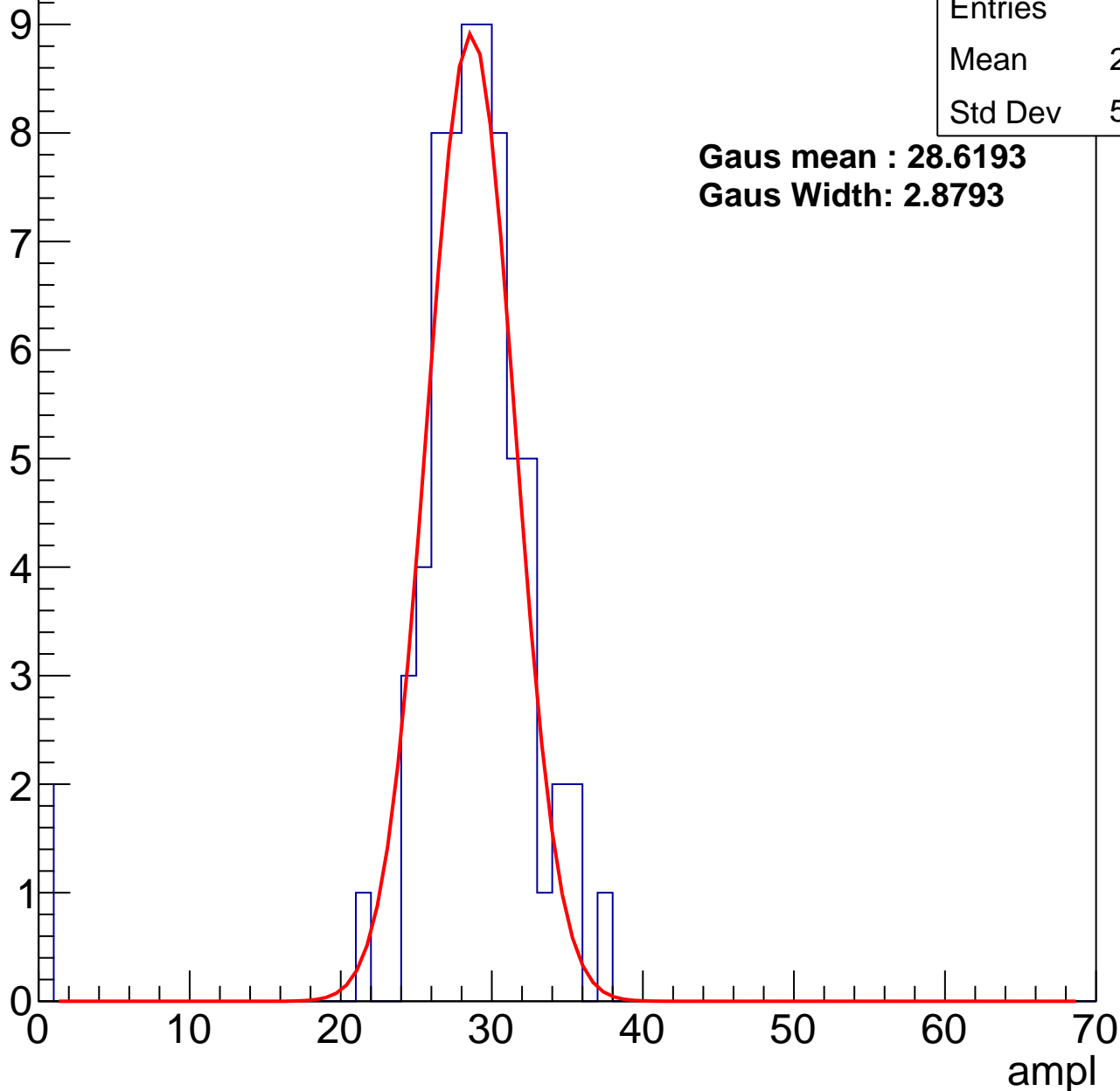
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.84
Std Dev	5.664

**Gaus mean : 28.6193**

**Gaus Width: 2.8793**



# B1L000S, U23-ch23, adc1

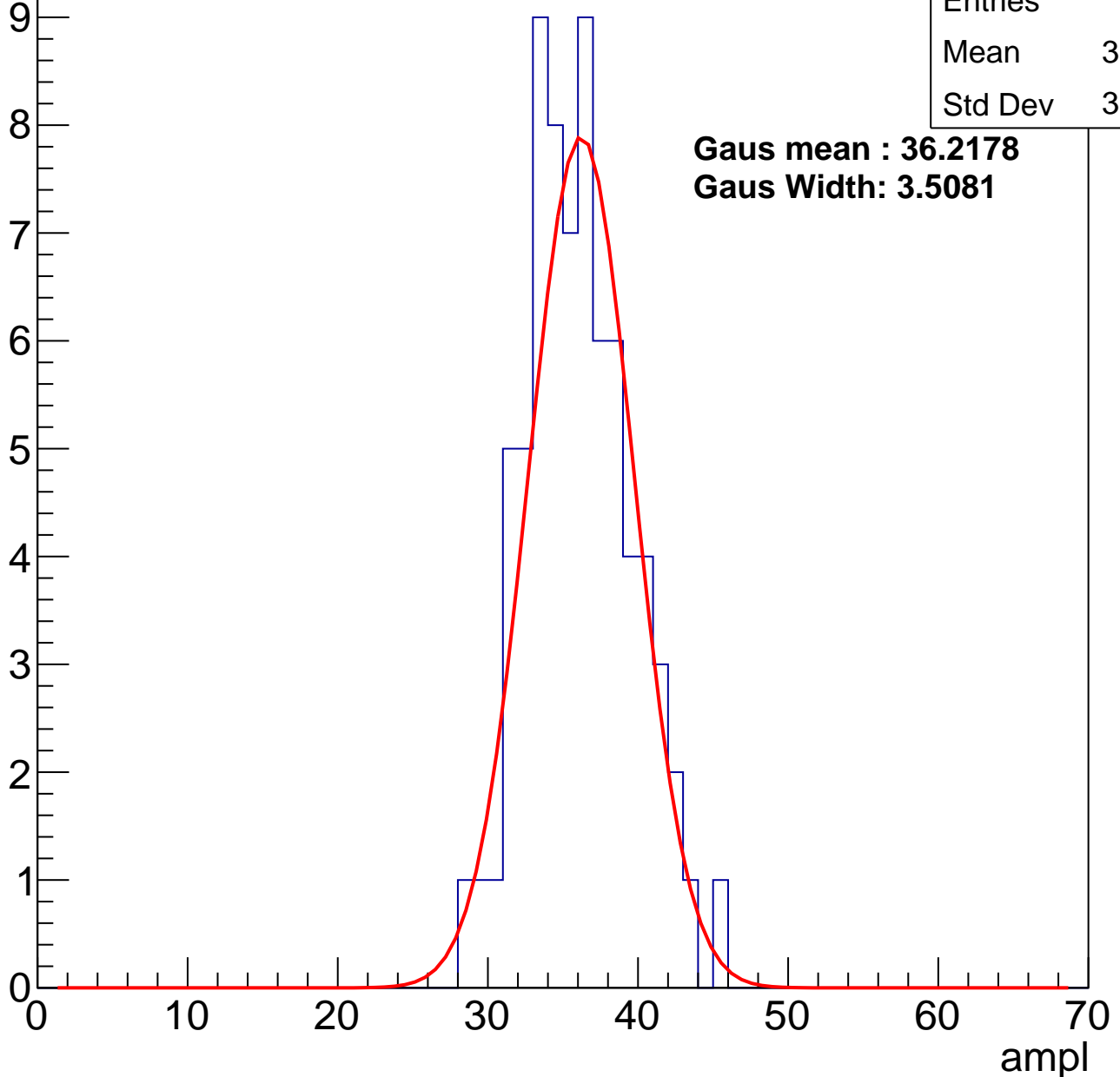
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	35.63
Std Dev	3.454

**Gaus mean : 36.2178**

**Gaus Width: 3.5081**



# B1L000S, U23-ch23, adc2

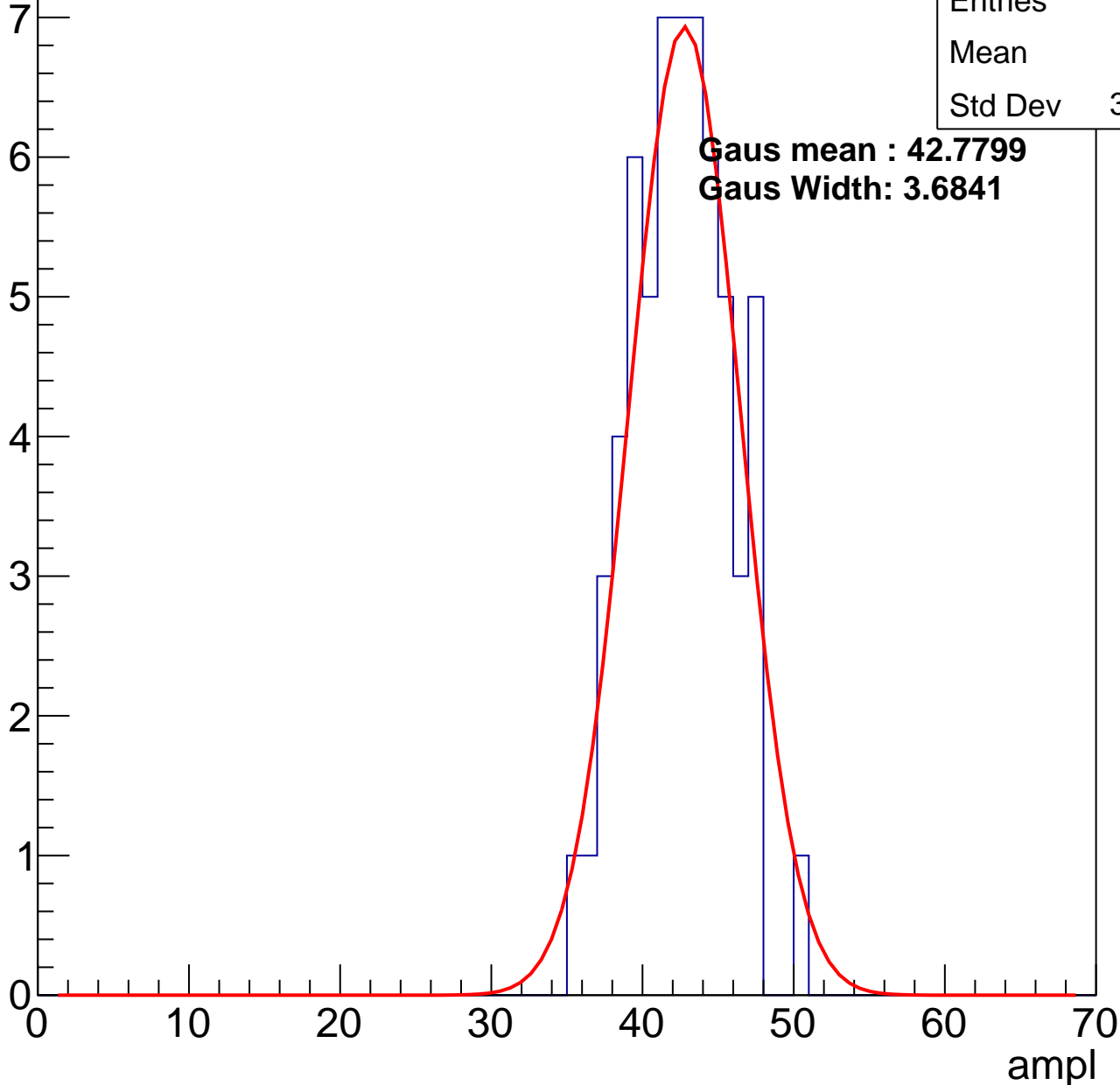
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42
Std Dev	3.183

**Gaus mean : 42.7799**

**Gaus Width: 3.6841**



# B1L000S, U23-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	48.05
Std Dev	3.013

Entry

10

8

6

4

2

0

0

10

20

30

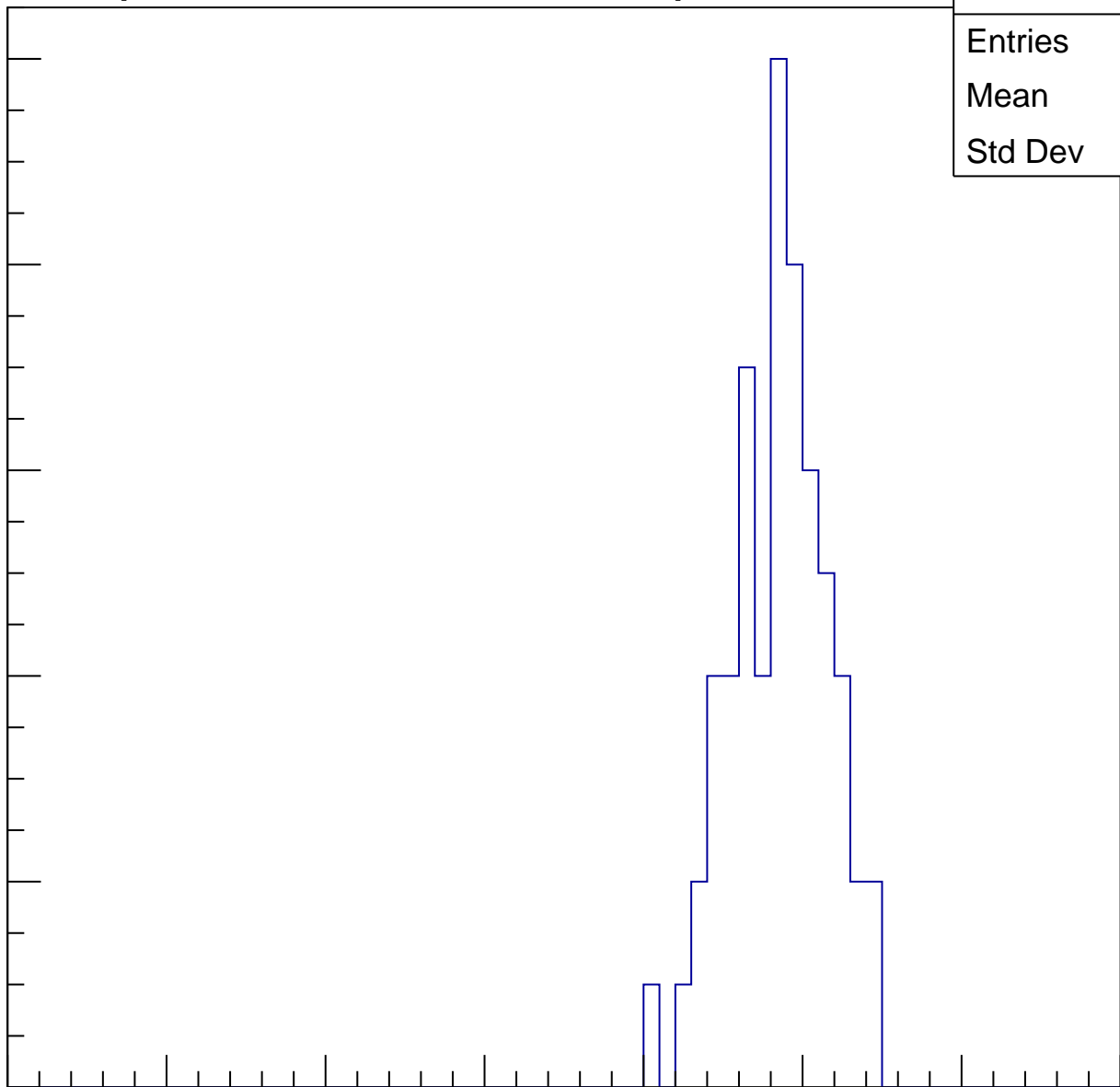
40

50

60

70

ampl



# B1L000S, U23-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	53.96
Std Dev	2.816

Entry

10

8

6

4

2

0

0

10

20

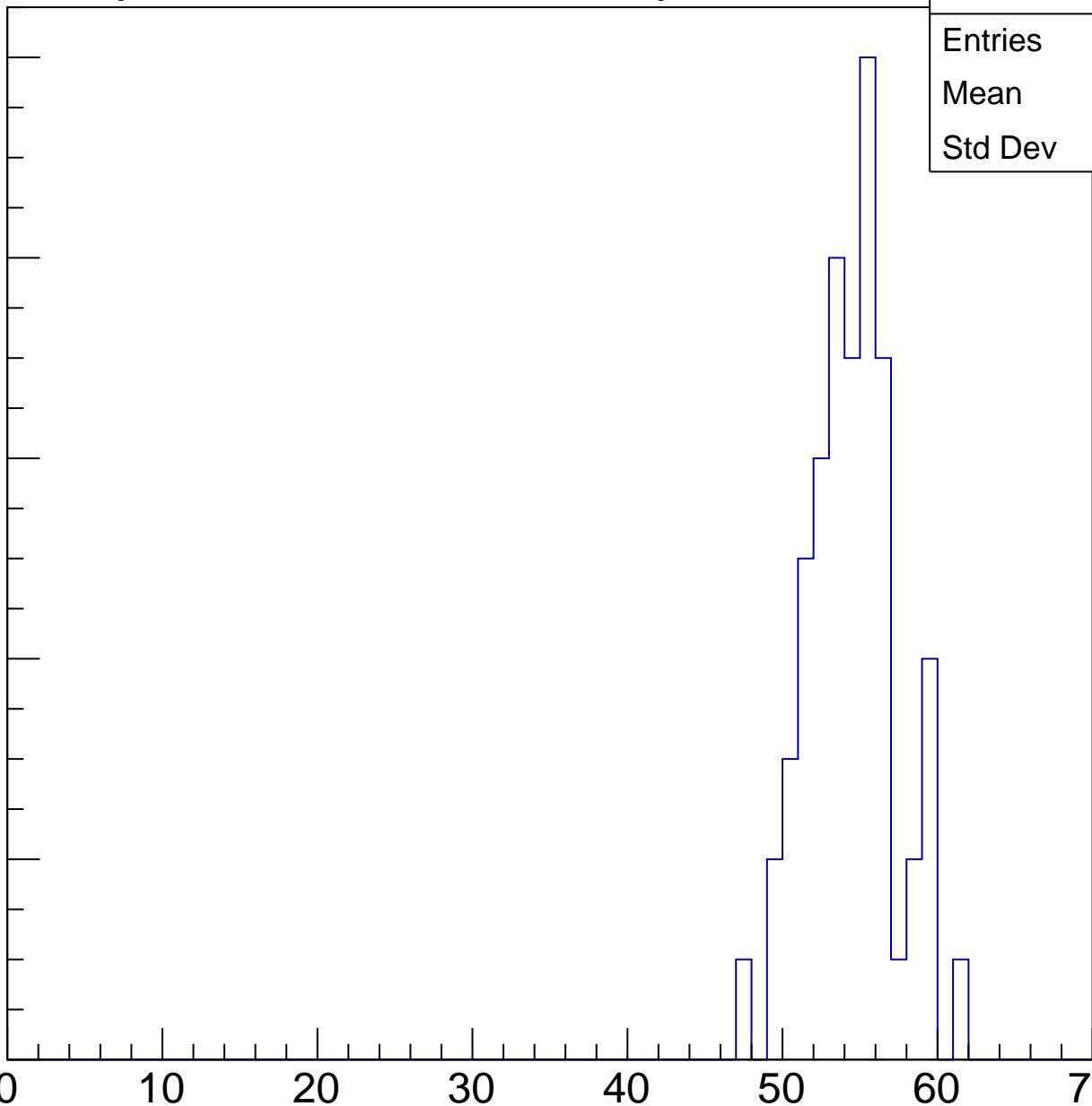
30

40

50

60

ampl



# B1L000S, U23-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 62

Mean 58.71

Std Dev 7.836

8

6

4

2

0

0

10

20

30

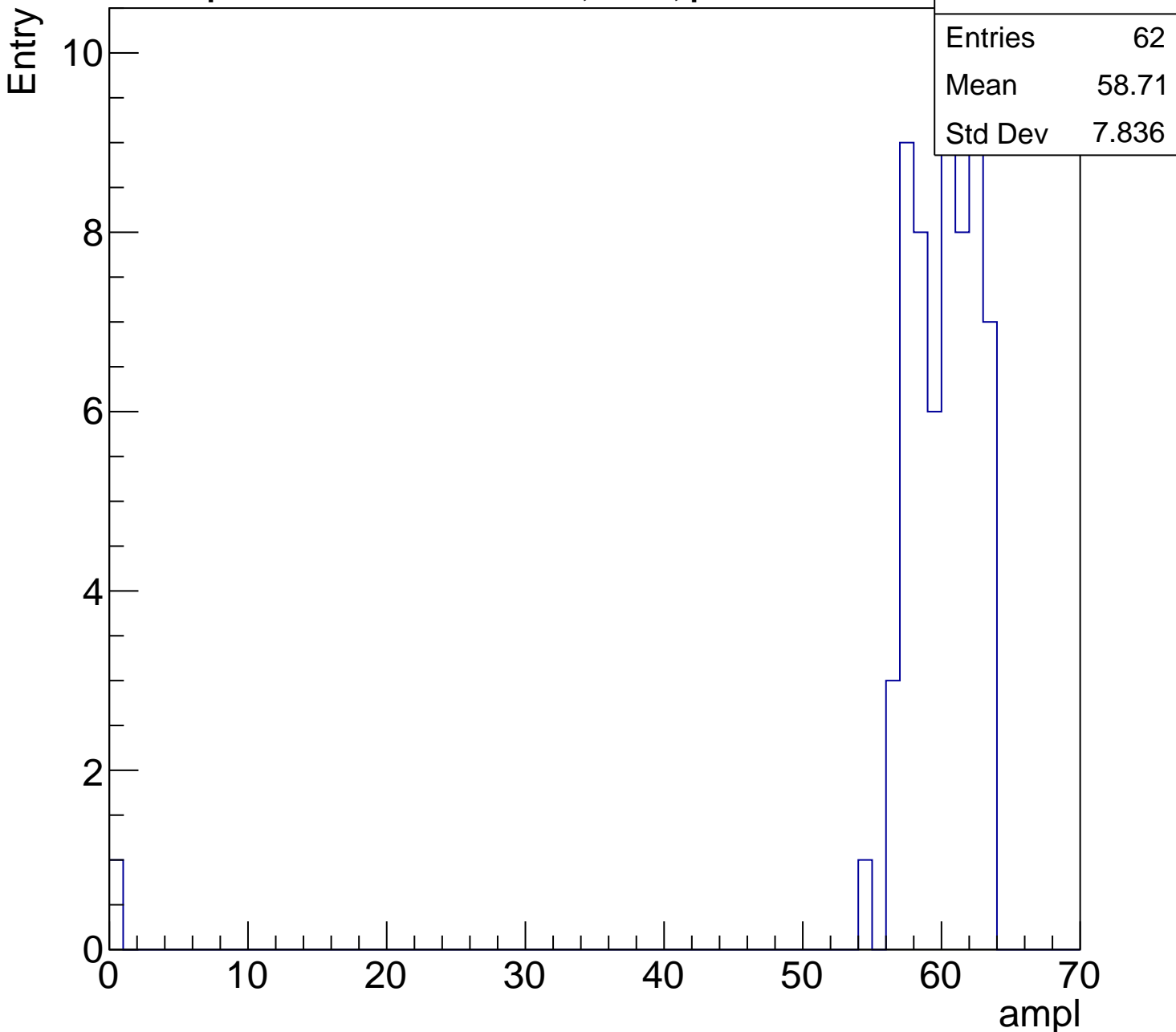
40

50

60

ampl

70



# B1L000S, U23-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	62.4
Std Dev	0.8



# B1L000S, U23-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch24, adc0

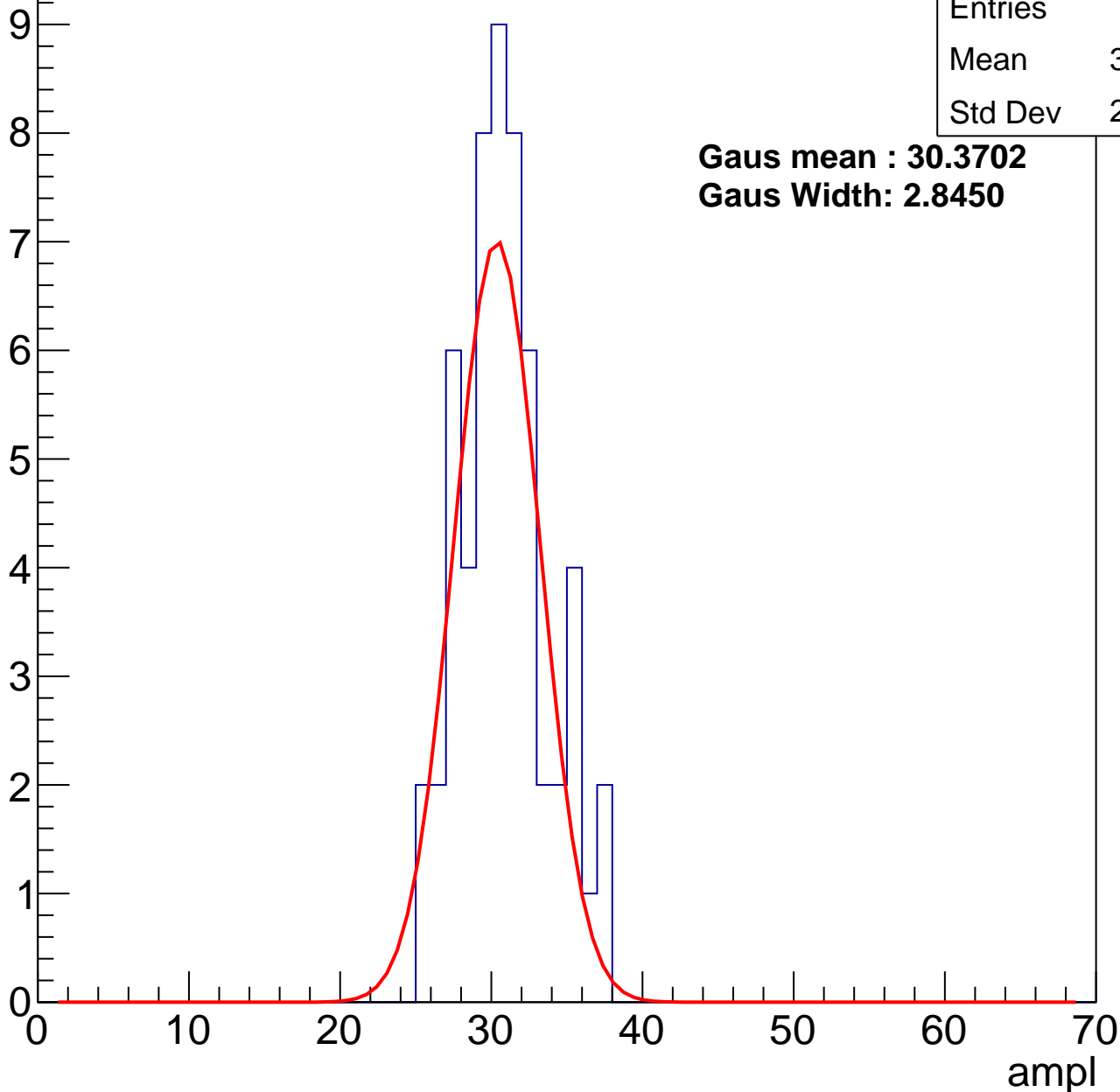
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	30.39
Std Dev	2.889

**Gaus mean : 30.3702**

**Gaus Width: 2.8450**



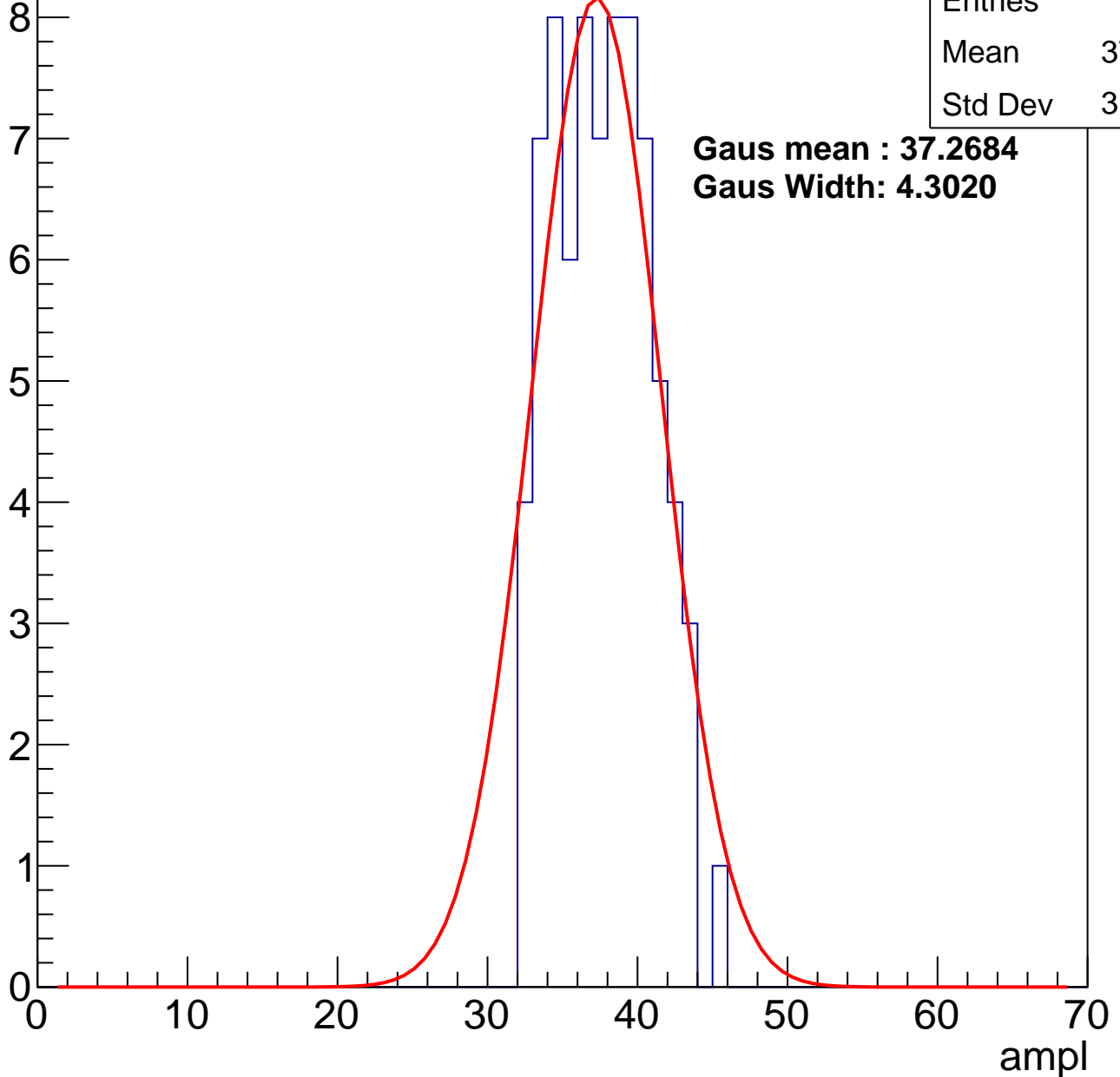
# B1L000S, U23-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	37.25
Std Dev	3.175

**Gaus mean : 37.2684**  
**Gaus Width: 4.3020**



# B1L000S, U23-ch24, adc2

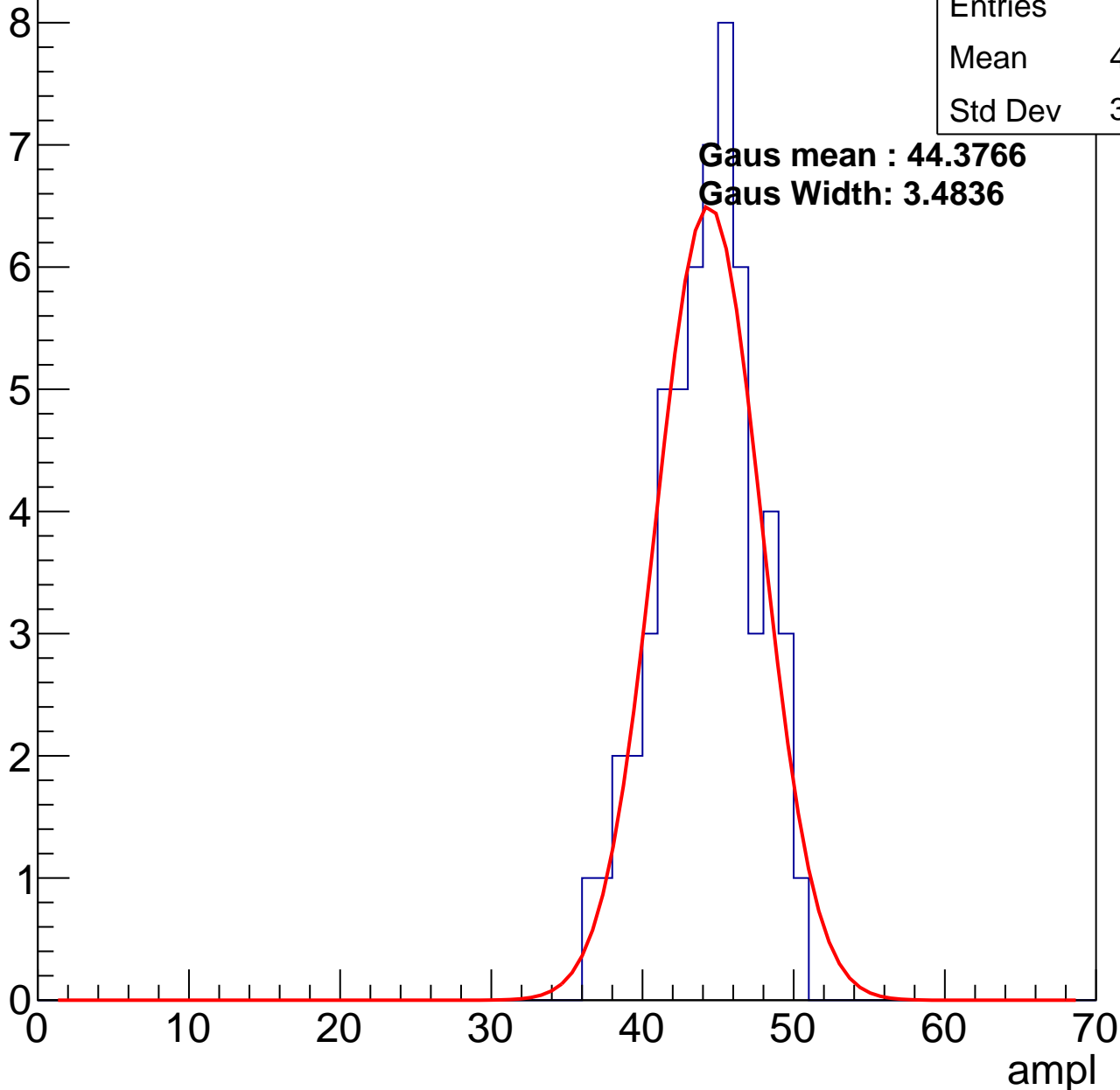
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	43.75
Std Dev	3.202

**Gaus mean : 44.3766**

**Gaus Width: 3.4836**

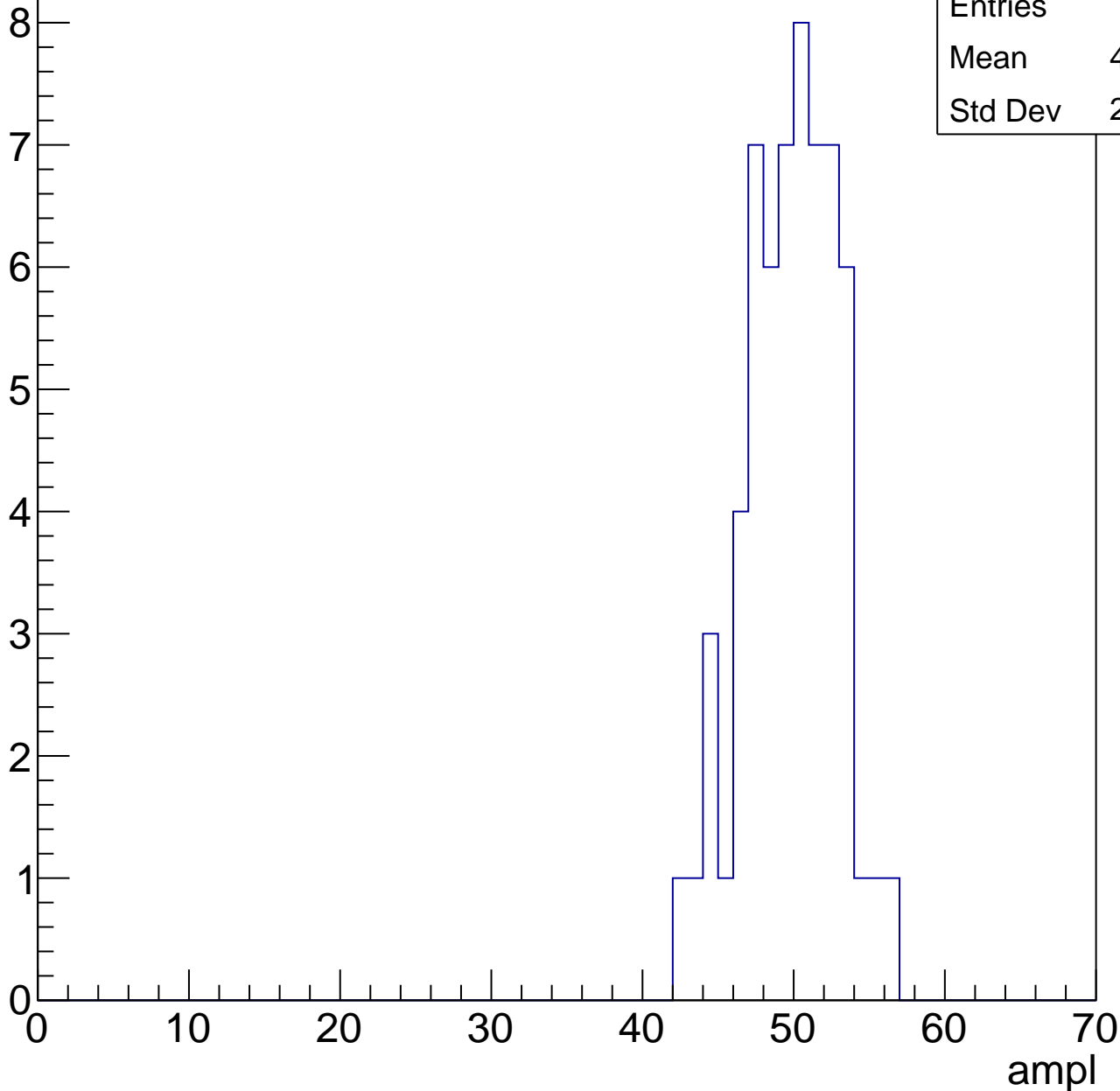


# B1L000S, U23-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	49.34
Std Dev	2.986

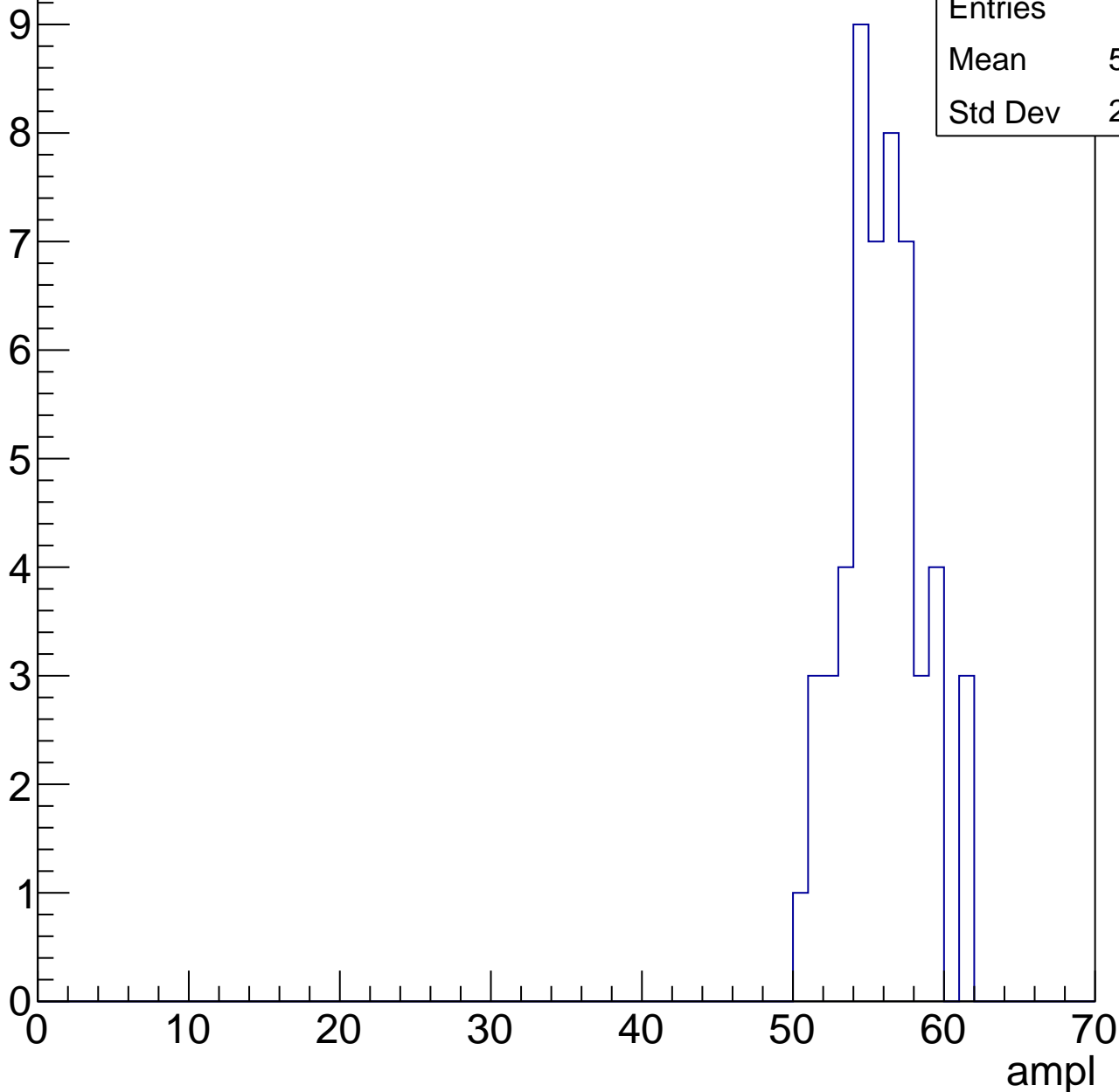


# B1L000S, U23-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	55.42
Std Dev	2.597

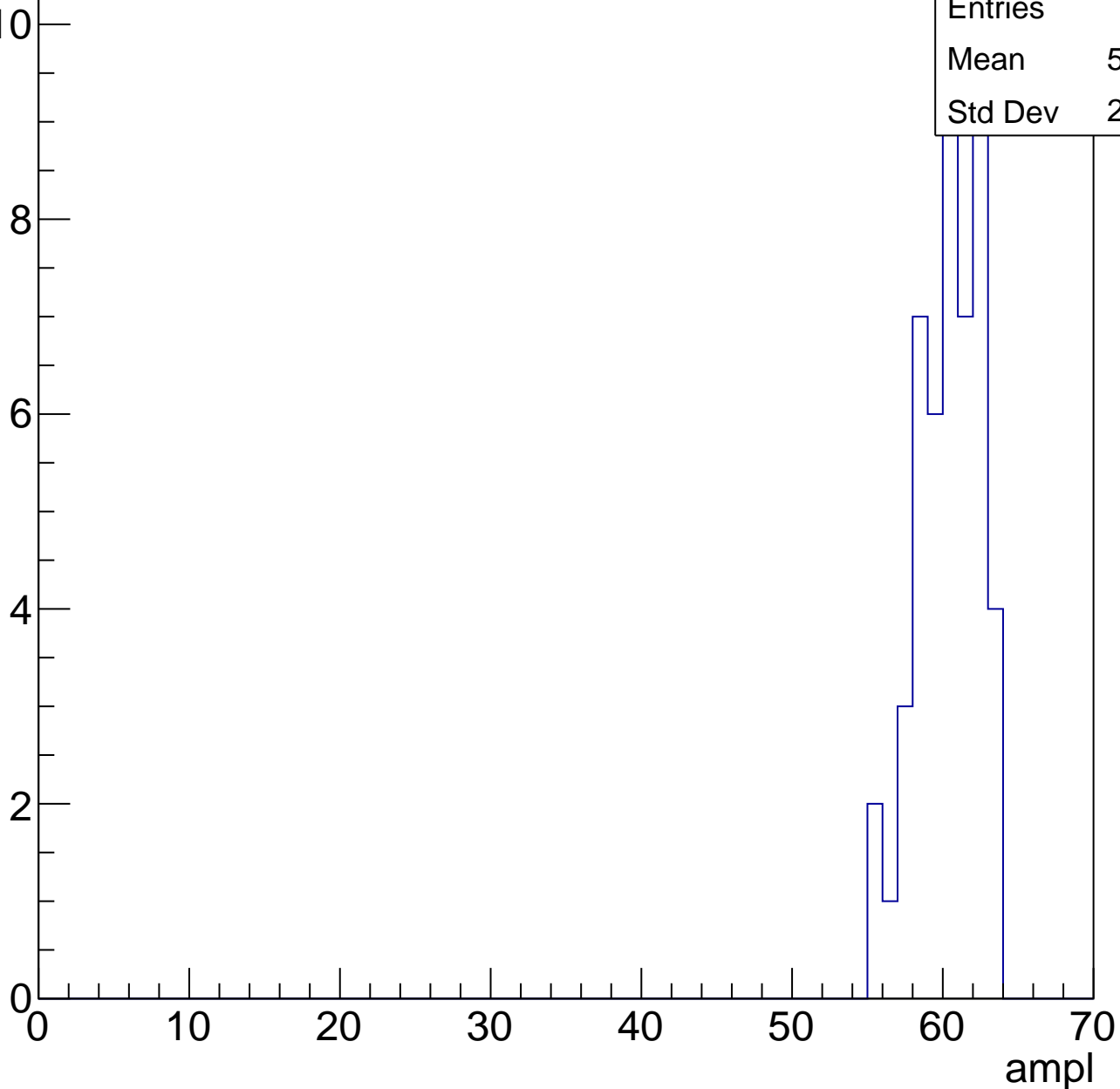


# B1L000S, U23-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	59.92
Std Dev	2.048

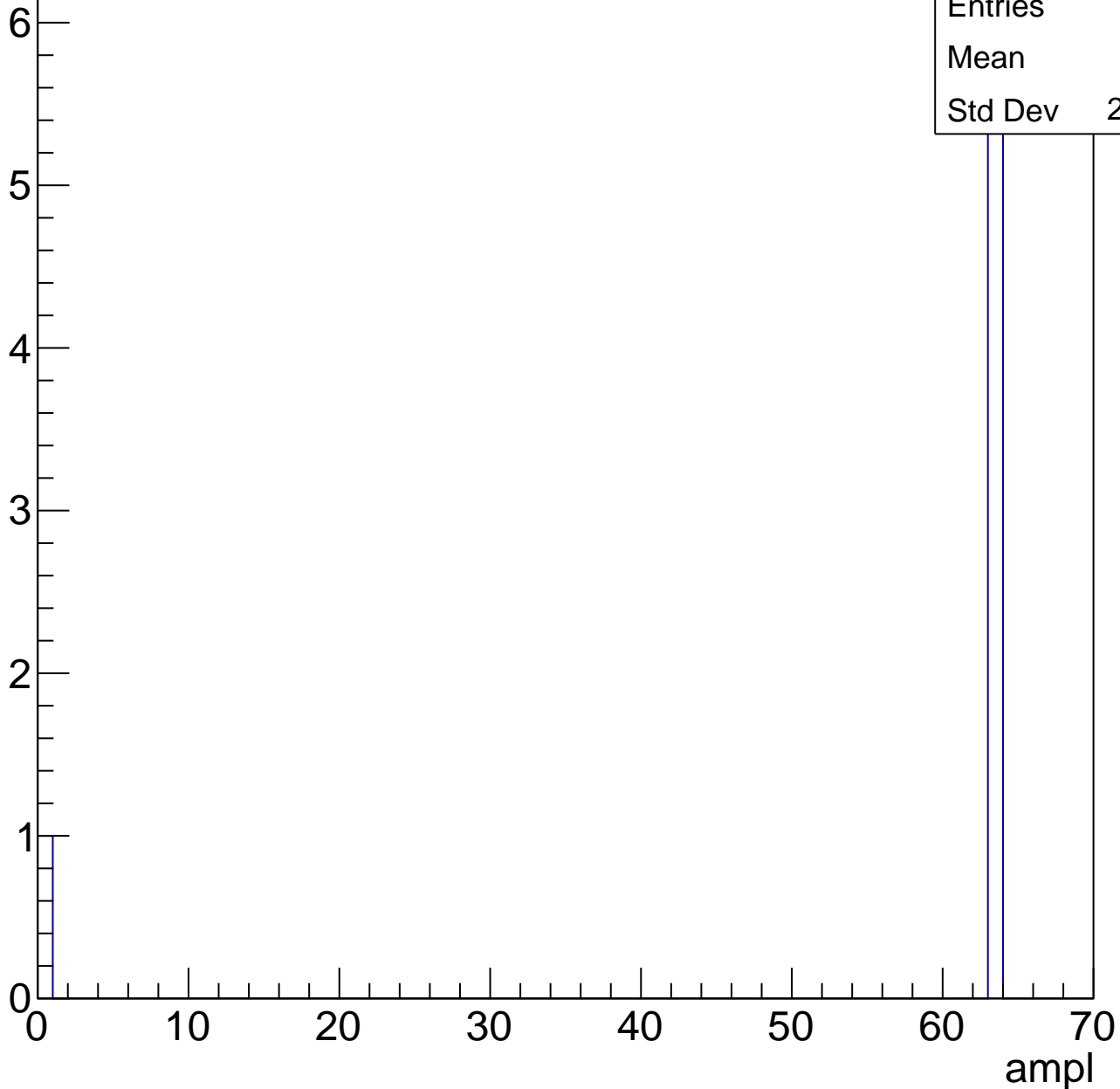


# B1L000S, U23-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	7
Mean	54
Std Dev	22.05





# B1L000S, U23-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch25, adc0

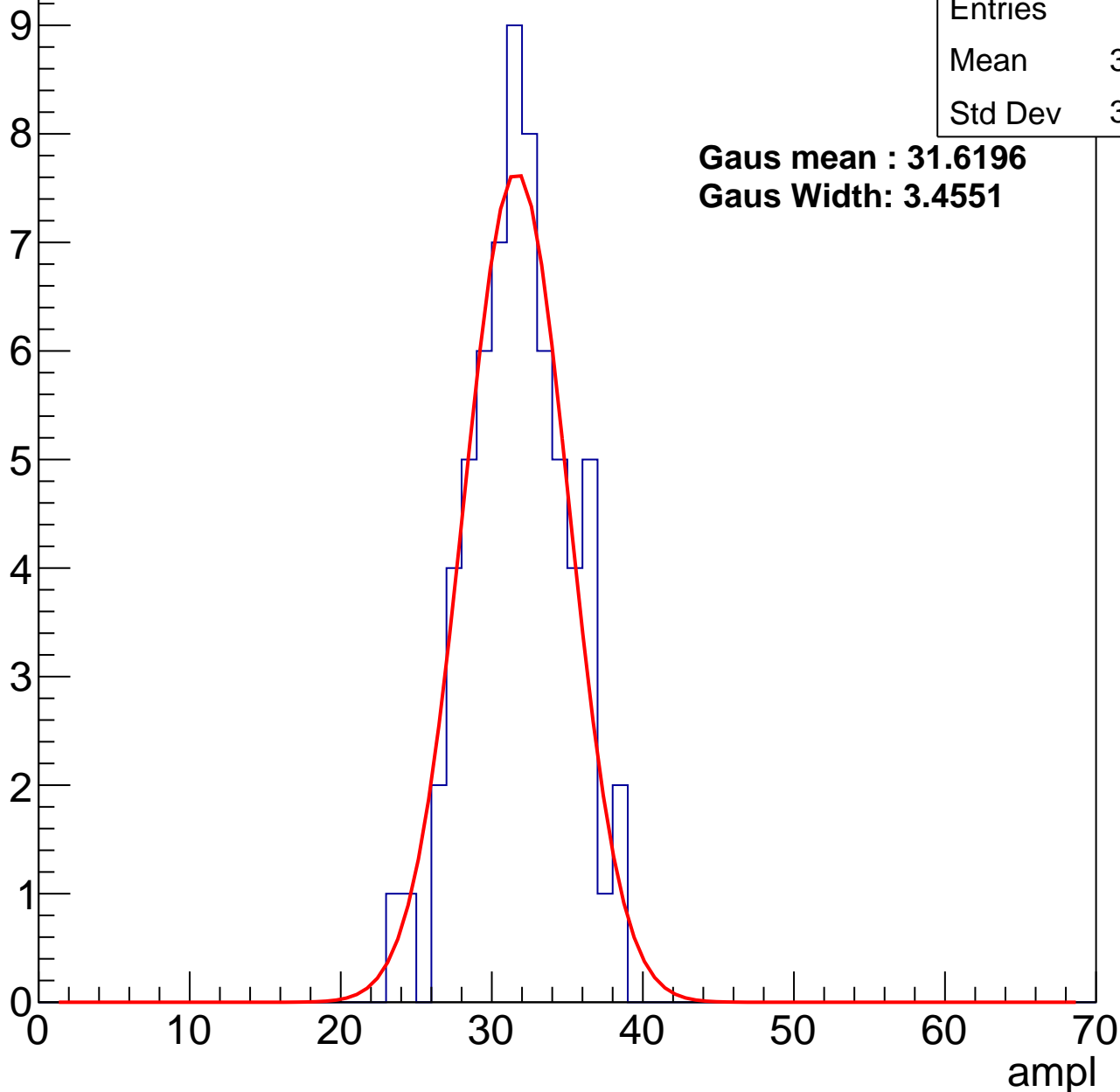
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	31.32
Std Dev	3.248

**Gaus mean : 31.6196**

**Gaus Width: 3.4551**



# B1L000S, U23-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	37.91
Std Dev	3.059

**Gaus mean : 38.3489**

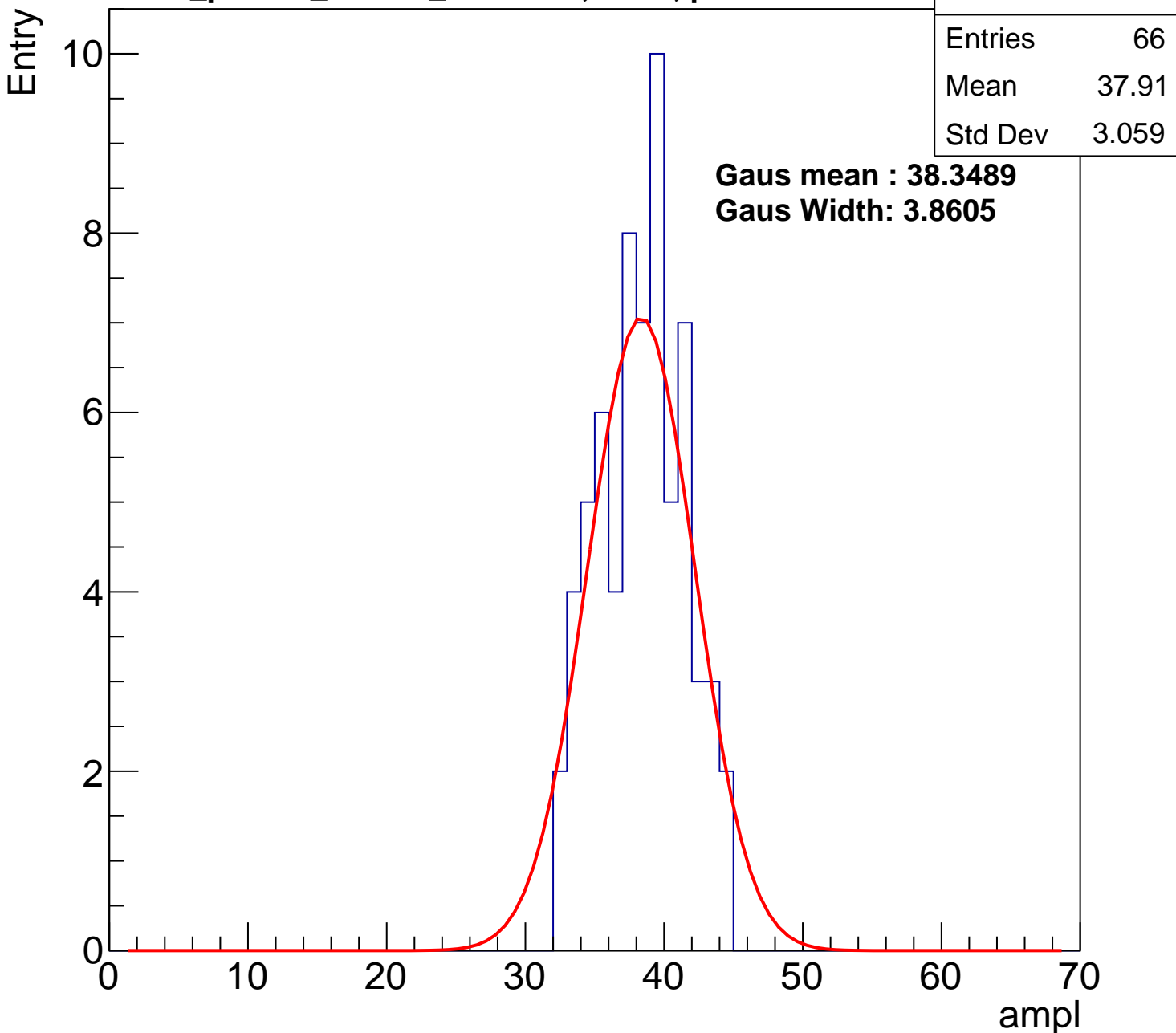
**Gaus Width: 3.8605**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch25, adc2

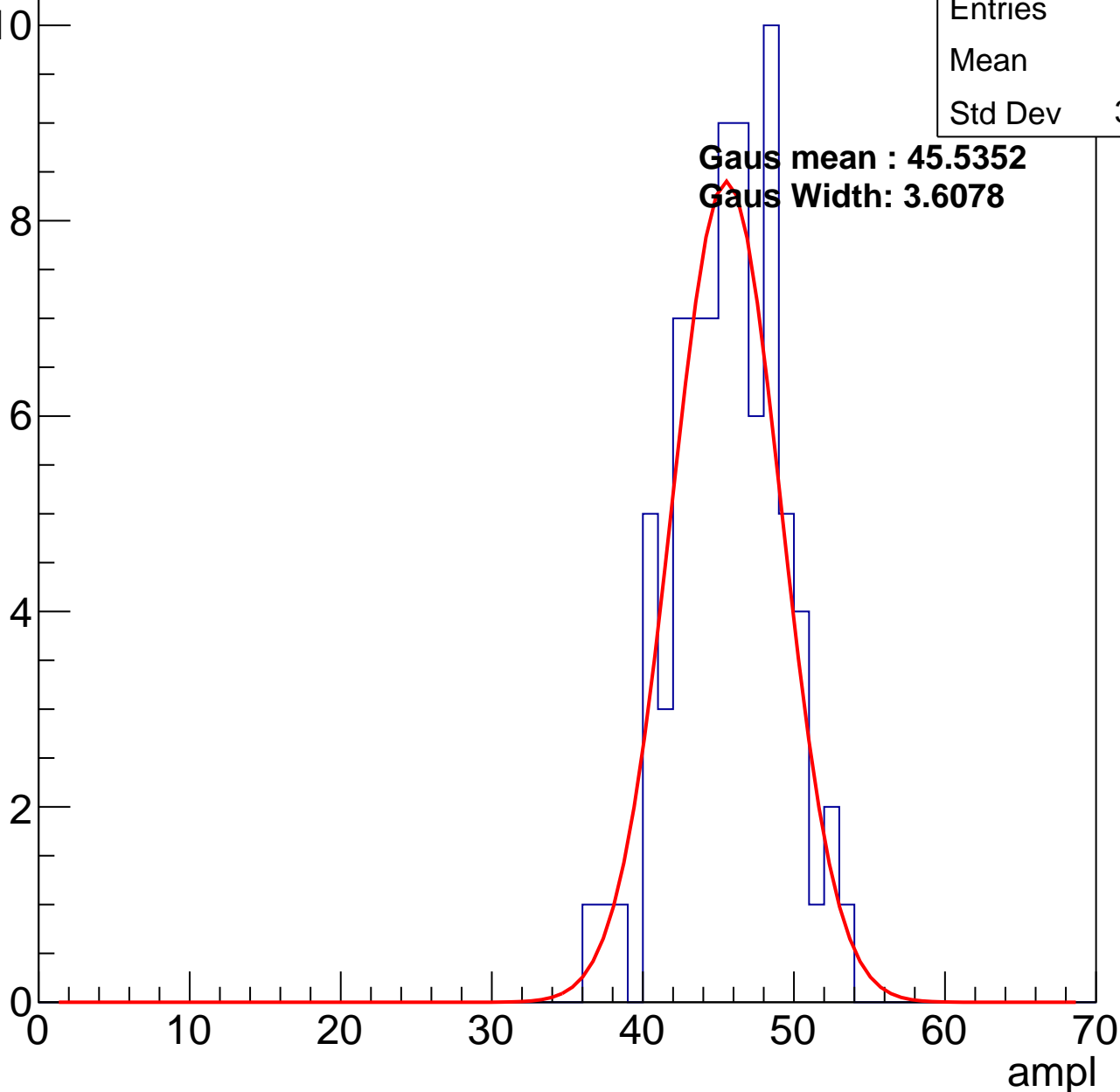
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	45.2
Std Dev	3.491

Entry

Gaus mean : 45.5352

Gaus Width: 3.6078

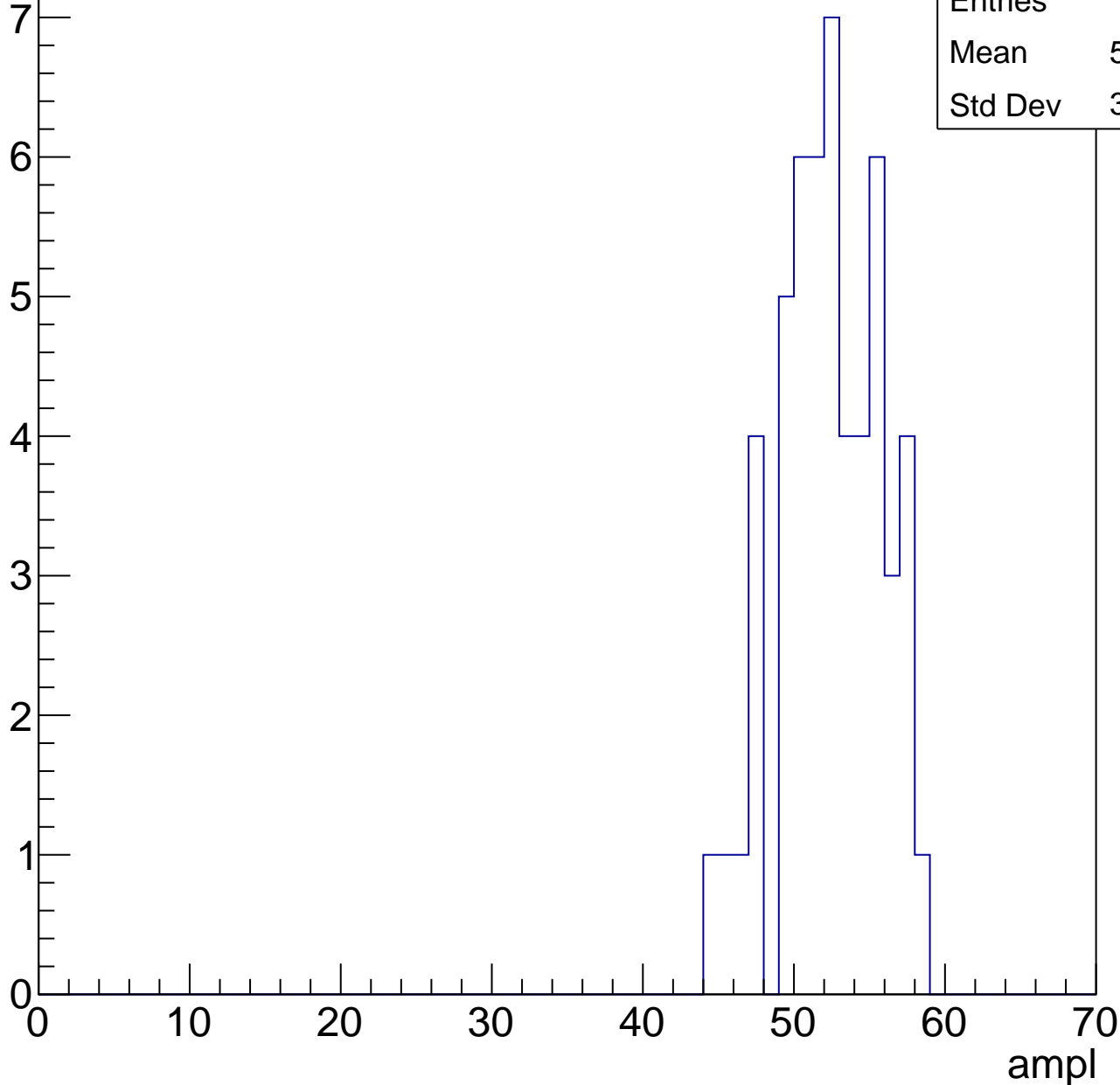


# B1L000S, U23-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	51.89
Std Dev	3.312



# B1L000S, U23-ch25, adc4

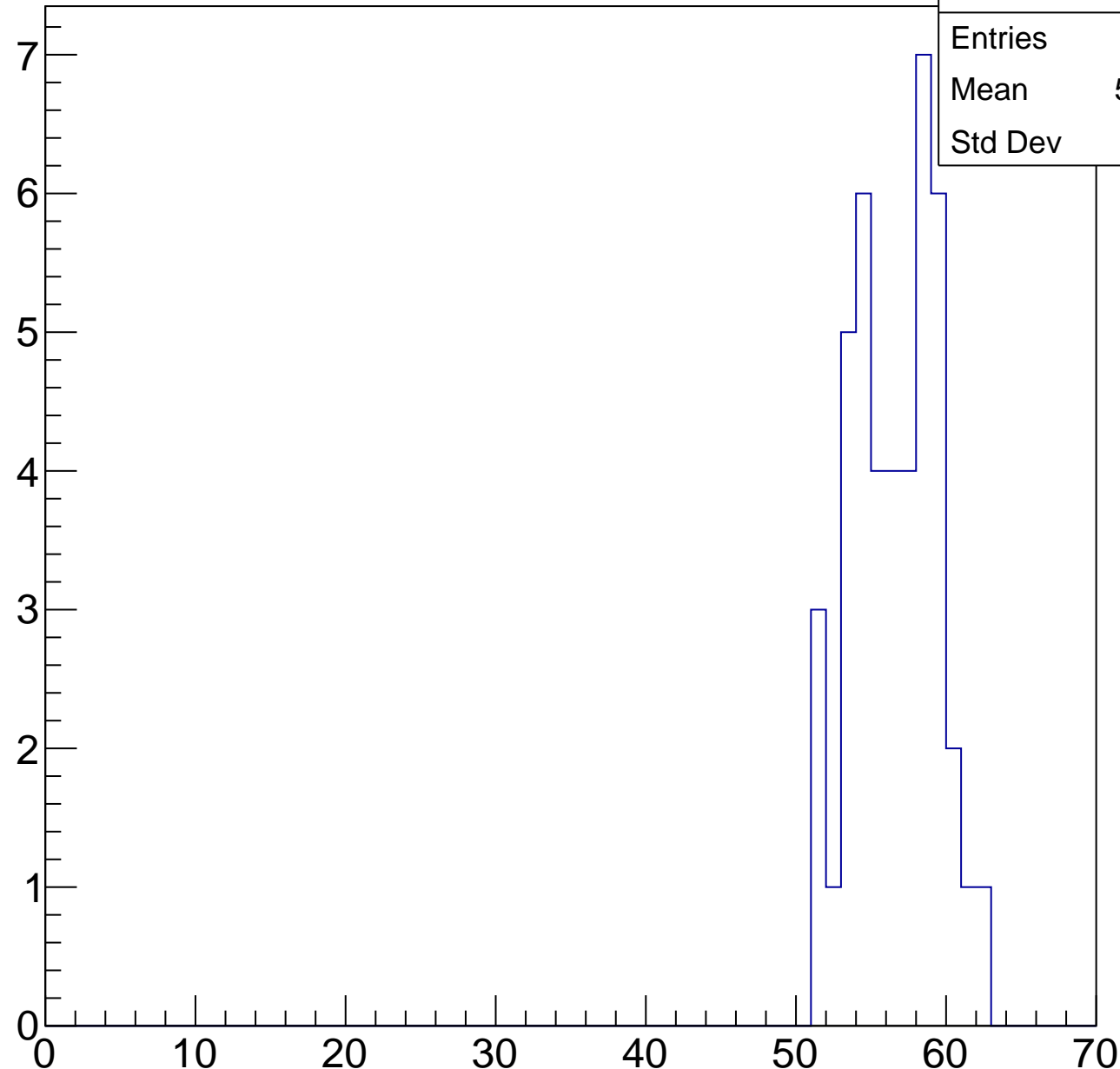
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	56.11
Std Dev	2.79

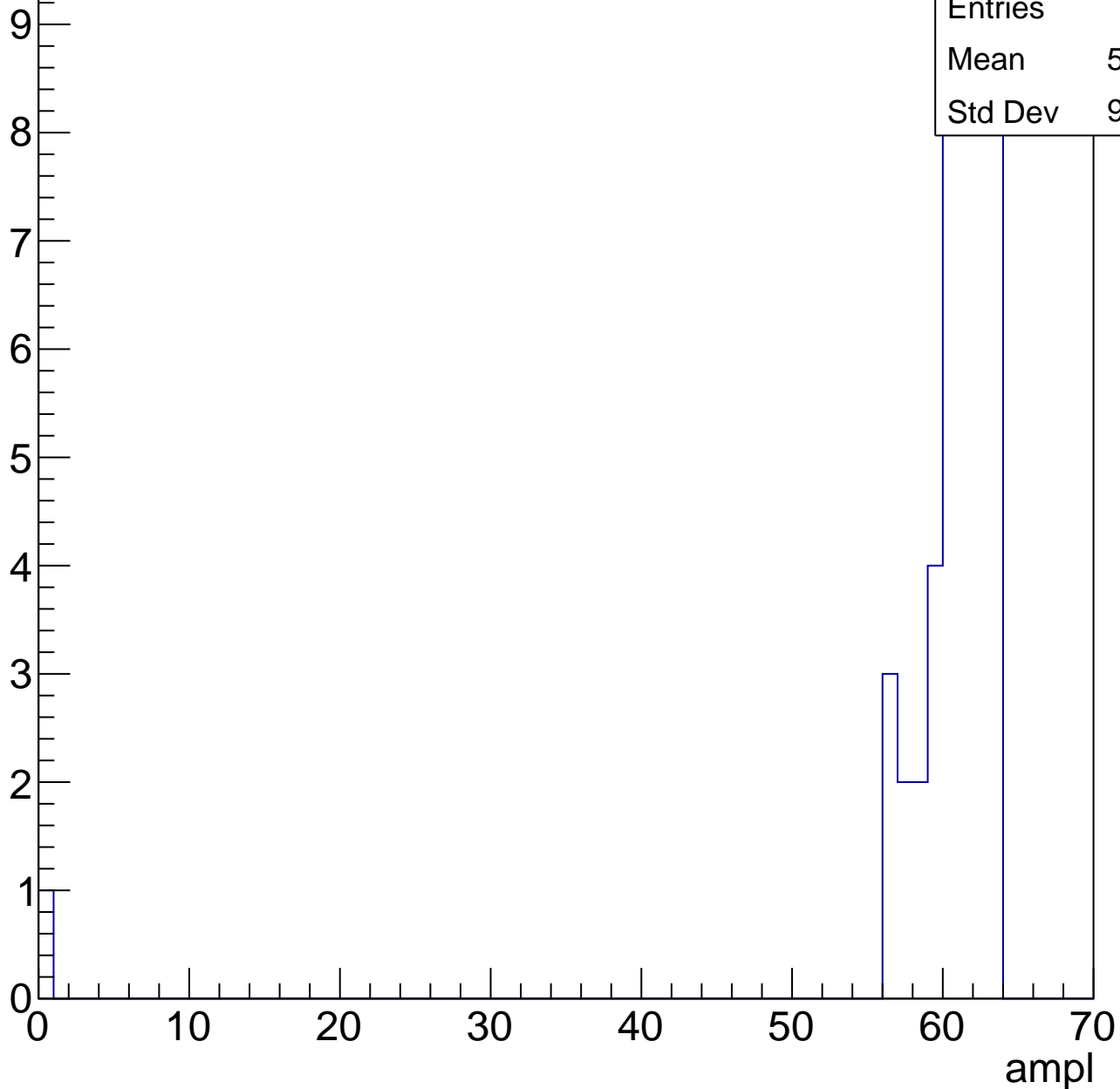
ampl



# B1L000S, U23-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch26, adc0

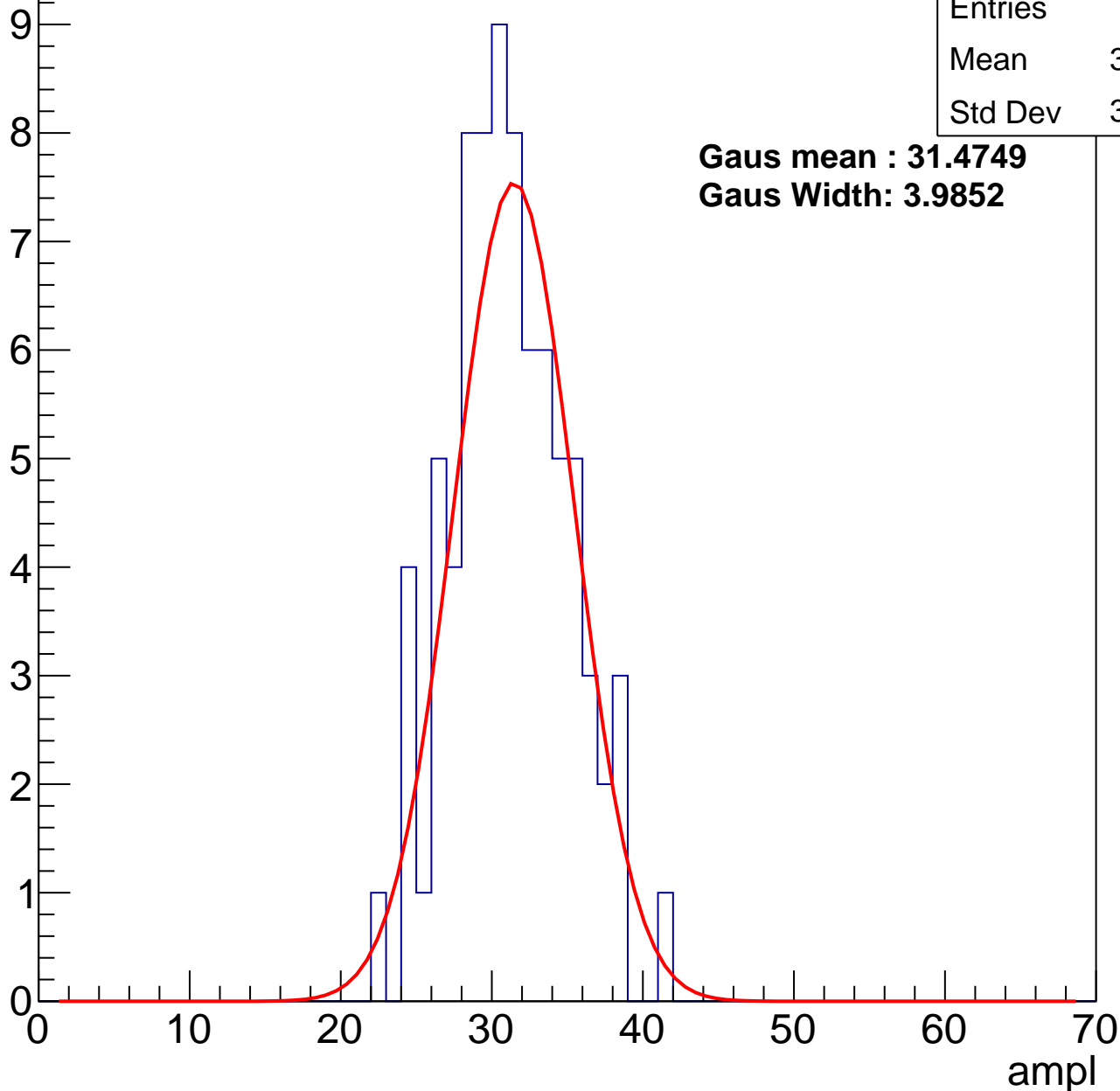
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	30.72
Std Dev	3.828

**Gaus mean : 31.4749**

**Gaus Width: 3.9852**



# B1L000S, U23-ch26, adc1

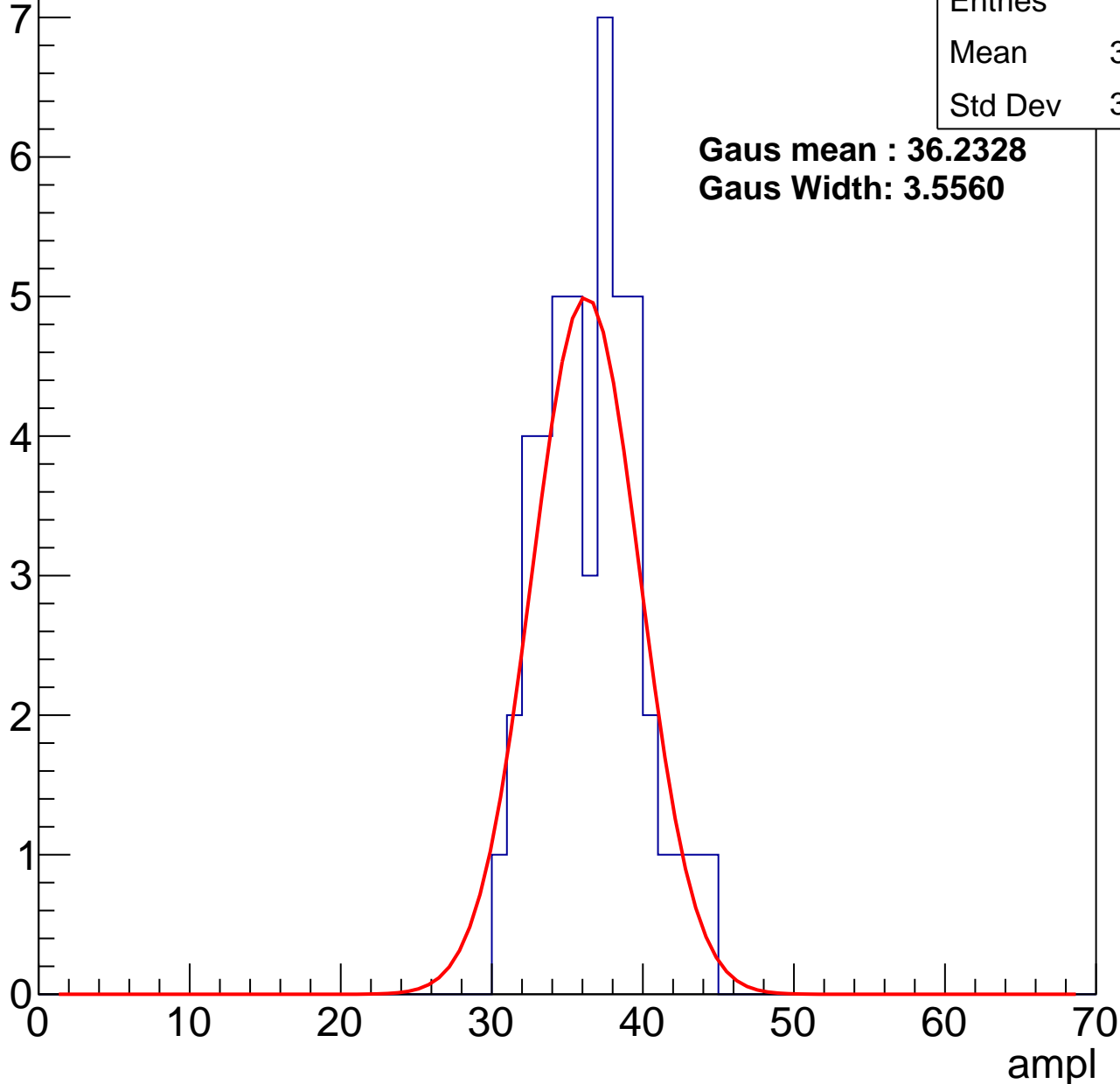
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	36.15
Std Dev	3.222

**Gaus mean : 36.2328**

**Gaus Width: 3.5560**



# B1L000S, U23-ch26, adc2

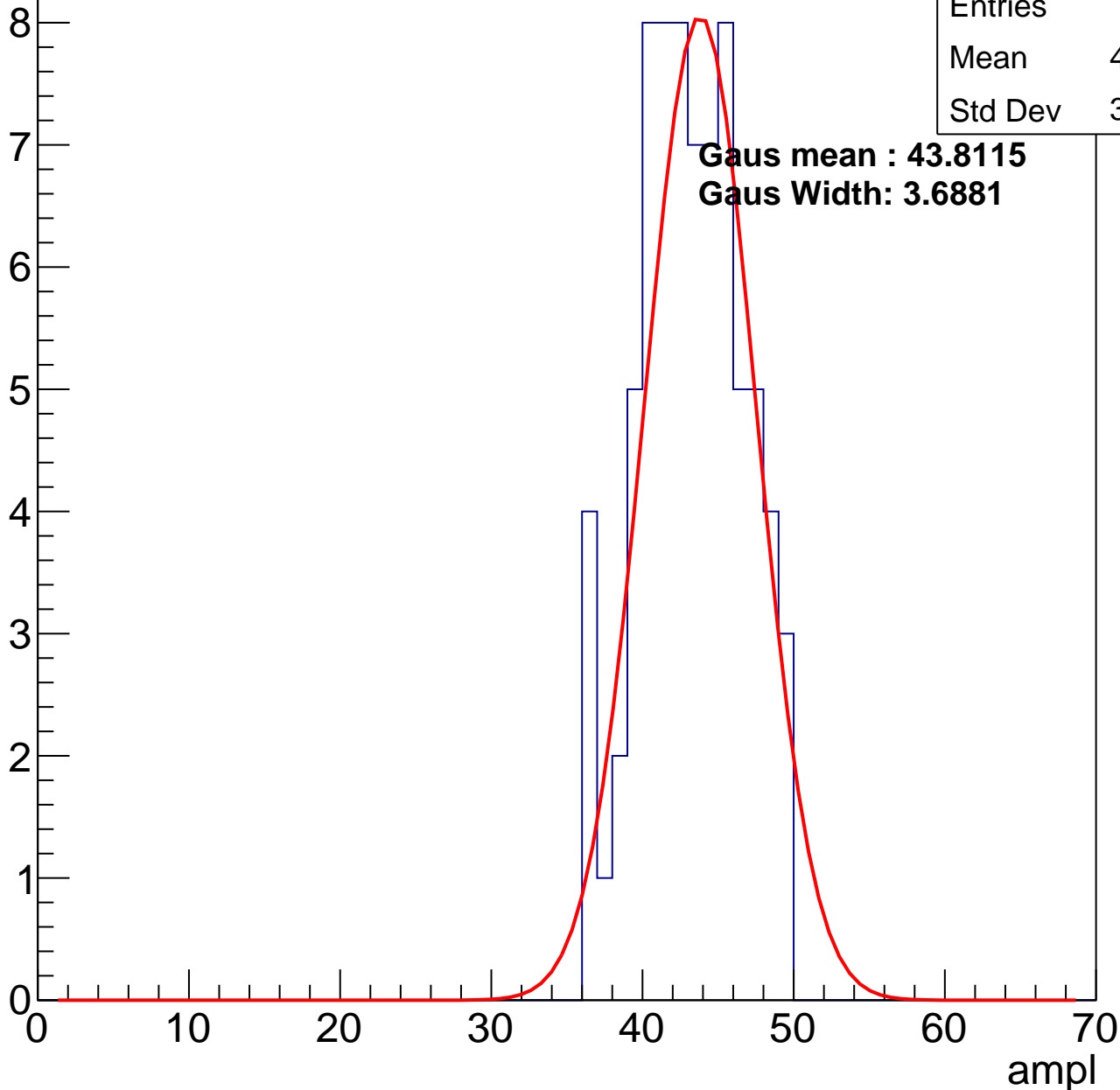
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.79
Std Dev	3.352

**Gaus mean : 43.8115**

**Gaus Width: 3.6881**

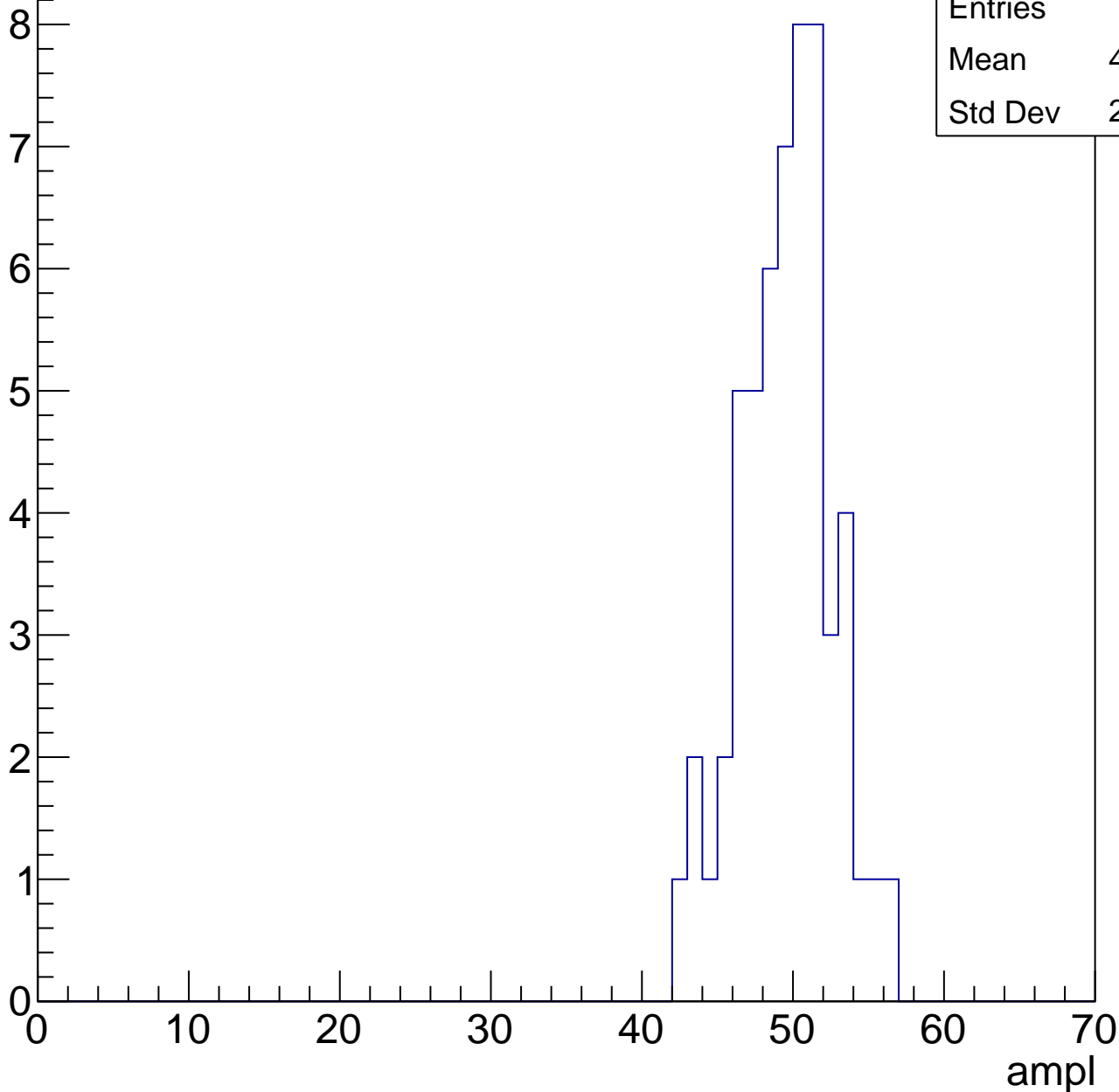


# B1L000S, U23-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

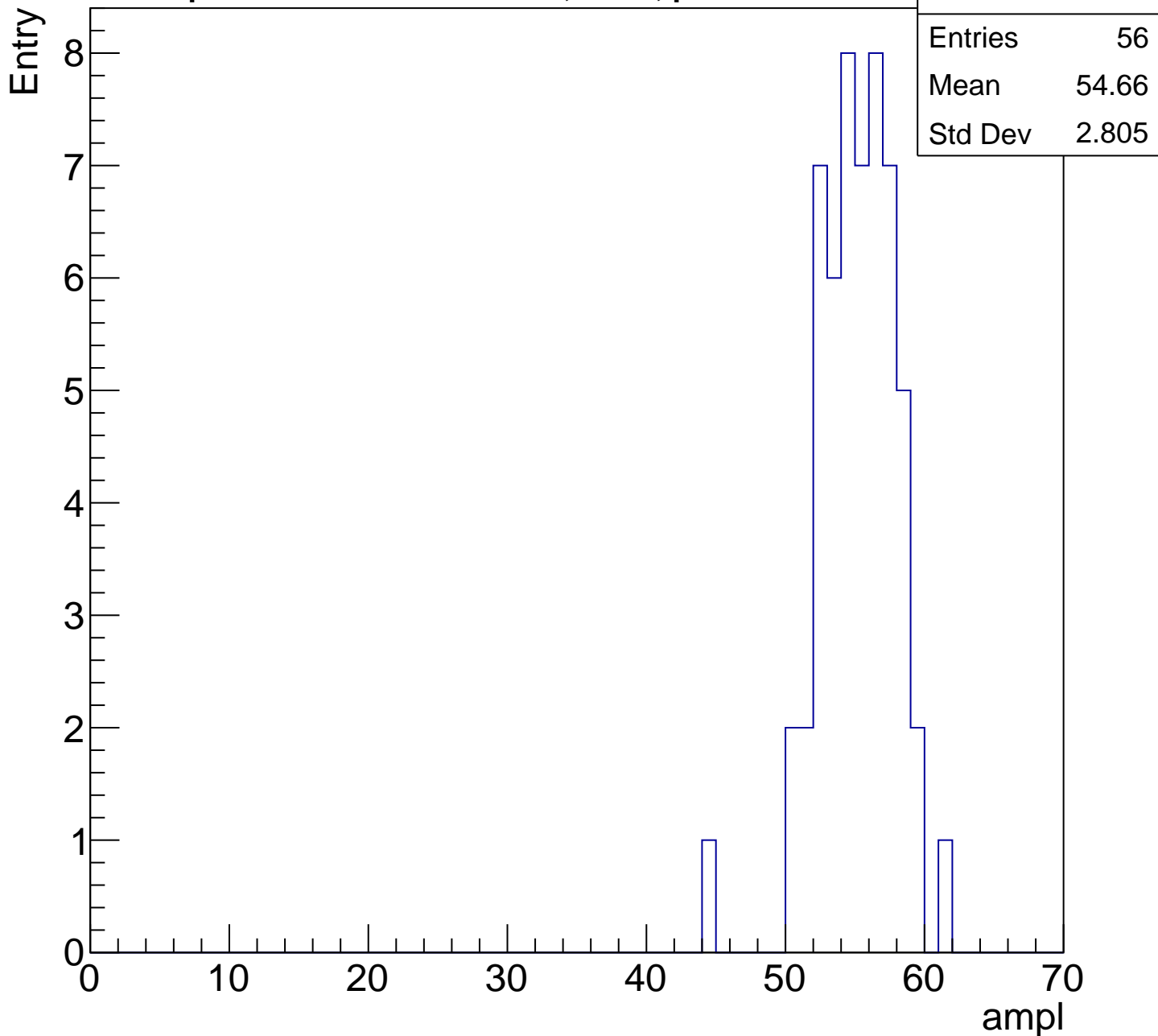
Entry

Entries	55
Mean	49.07
Std Dev	2.984



# B1L000S, U23-ch26, adc4

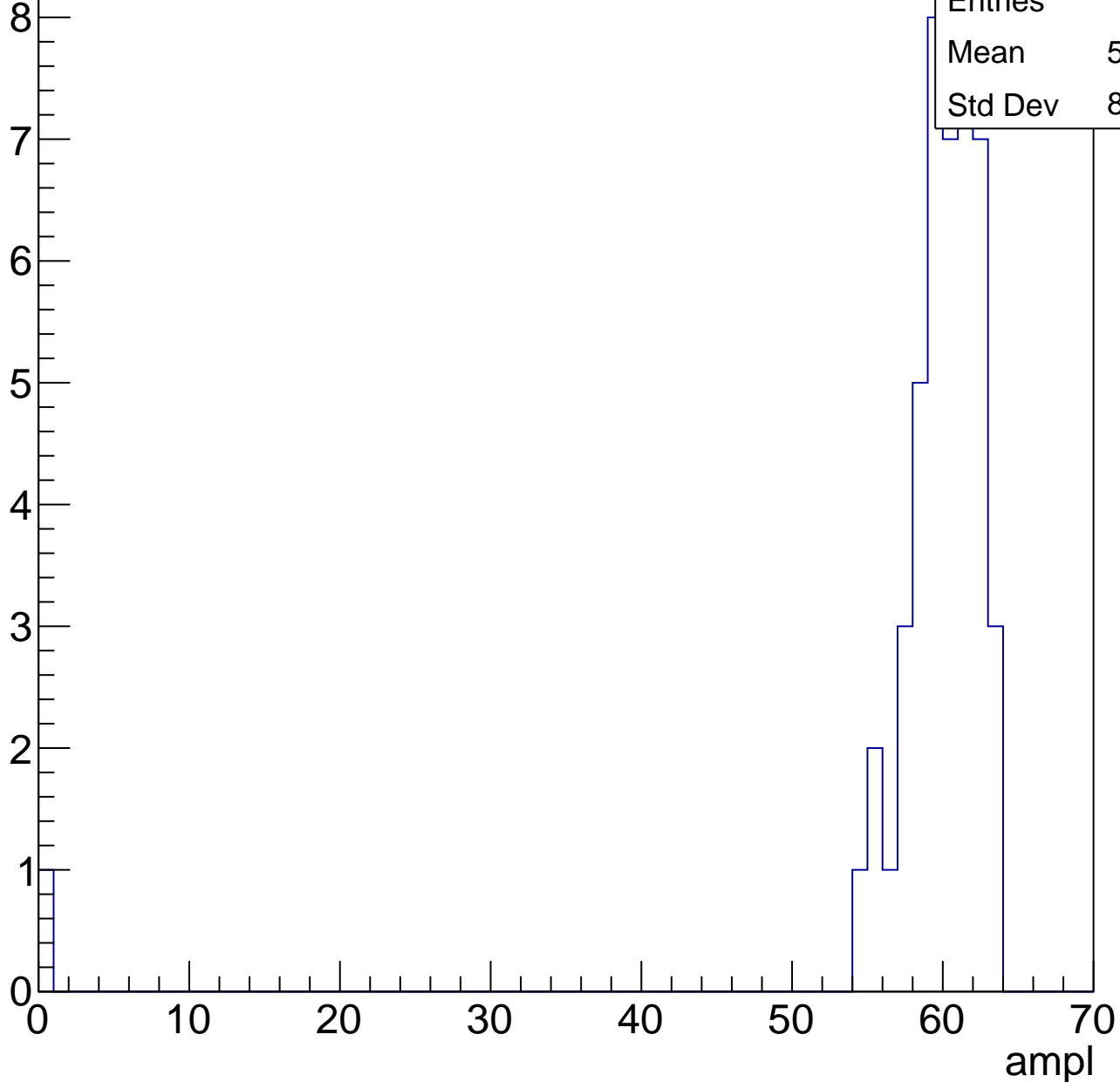
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

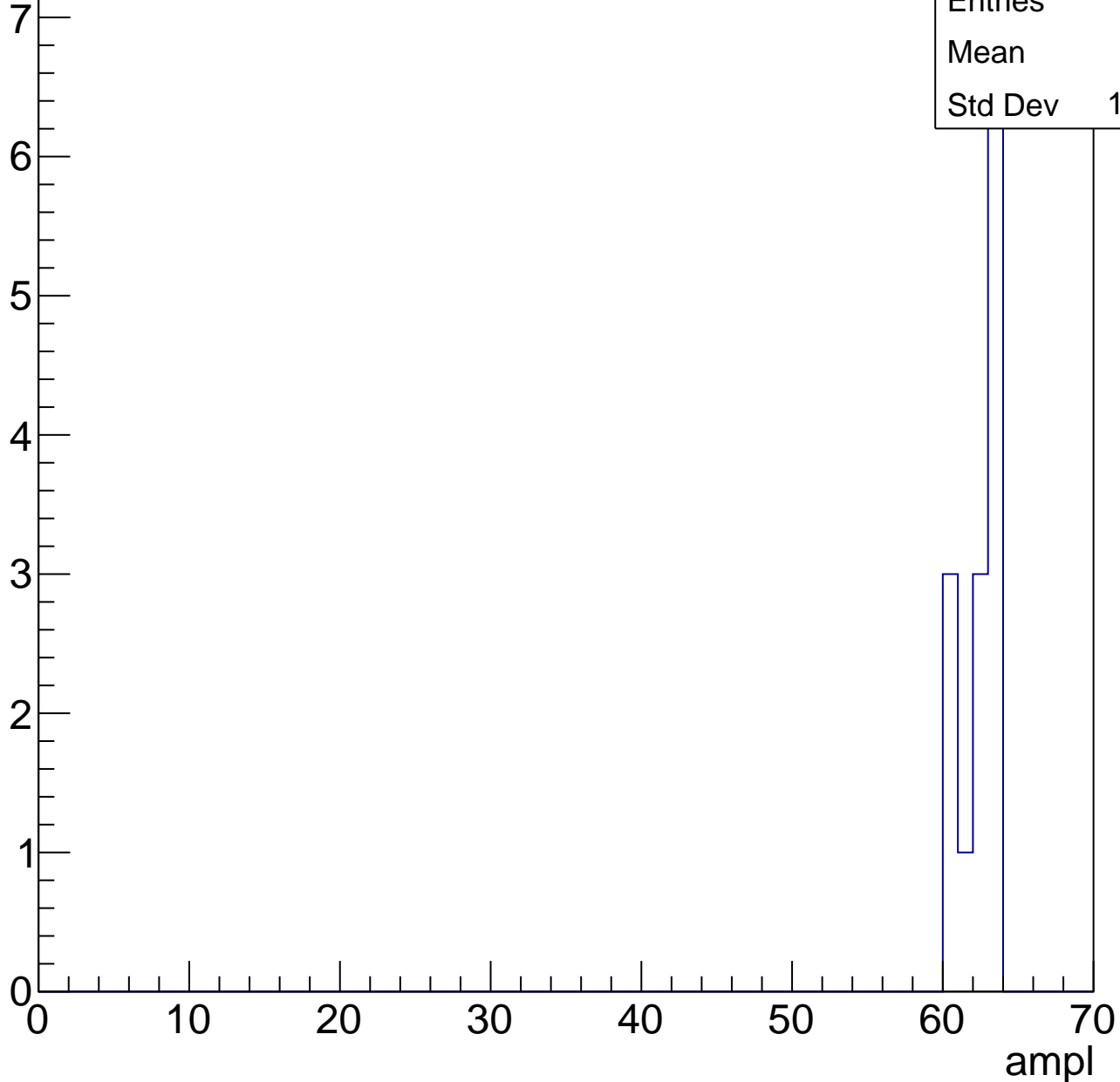
Entry



# B1L000S, U23-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	14
Mean	62
Std Dev	1.195



# B1L000S, U23-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L000S, U23-ch27, adc0

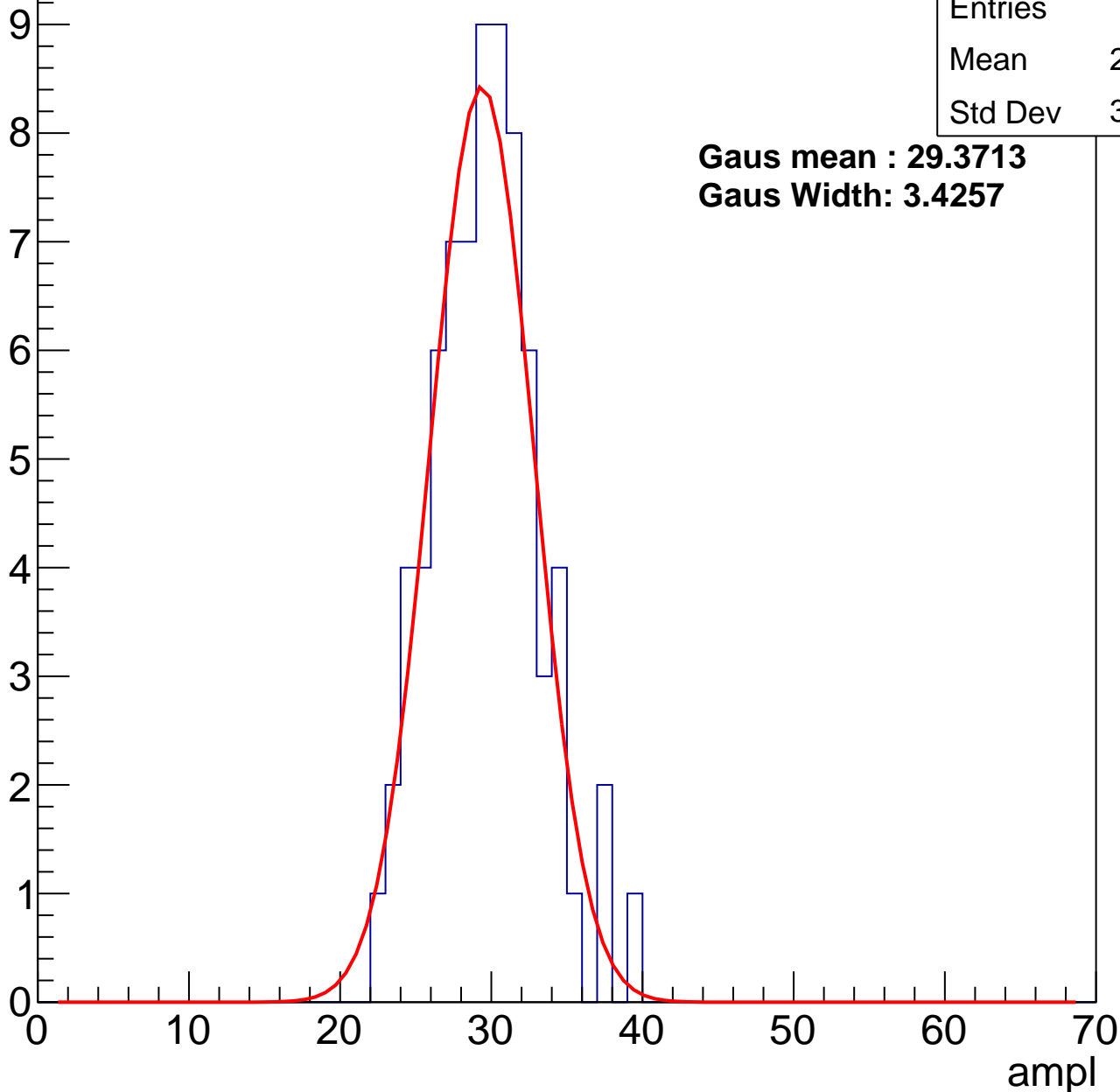
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29.18
Std Dev	3.446

**Gaus mean : 29.3713**

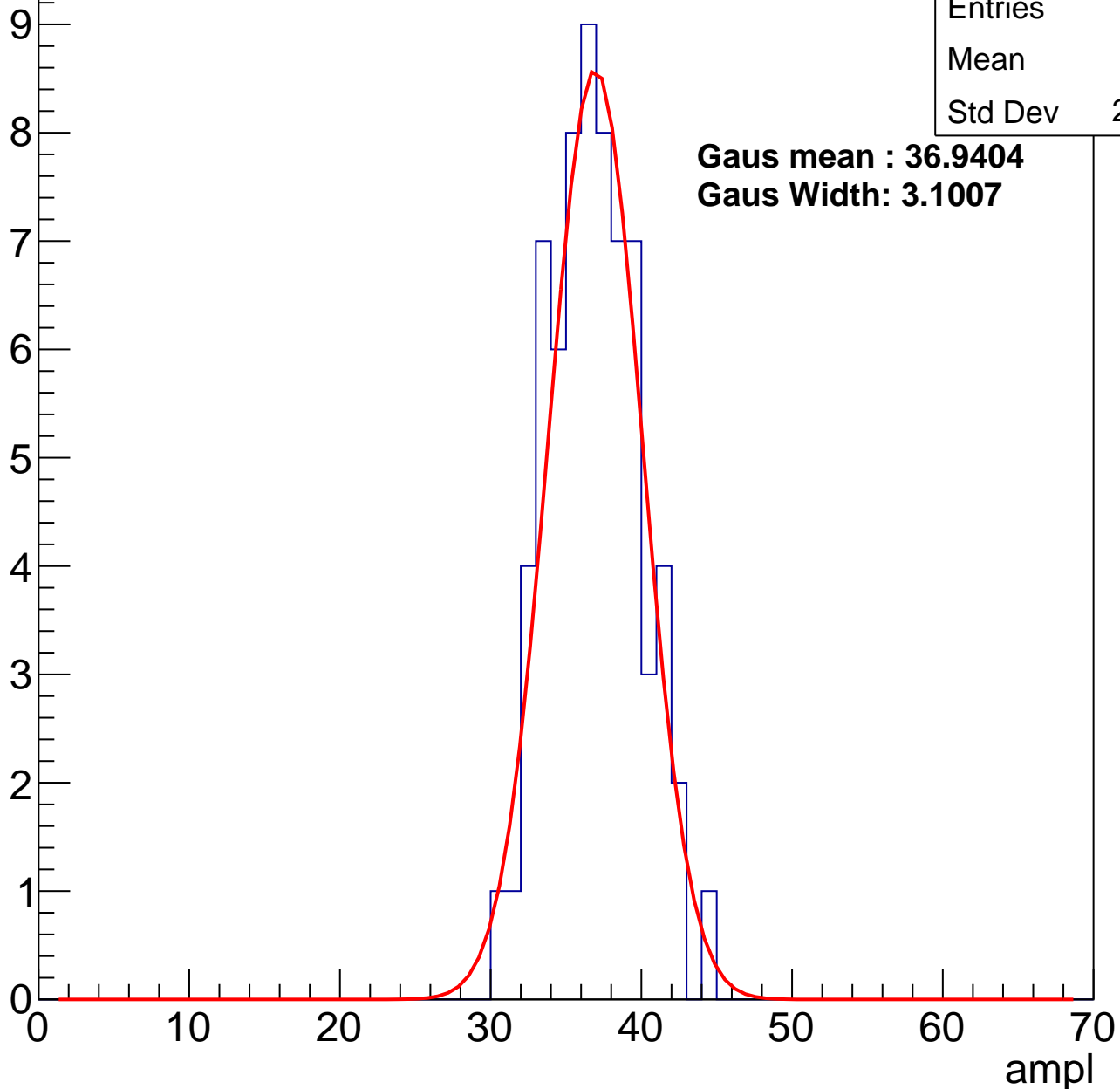
**Gaus Width: 3.4257**



# B1L000S, U23-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch27, adc2

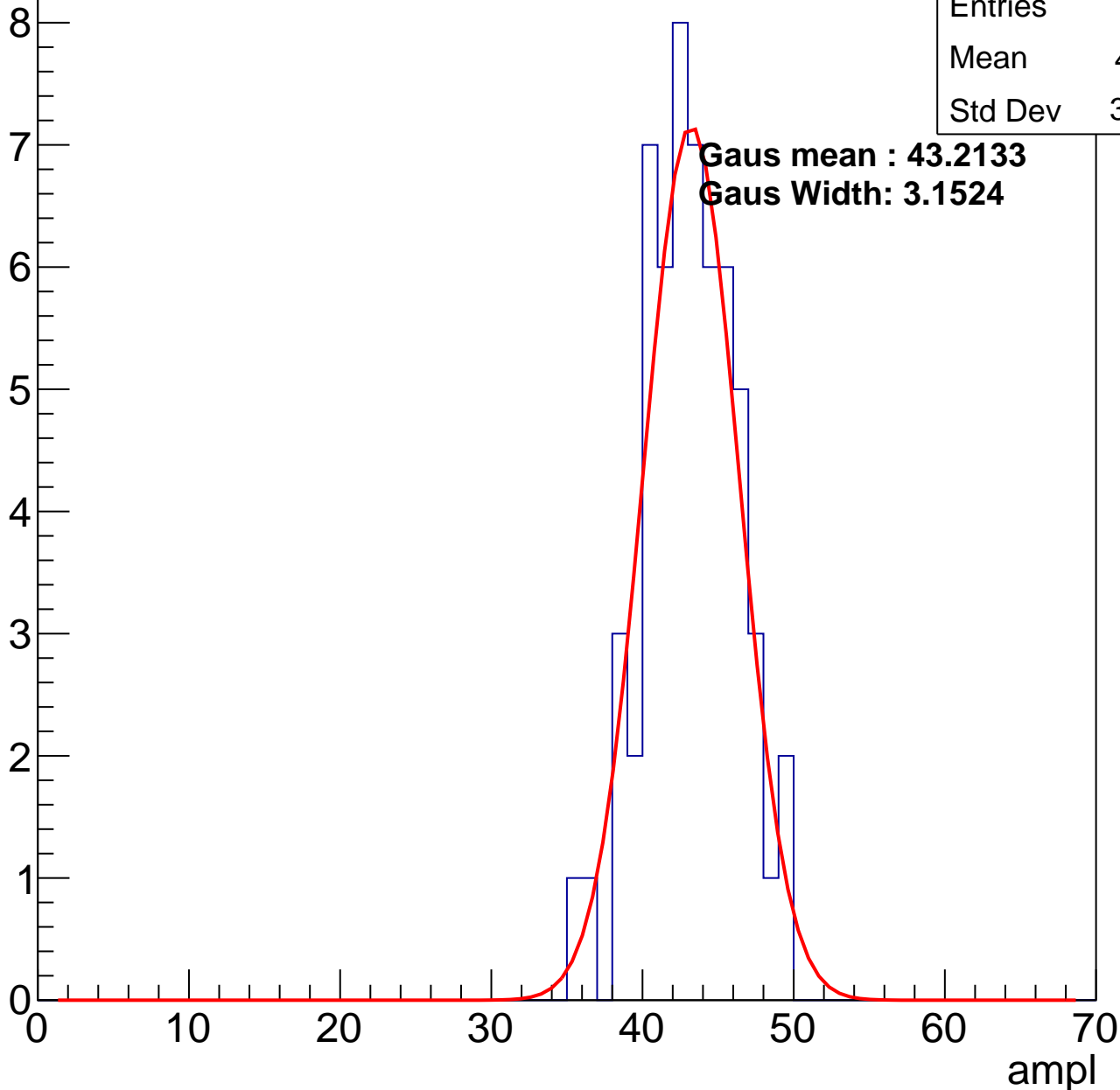
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.71
Std Dev	3.023

**Gaus mean : 43.2133**

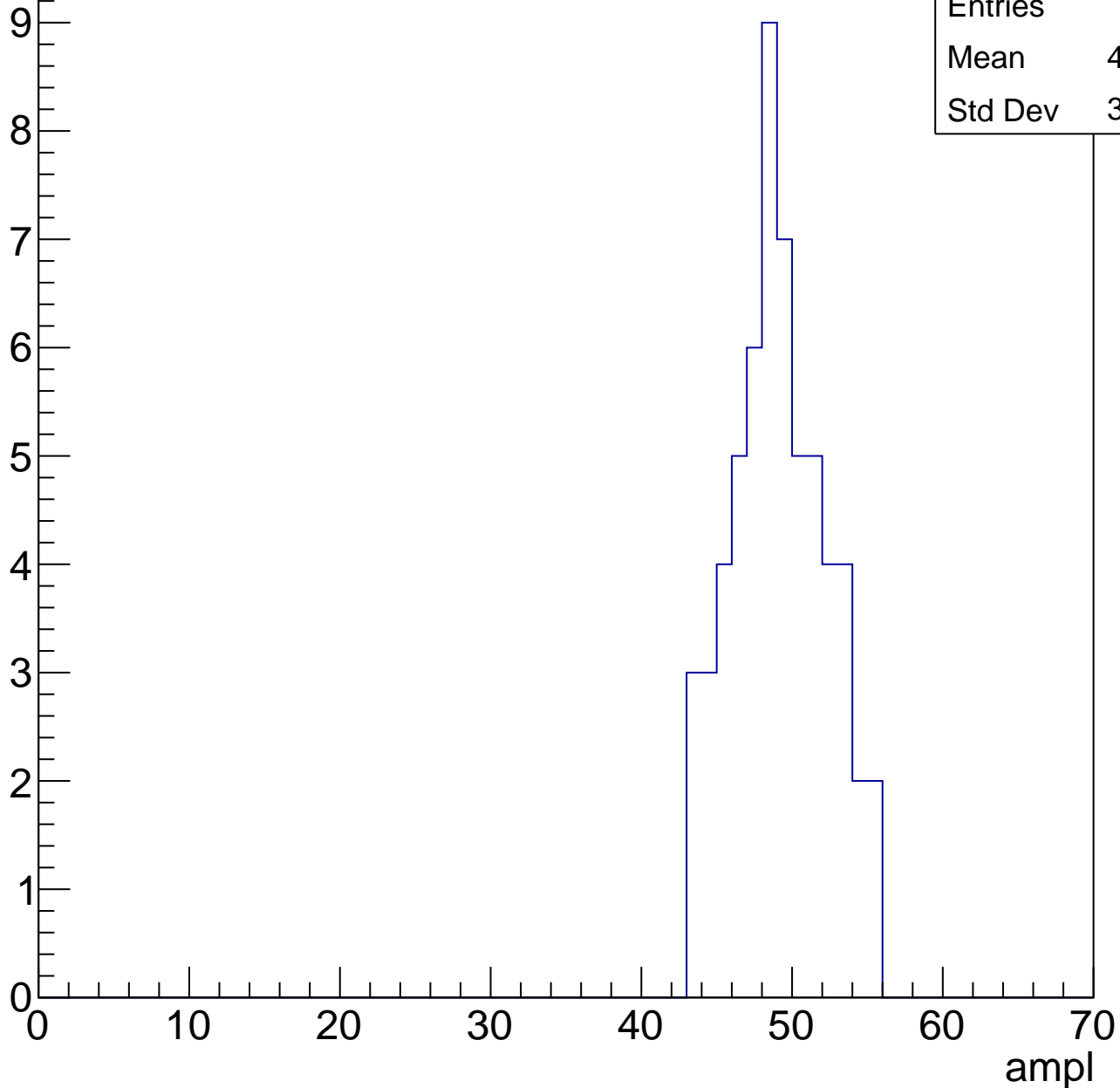
**Gaus Width: 3.1524**



# B1L000S, U23-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



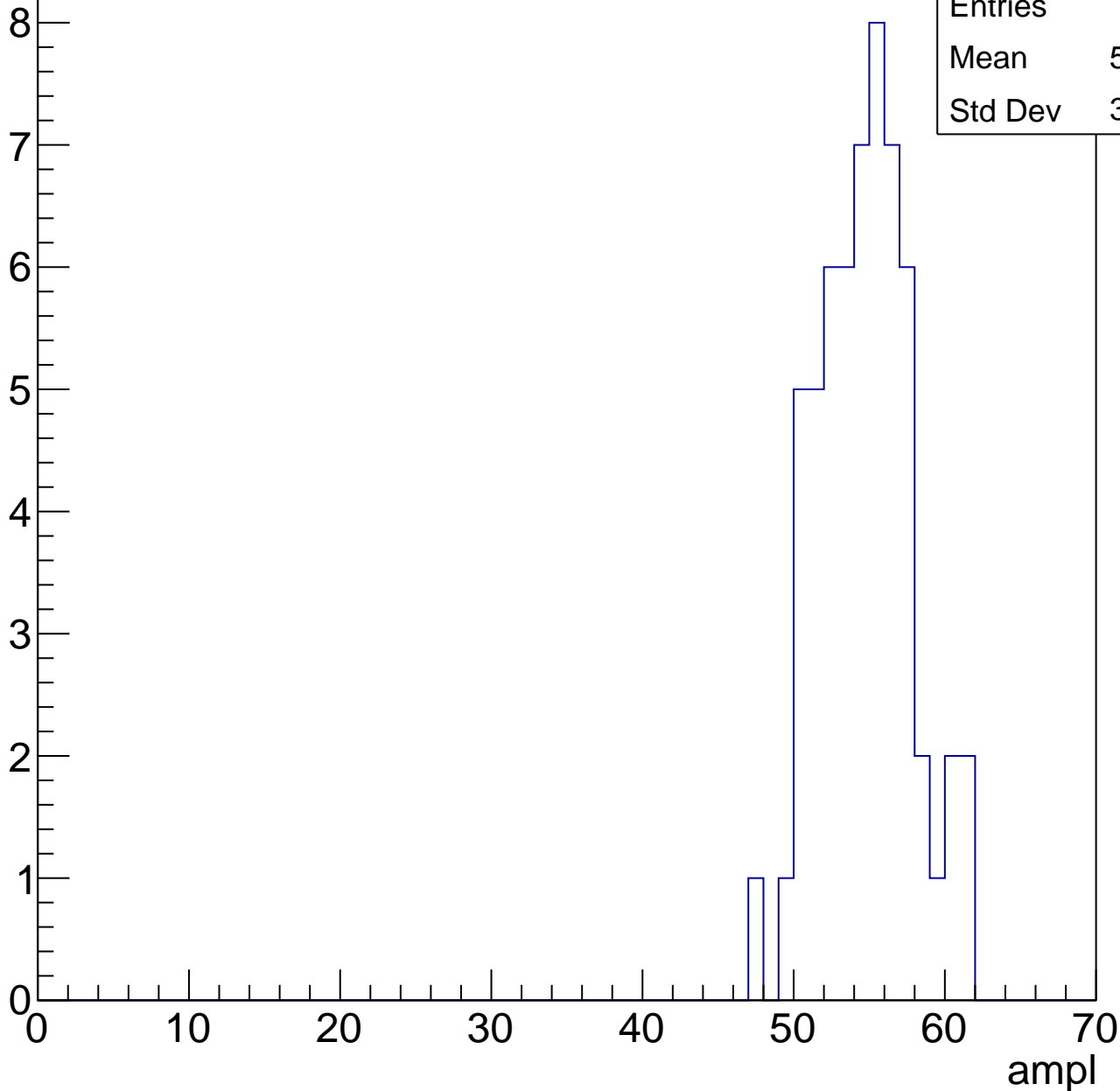
Entries	59
Mean	48.66
Std Dev	3.095

# B1L000S, U23-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	54.24
Std Dev	3.033

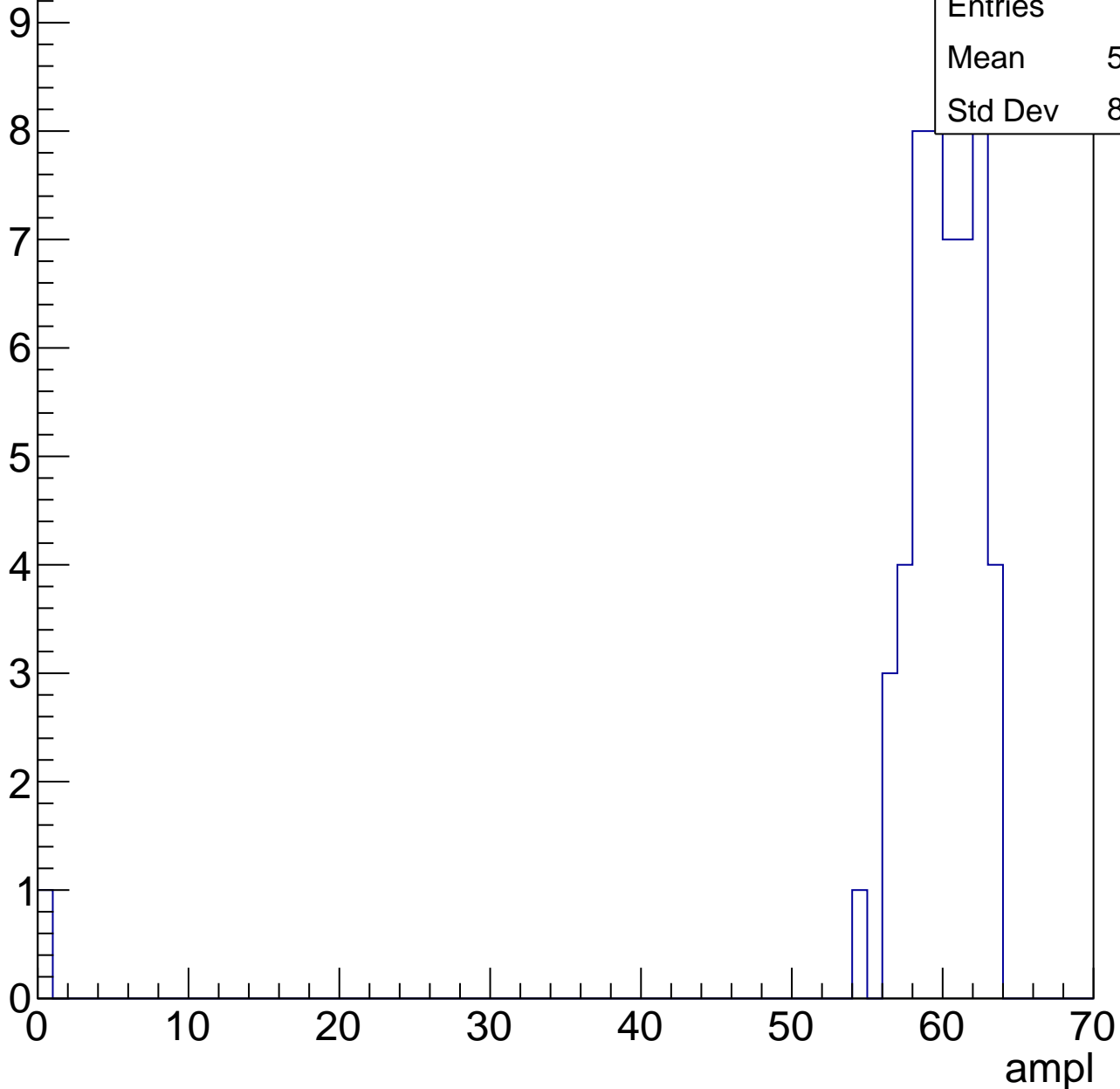


# B1L000S, U23-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	58.52
Std Dev	8.464

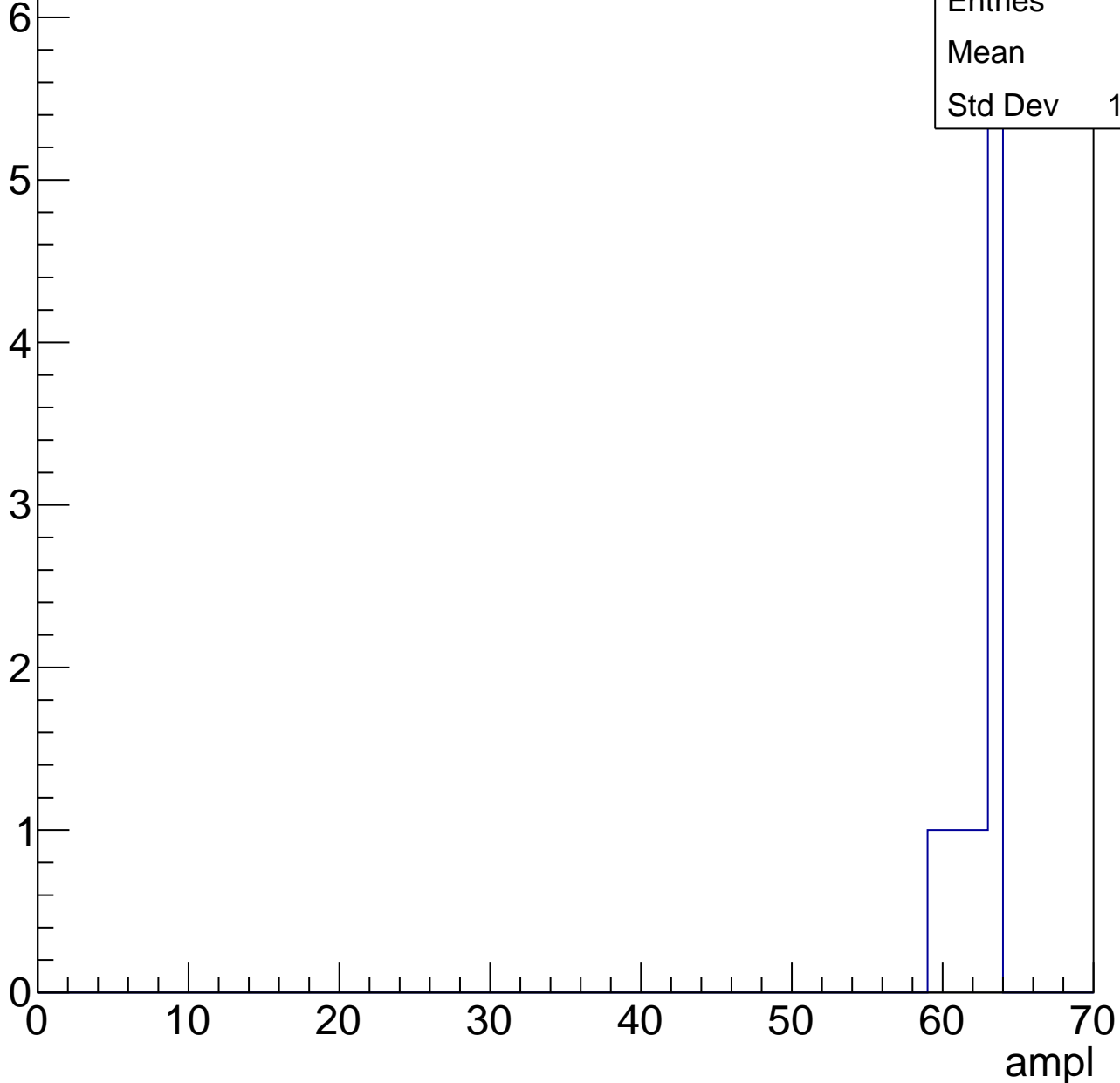


# B1L000S, U23-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	10
Mean	62
Std Dev	1.414





# B1L000S, U23-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch28, adc0

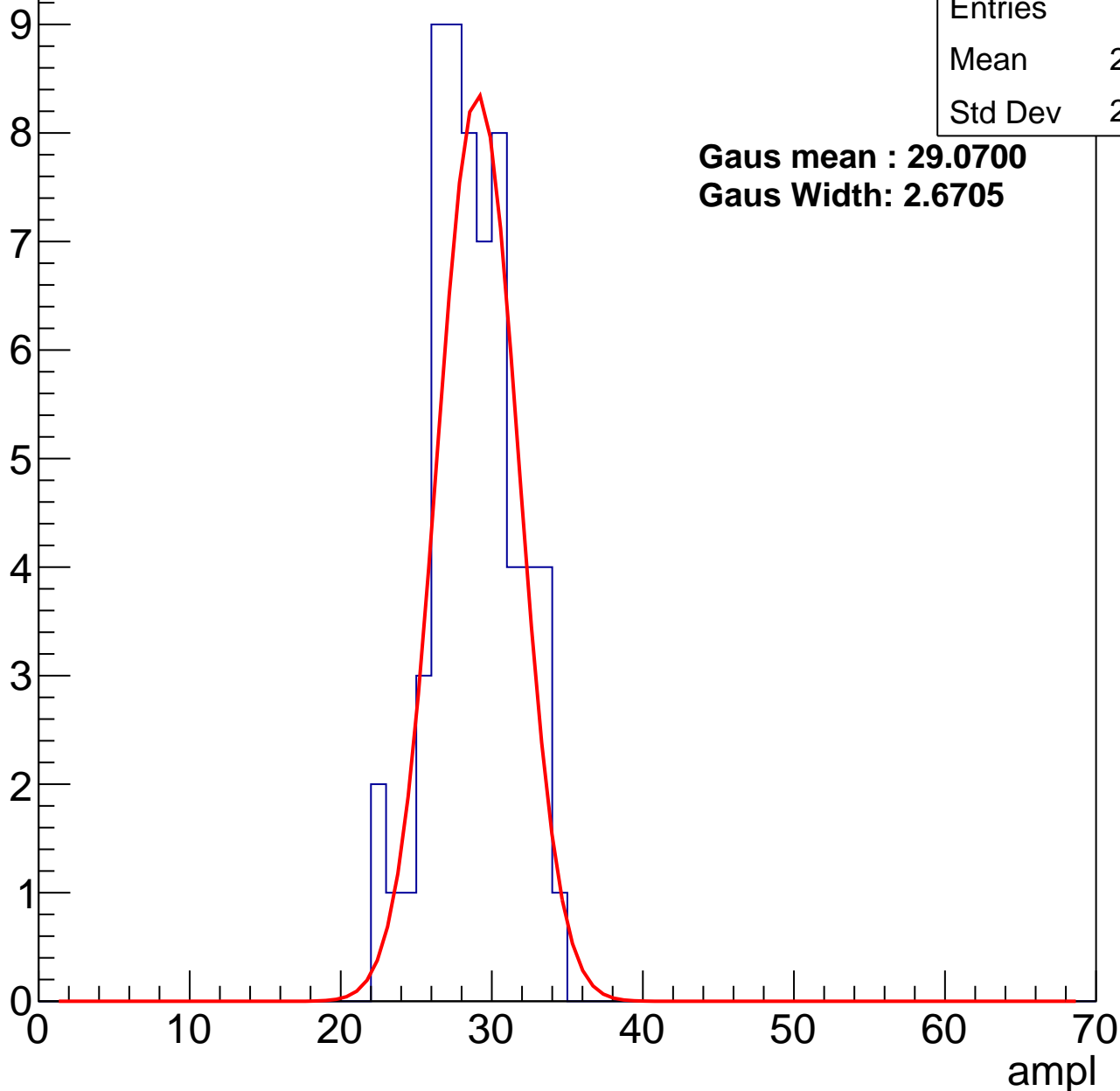
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.33
Std Dev	2.726

**Gaus mean : 29.0700**

**Gaus Width: 2.6705**

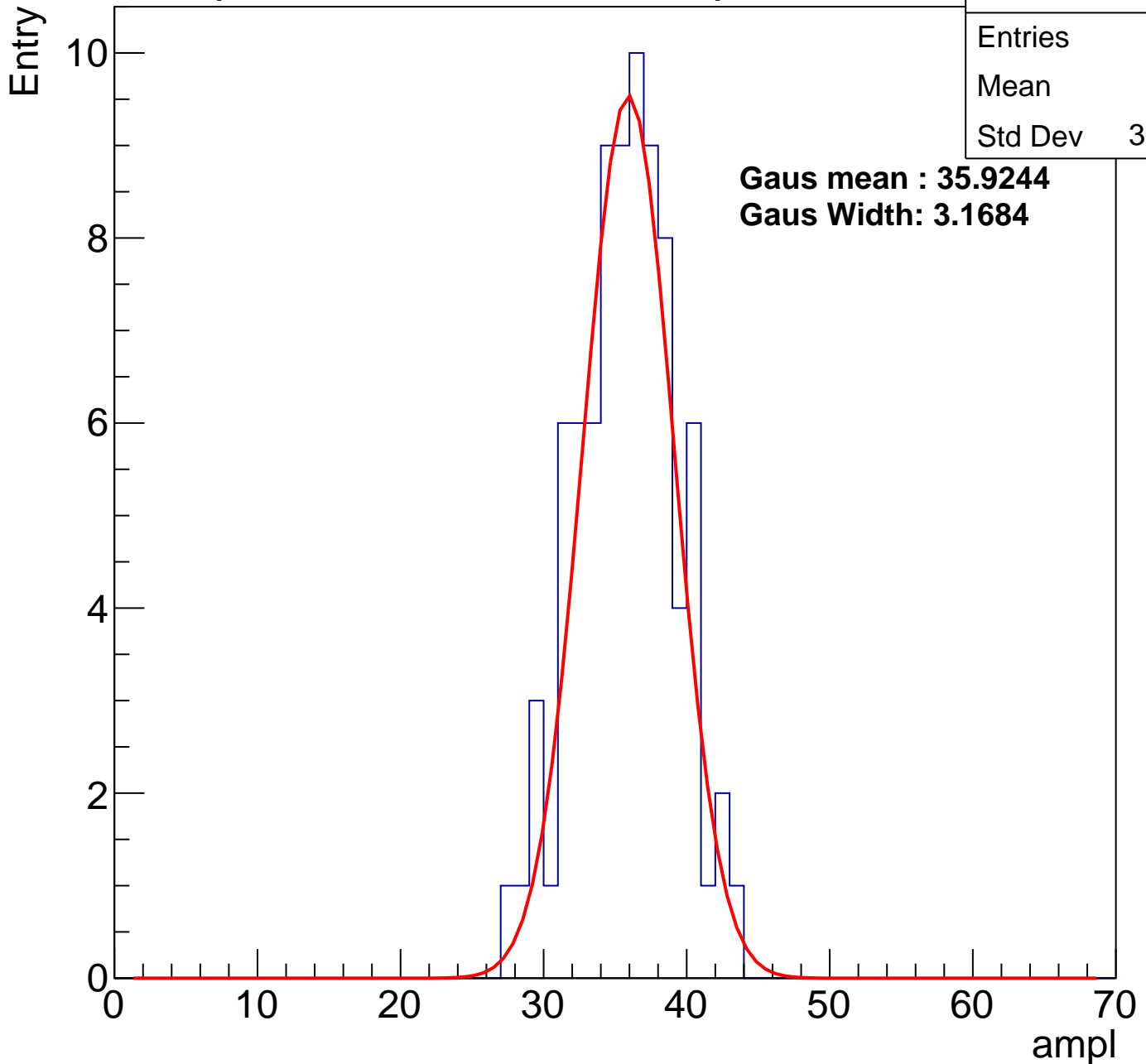


# B1L000S, U23-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	35.3
Std Dev	3.386

**Gaus mean : 35.9244**  
**Gaus Width: 3.1684**



# B1L000S, U23-ch28, adc2

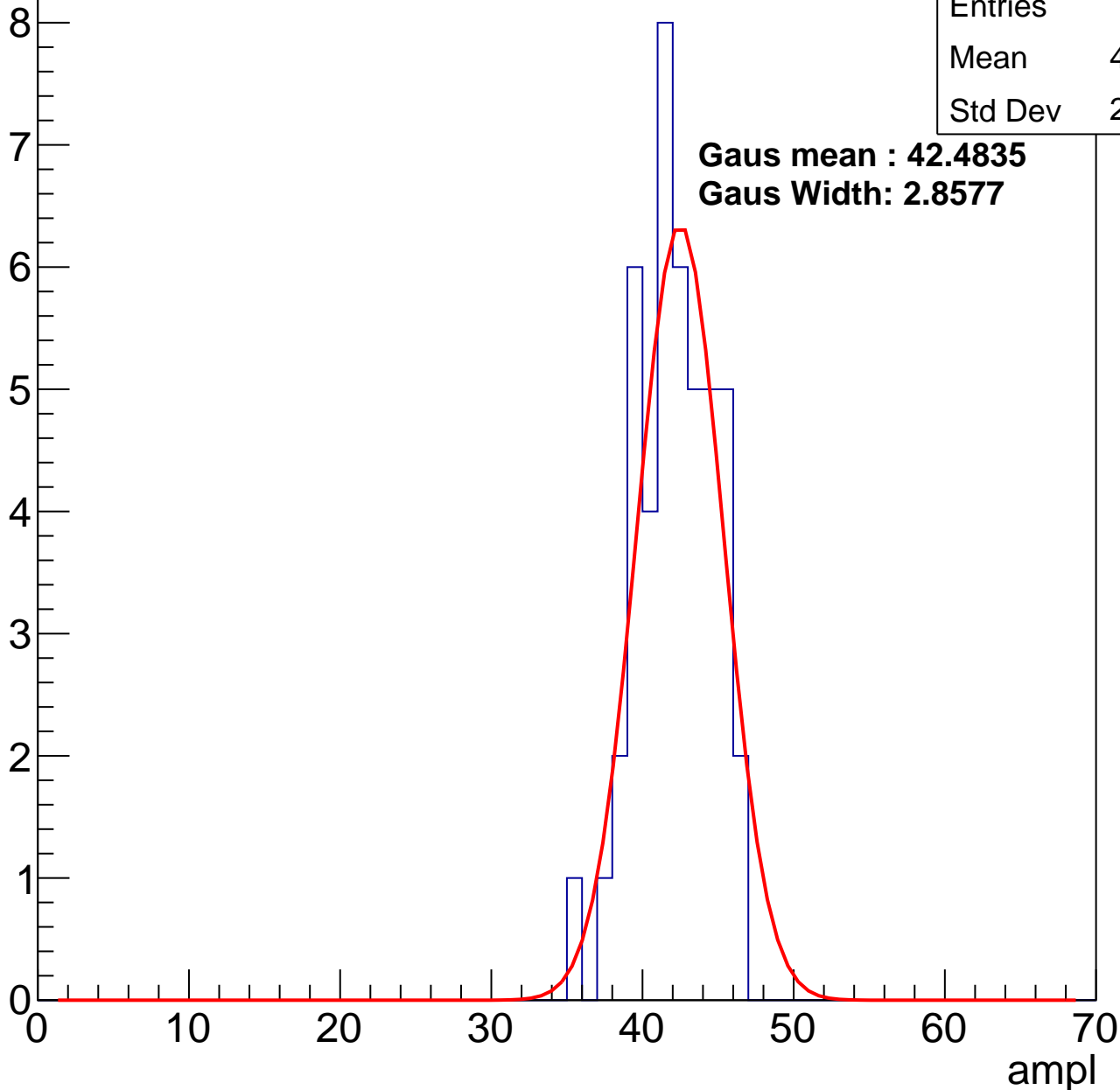
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	41.64
Std Dev	2.496

**Gaus mean : 42.4835**

**Gaus Width: 2.8577**



# B1L000S, U23-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

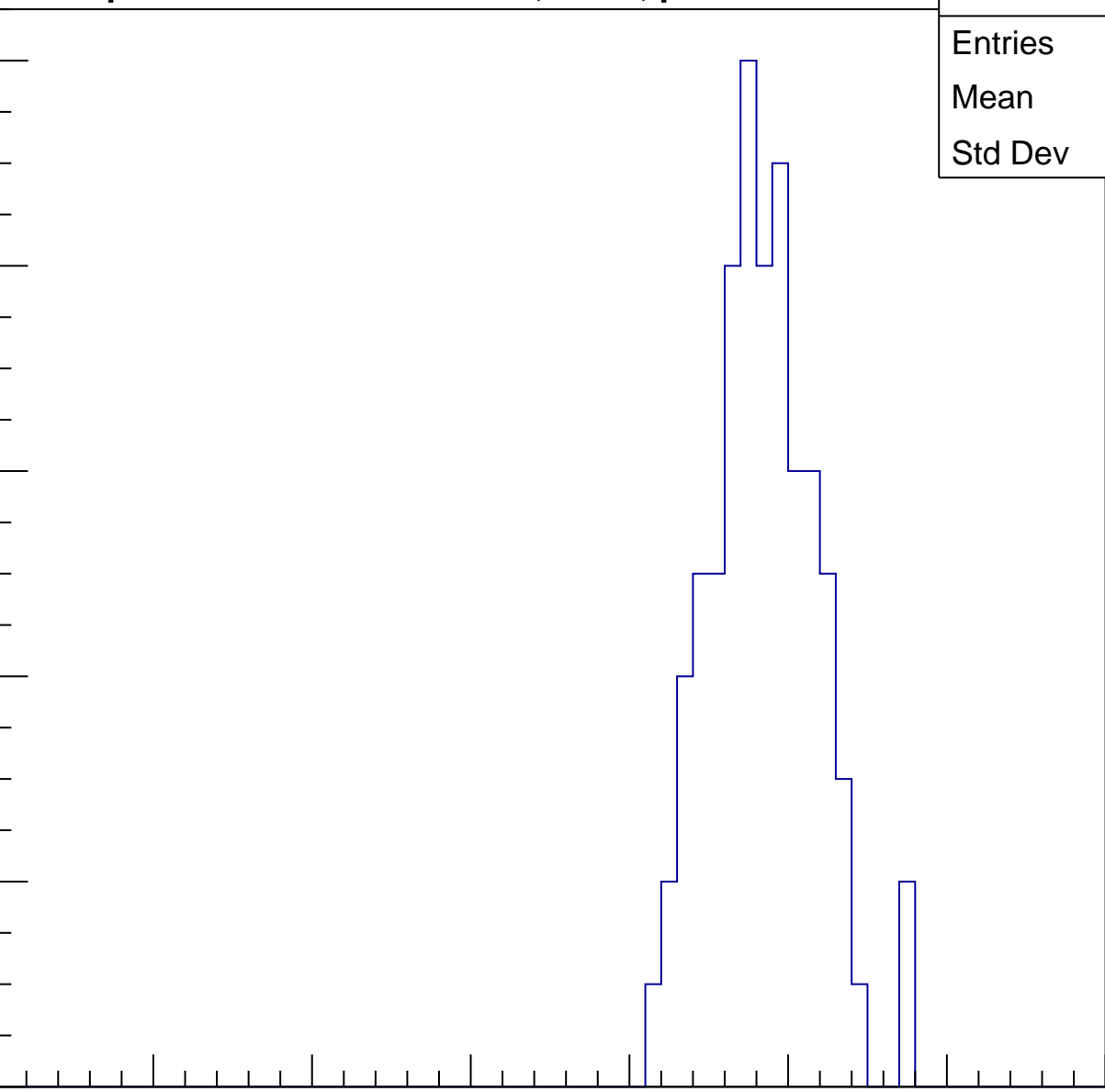
Entries	75
Mean	47.97
Std Dev	3.323

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

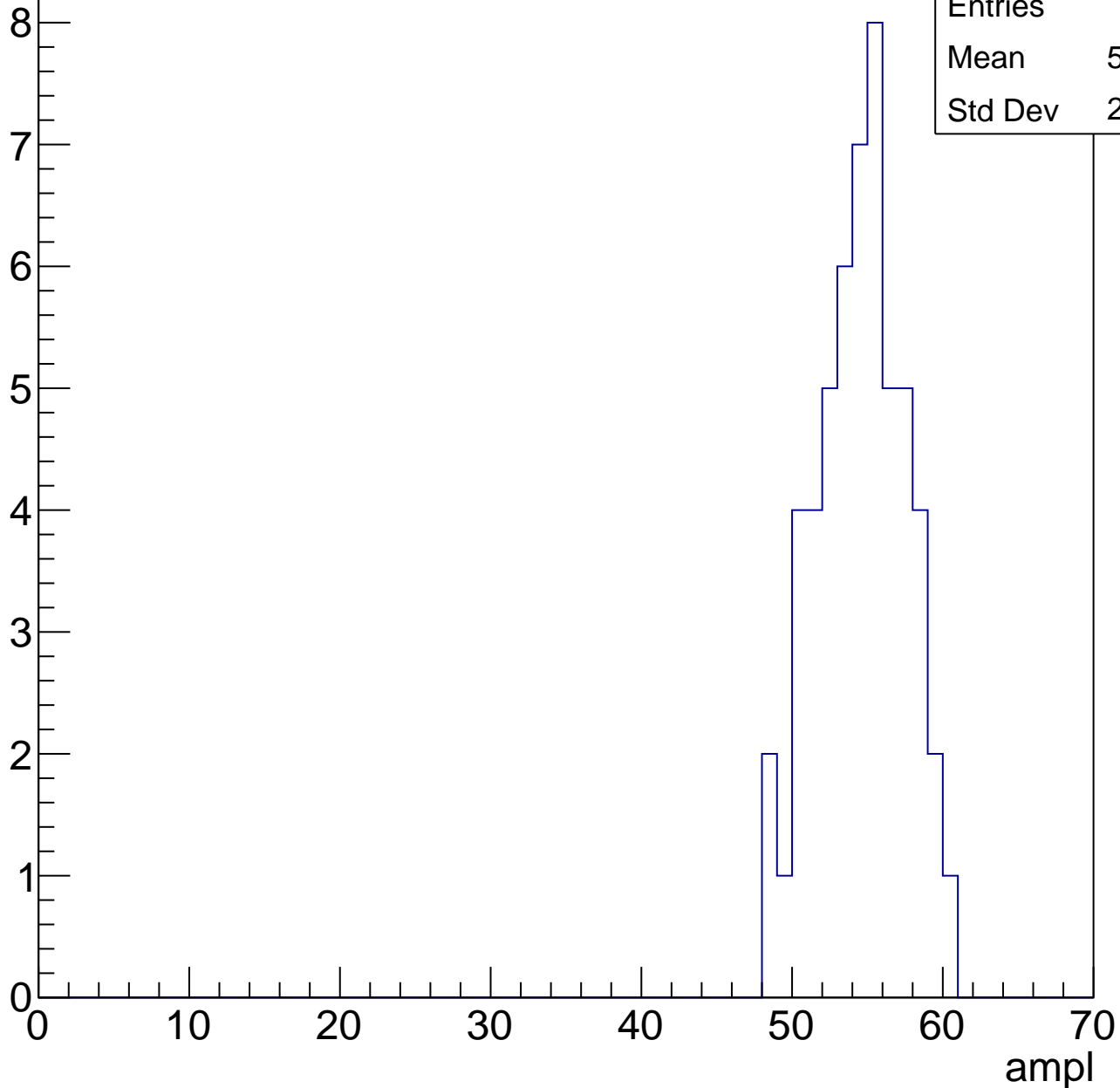
ampl



# B1L000S, U23-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	54
Mean	54.07
Std Dev	2.873

# B1L000S, U23-ch28, adc5

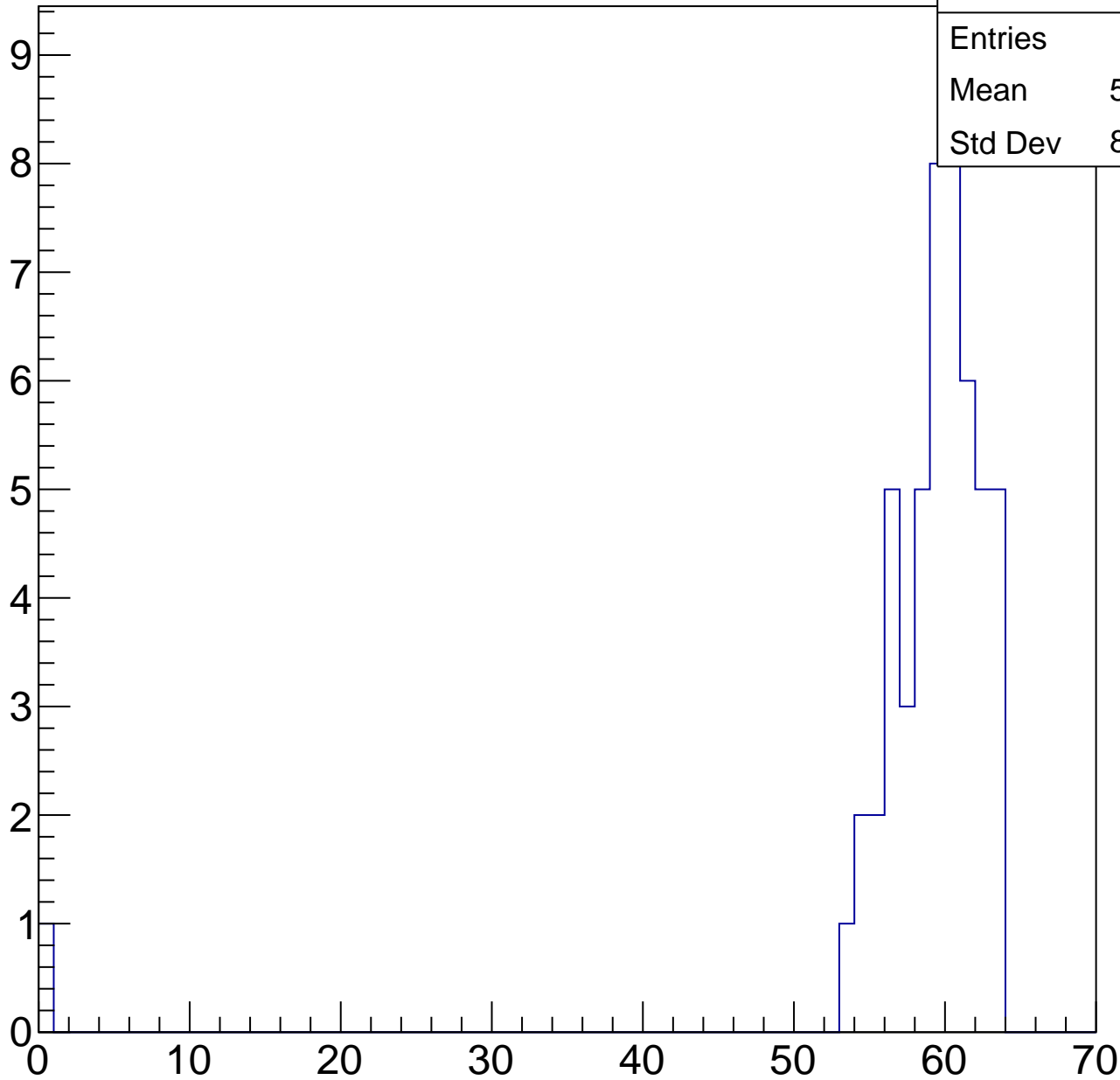
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	57.98
Std Dev	8.509

ampl

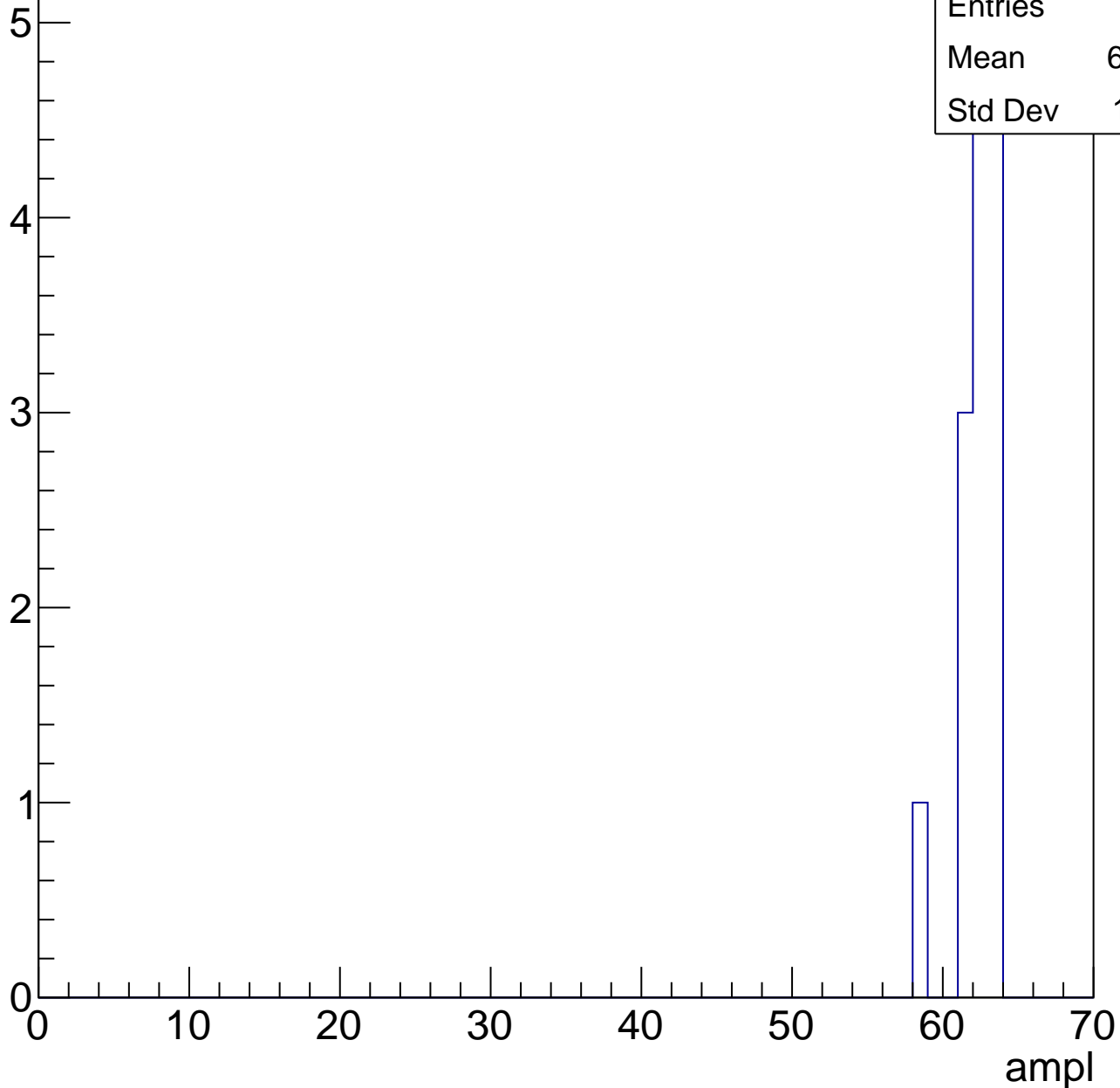


# B1L000S, U23-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.301





# B1L000S, U23-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch29, adc0

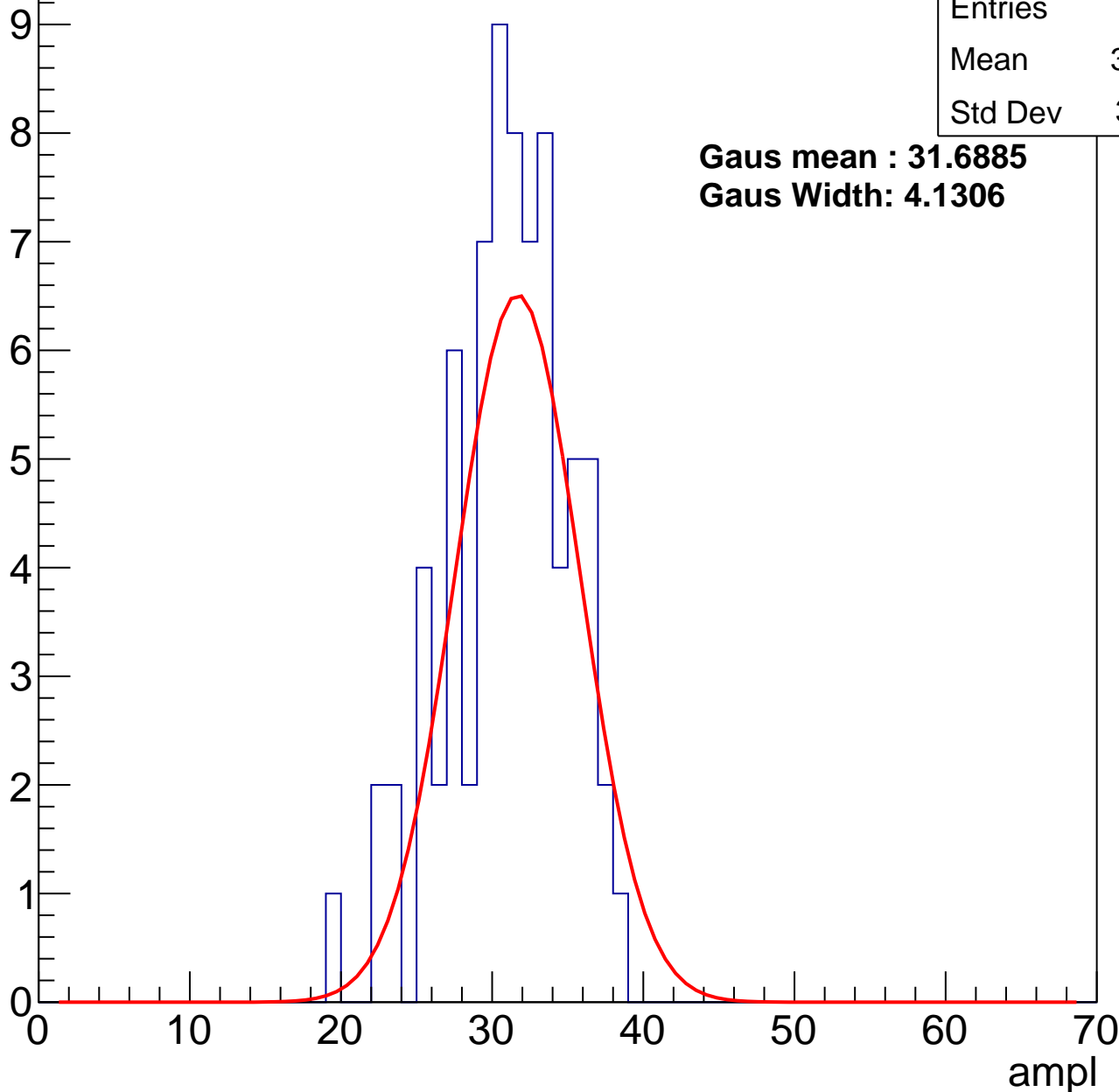
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	30.55
Std Dev	3.941

**Gaus mean : 31.6885**

**Gaus Width: 4.1306**



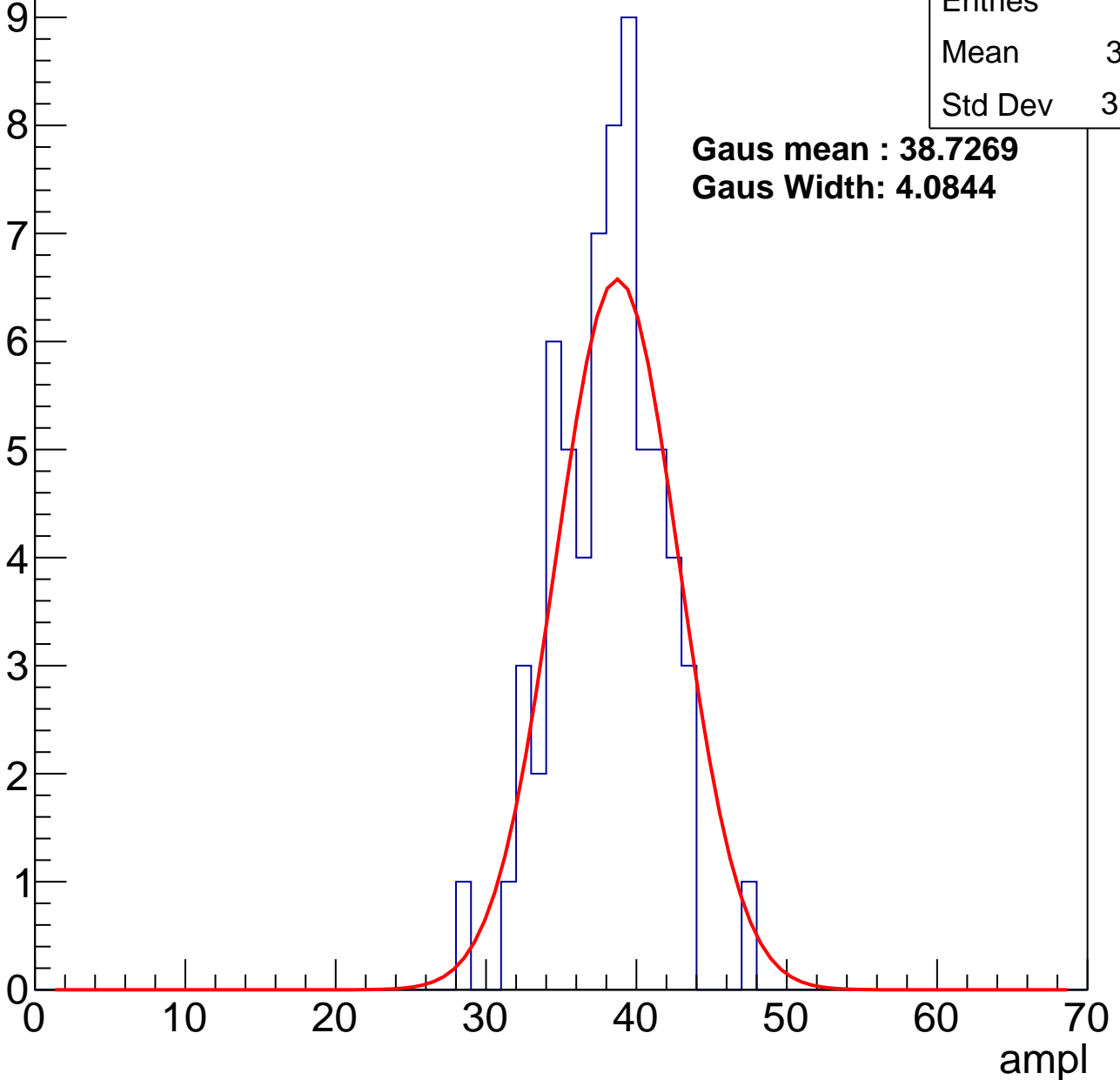
# B1L000S, U23-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	37.61
Std Dev	3.435

**Gaus mean : 38.7269**  
**Gaus Width: 4.0844**



# B1L000S, U23-ch29, adc2

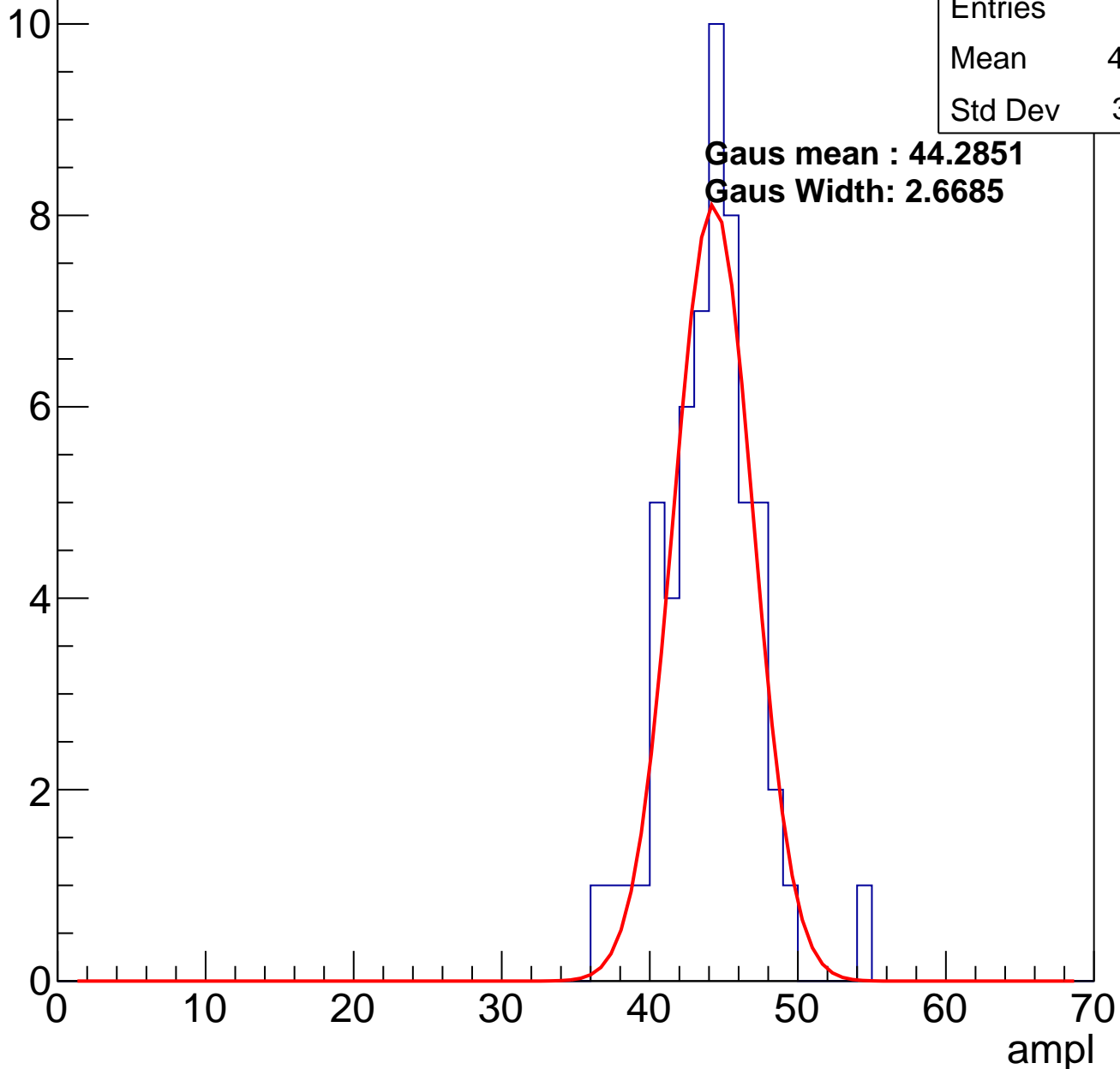
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	43.64
Std Dev	3.061

**Gaus mean : 44.2851**

**Gaus Width: 2.6685**

Entry



# B1L000S, U23-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

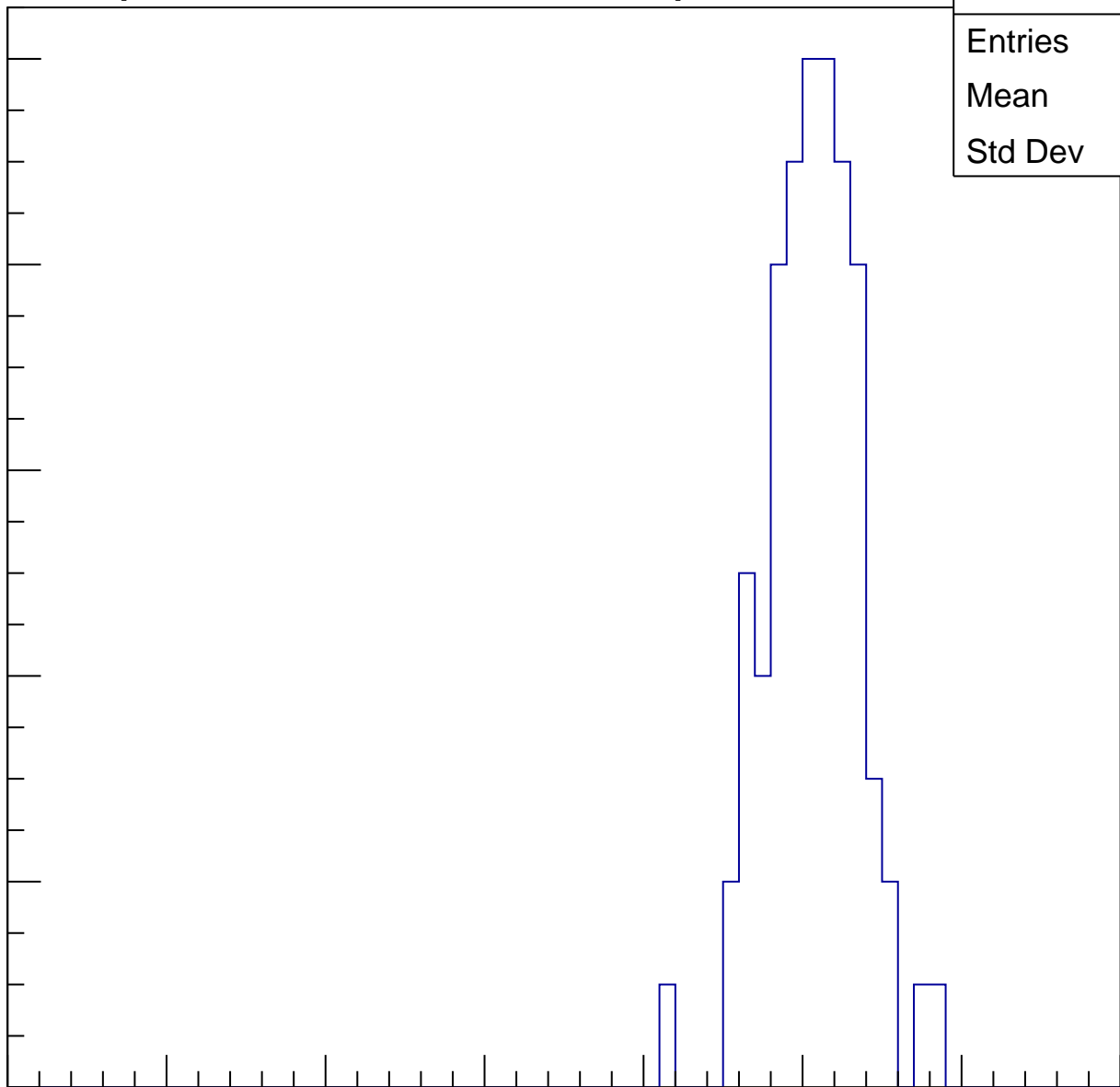
Entries	73
Mean	50.18
Std Dev	2.902

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

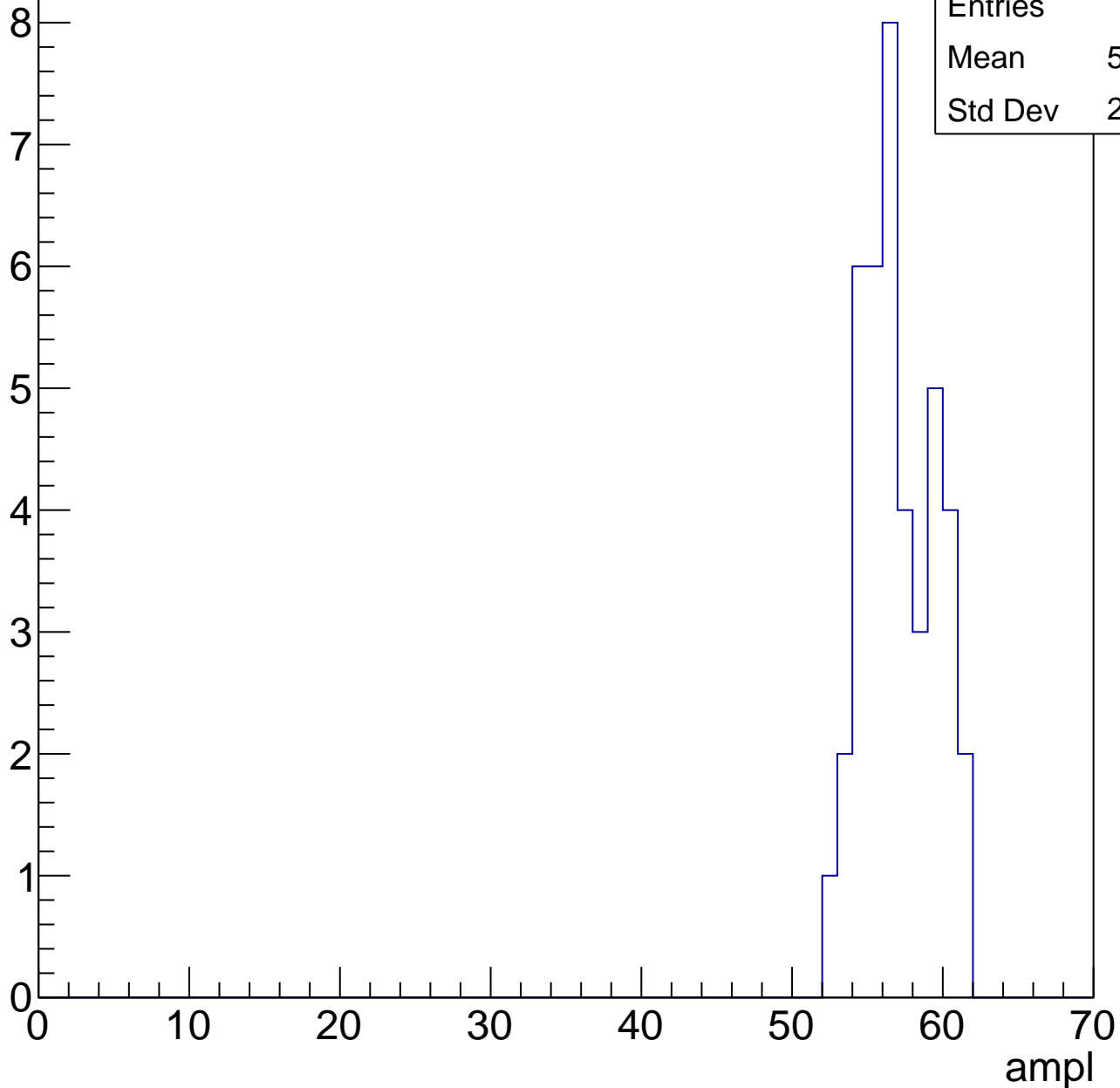
ampl



# B1L000S, U23-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

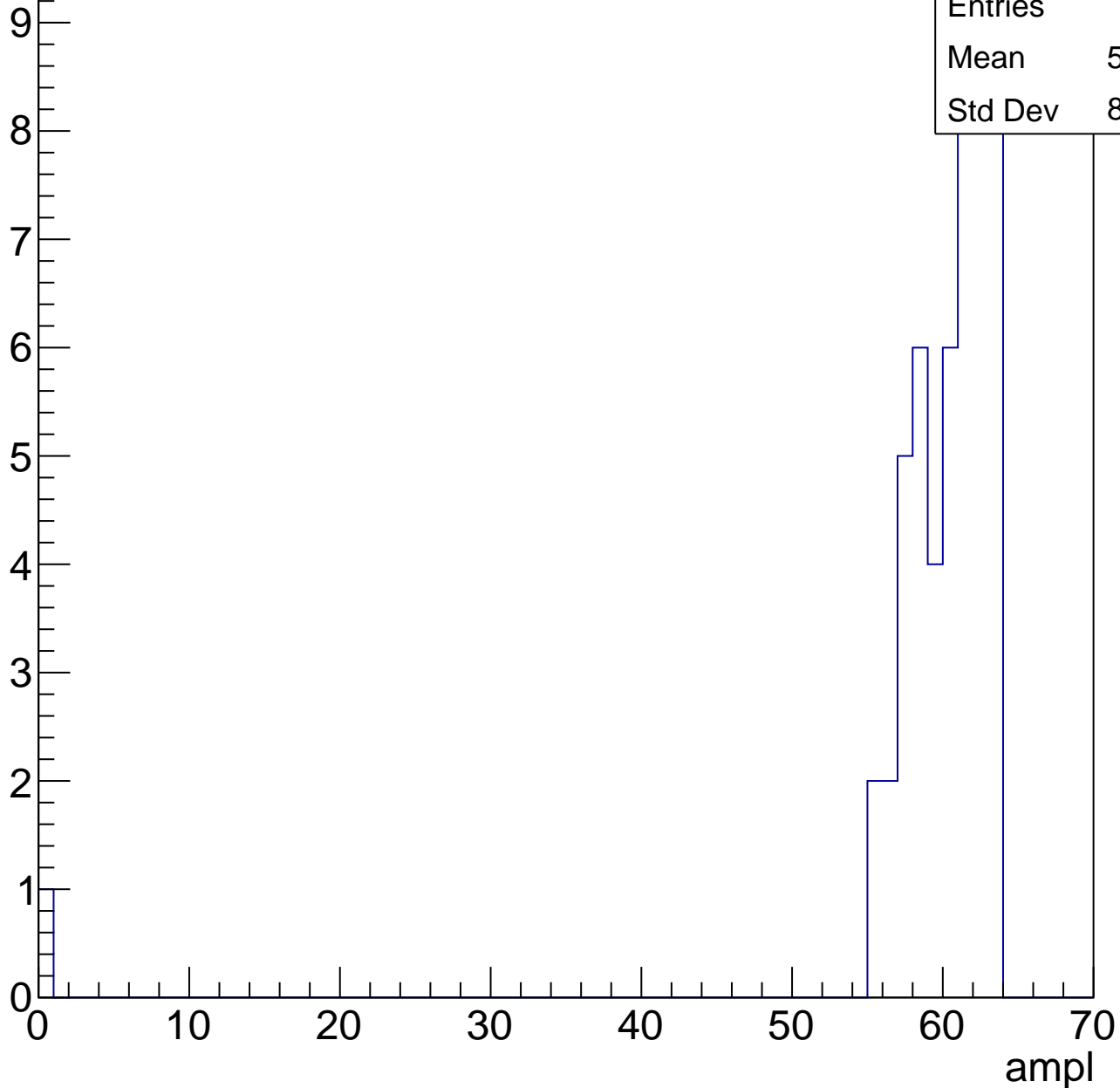
Entry



# B1L000S, U23-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch30, adc0

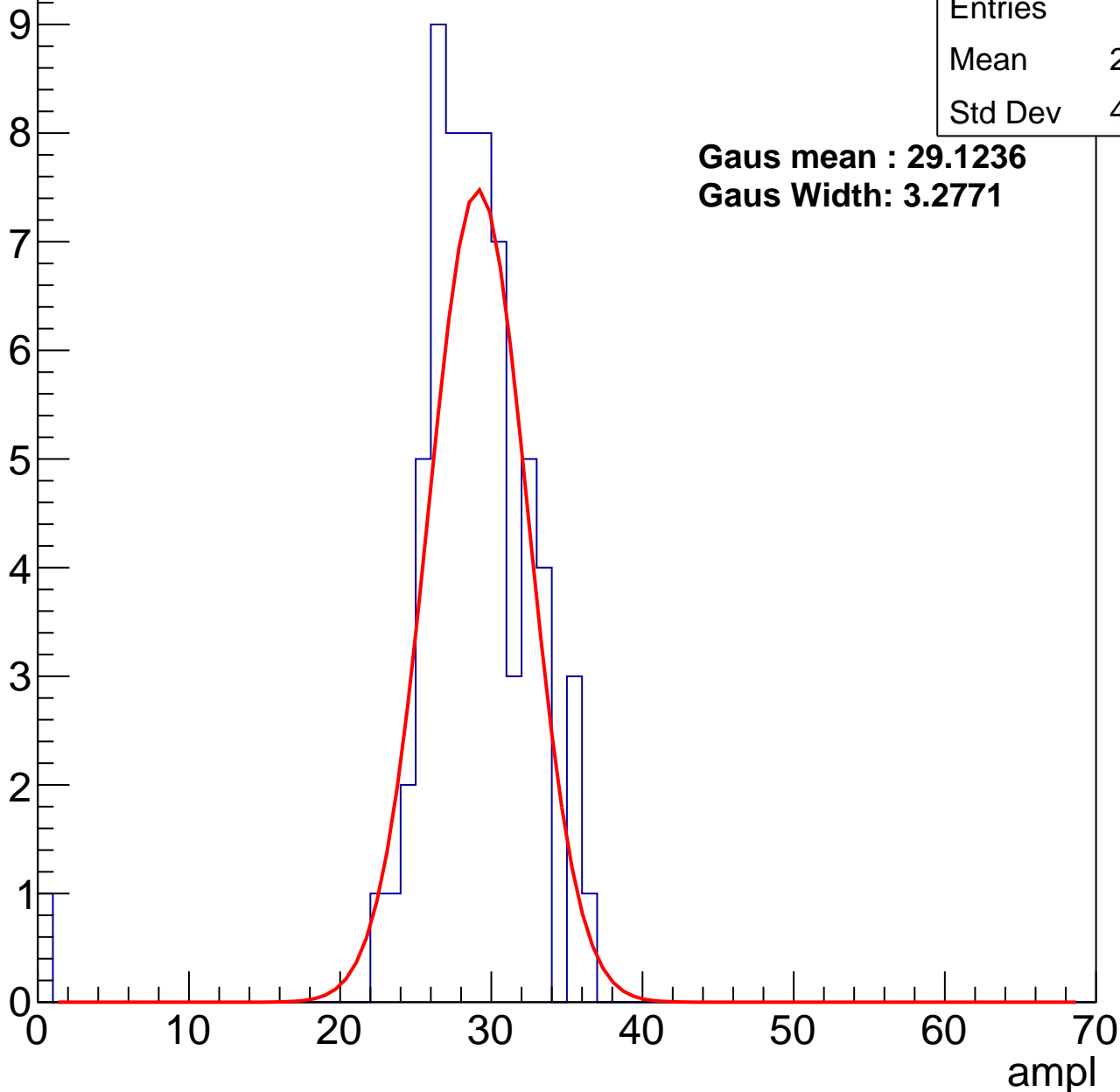
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	28.18
Std Dev	4.635

**Gaus mean : 29.1236**

**Gaus Width: 3.2771**



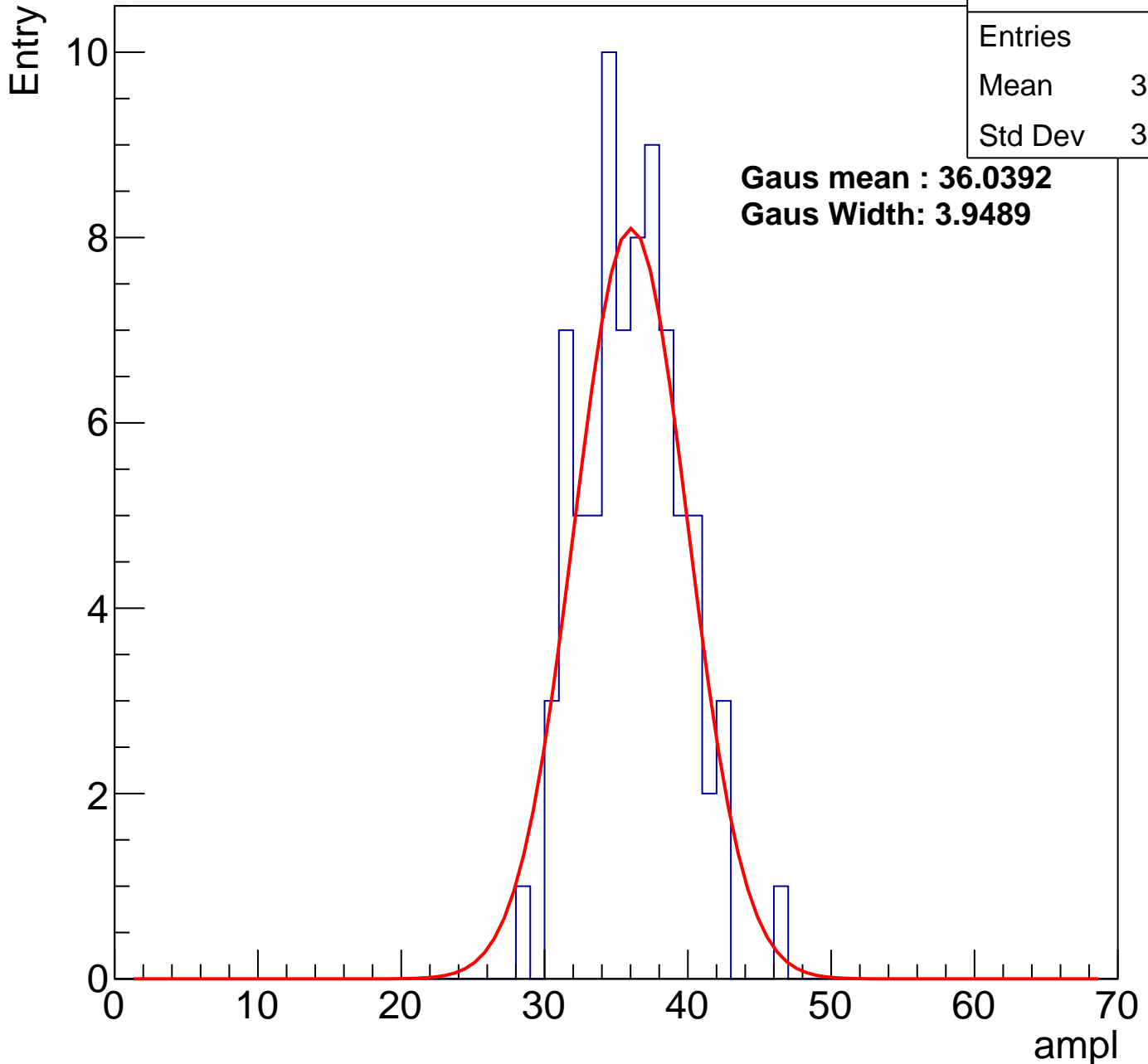
# B1L000S, U23-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	35.65
Std Dev	3.452

**Gaus mean : 36.0392**

**Gaus Width: 3.9489**



# B1L000S, U23-ch30, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	41.75
Std Dev	2.828

**Gaus mean : 42.3862**

**Gaus Width: 2.7651**

10

8

6

4

2

0

0

10

20

30

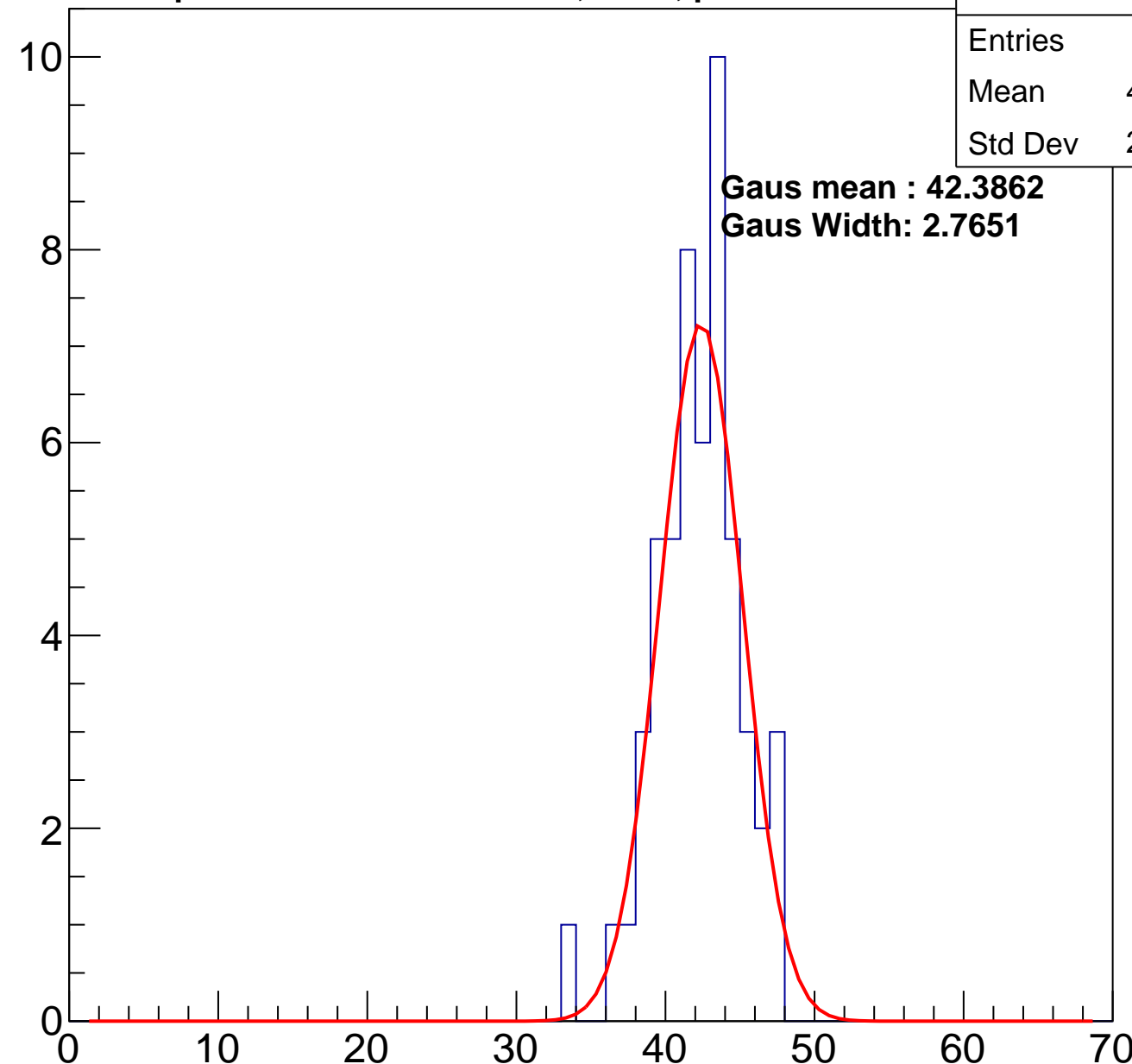
40

50

60

70

ampl

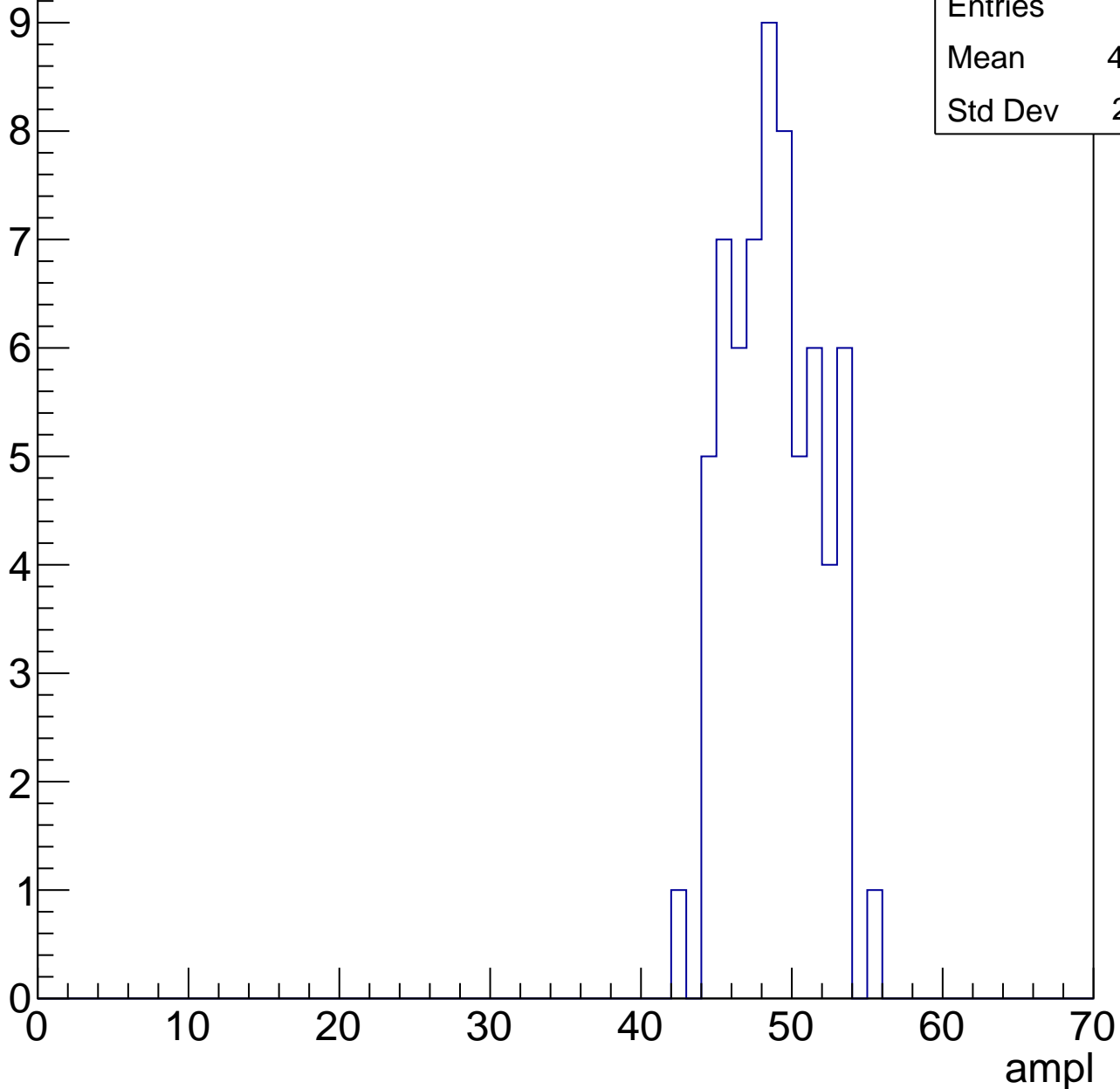


# B1L000S, U23-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	48.35
Std Dev	2.901

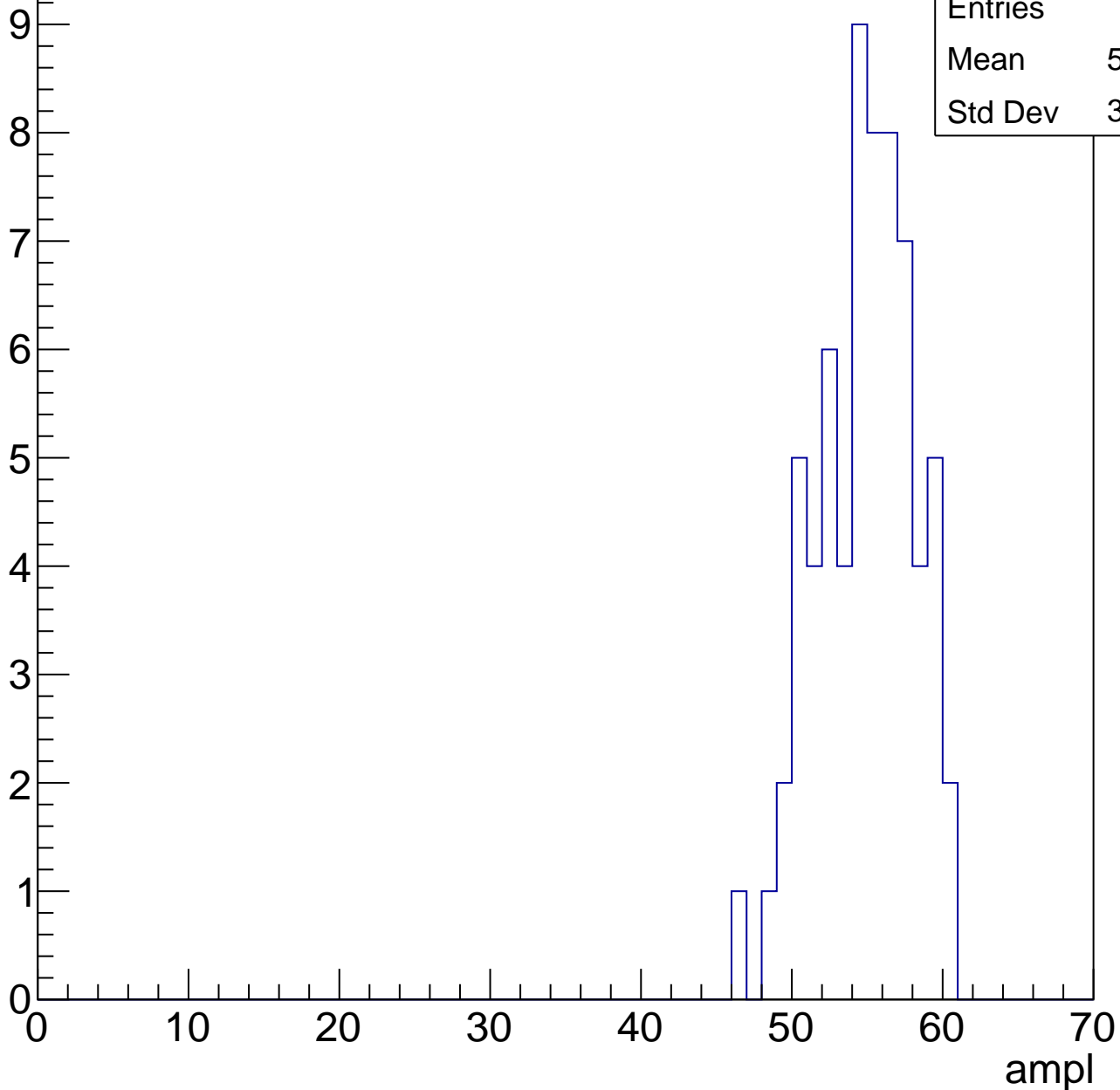


# B1L000S, U23-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	54.39
Std Dev	3.133



# B1L000S, U23-ch30, adc5

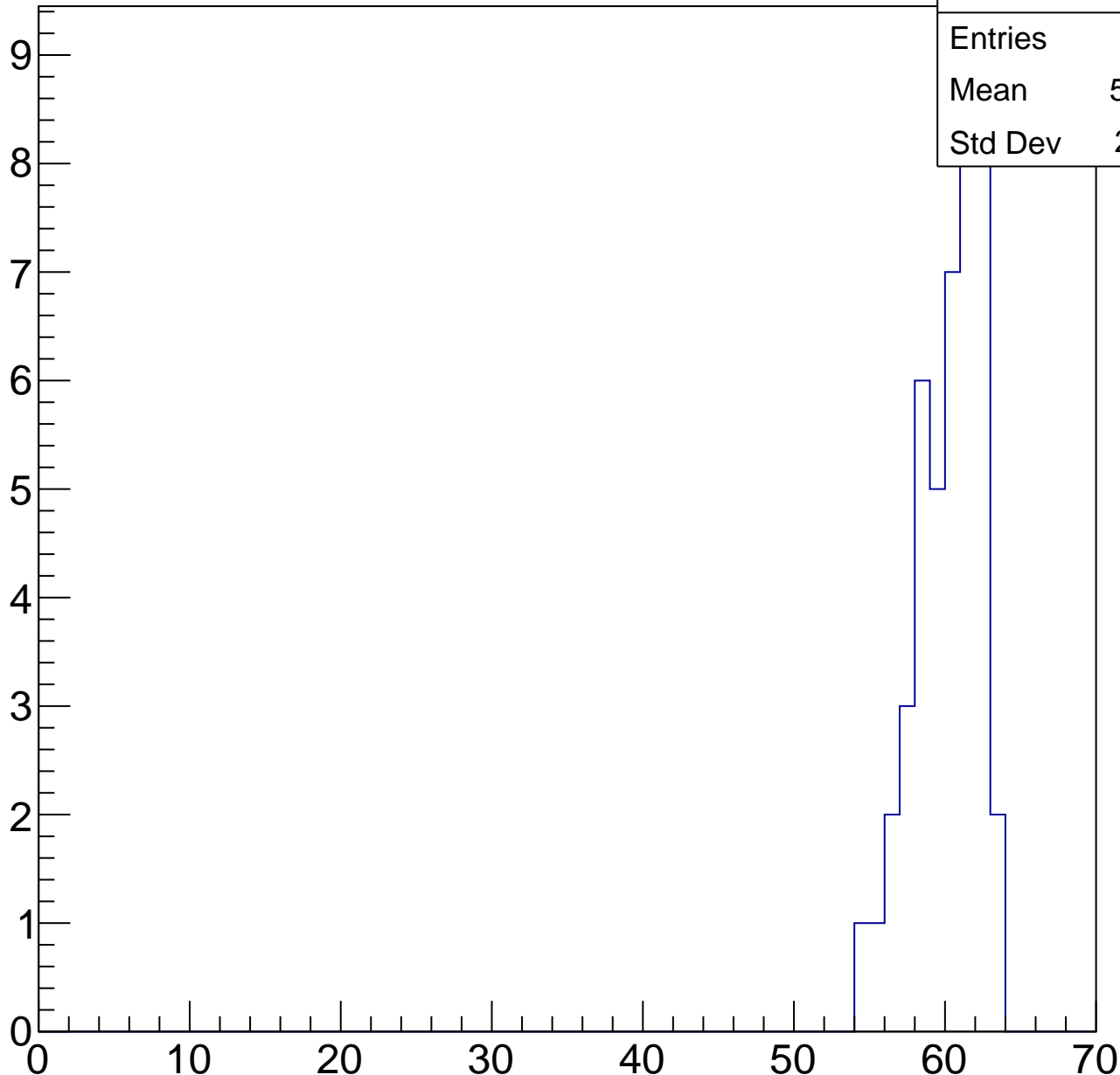
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	59.68
Std Dev	2.151

ampl

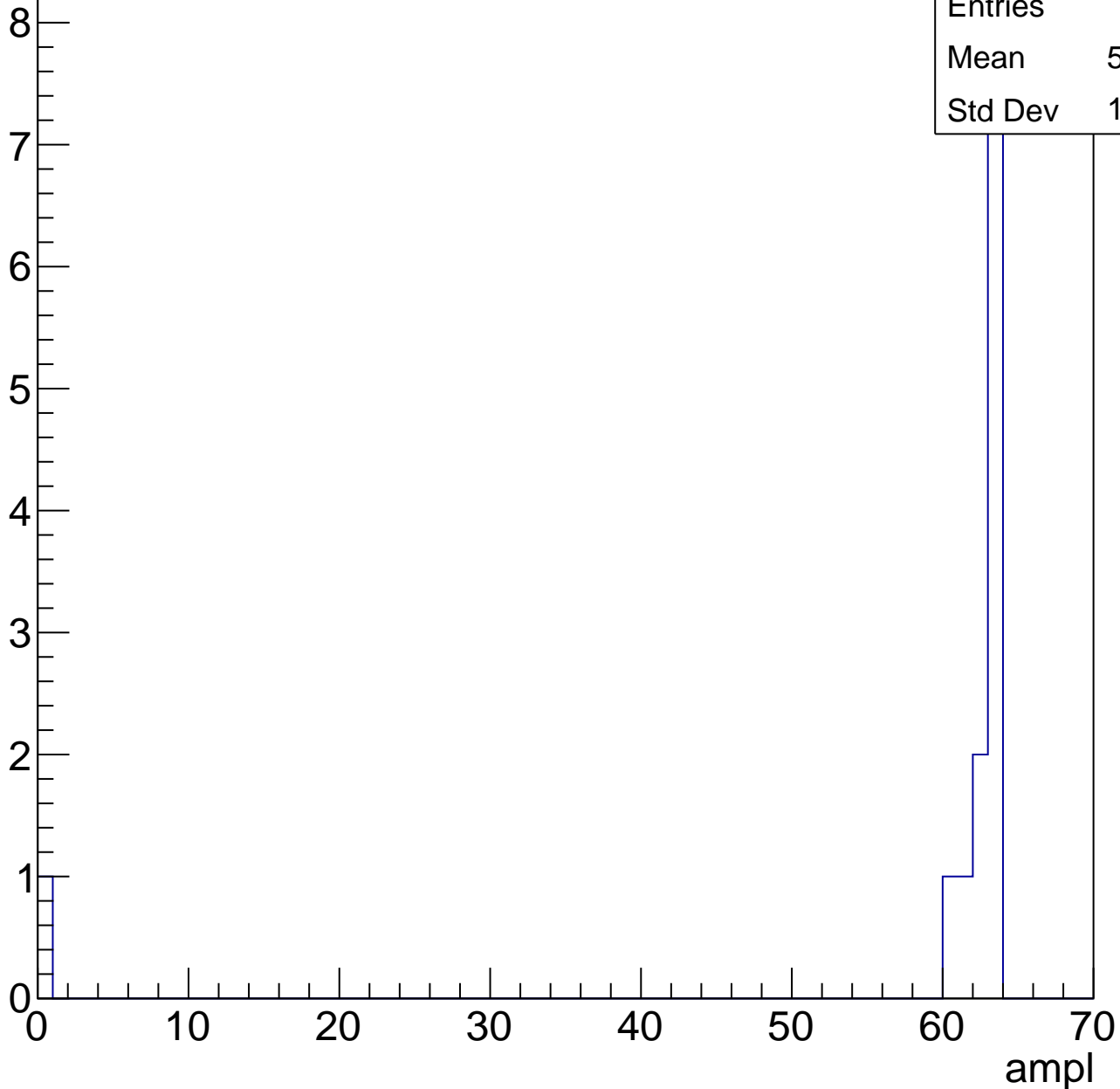


# B1L000S, U23-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.62
Std Dev	16.66





# B1L000S, U23-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch31, adc0

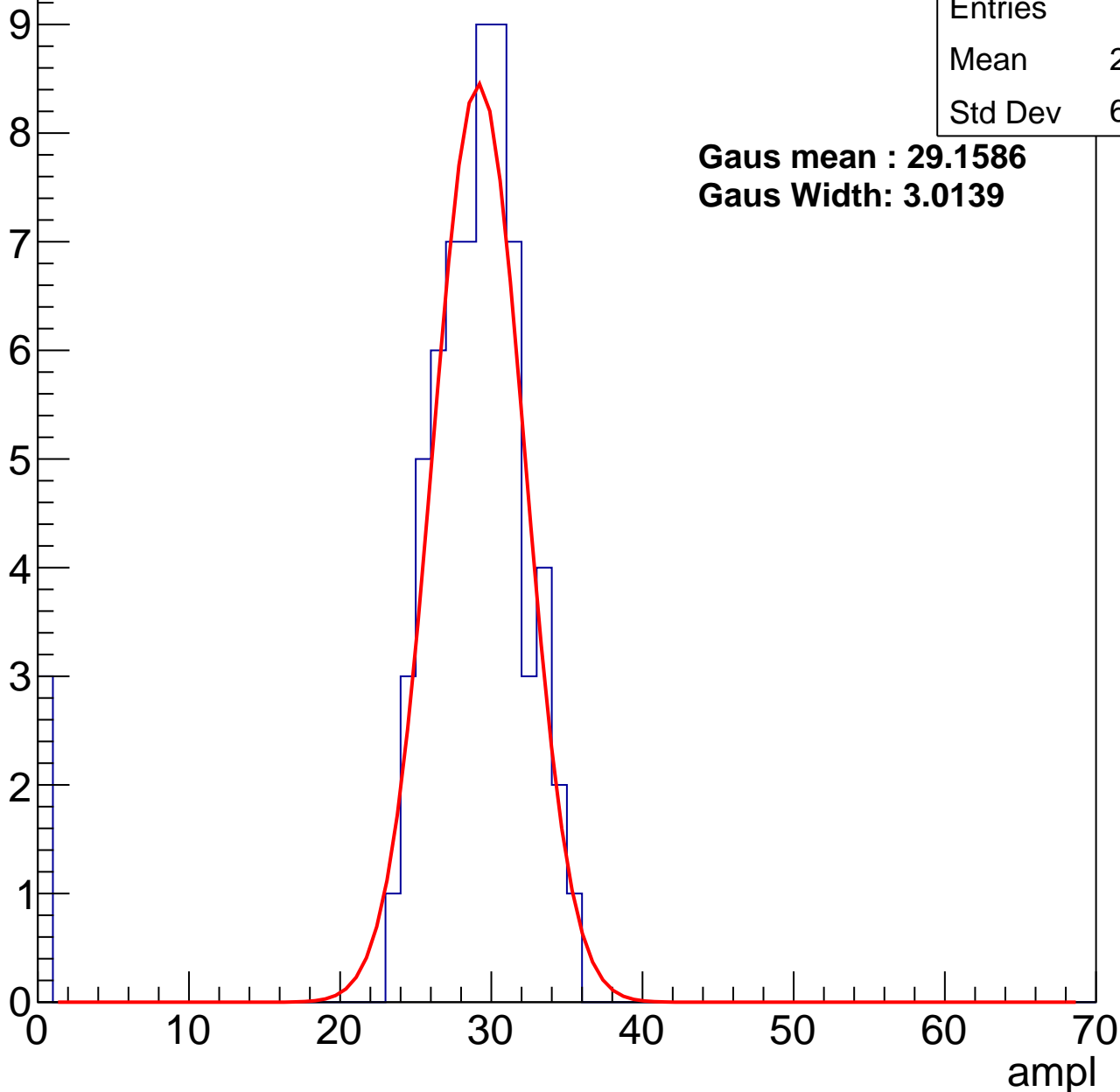
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	27.46
Std Dev	6.532

**Gaus mean : 29.1586**

**Gaus Width: 3.0139**



# B1L000S, U23-ch31, adc1

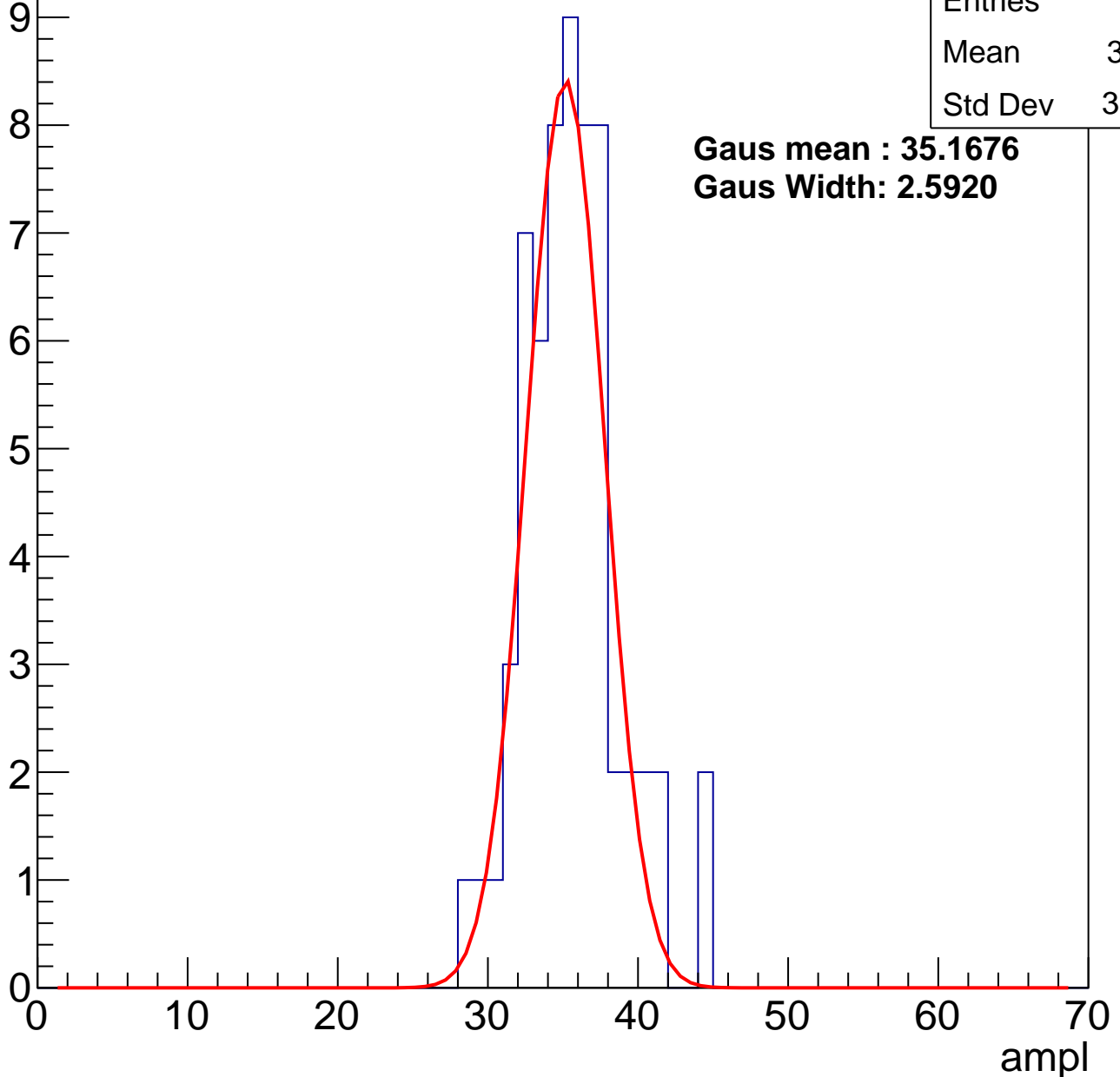
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.11
Std Dev	3.178

**Gaus mean : 35.1676**

**Gaus Width: 2.5920**



# B1L000S, U23-ch31, adc2

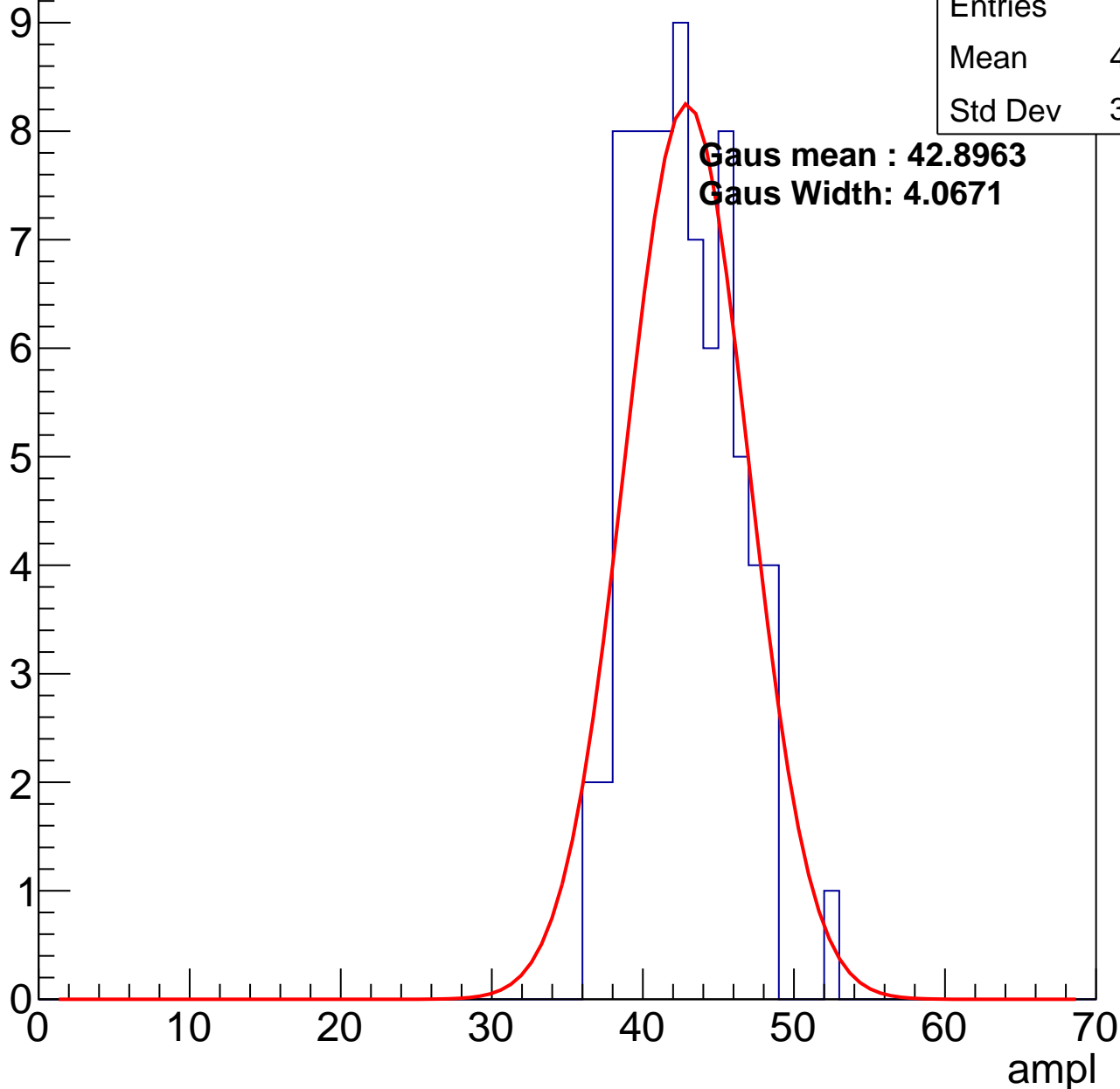
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	42.19
Std Dev	3.328

**Gaus mean : 42.8963**

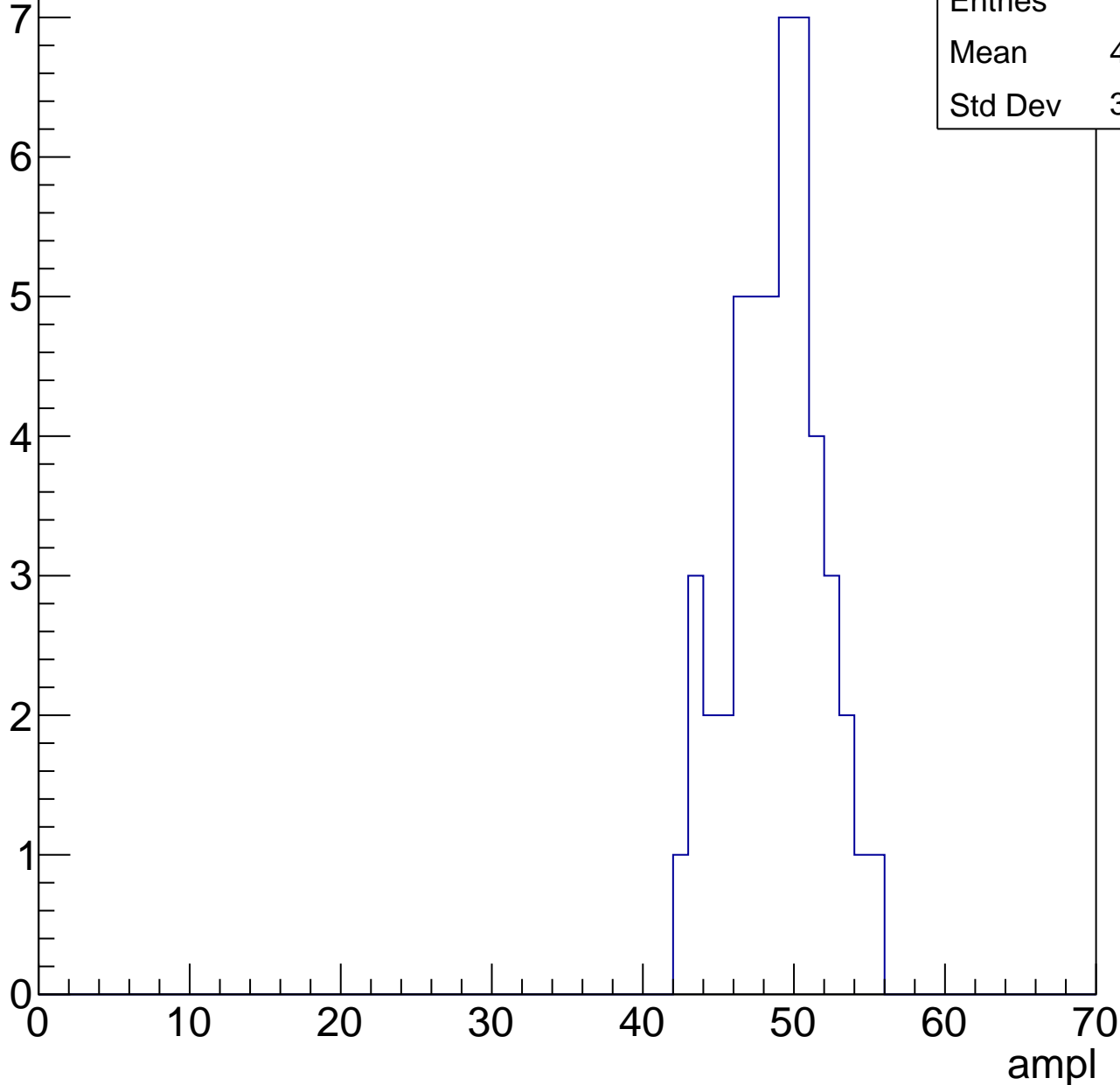
**Gaus Width: 4.0671**



# B1L000S, U23-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

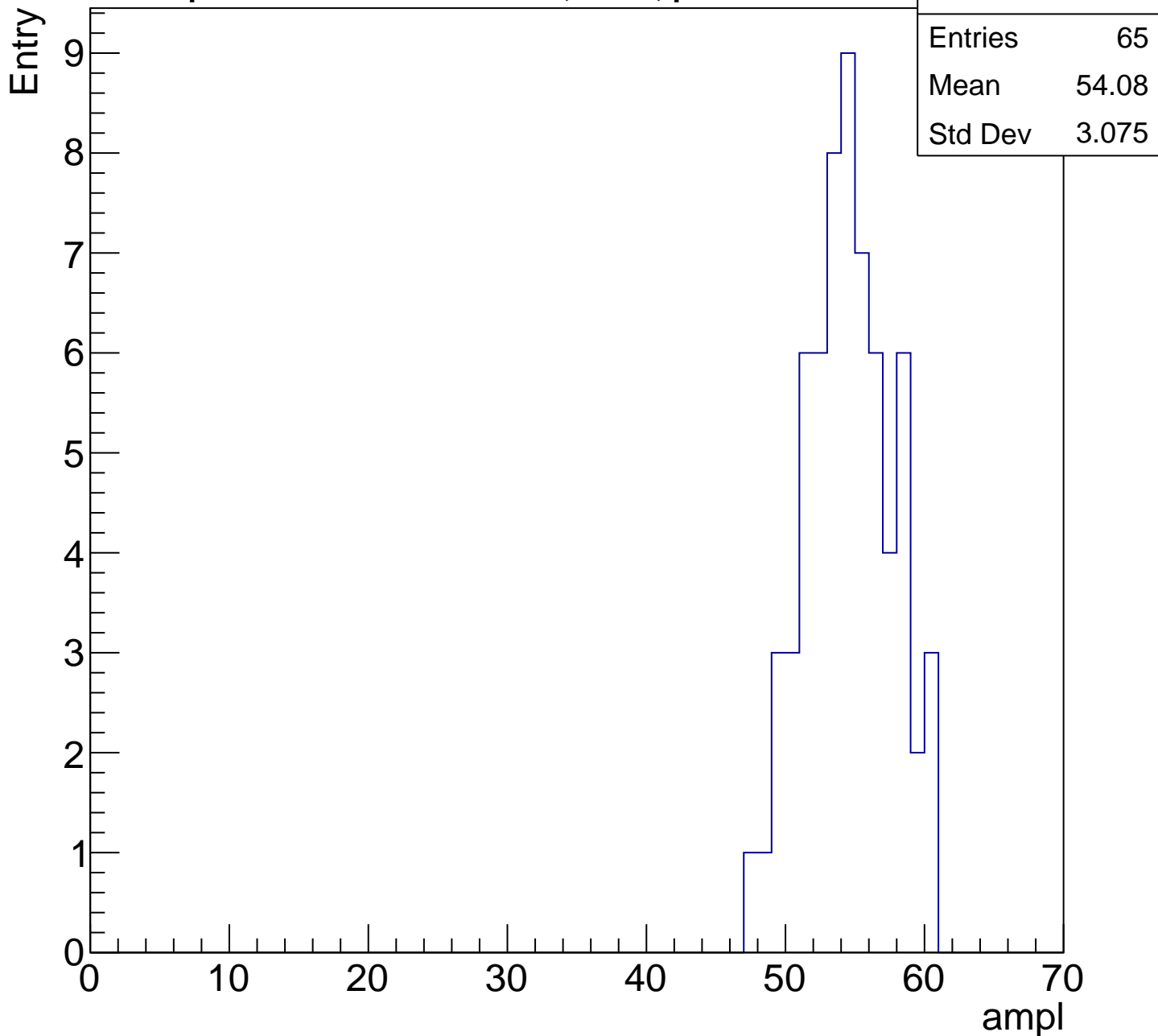
Entry



Entries	48
Mean	48.38
Std Dev	3.004

# B1L000S, U23-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

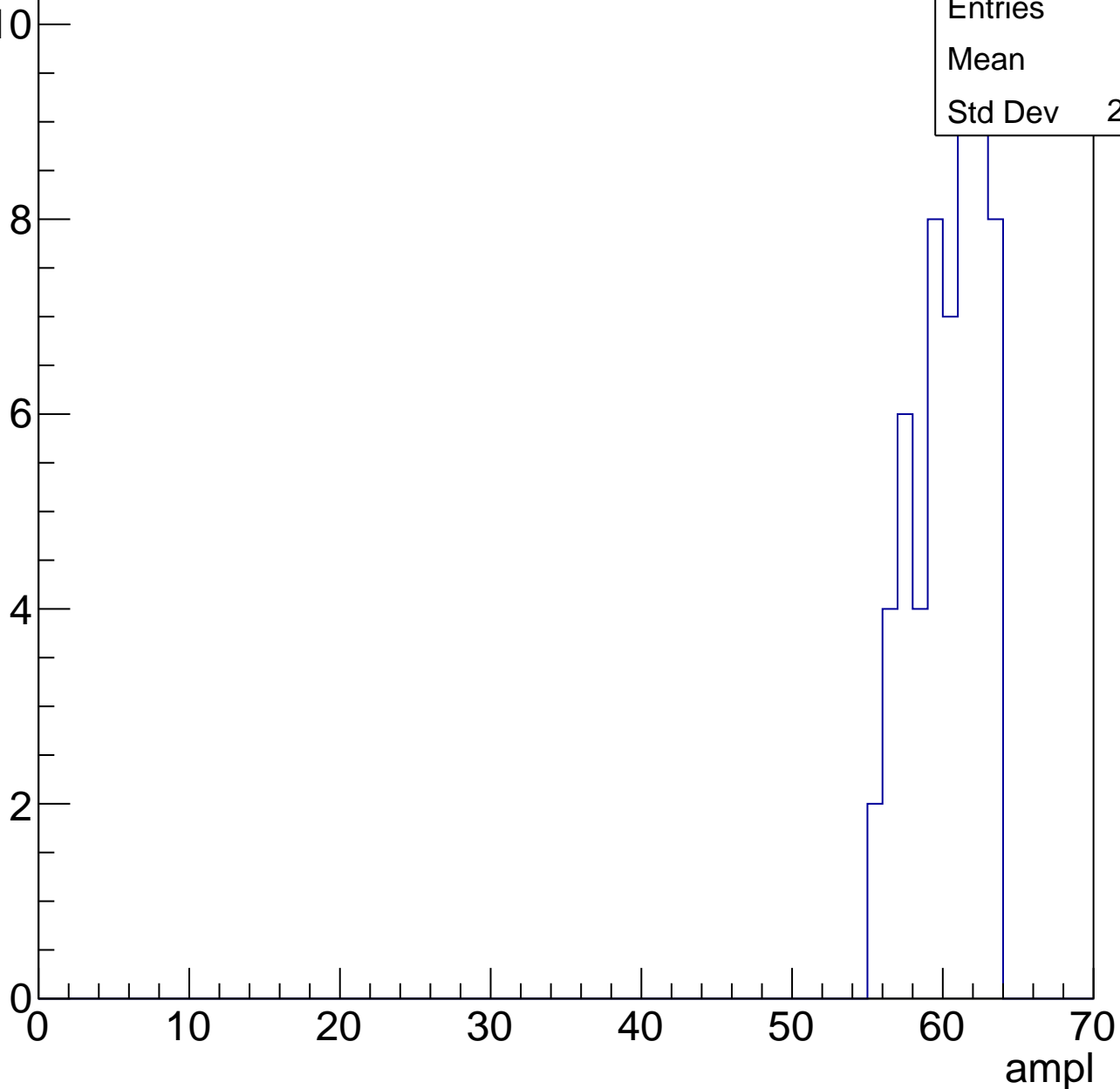


# B1L000S, U23-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	59.9
Std Dev	2.305



# B1L000S, U23-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch32, adc0

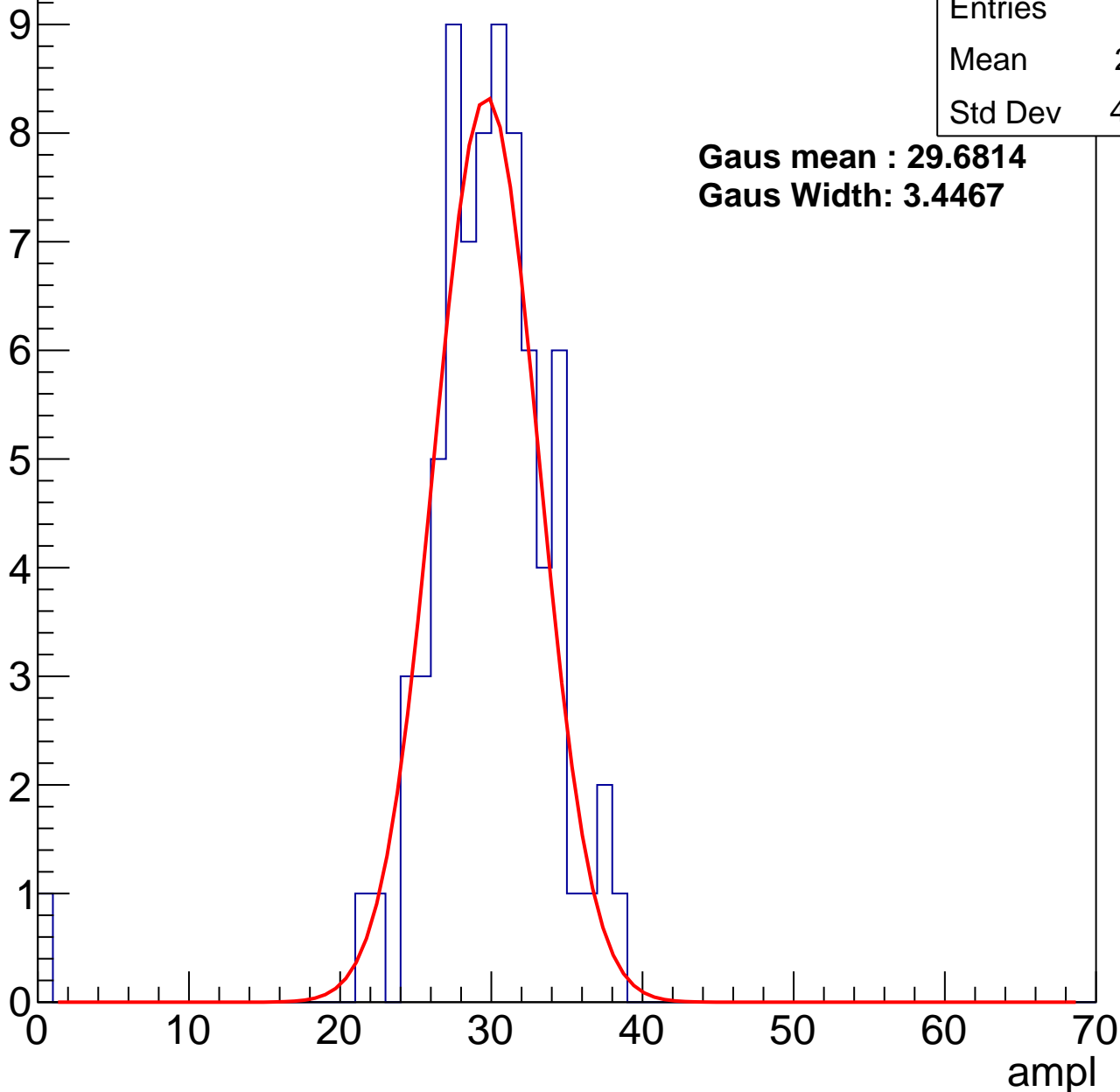
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	29.21
Std Dev	4.819

**Gaus mean : 29.6814**

**Gaus Width: 3.4467**



# B1L000S, U23-ch32, adc1

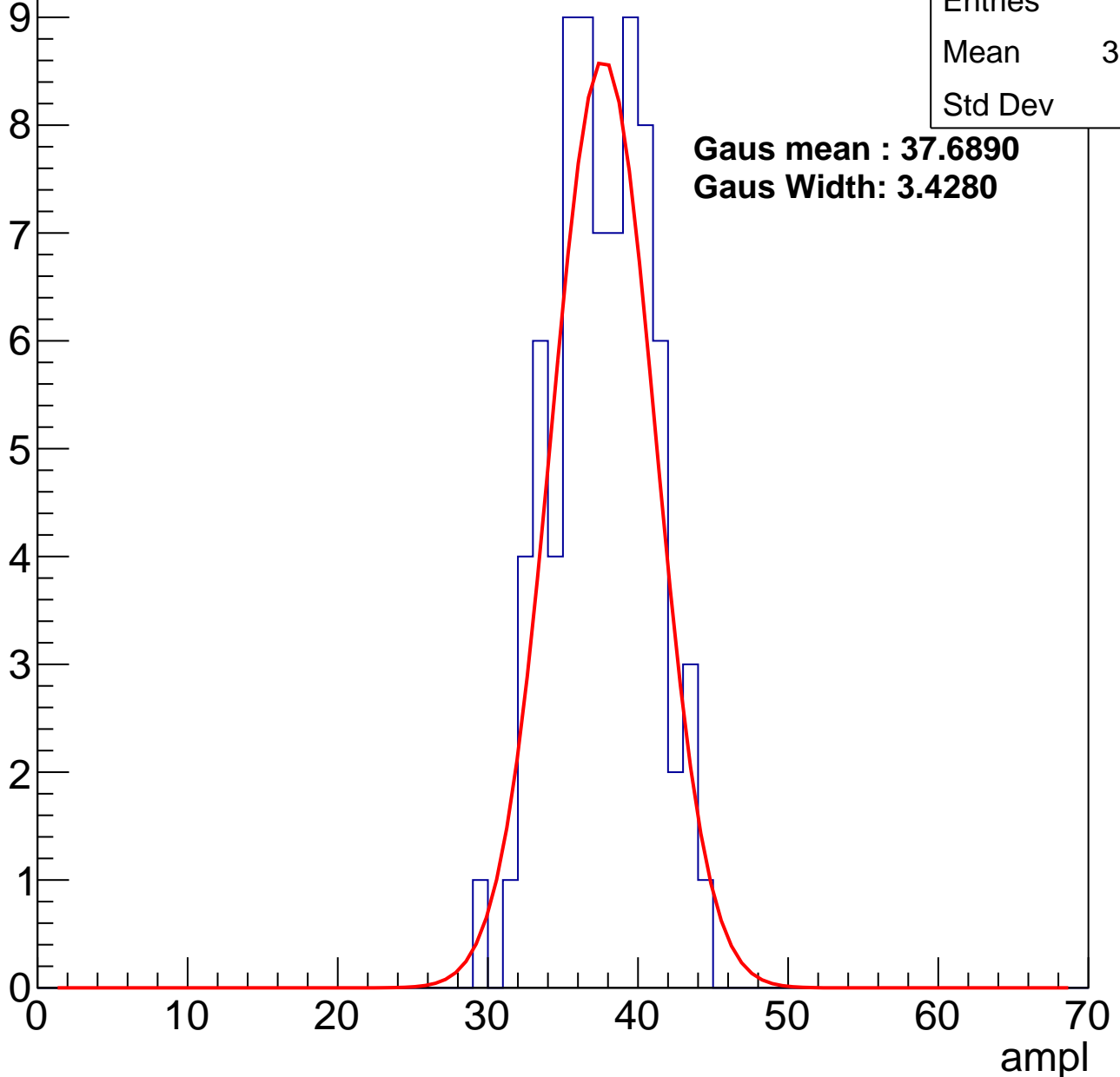
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	37.14
Std Dev	3.21

**Gaus mean : 37.6890**

**Gaus Width: 3.4280**



# B1L000S, U23-ch32, adc2

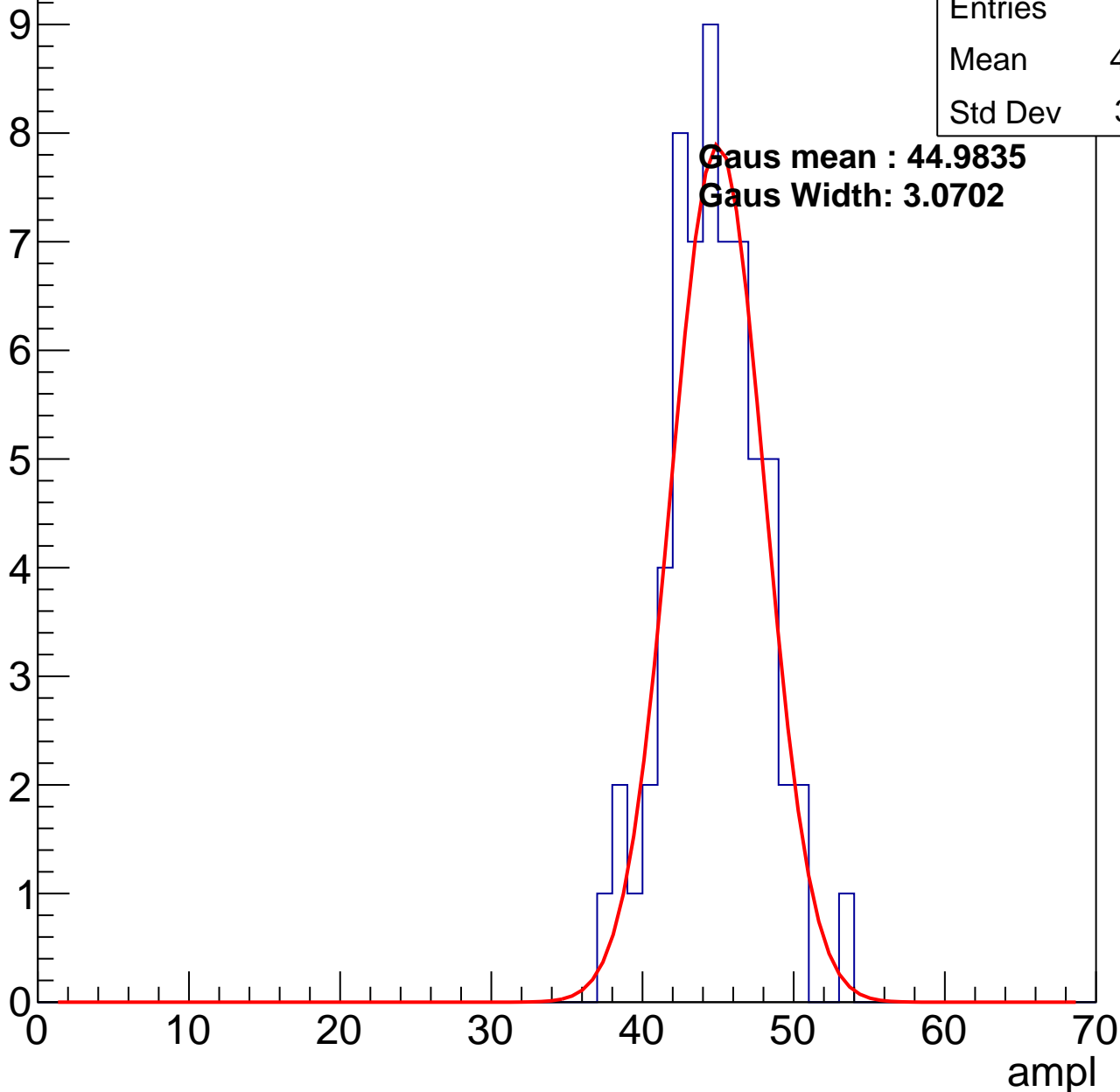
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	44.32
Std Dev	3.111

**Gaus mean : 44.9835**

**Gaus Width: 3.0702**

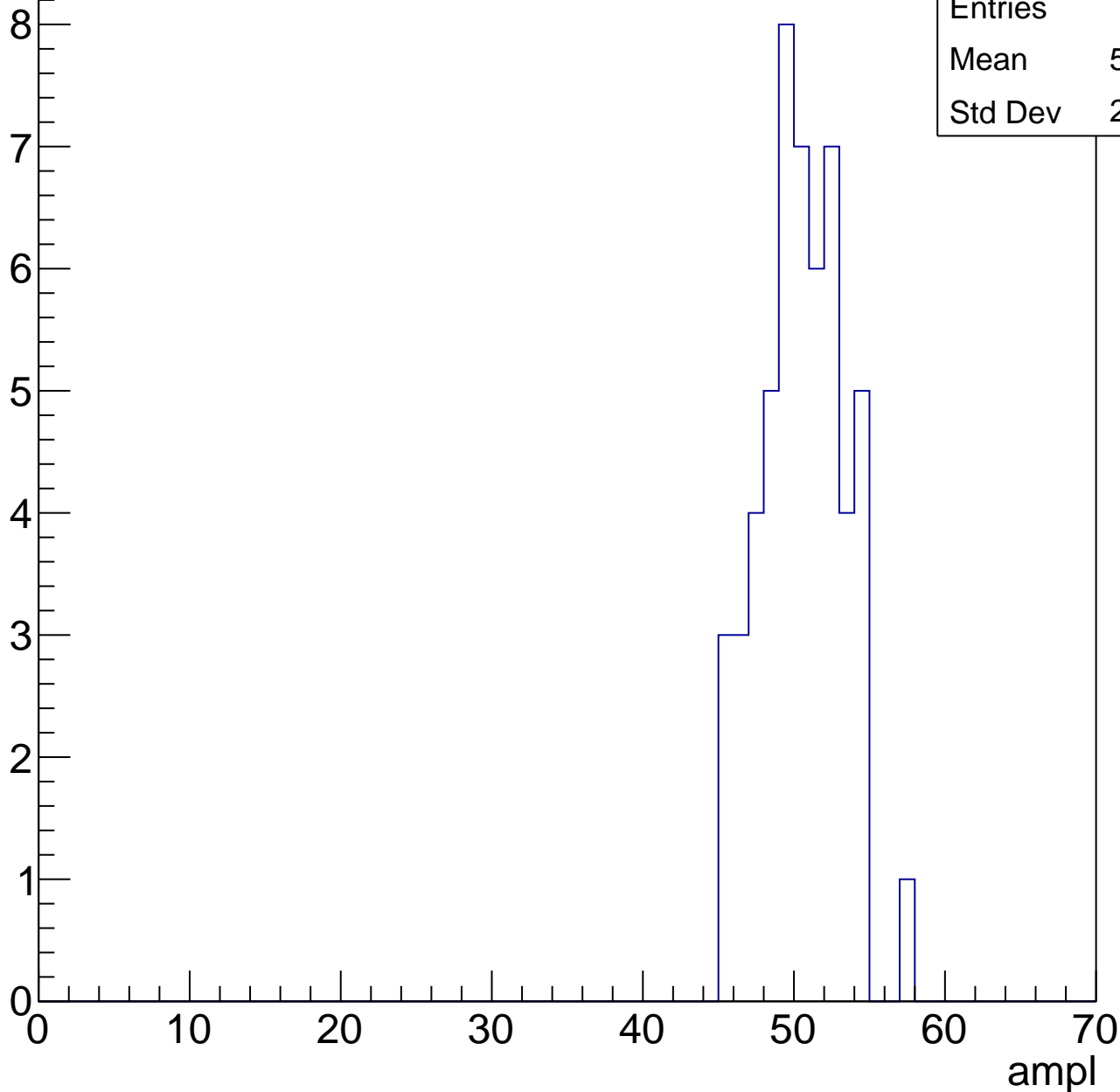


# B1L000S, U23-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

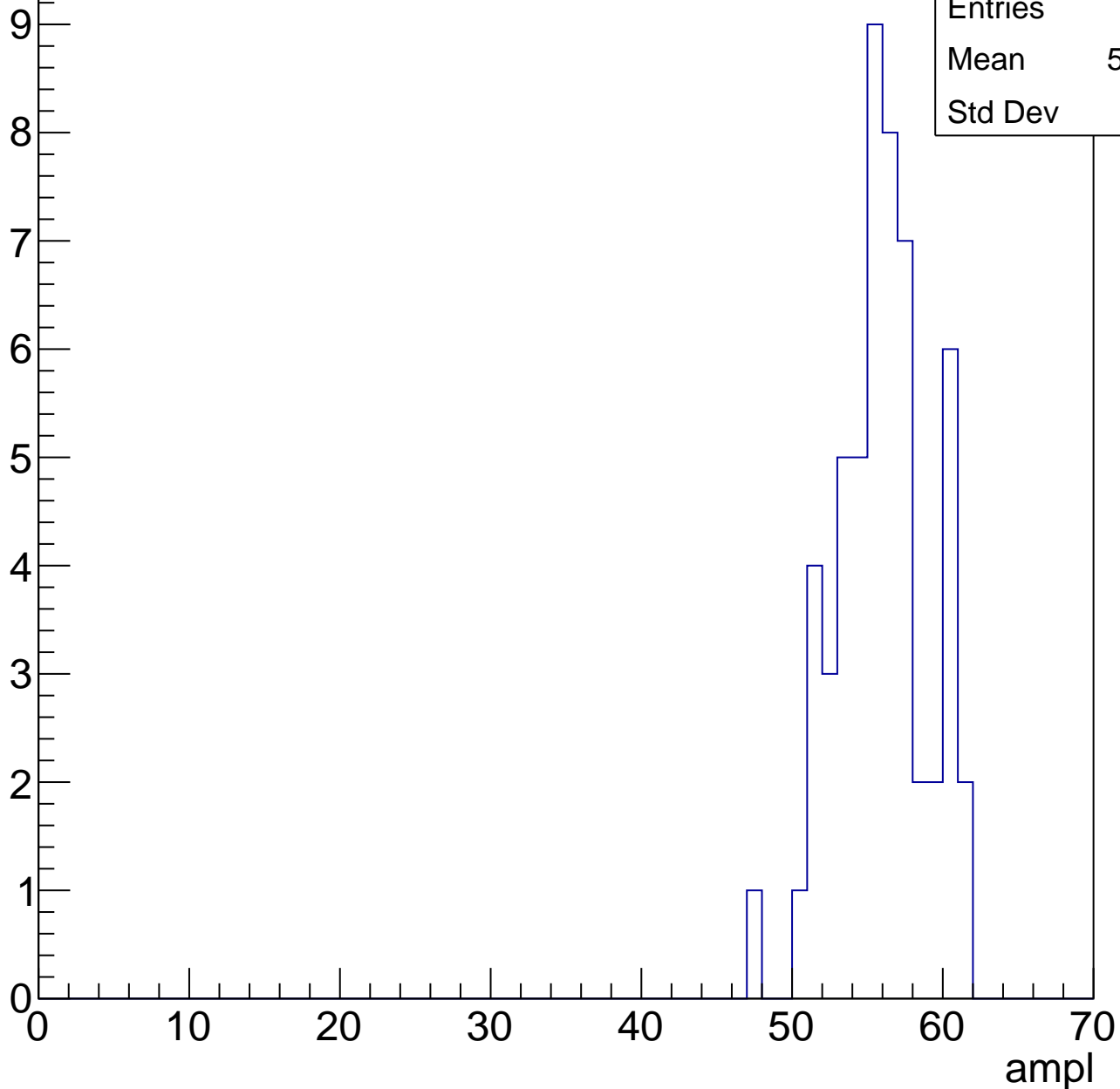
Entries	53
Mean	50.04
Std Dev	2.698



# B1L000S, U23-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

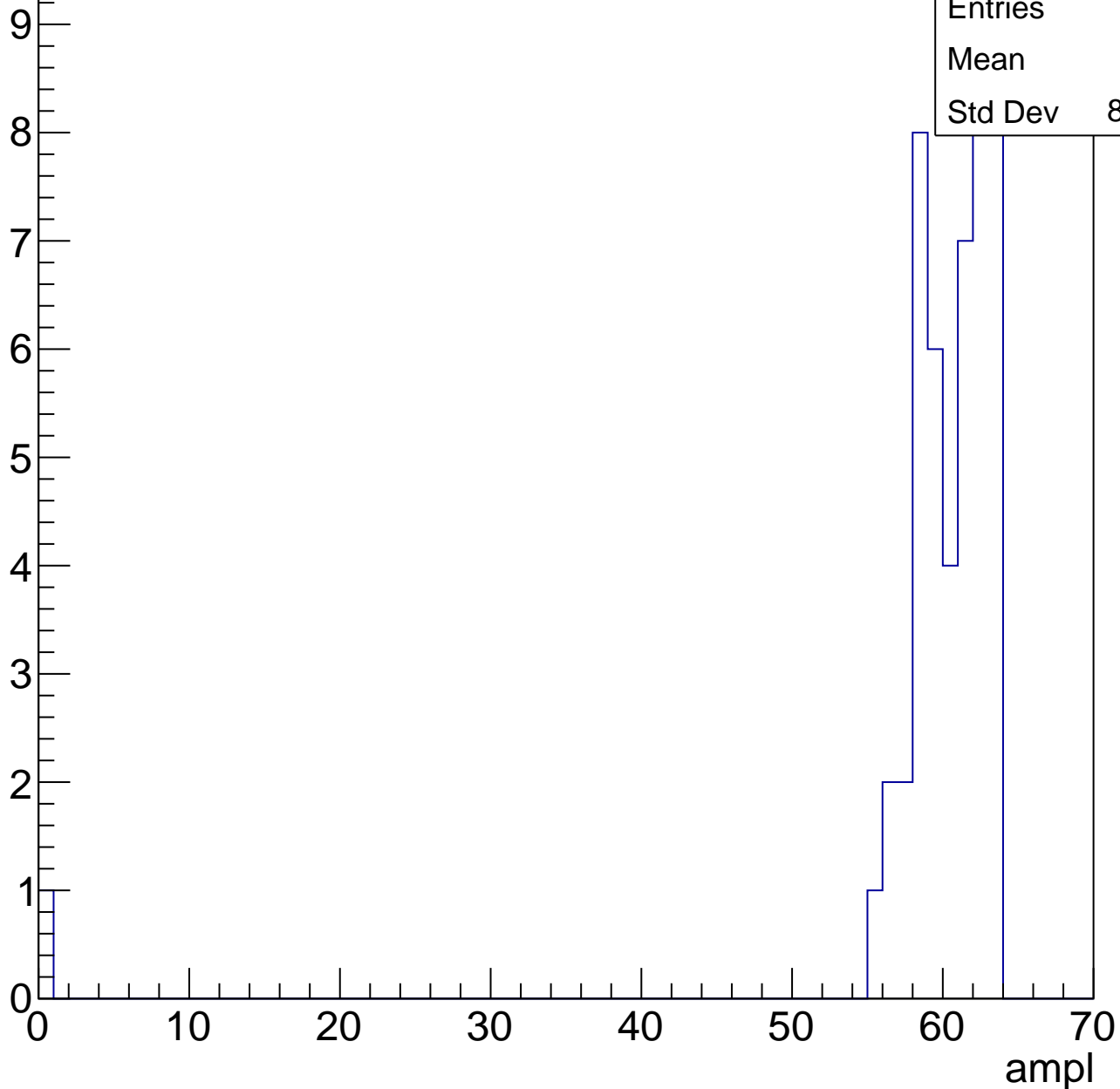


Entries	55
Mean	55.45
Std Dev	3.02

# B1L000S, U23-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

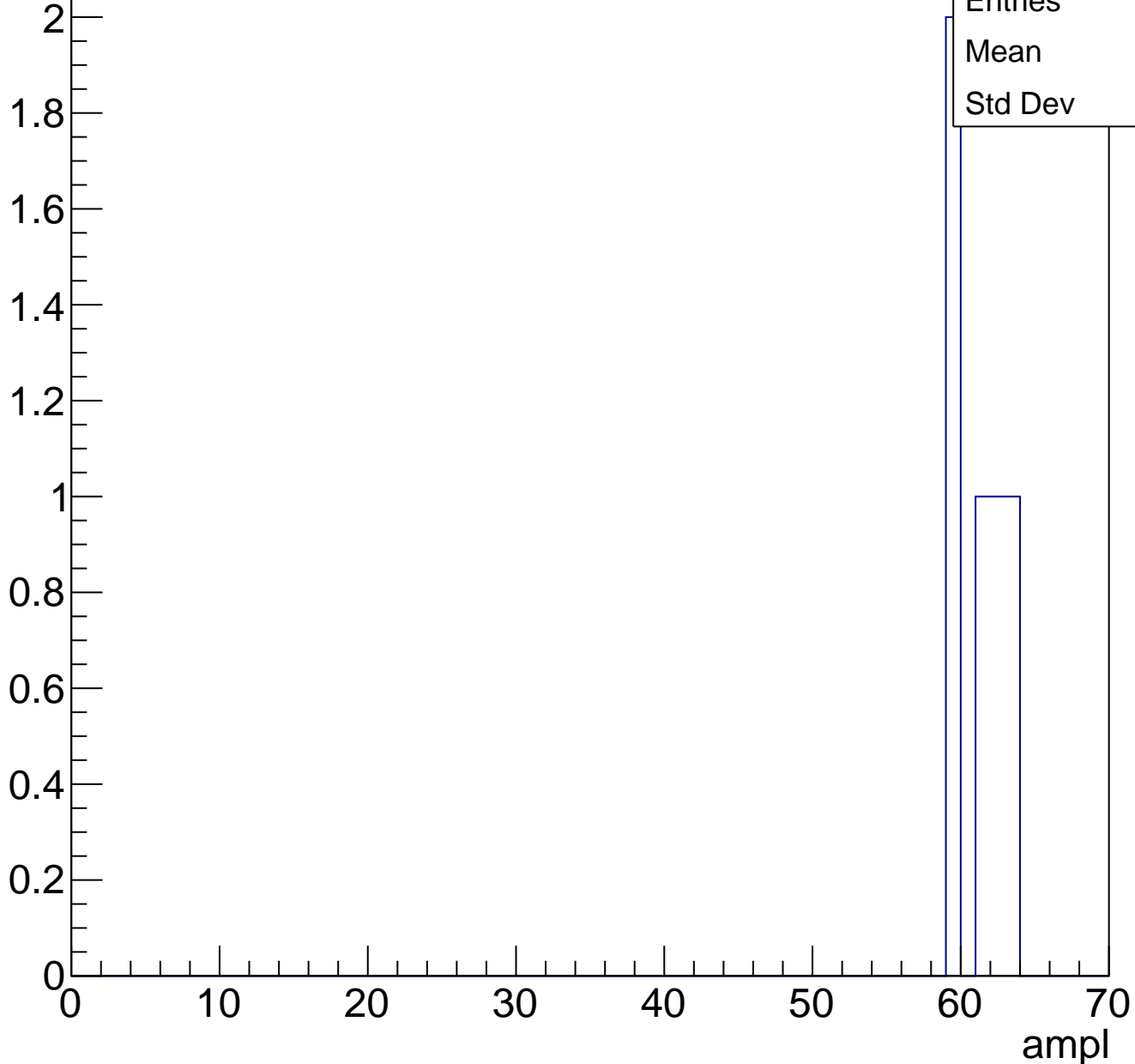
Entry



# B1L000S, U23-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch33, adc0

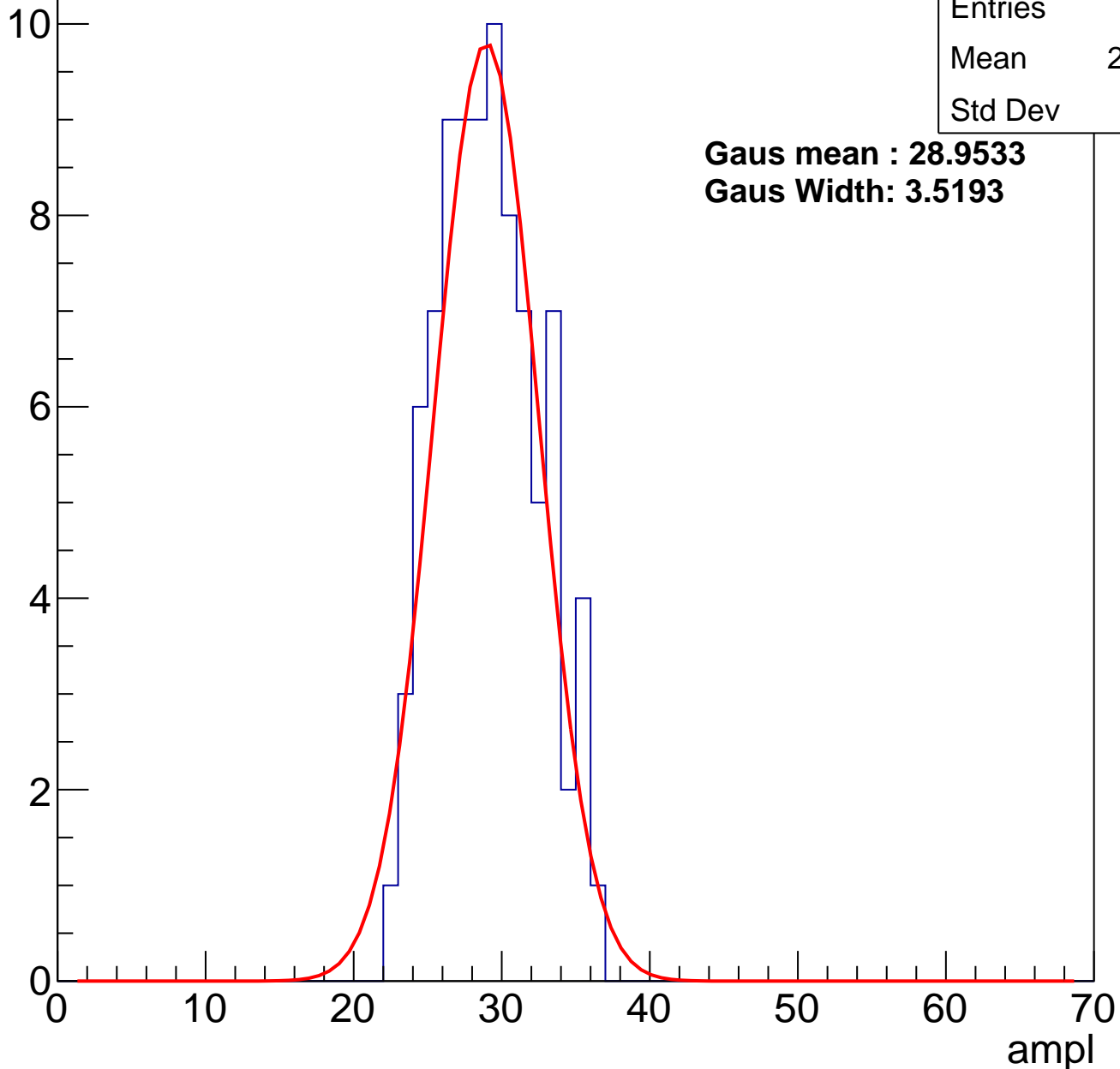
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	88
Mean	28.65
Std Dev	3.32

**Gaus mean : 28.9533**

**Gaus Width: 3.5193**

Entry



# B1L000S, U23-ch33, adc1

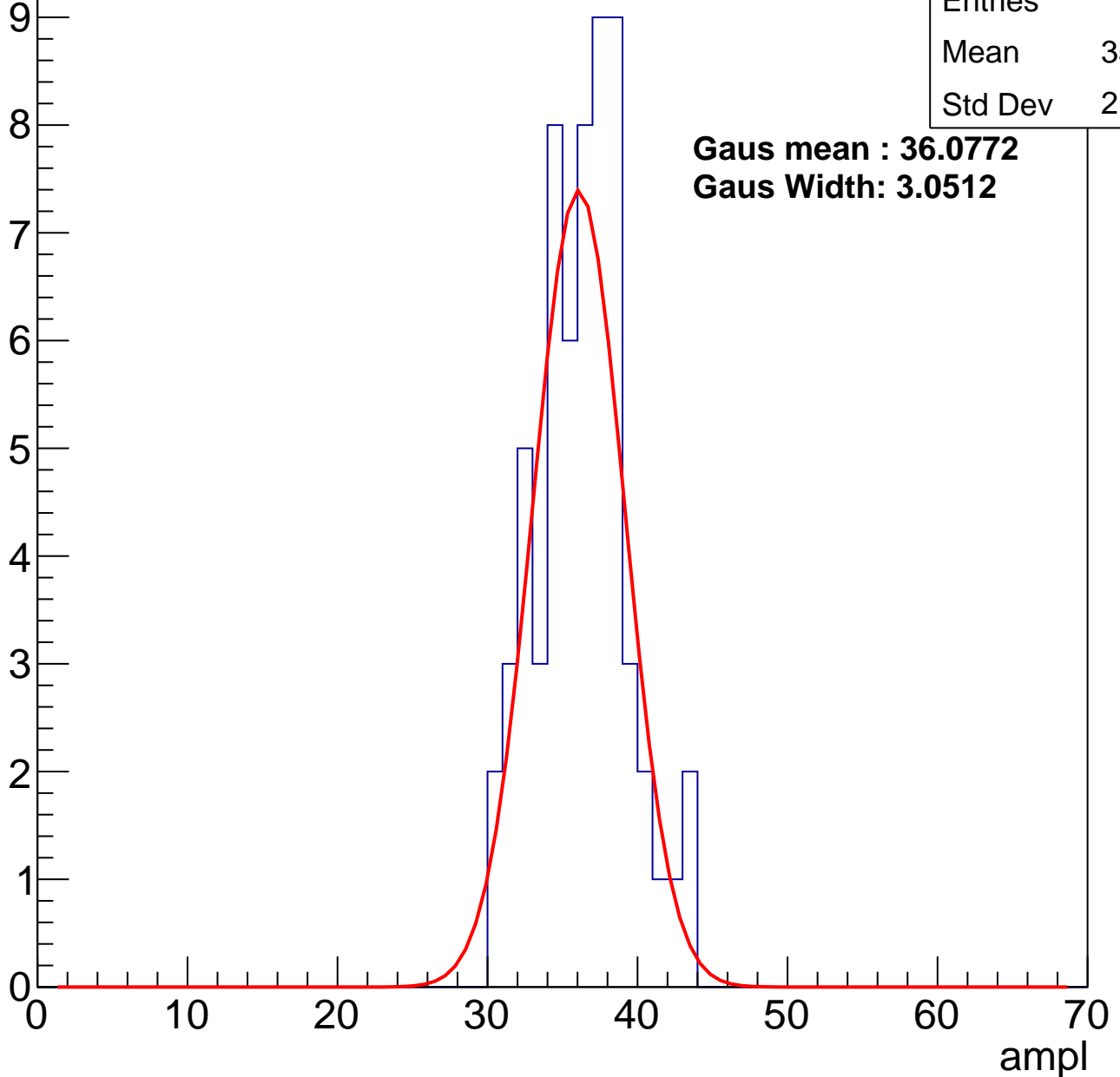
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.85
Std Dev	2.988

**Gaus mean : 36.0772**

**Gaus Width: 3.0512**



# B1L000S, U23-ch33, adc2

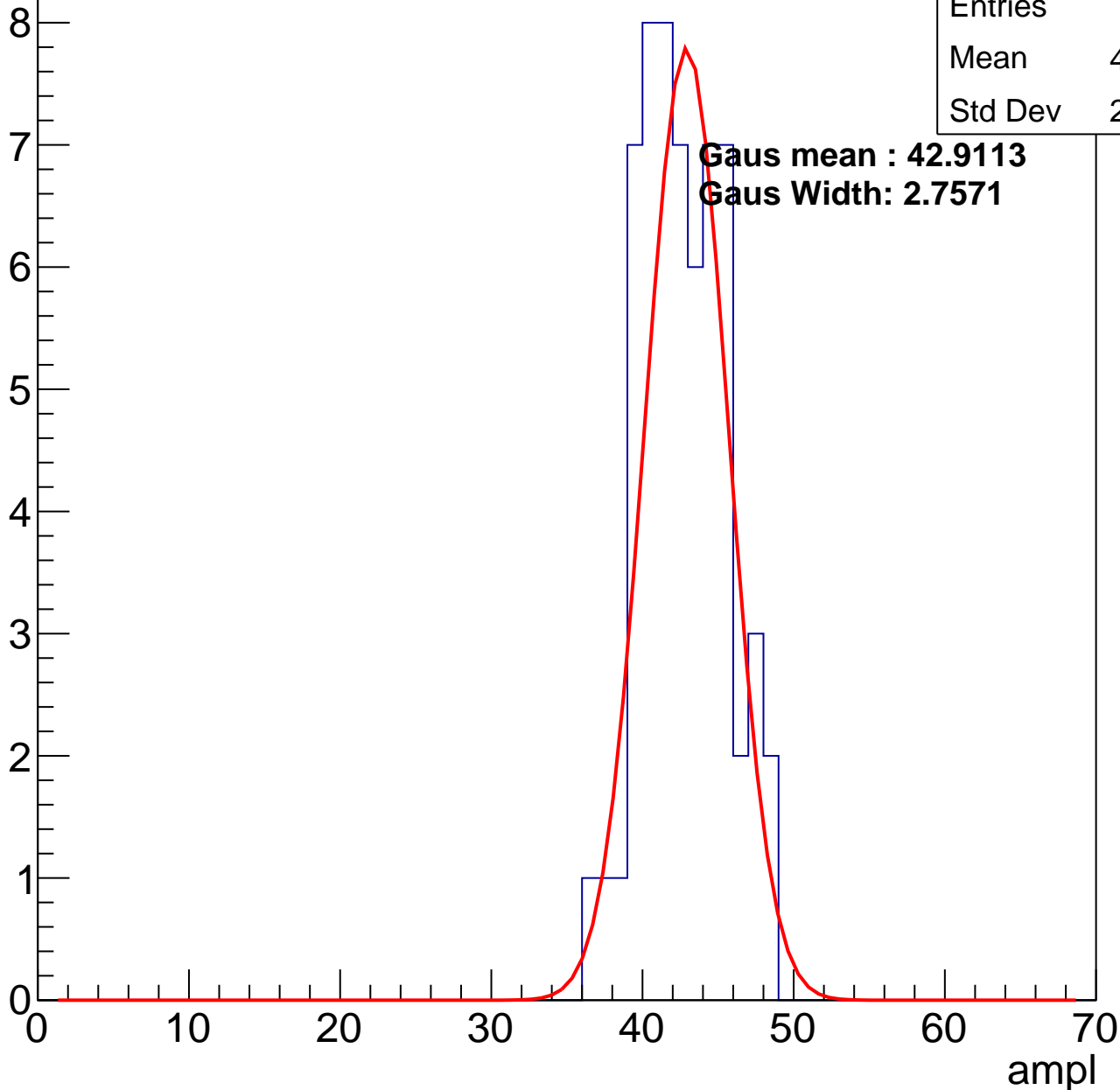
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.27
Std Dev	2.744

**Gaus mean : 42.9113**

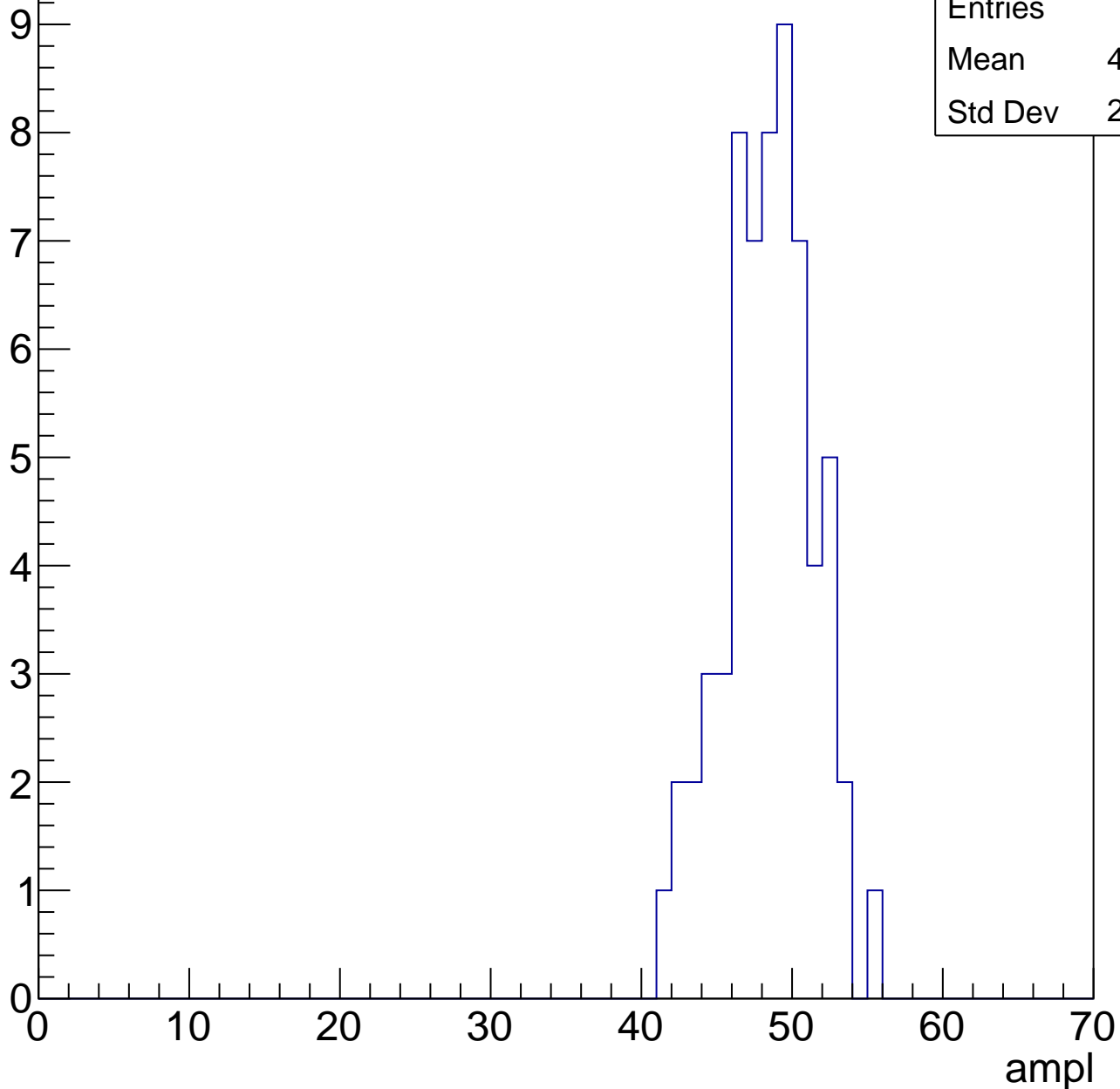
**Gaus Width: 2.7571**



# B1L000S, U23-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

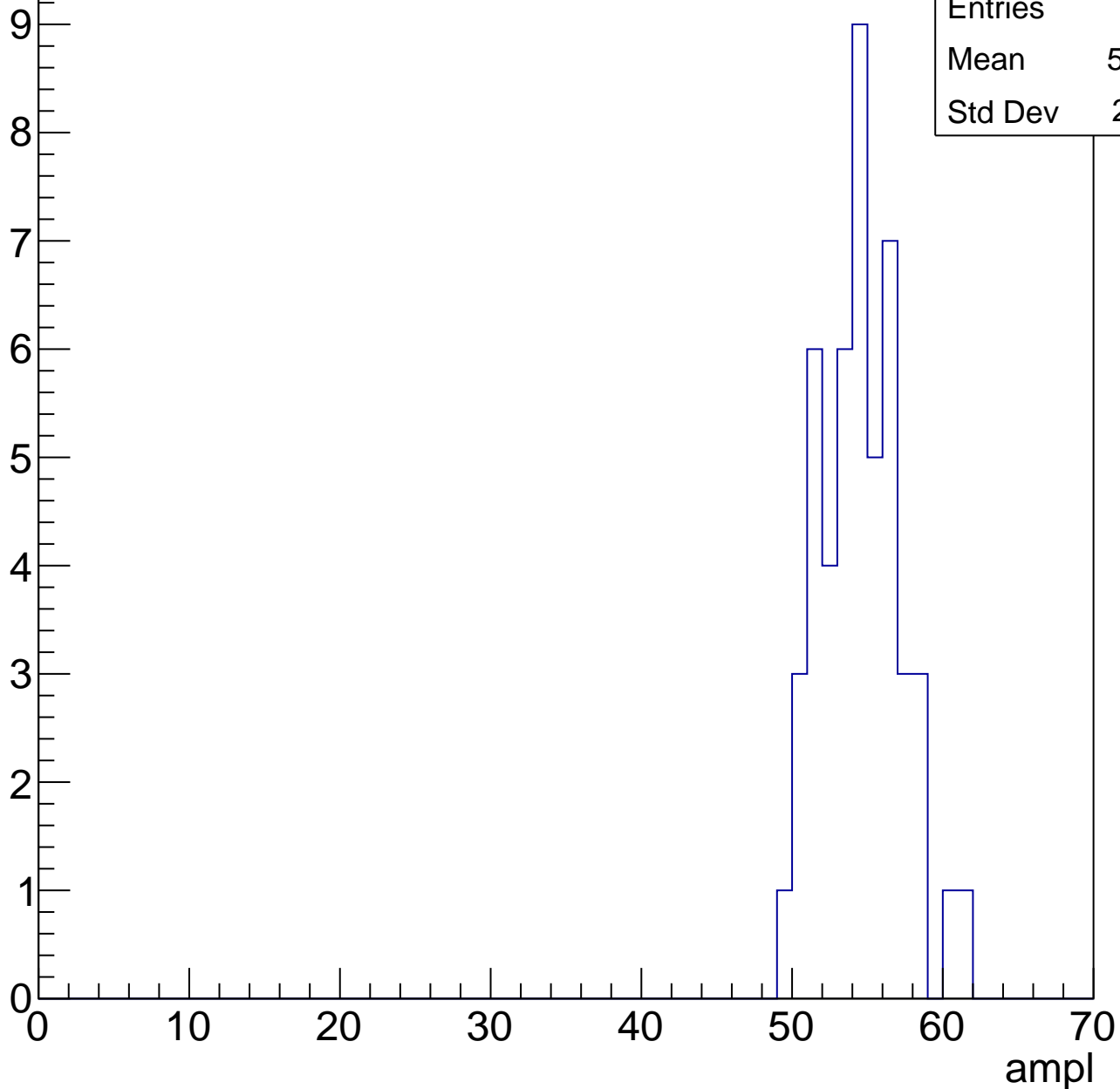
Entry



# B1L000S, U23-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	58.16
Std Dev	7.646

Entry

10

8

6

4

2

0

0

10

20

30

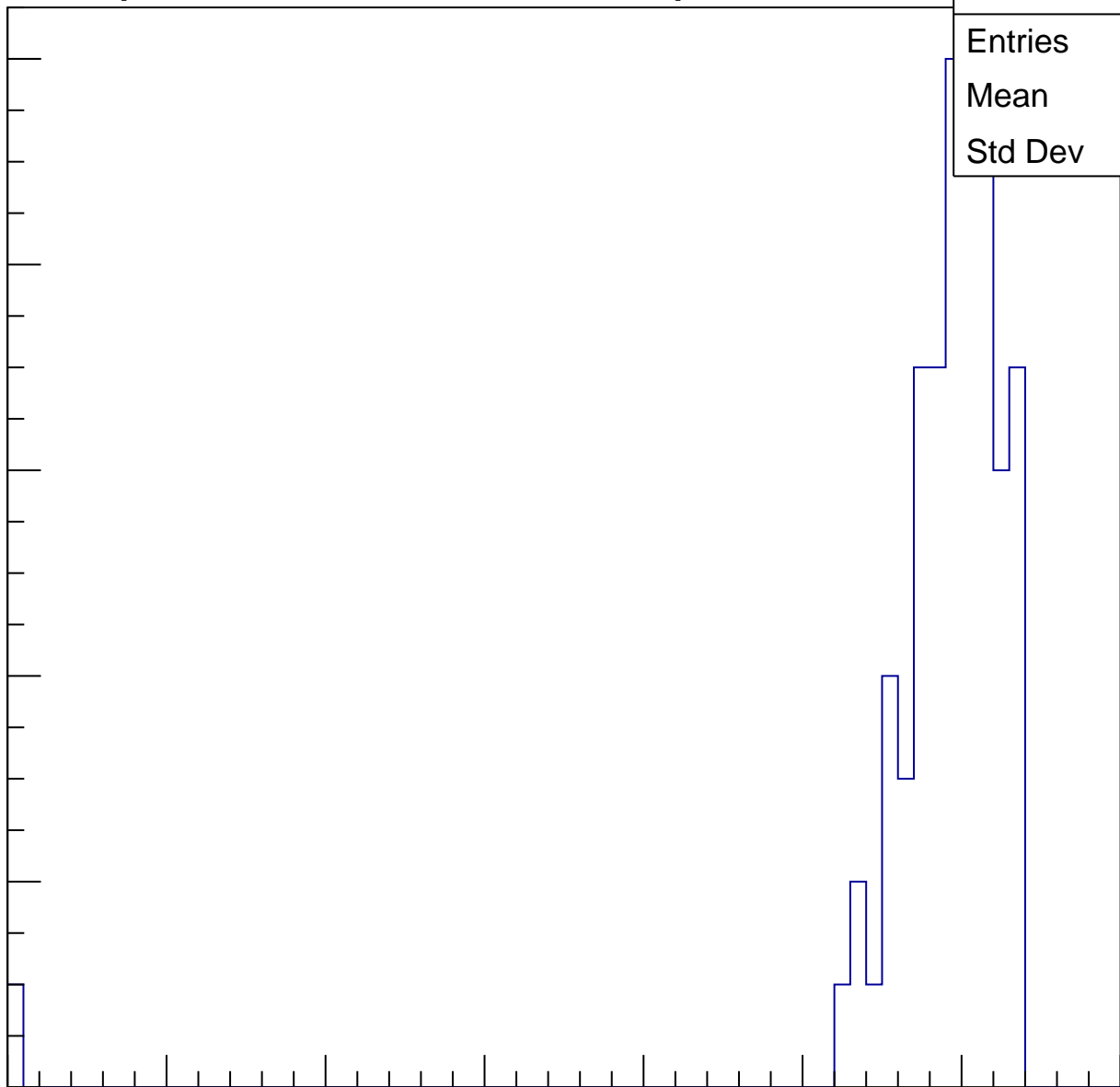
40

50

60

70

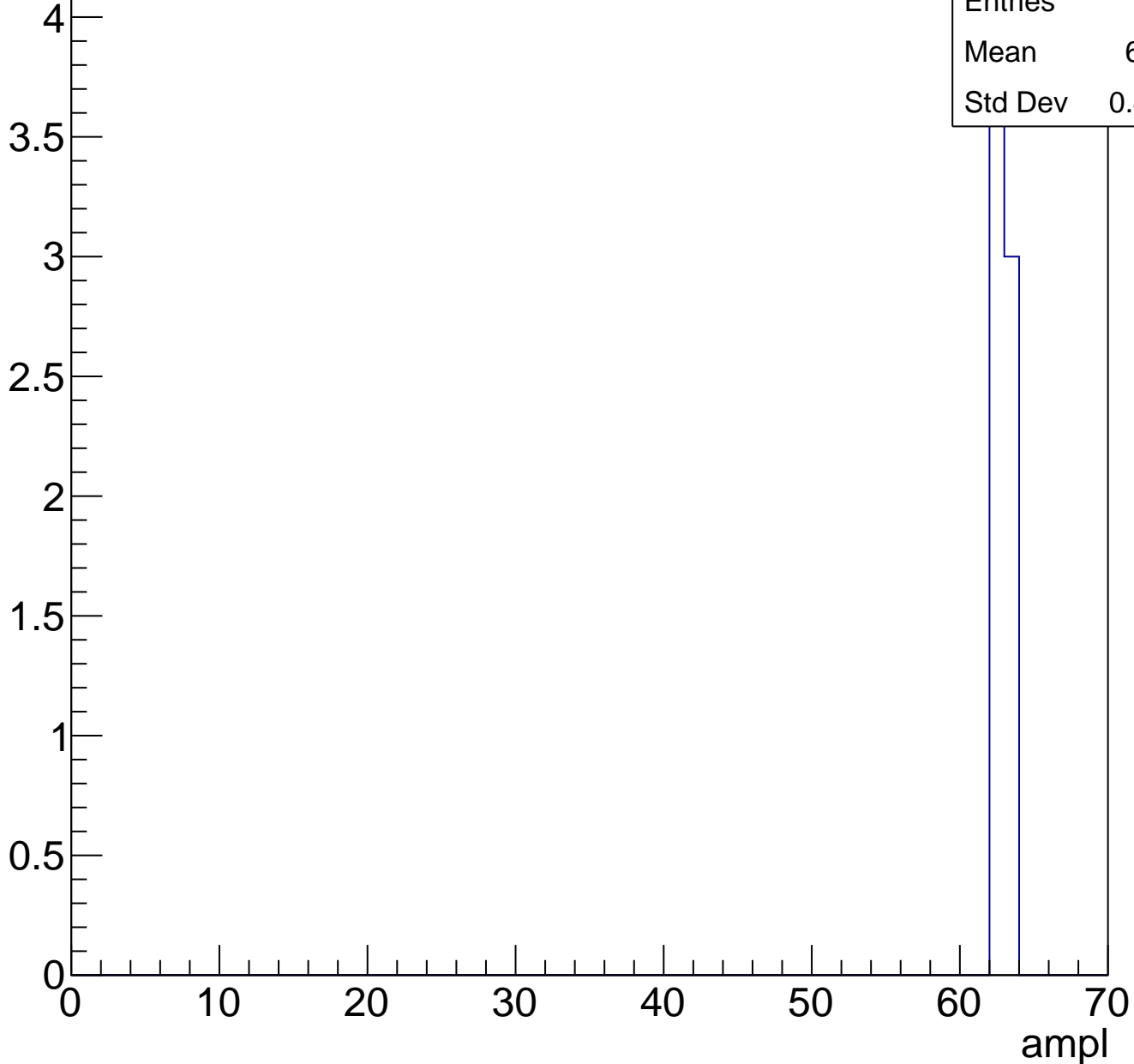
ampl



# B1L000S, U23-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch34, adc0

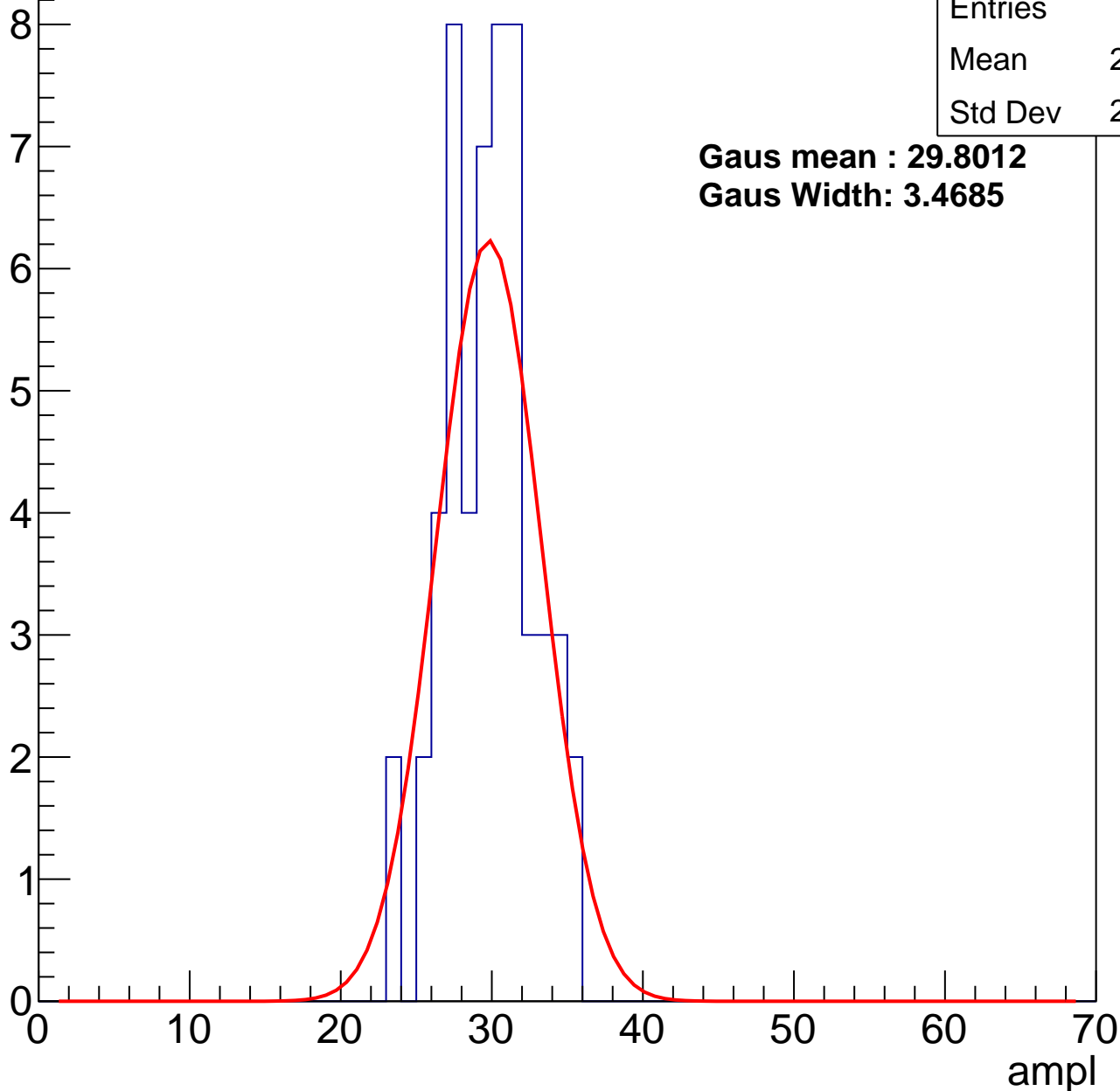
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	29.37
Std Dev	2.824

**Gaus mean : 29.8012**

**Gaus Width: 3.4685**



# B1L000S, U23-ch34, adc1

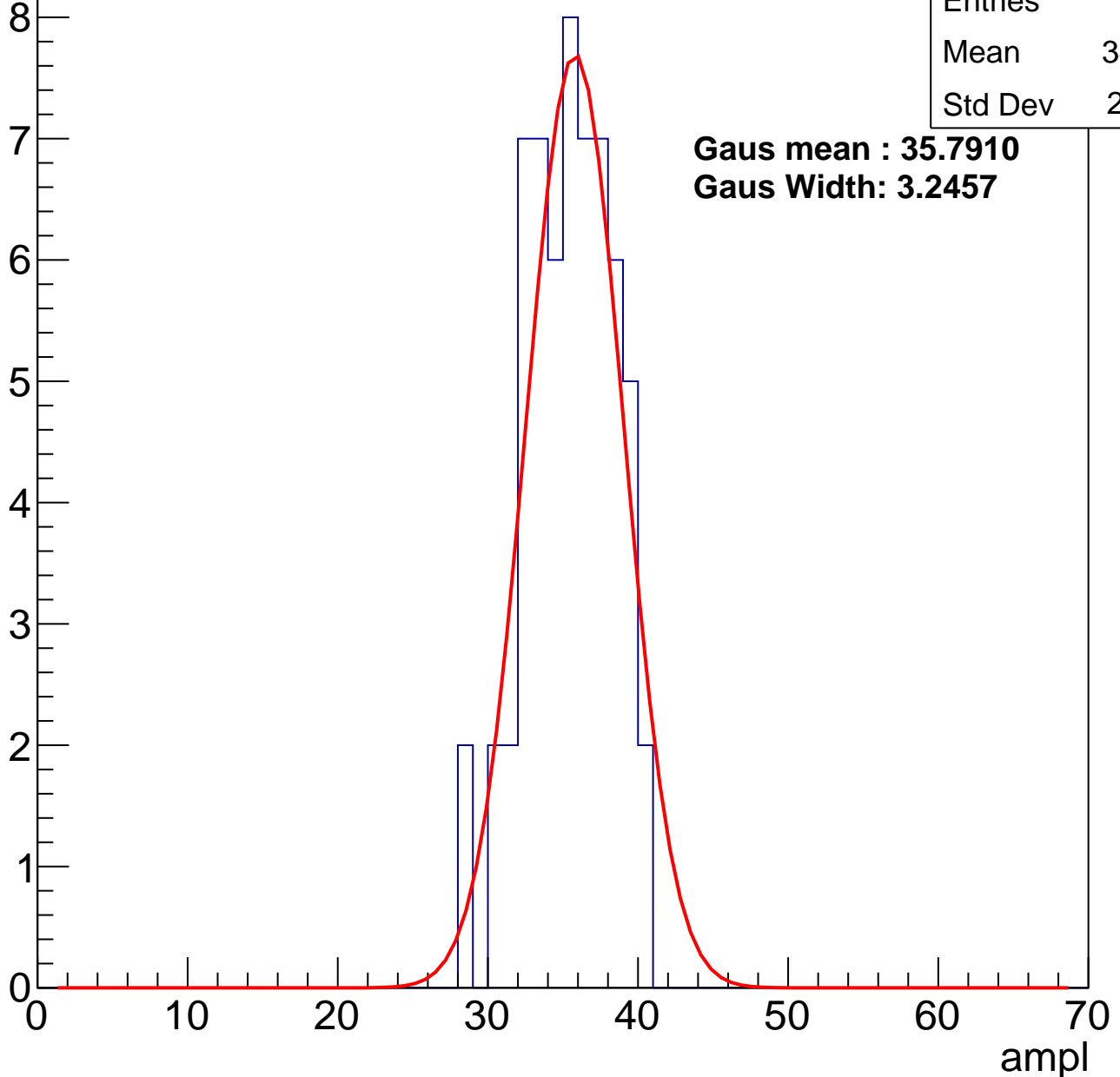
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.93
Std Dev	2.851

**Gaus mean : 35.7910**

**Gaus Width: 3.2457**



# B1L000S, U23-ch34, adc2

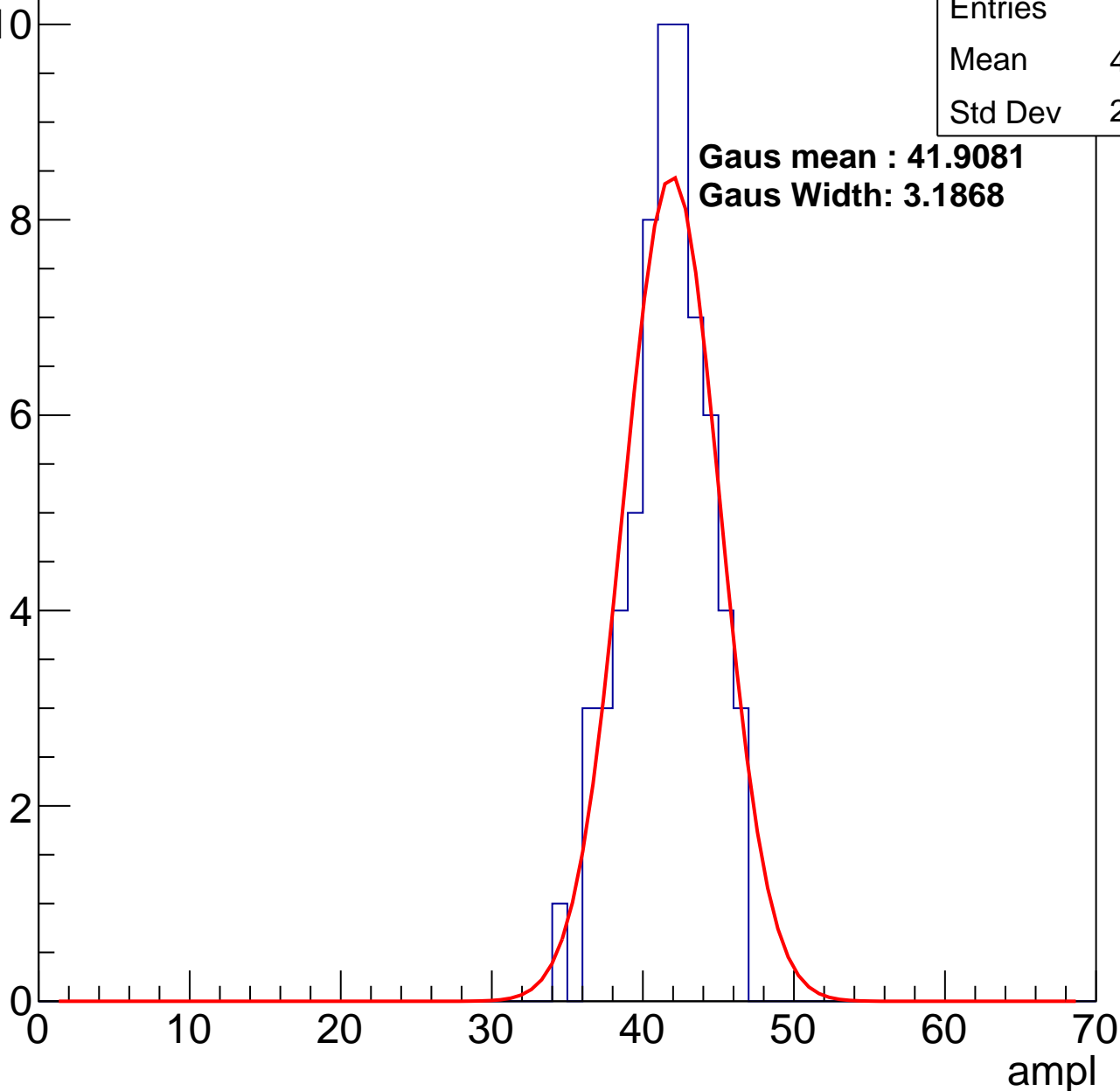
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.14
Std Dev	2.698

**Gaus mean : 41.9081**

**Gaus Width: 3.1868**



# B1L000S, U23-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	47.89
Std Dev	2.6

Entry

10

8

6

4

2

0

0

10

20

30

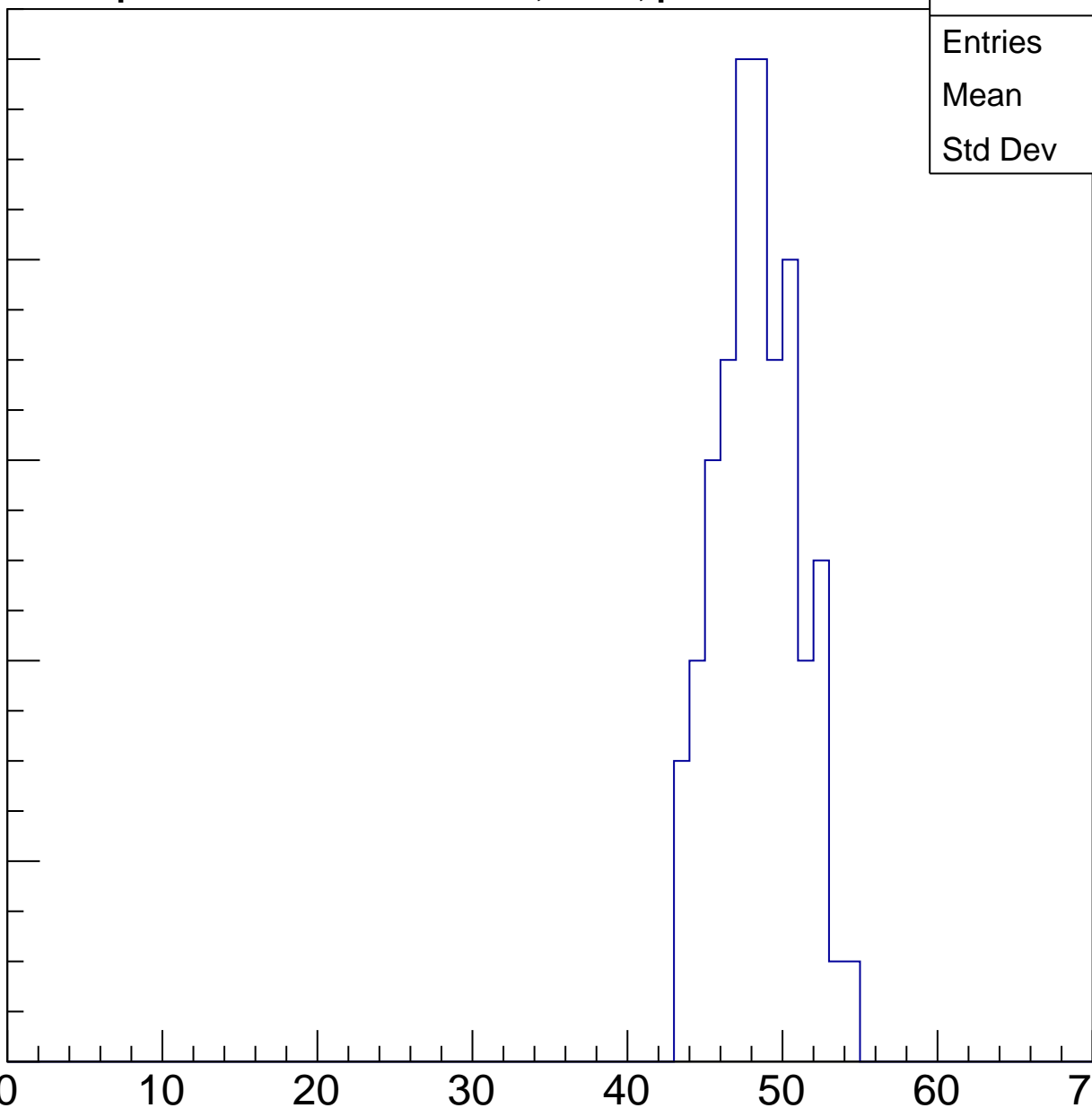
40

50

60

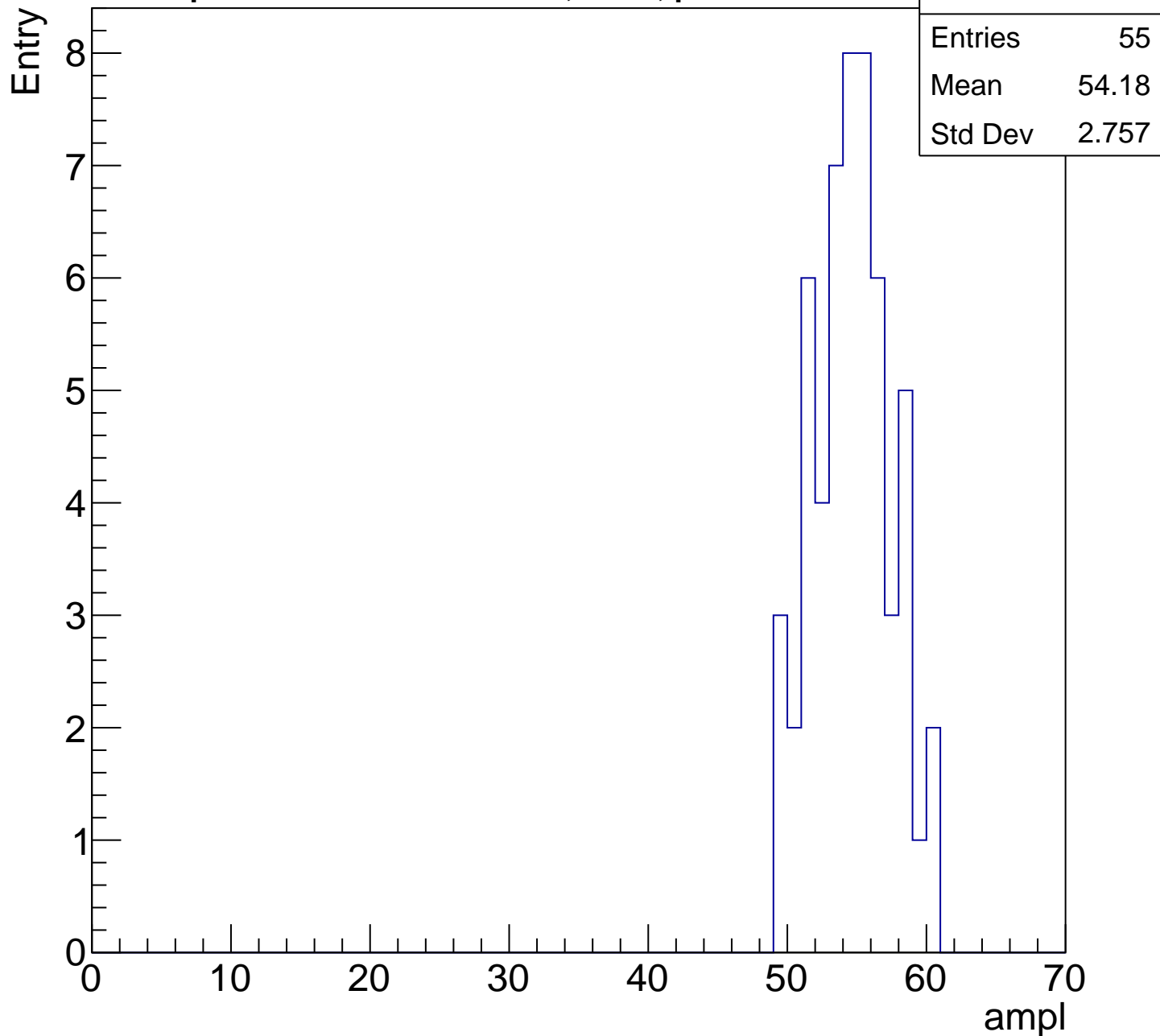
70

ampl



# B1L000S, U23-ch34, adc4

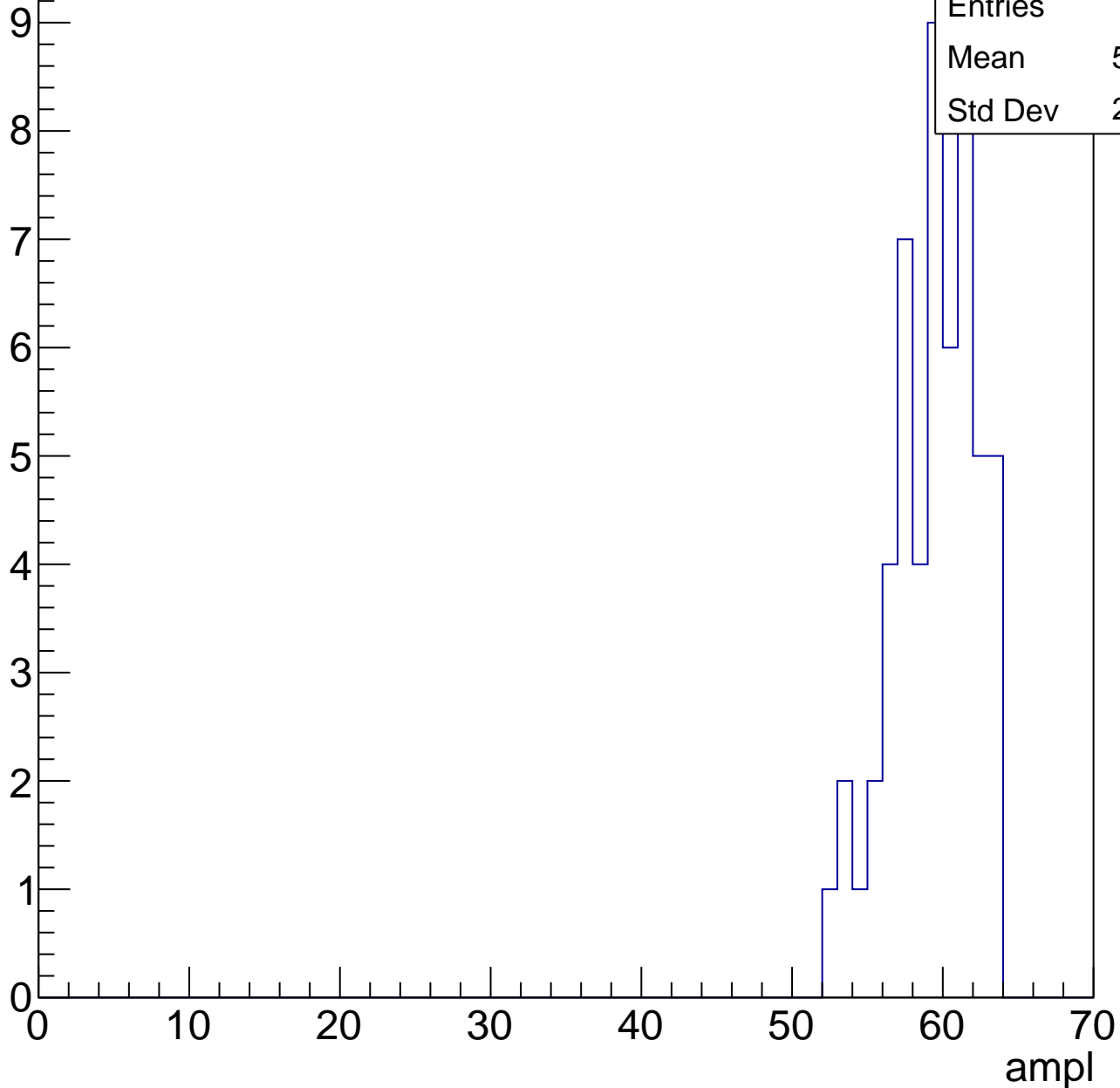
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

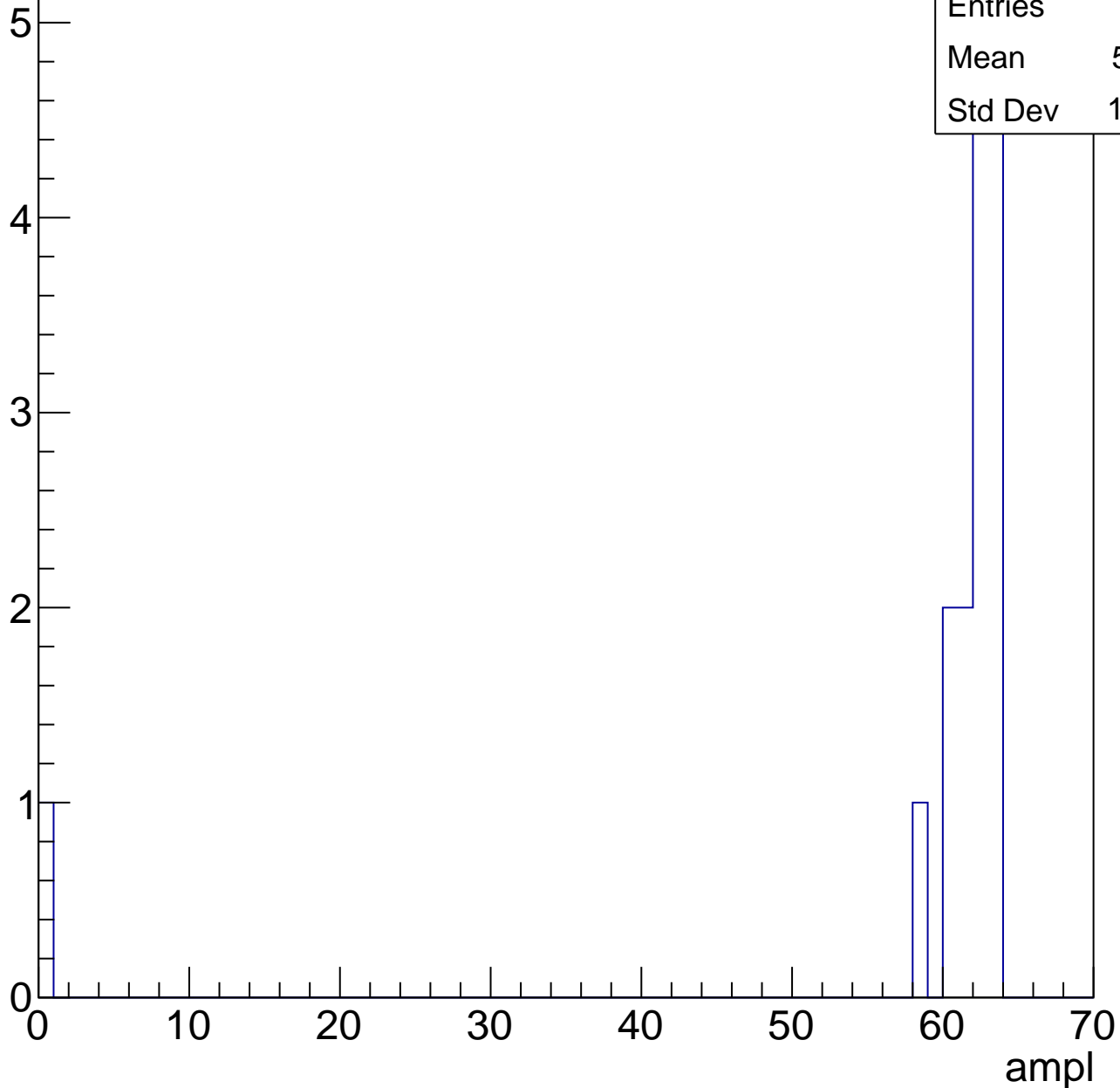


# B1L000S, U23-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.81
Std Dev	14.99





# B1L000S, U23-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch35, adc0

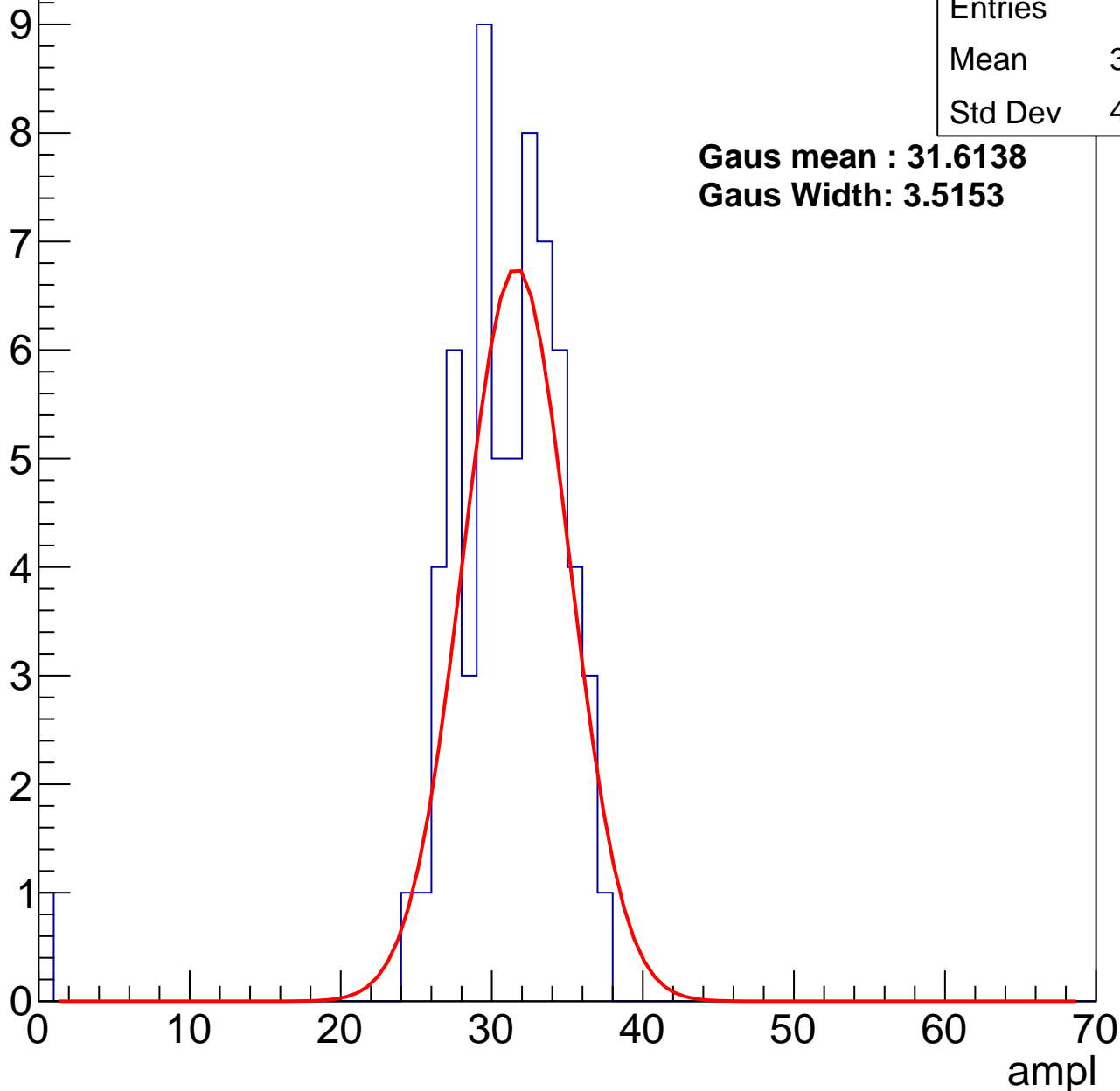
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	30.33
Std Dev	4.915

**Gaus mean : 31.6138**

**Gaus Width: 3.5153**



# B1L000S, U23-ch35, adc1

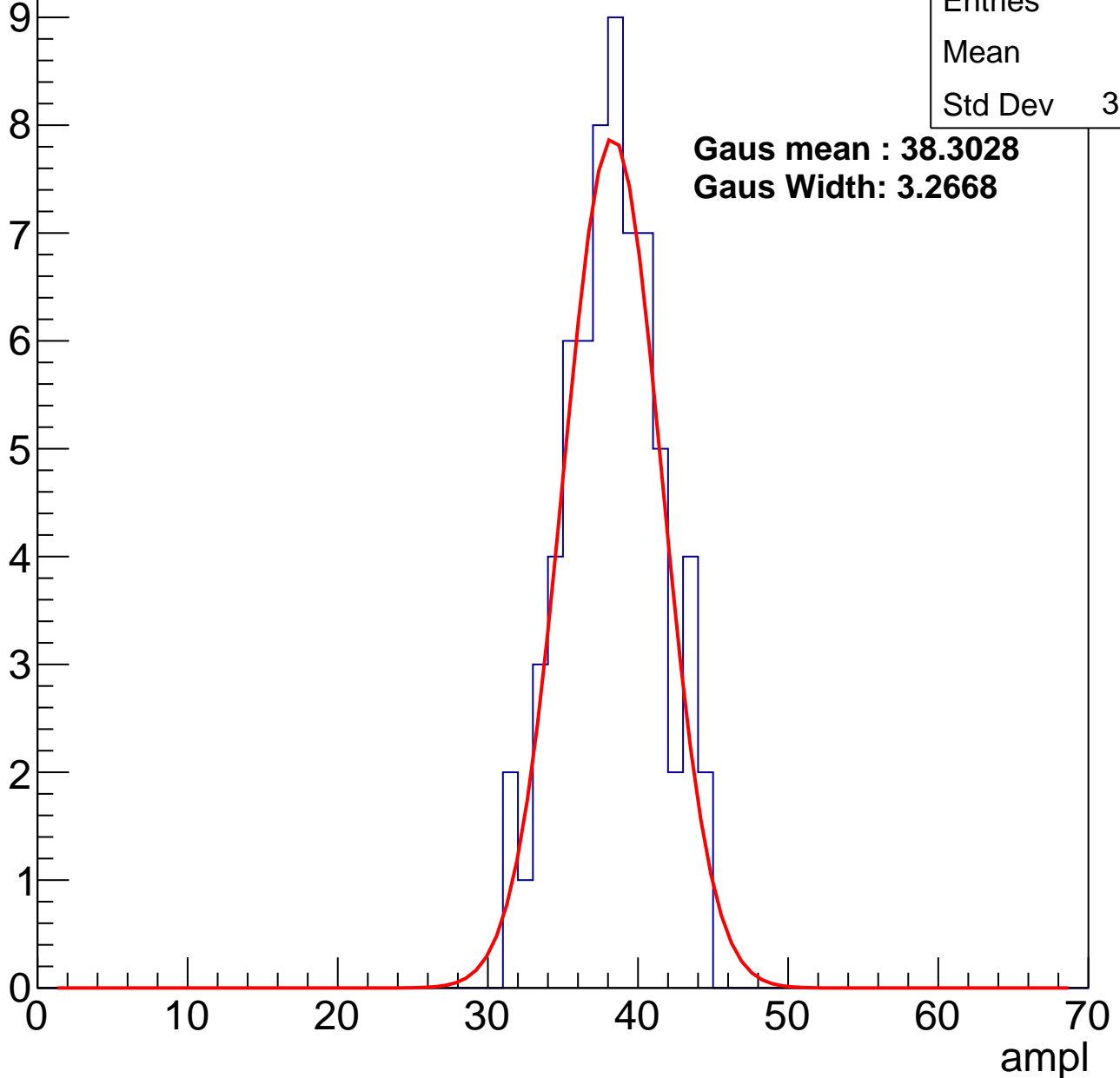
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	37.8
Std Dev	3.115

**Gaus mean : 38.3028**

**Gaus Width: 3.2668**



# B1L000S, U23-ch35, adc2

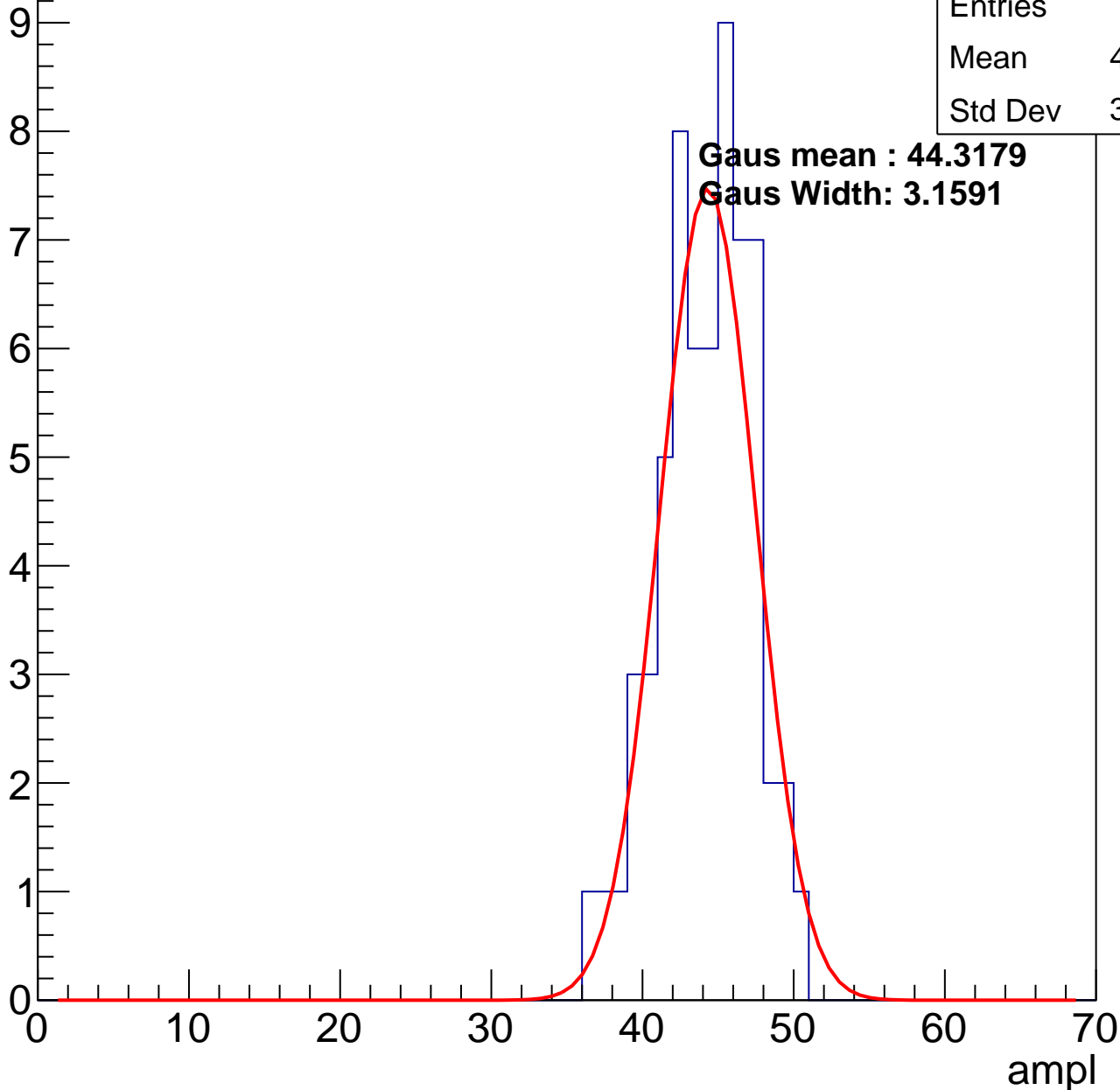
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.73
Std Dev	3.028

**Gaus mean : 44.3179**

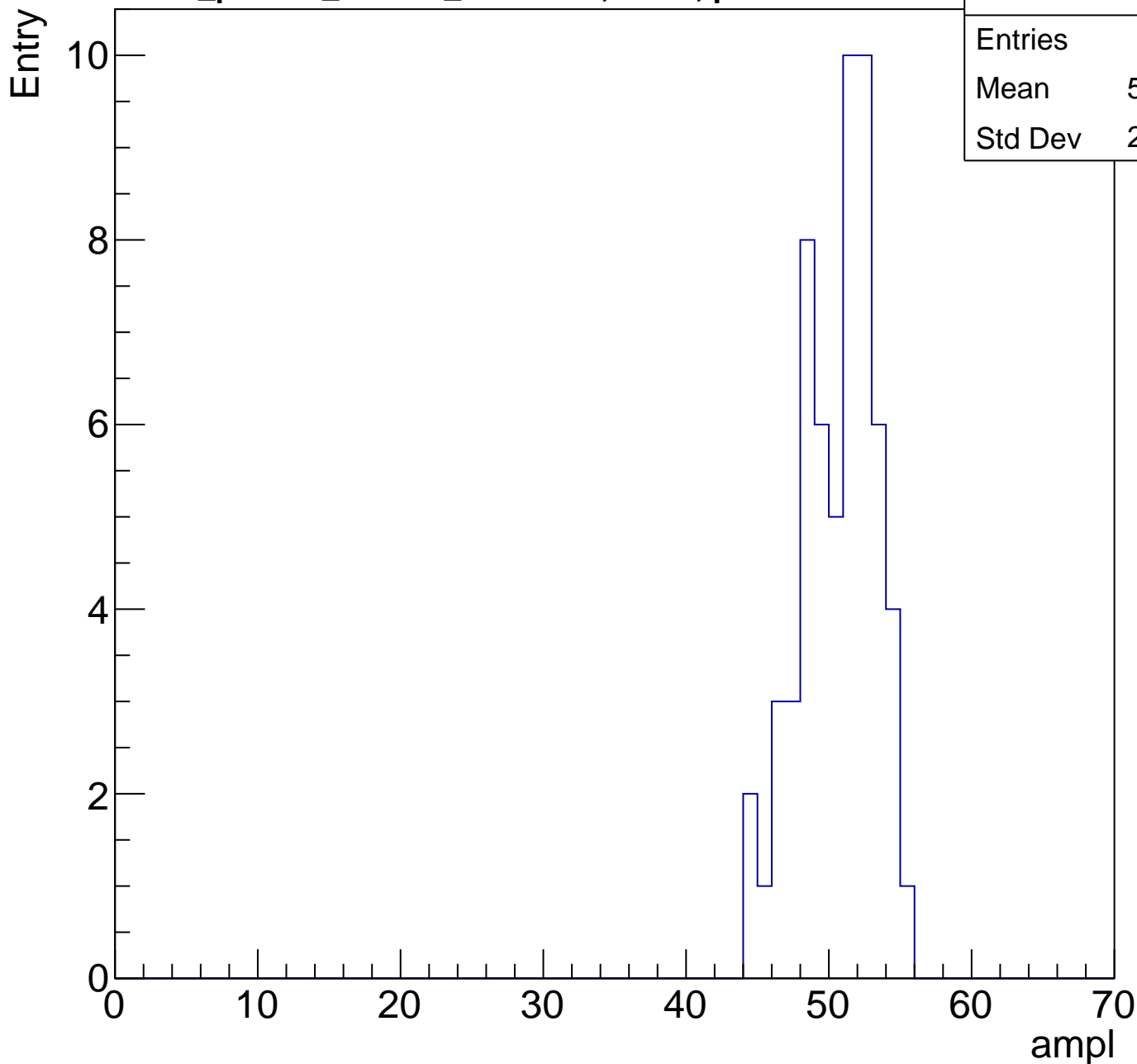
**Gaus Width: 3.1591**



# B1L000S, U23-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	59
Mean	50.15
Std Dev	2.609

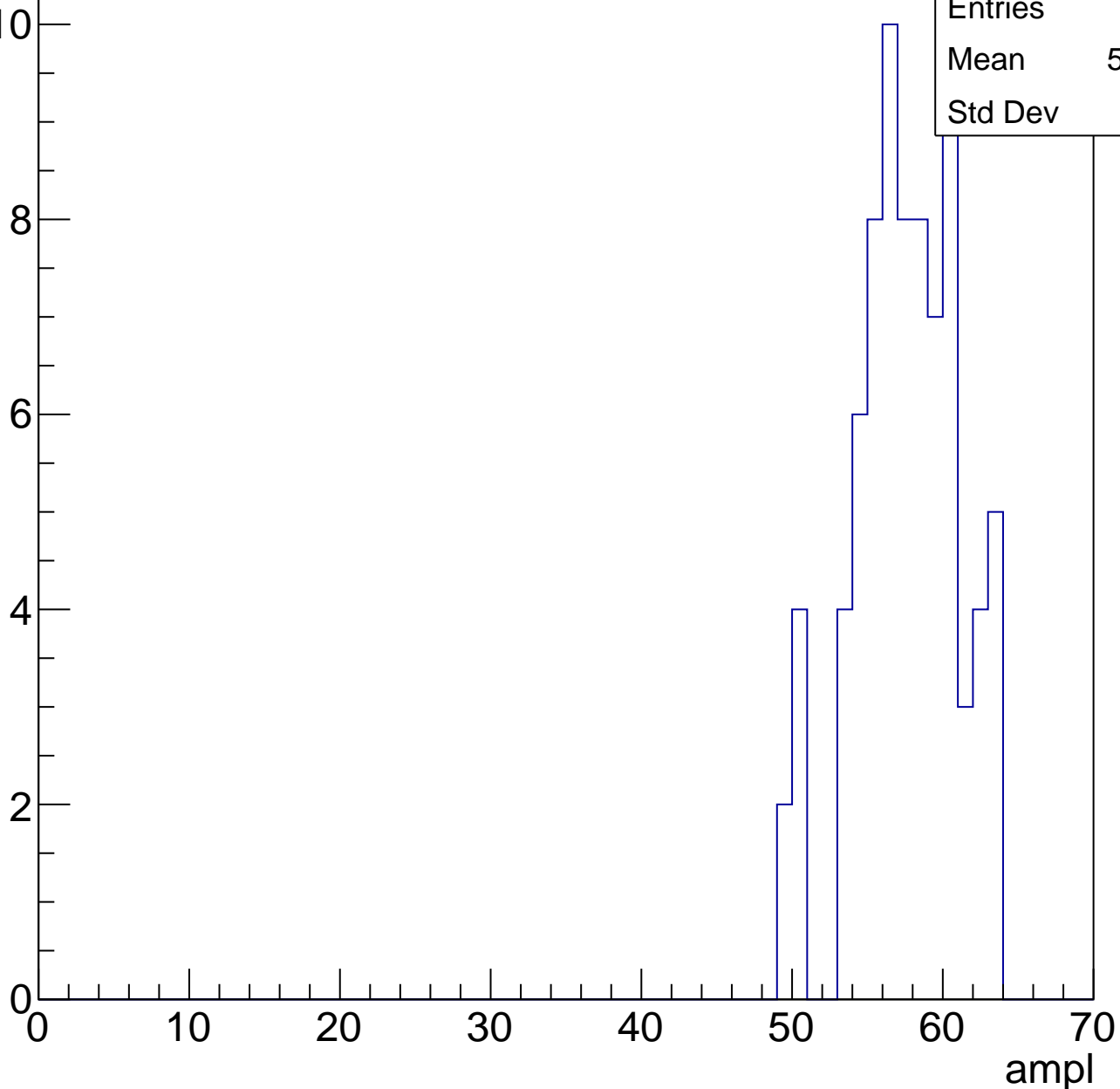


# B1L000S, U23-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	57.09
Std Dev	3.45

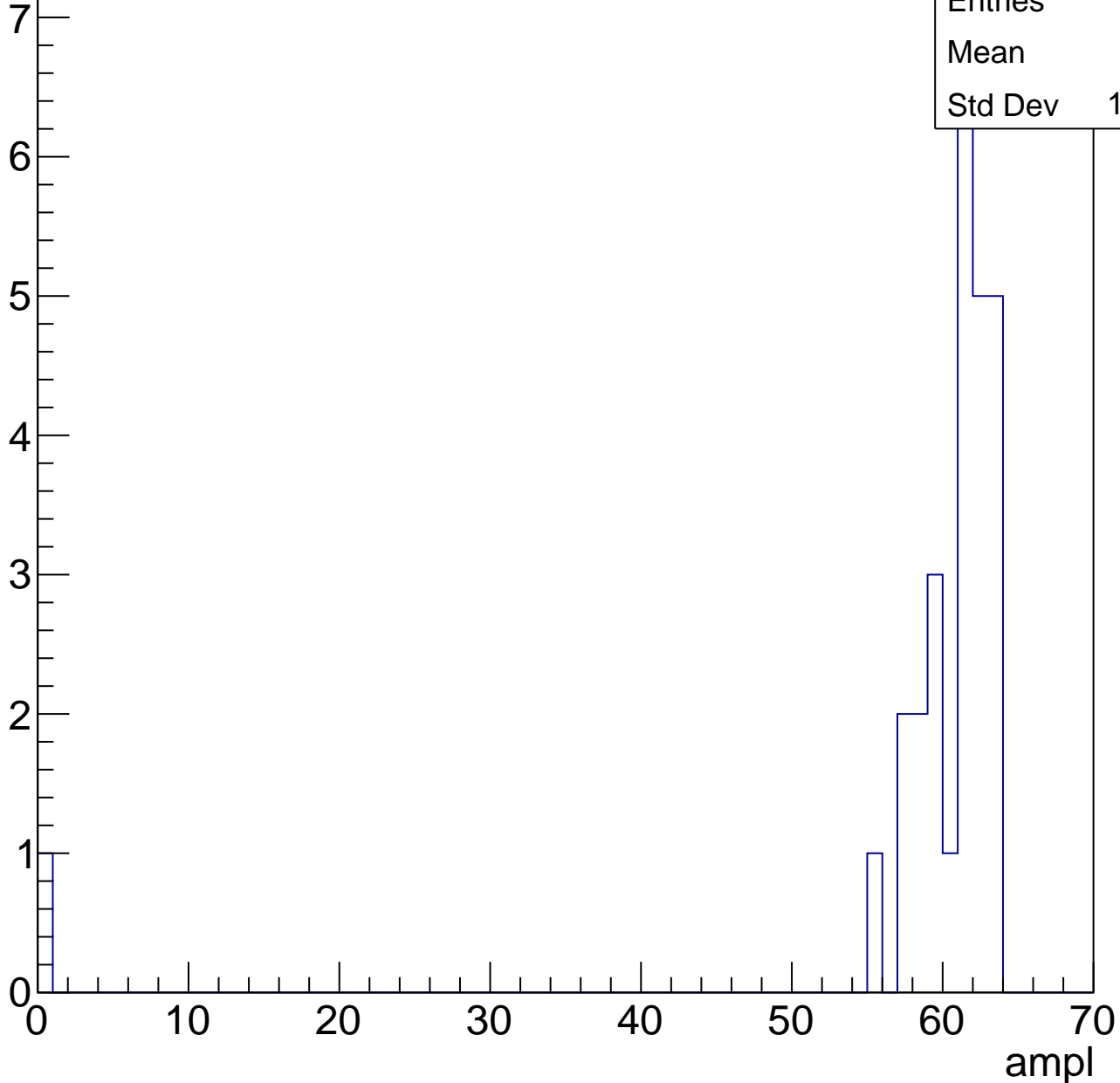


# B1L000S, U23-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.3
Std Dev	11.62



# B1L000S, U23-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	29
Std Dev	4.394

**Gaus mean : 29.8383**

**Gaus Width: 3.2588**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

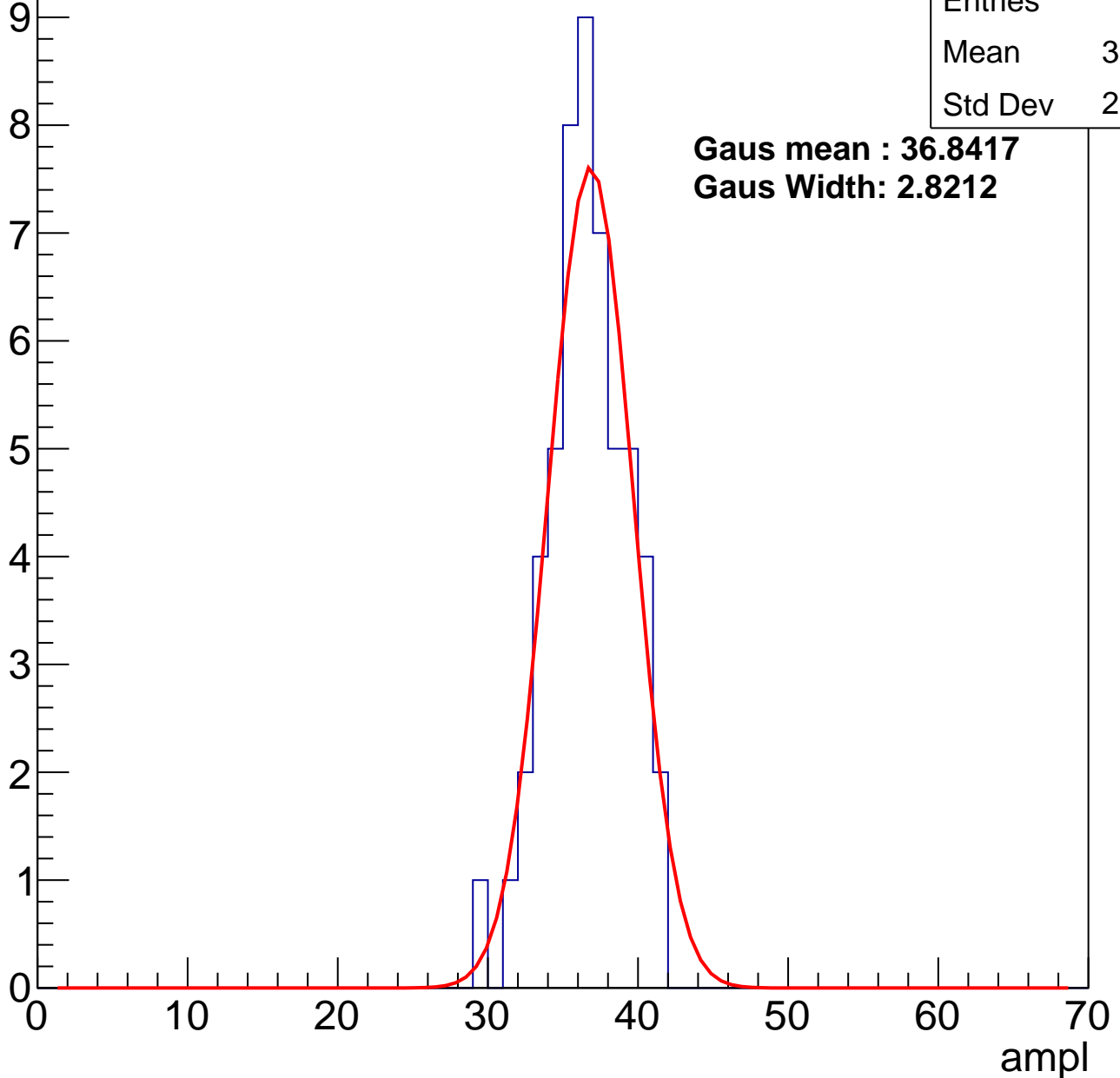
# B1L000S, U23-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	36.15
Std Dev	2.587

**Gaus mean : 36.8417**  
**Gaus Width: 2.8212**



# B1L000S, U23-ch36, adc2

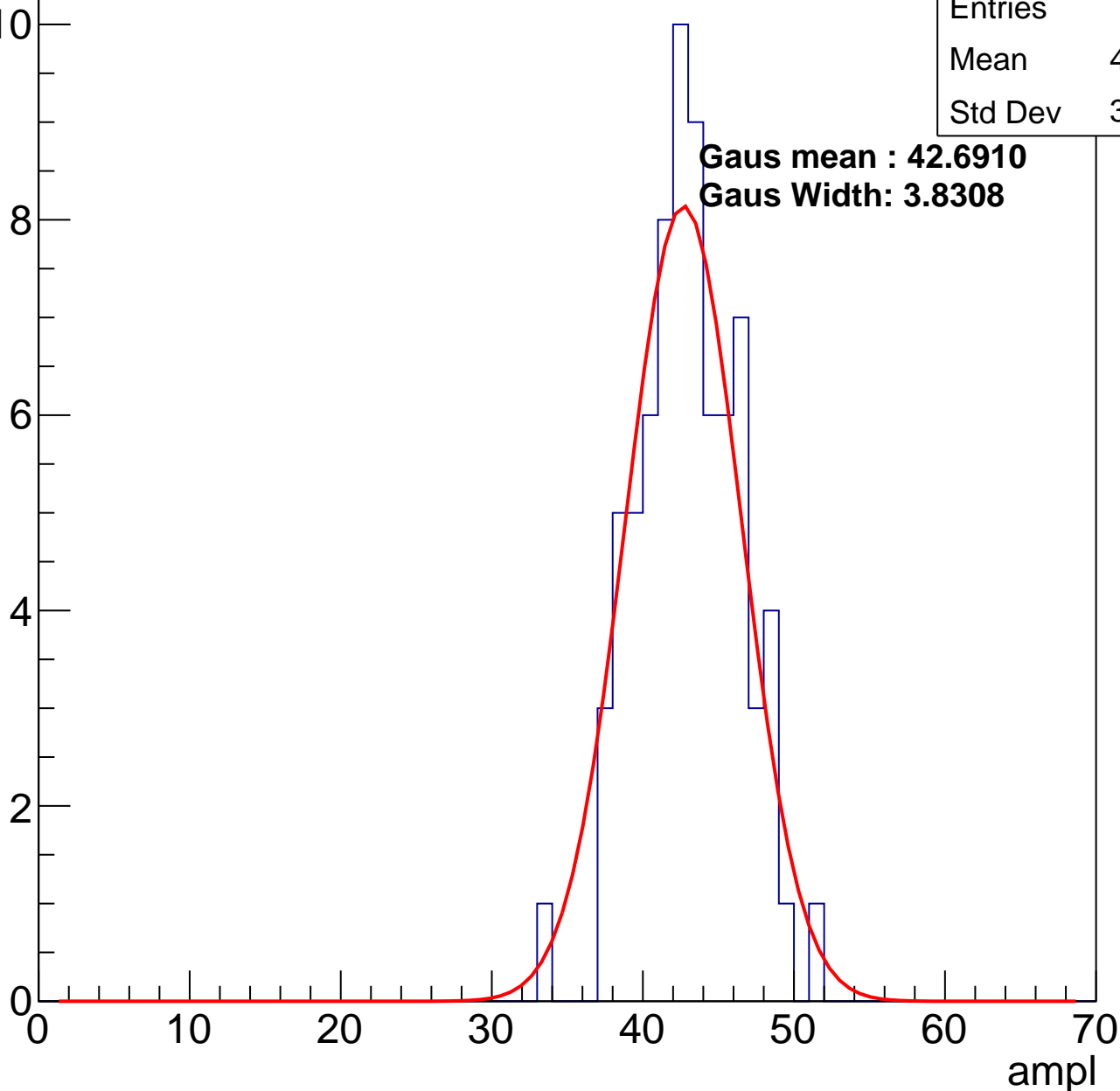
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.57
Std Dev	3.339

**Gaus mean : 42.6910**

**Gaus Width: 3.8308**

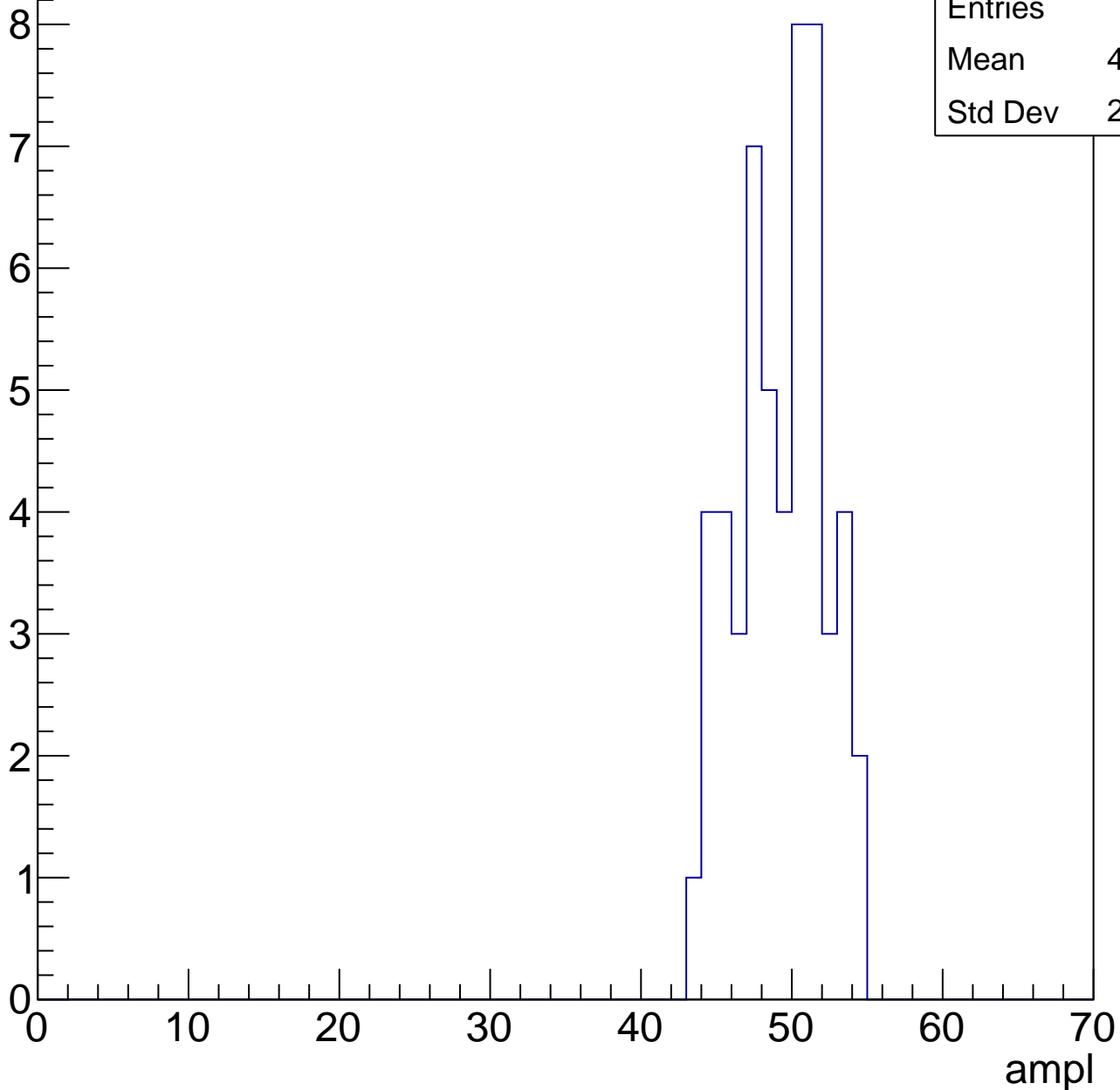


# B1L000S, U23-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

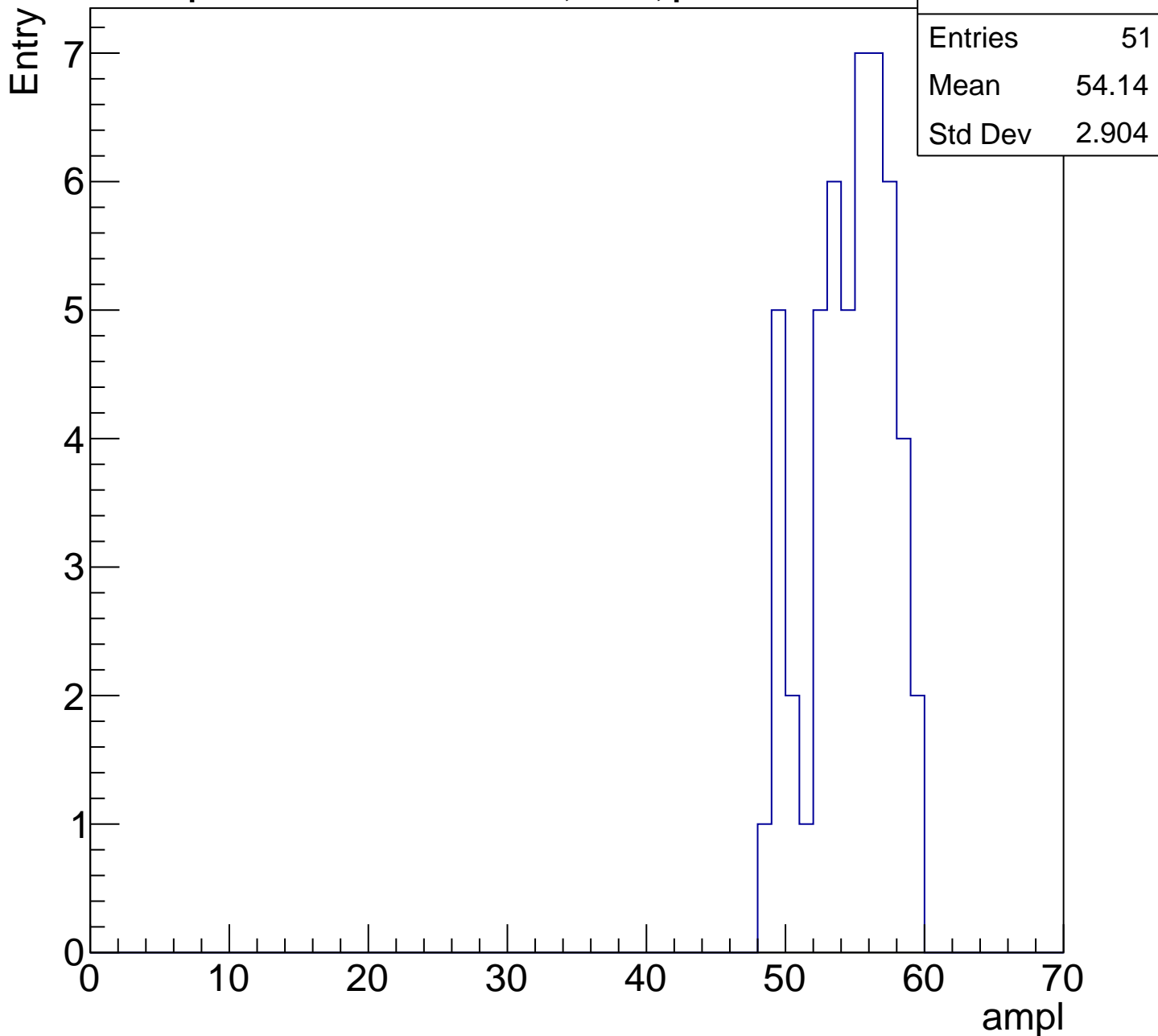
Entry

Entries	53
Mean	48.79
Std Dev	2.877



# B1L000S, U23-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

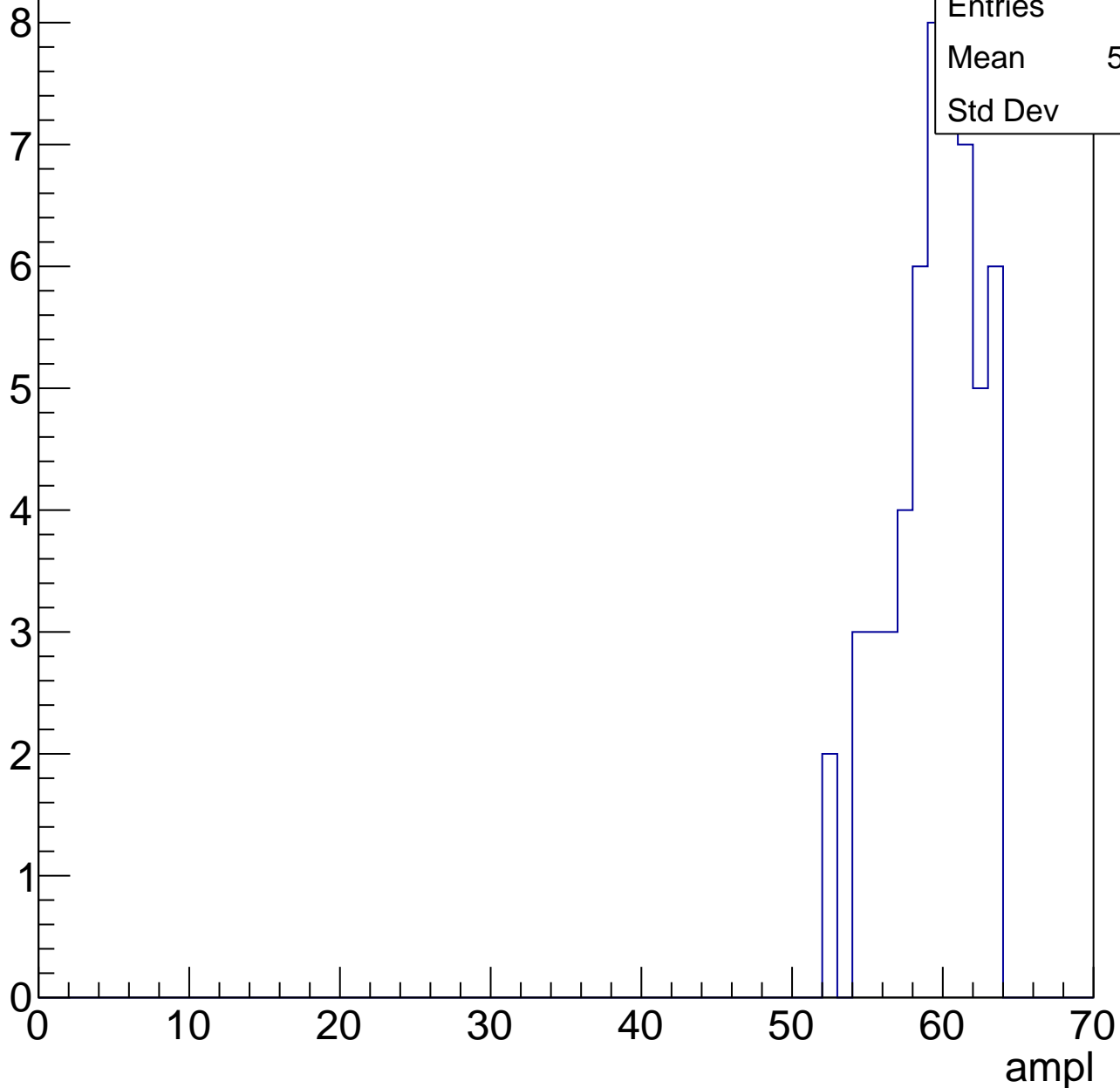


# B1L000S, U23-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.95
Std Dev	2.85

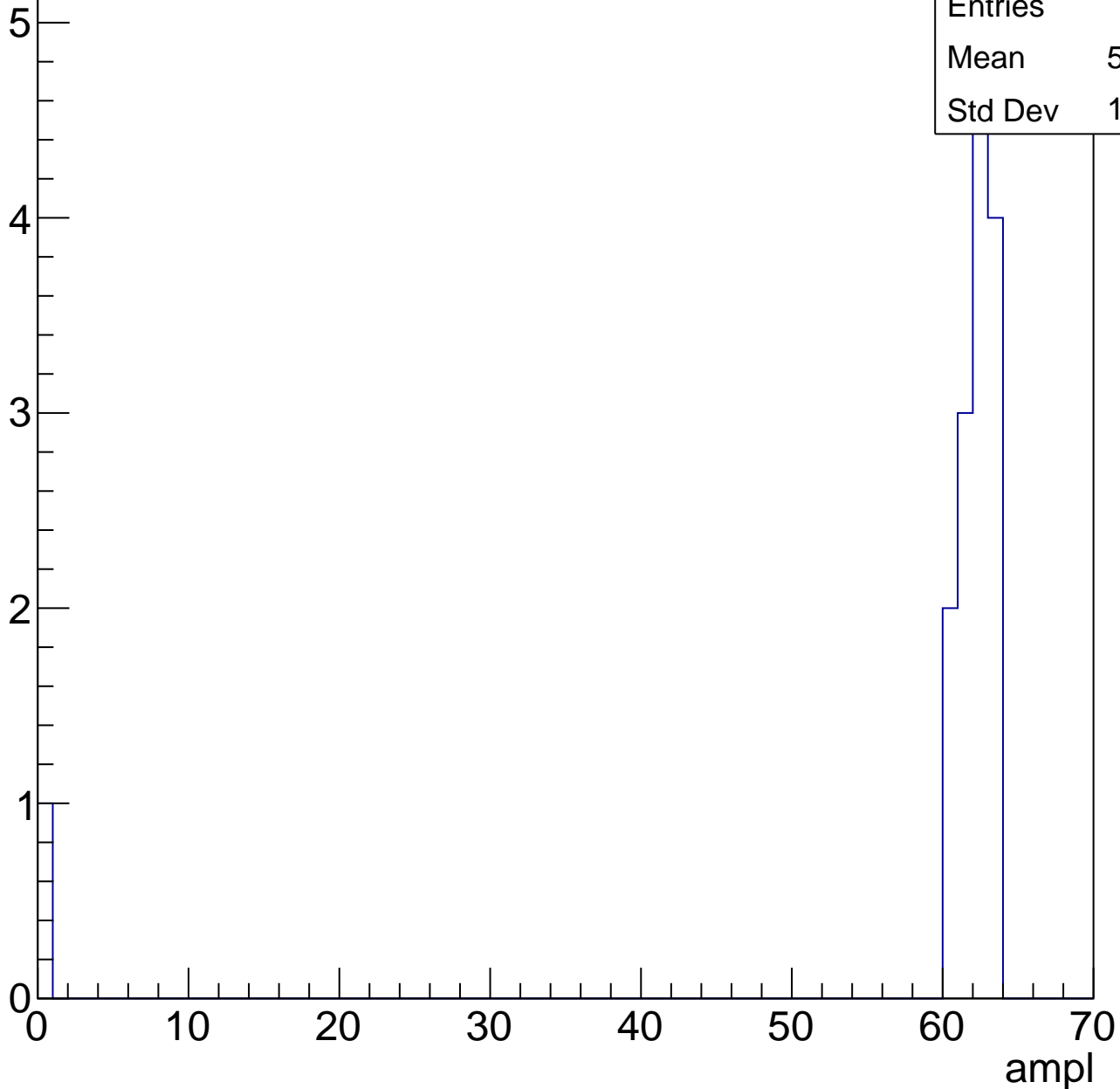


# B1L000S, U23-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.67
Std Dev	15.44





# B1L000S, U23-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U23-ch37, adc0

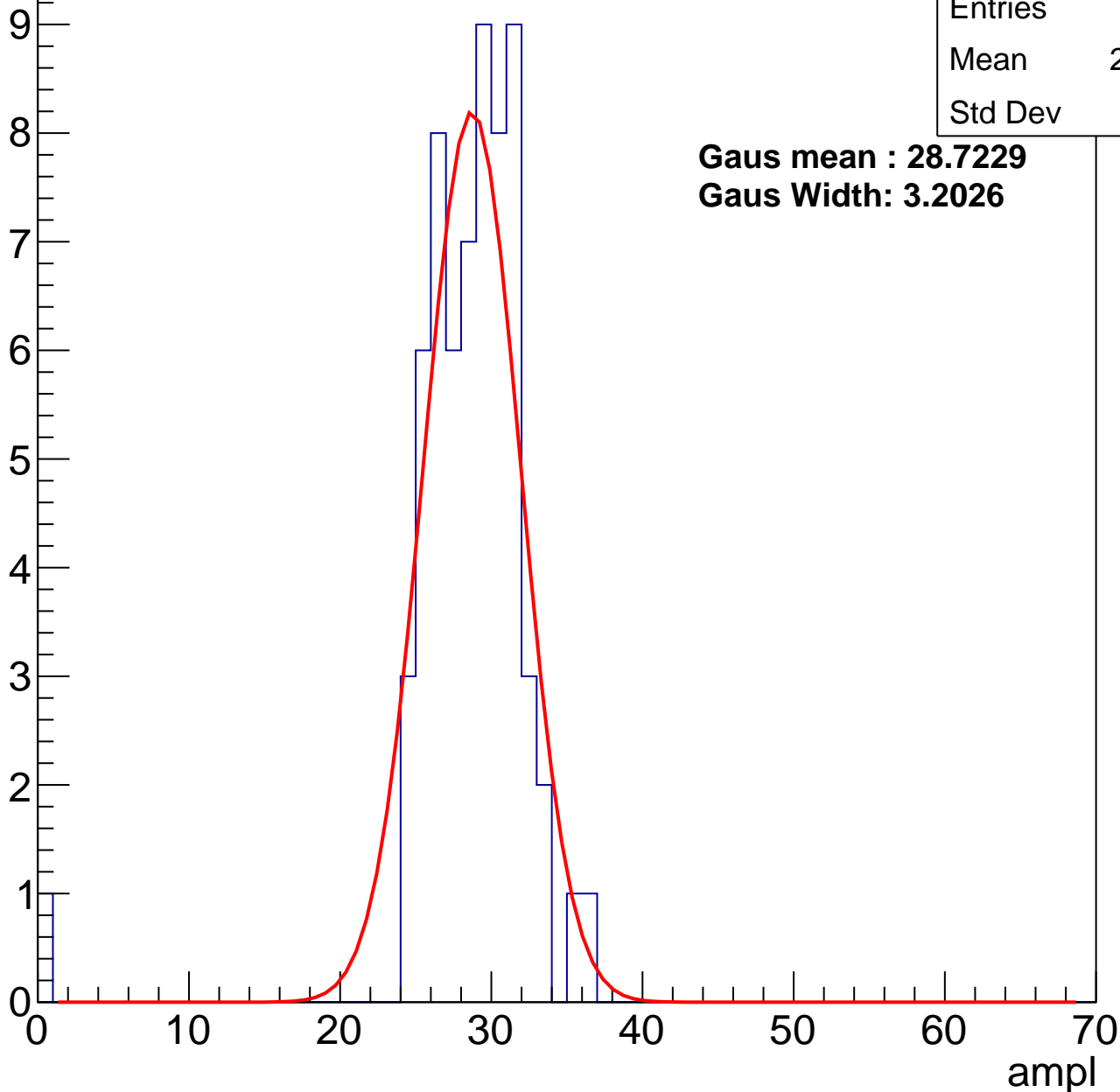
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.14
Std Dev	4.43

**Gaus mean : 28.7229**

**Gaus Width: 3.2026**



# B1L000S, U23-ch37, adc1

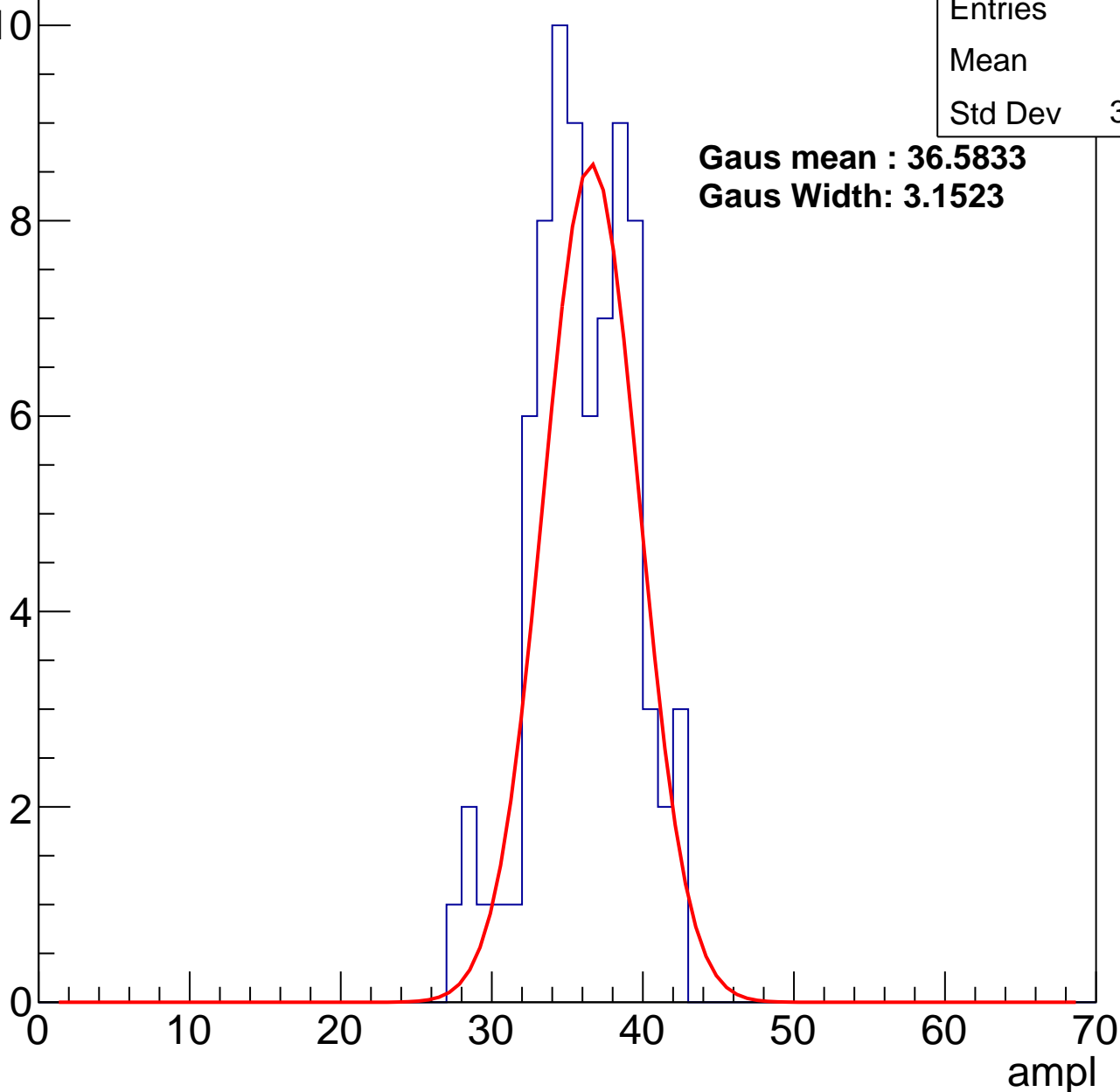
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	35.6
Std Dev	3.312

**Gaus mean : 36.5833**

**Gaus Width: 3.1523**



# B1L000S, U23-ch37, adc2

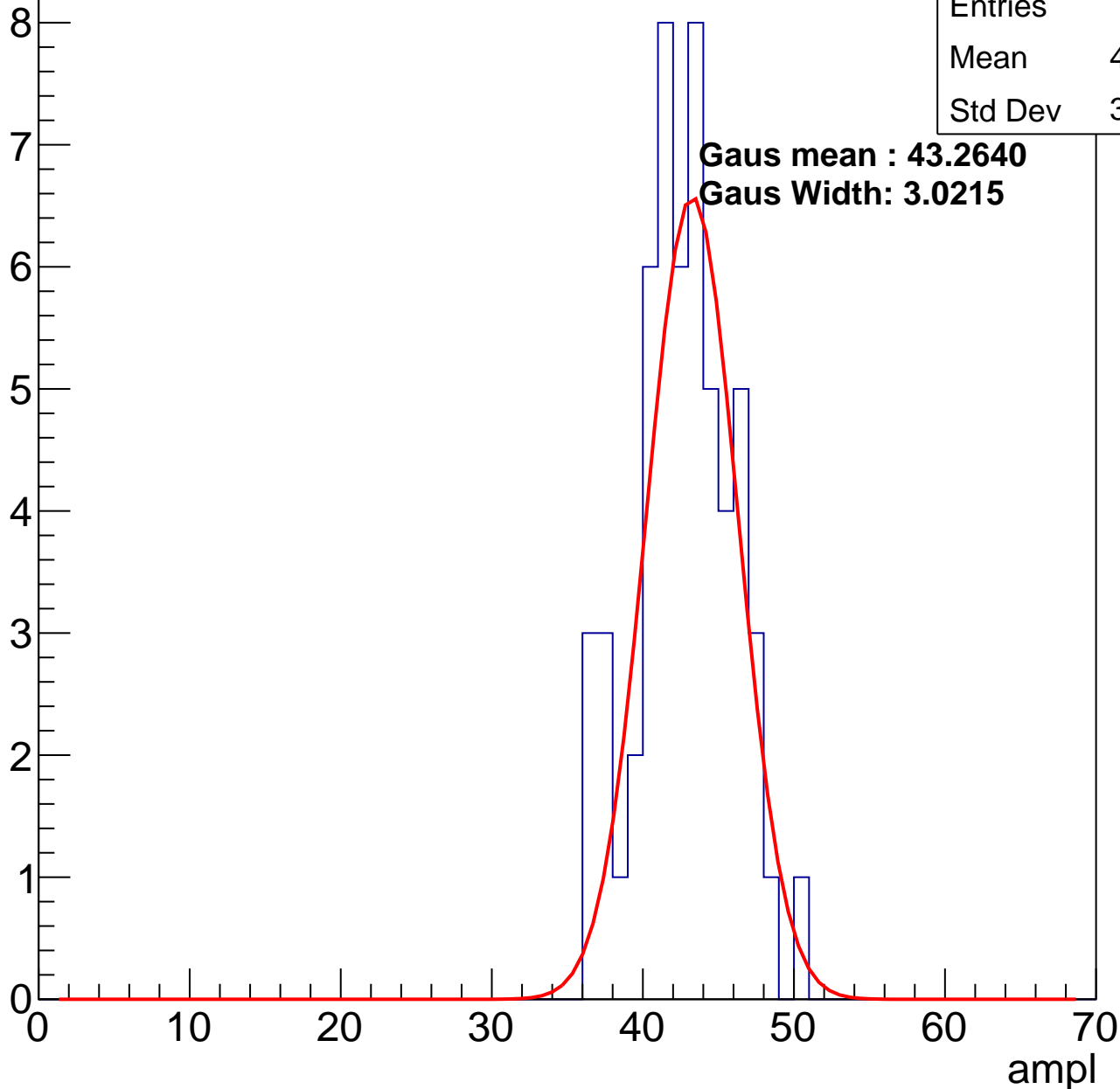
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.29
Std Dev	3.172

**Gaus mean : 43.2640**

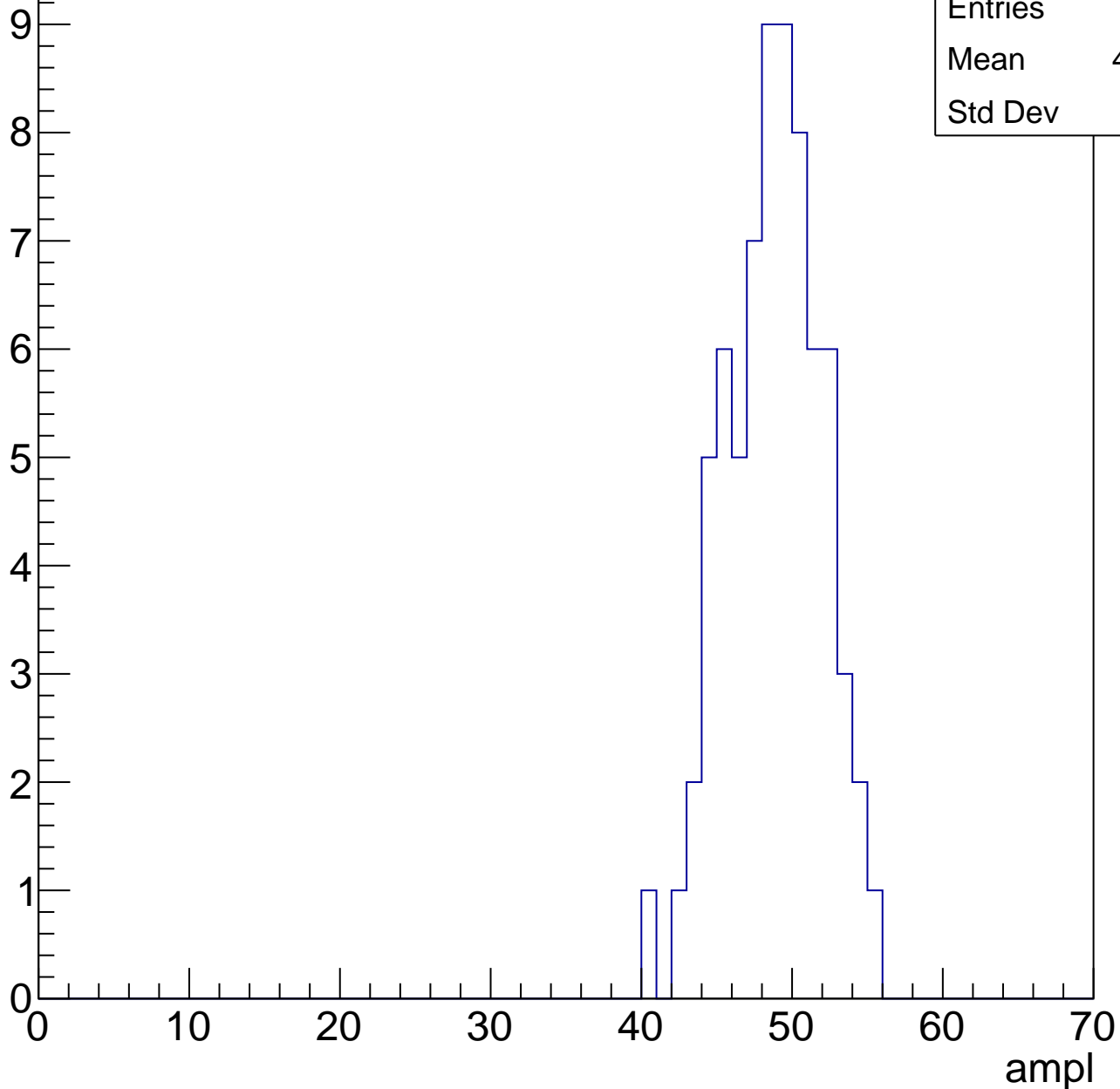
**Gaus Width: 3.0215**



# B1L000S, U23-ch37, adc3

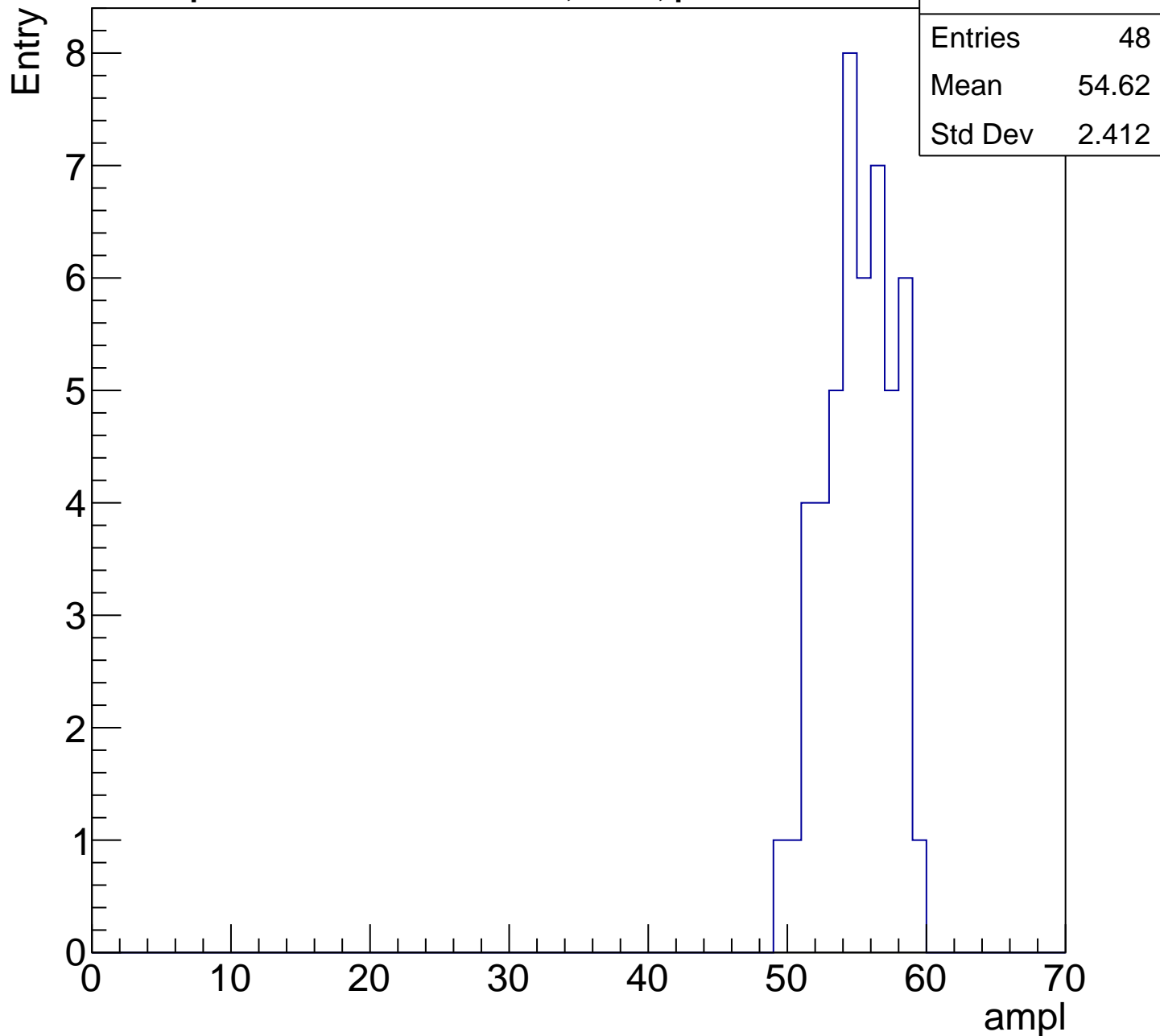
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch37, adc5

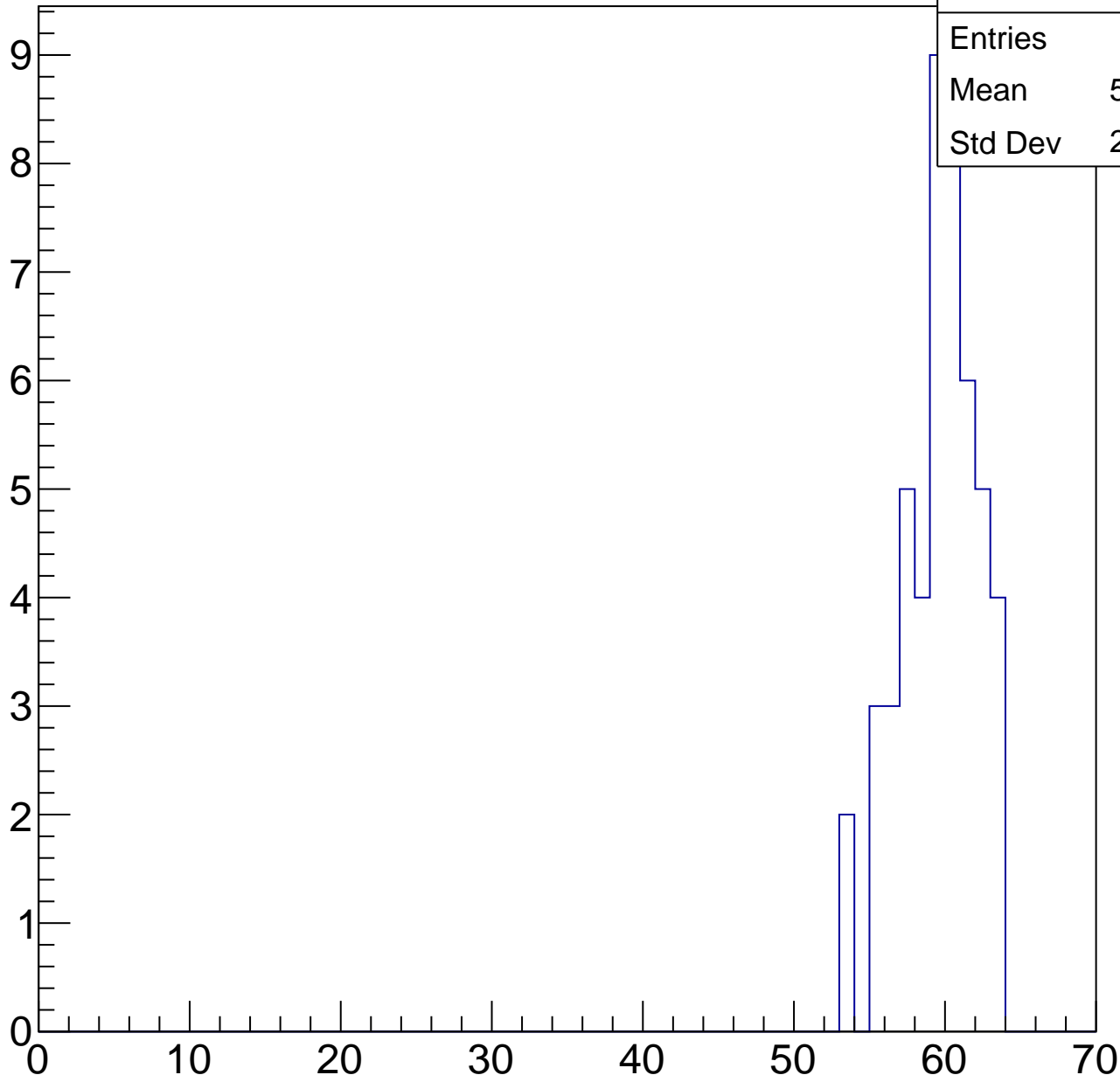
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	59.08
Std Dev	2.522

ampl

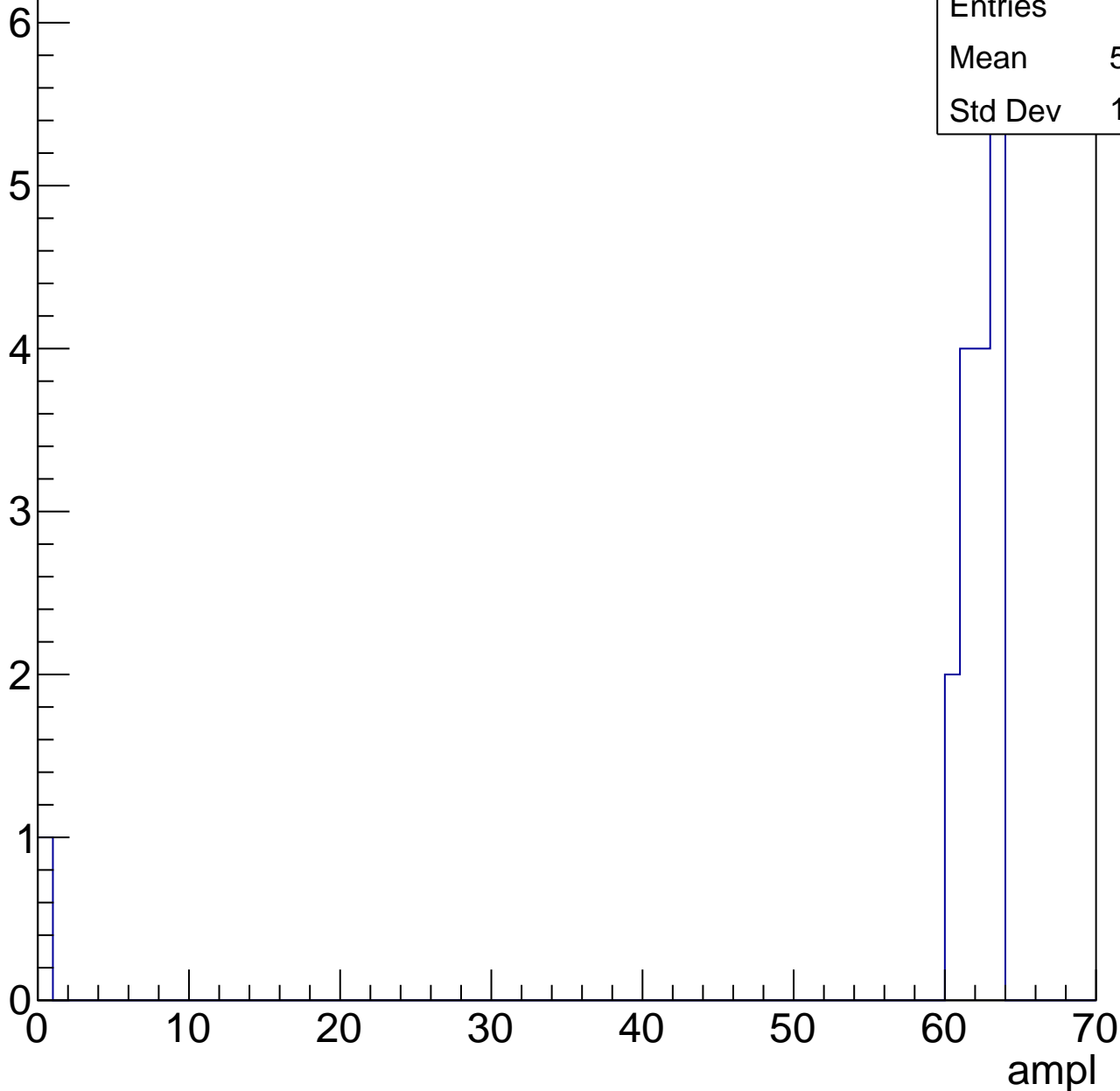


# B1L000S, U23-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.24
Std Dev	14.59

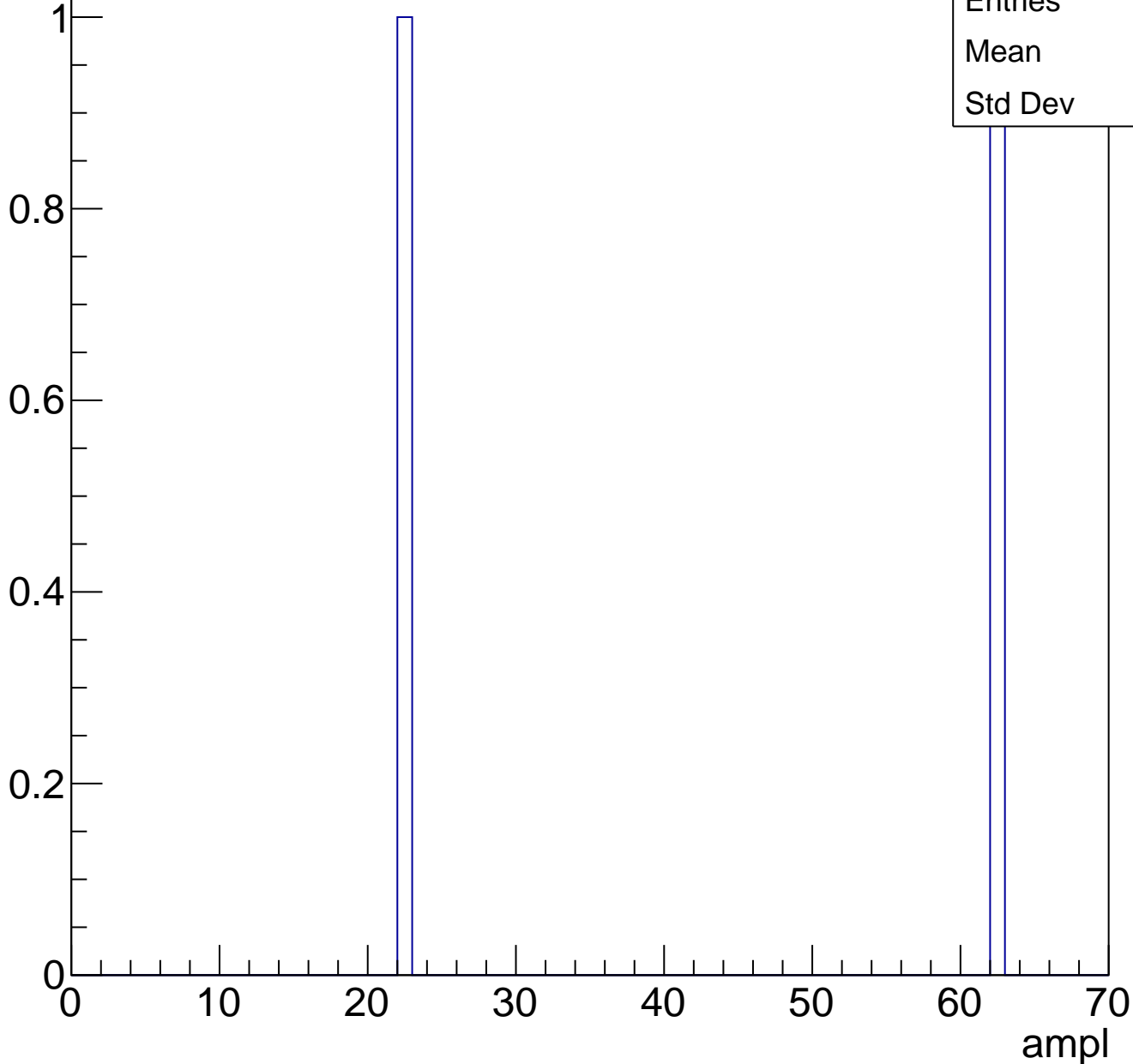




# B1L000S, U23-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch38, adc0

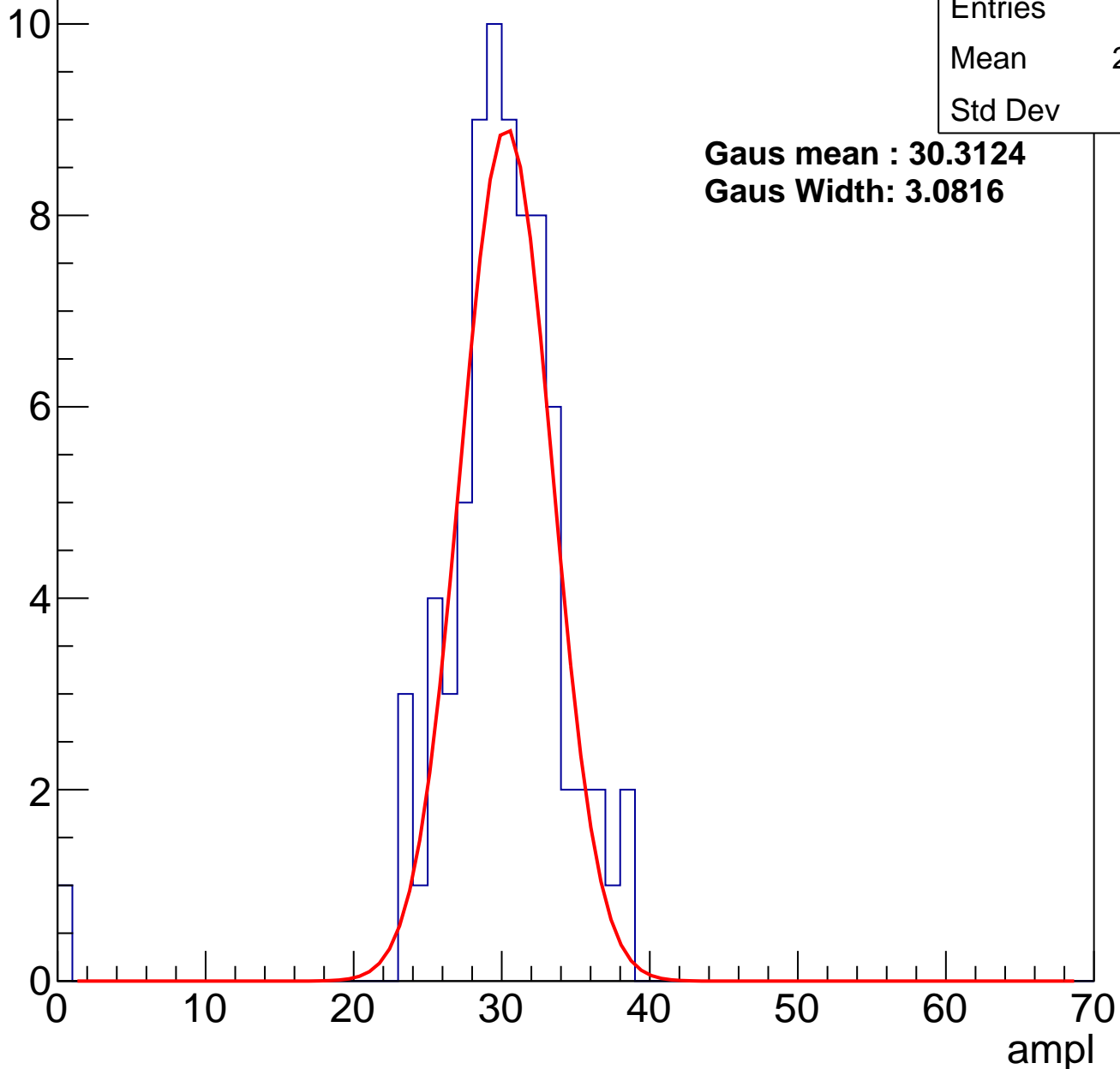
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	29.51
Std Dev	4.77

**Gaus mean : 30.3124**

**Gaus Width: 3.0816**

Entry



# B1L000S, U23-ch38, adc1

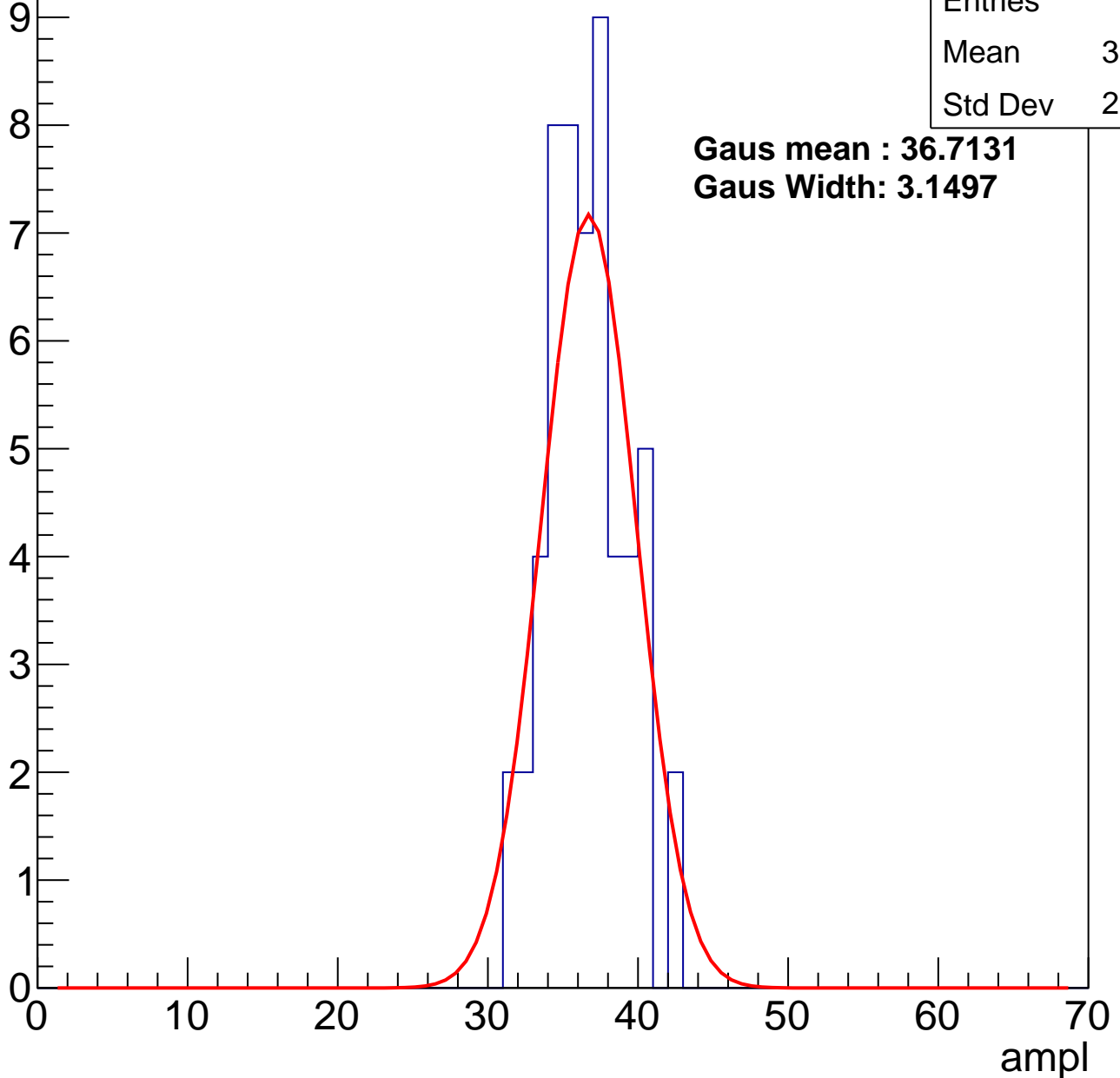
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	36.13
Std Dev	2.594

**Gaus mean : 36.7131**

**Gaus Width: 3.1497**



# B1L000S, U23-ch38, adc2

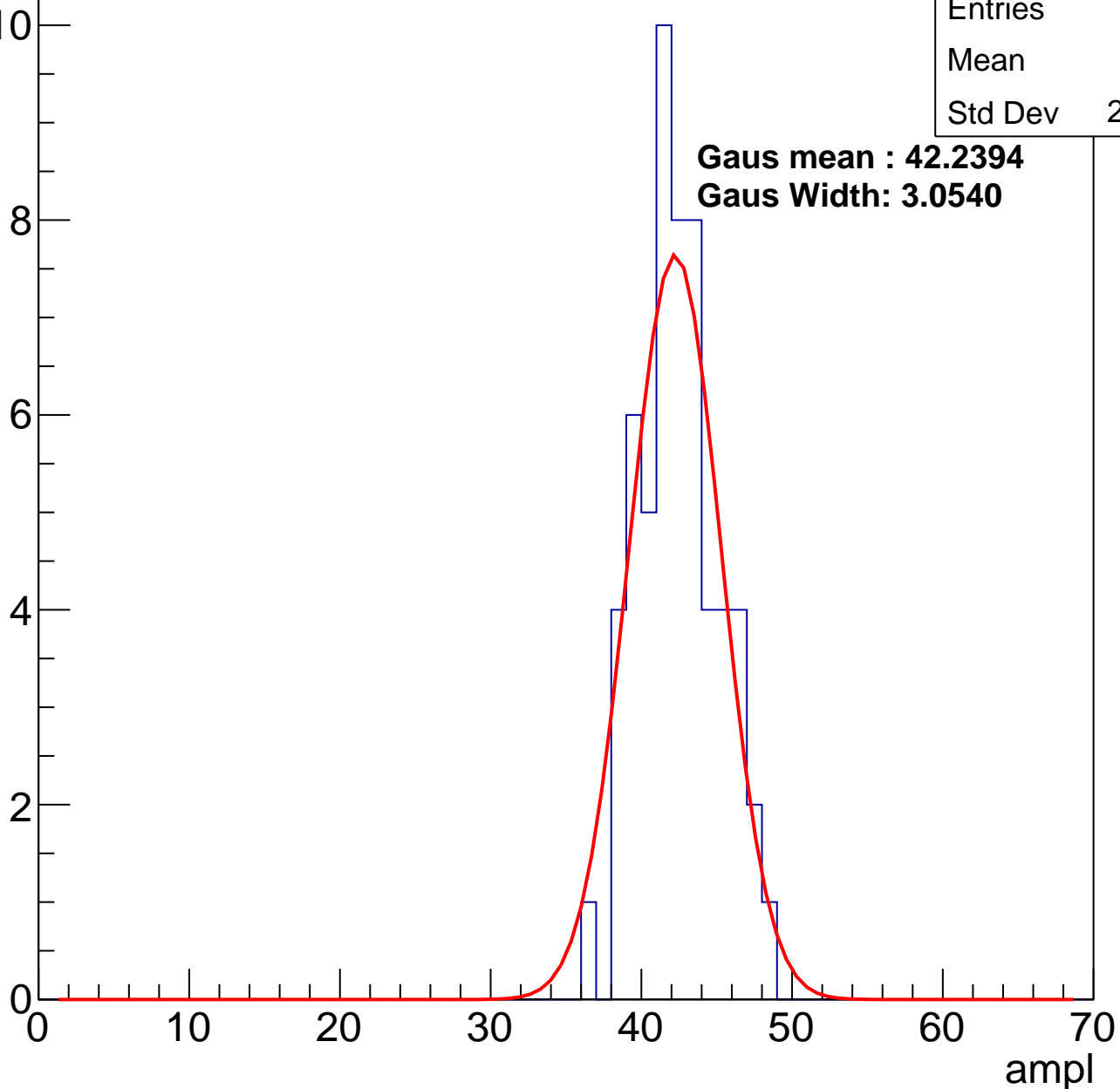
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42
Std Dev	2.629

**Gaus mean : 42.2394**

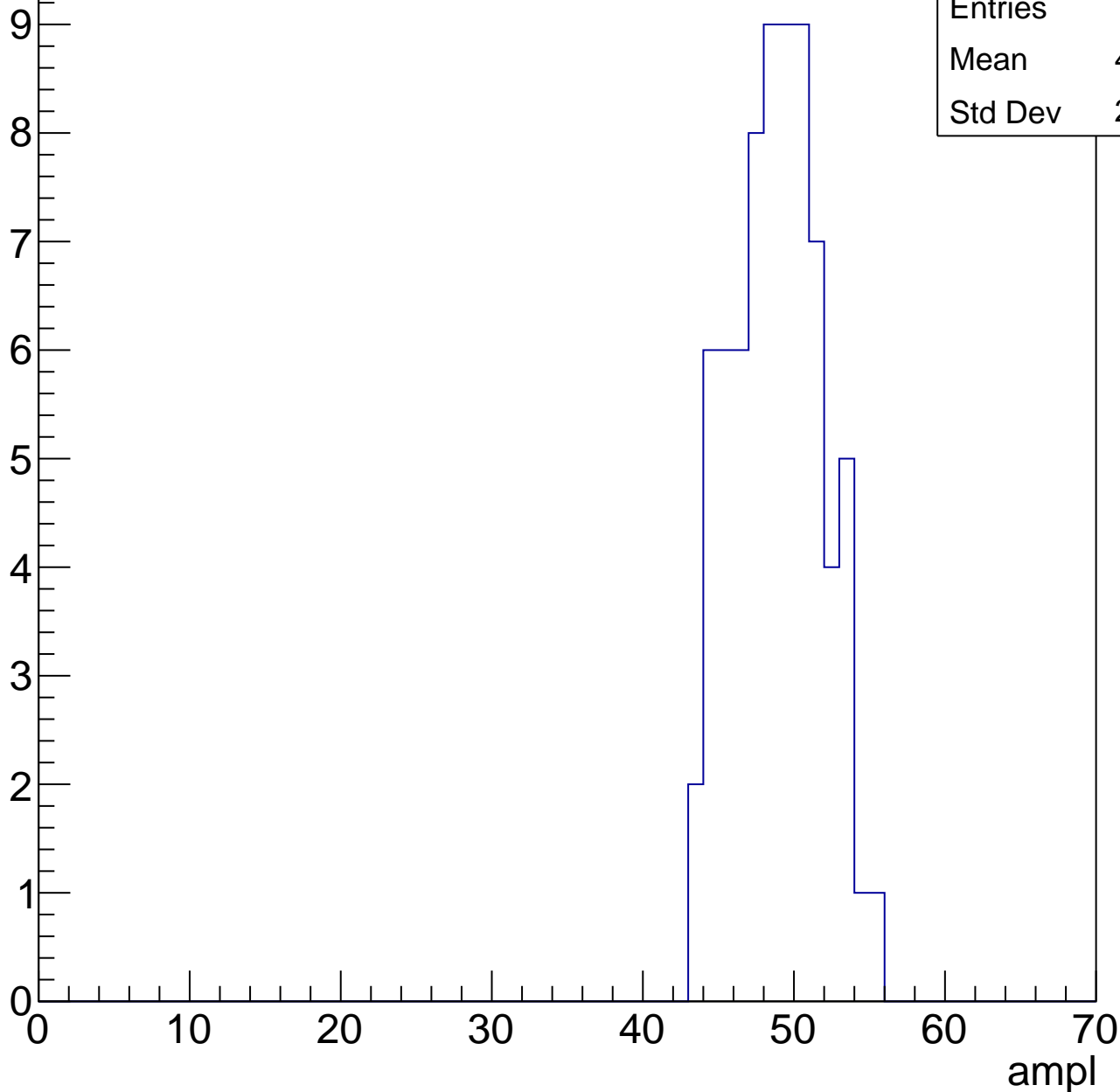
**Gaus Width: 3.0540**



# B1L000S, U23-ch38, adc3

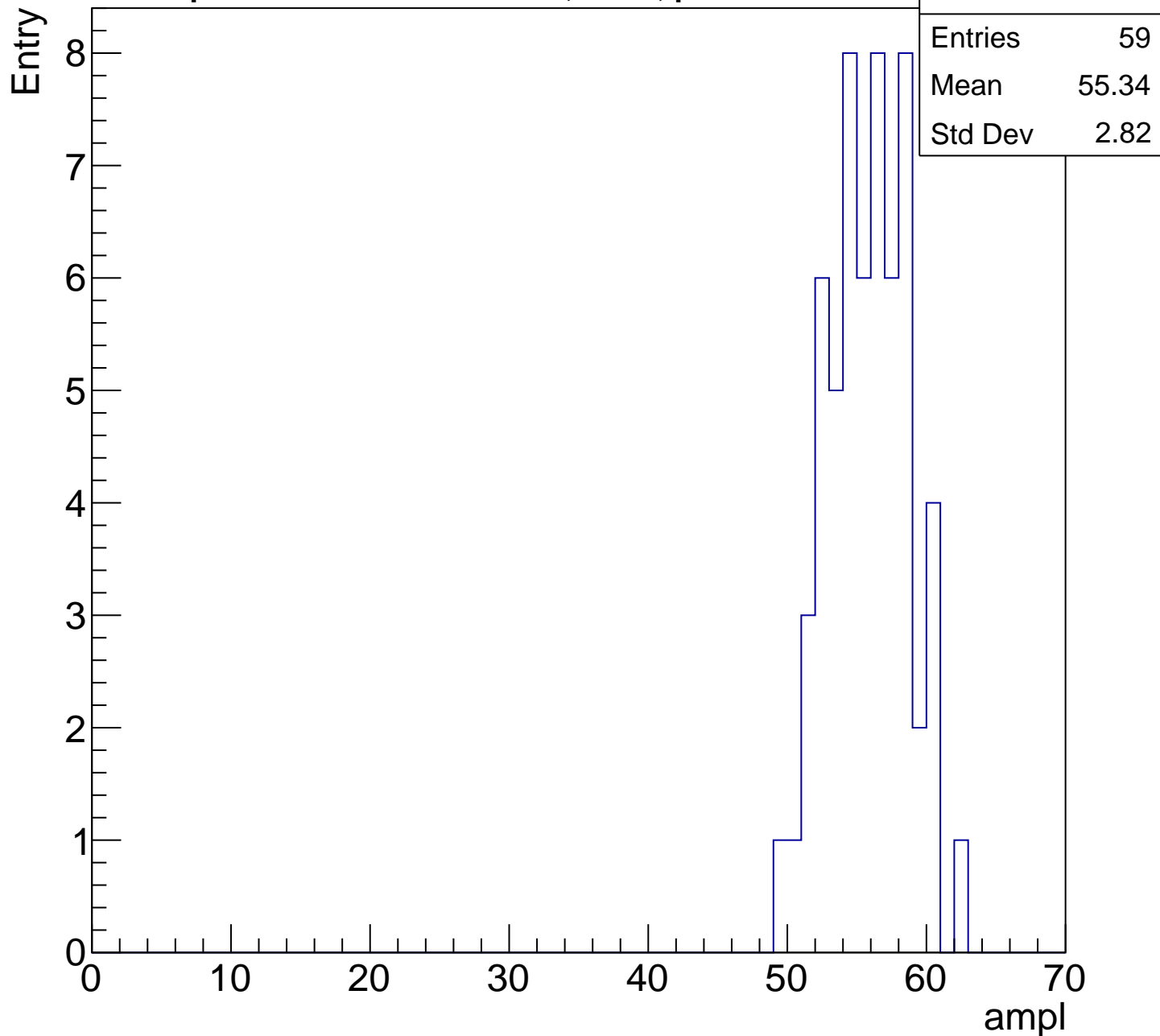
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

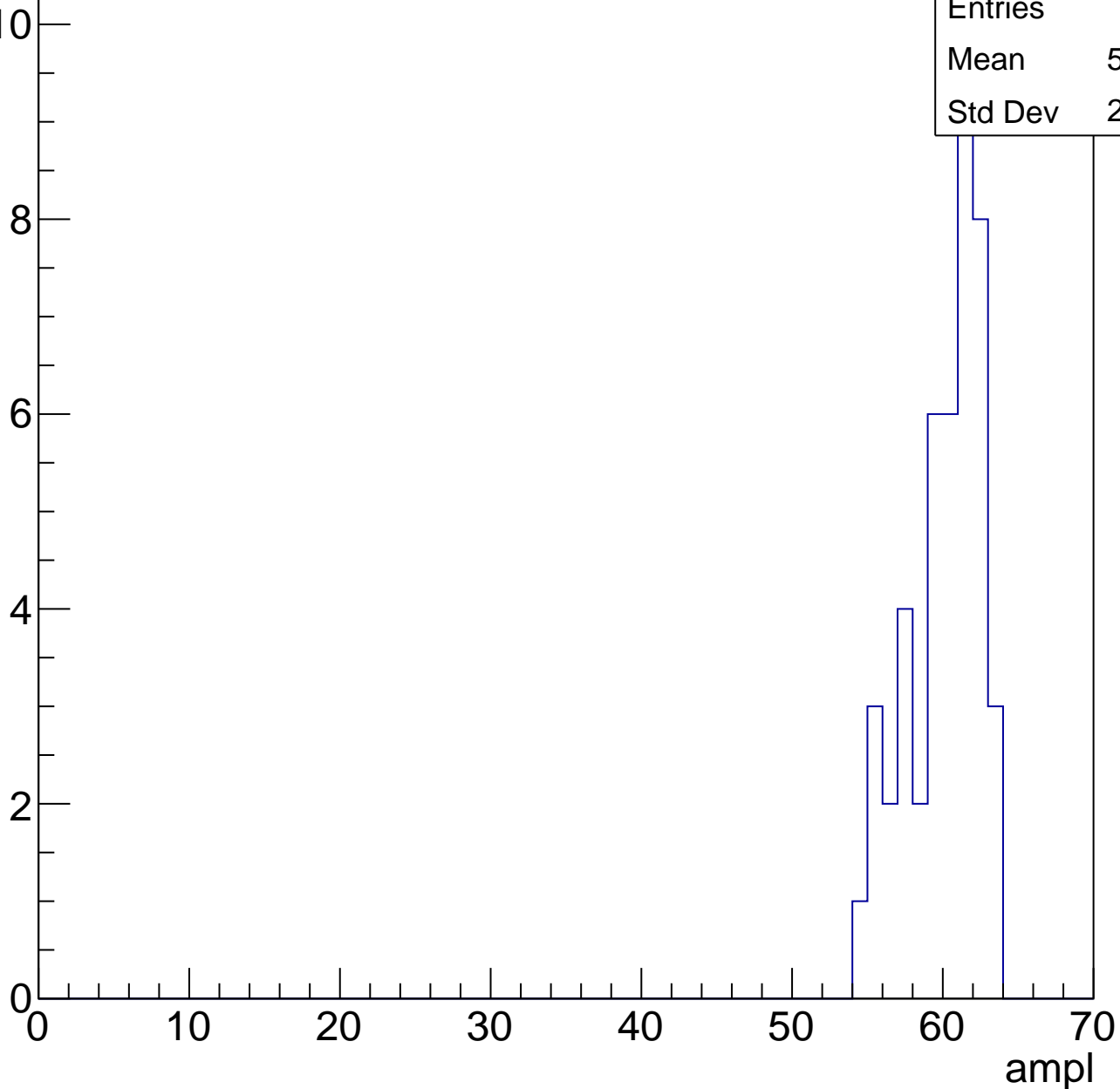


# B1L000S, U23-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	59.64
Std Dev	2.387

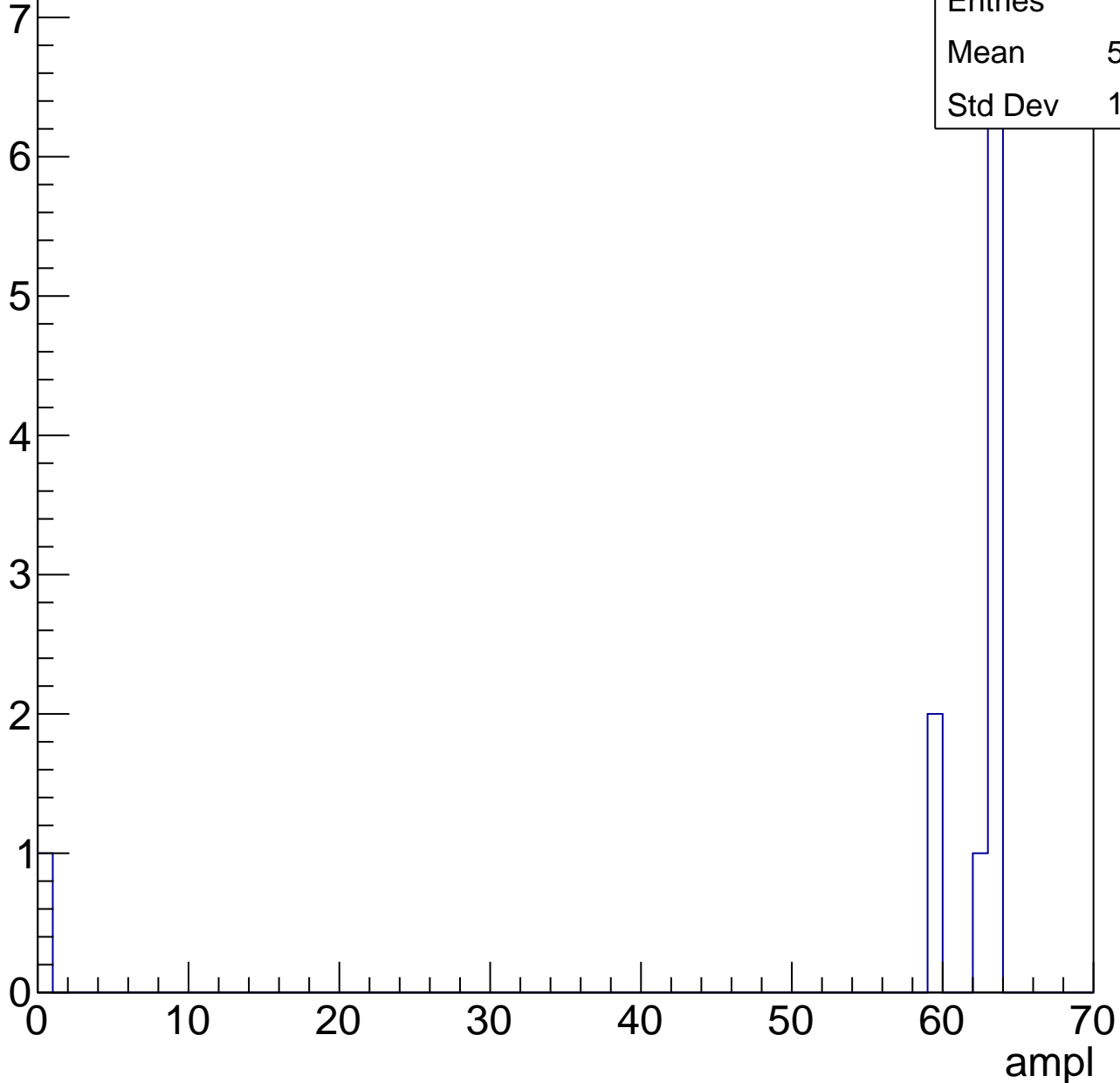


# B1L000S, U23-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	56.45
Std Dev	17.92





# B1L000S, U23-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U23-ch39, adc0

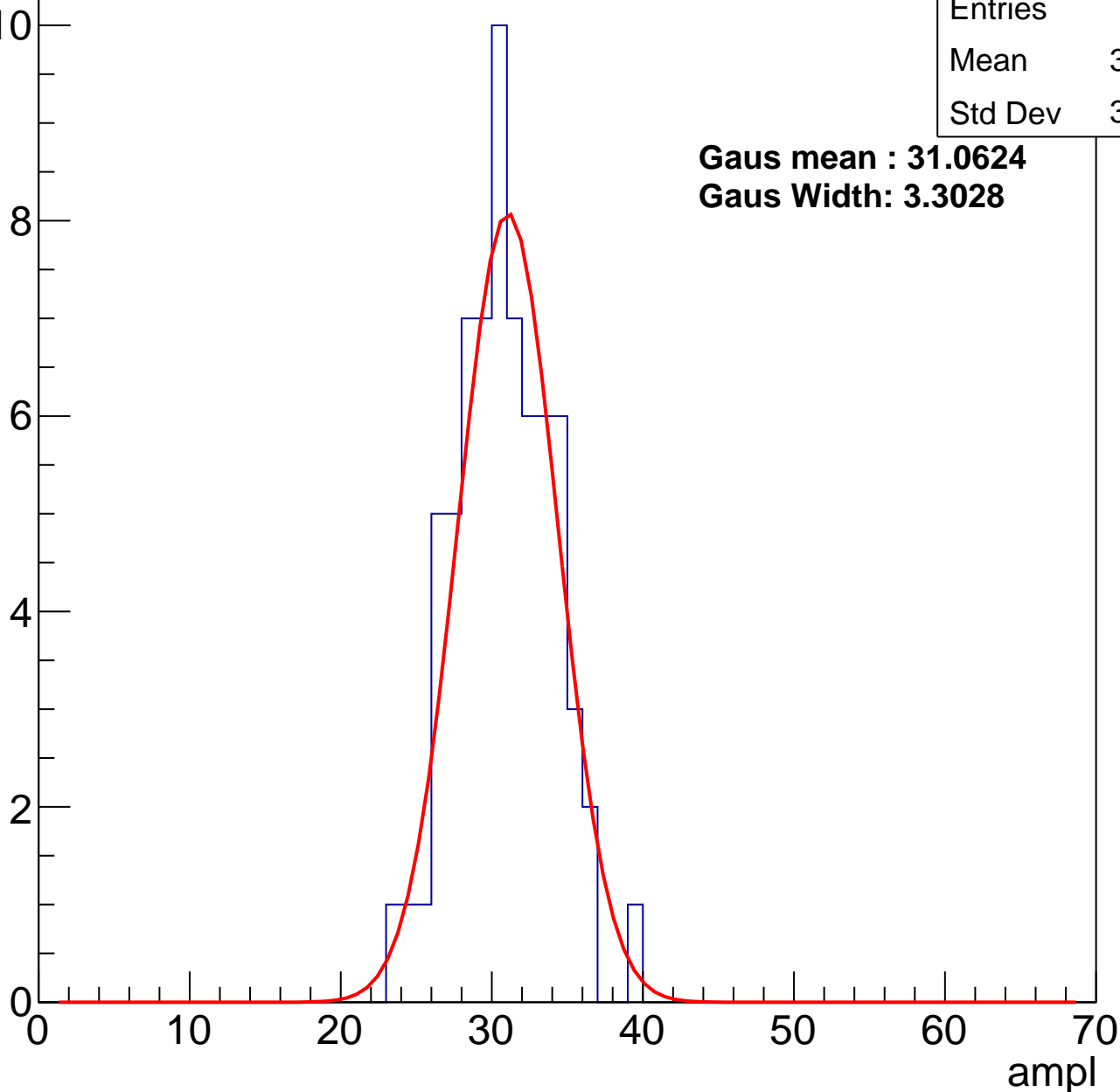
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.34
Std Dev	3.142

**Gaus mean : 31.0624**

**Gaus Width: 3.3028**



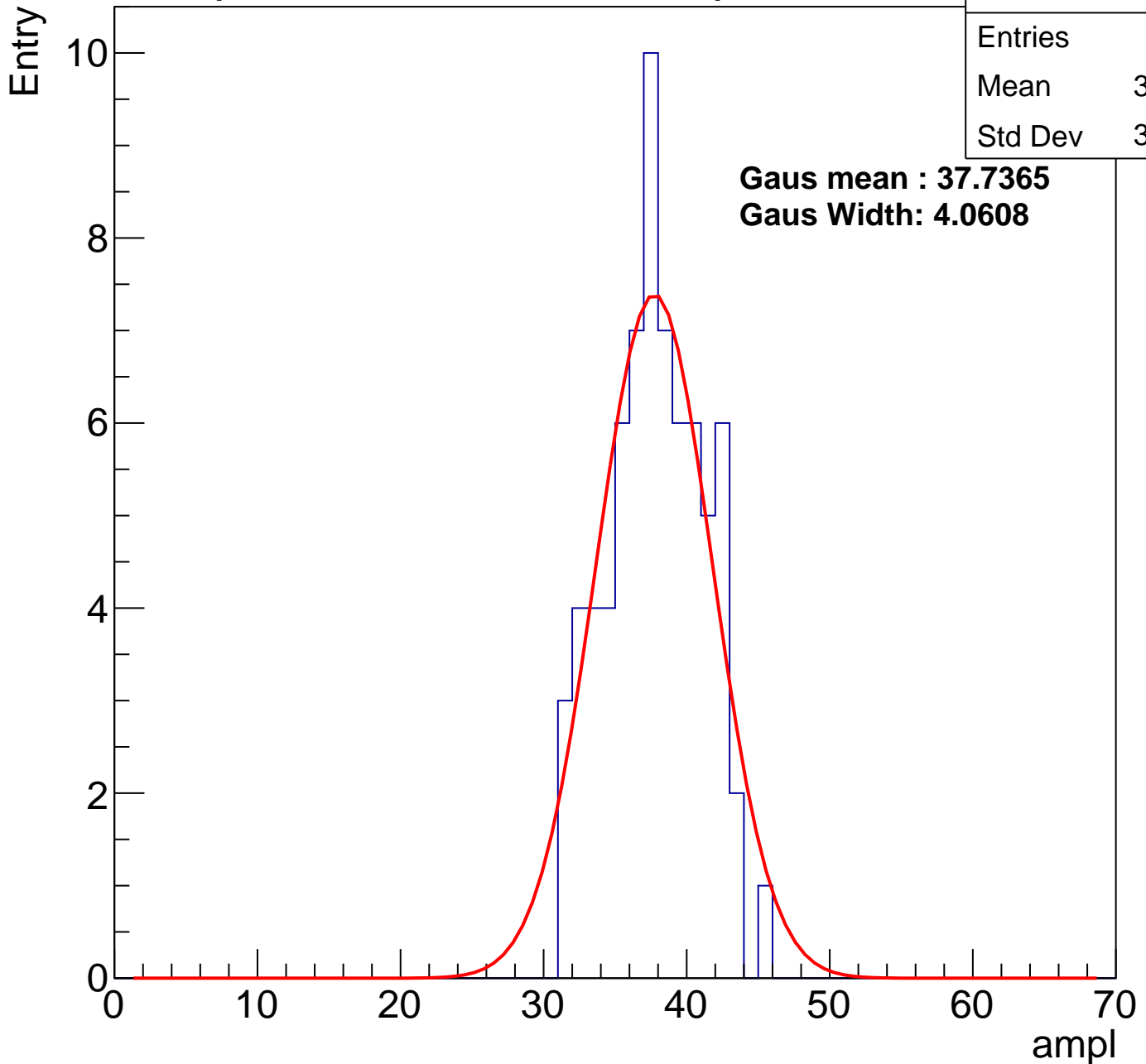
# B1L000S, U23-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	37.31
Std Dev	3.321

**Gaus mean : 37.7365**

**Gaus Width: 4.0608**



# B1L000S, U23-ch39, adc2

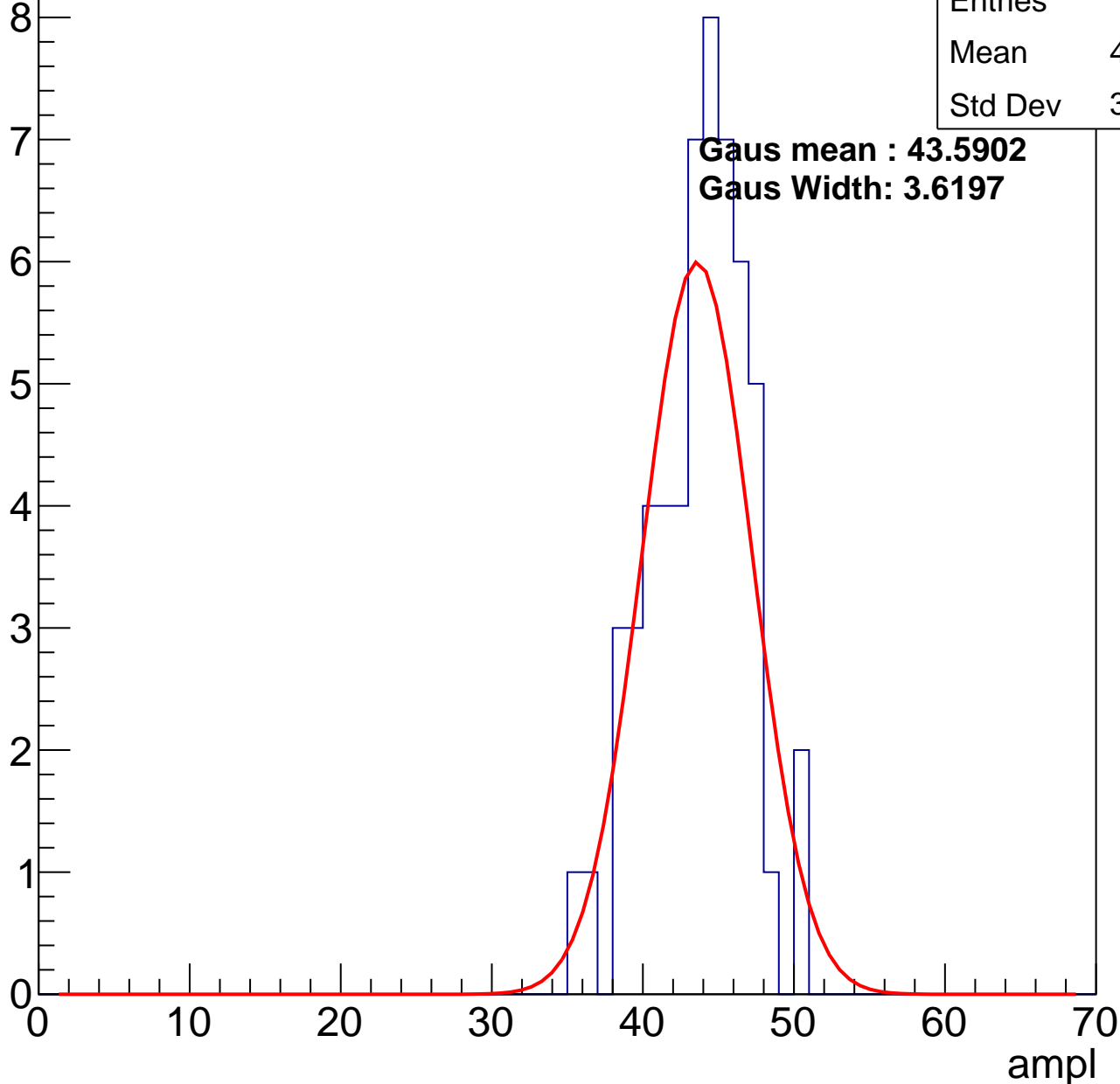
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	43.23
Std Dev	3.224

**Gaus mean : 43.5902**

**Gaus Width: 3.6197**

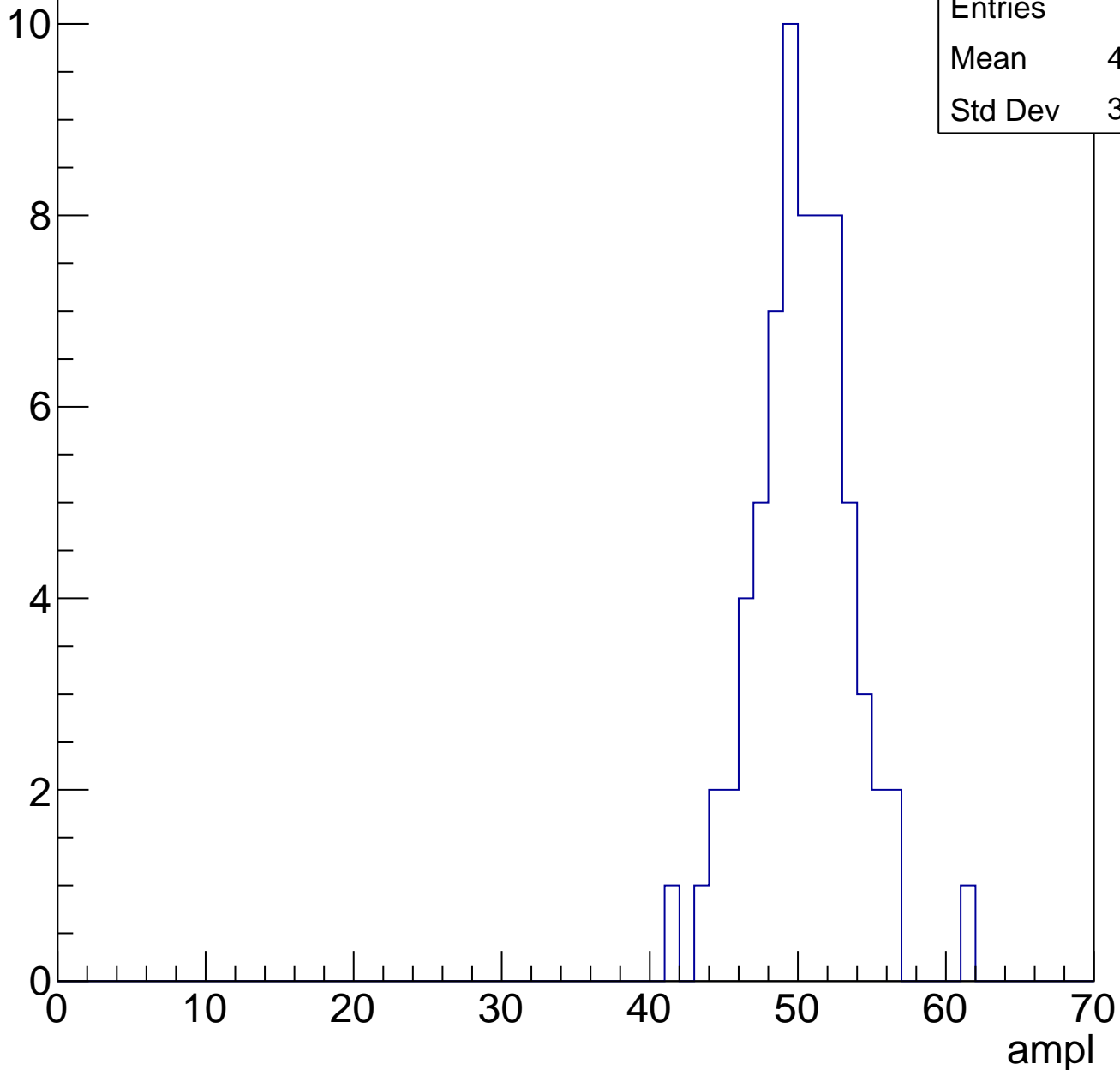


# B1L000S, U23-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

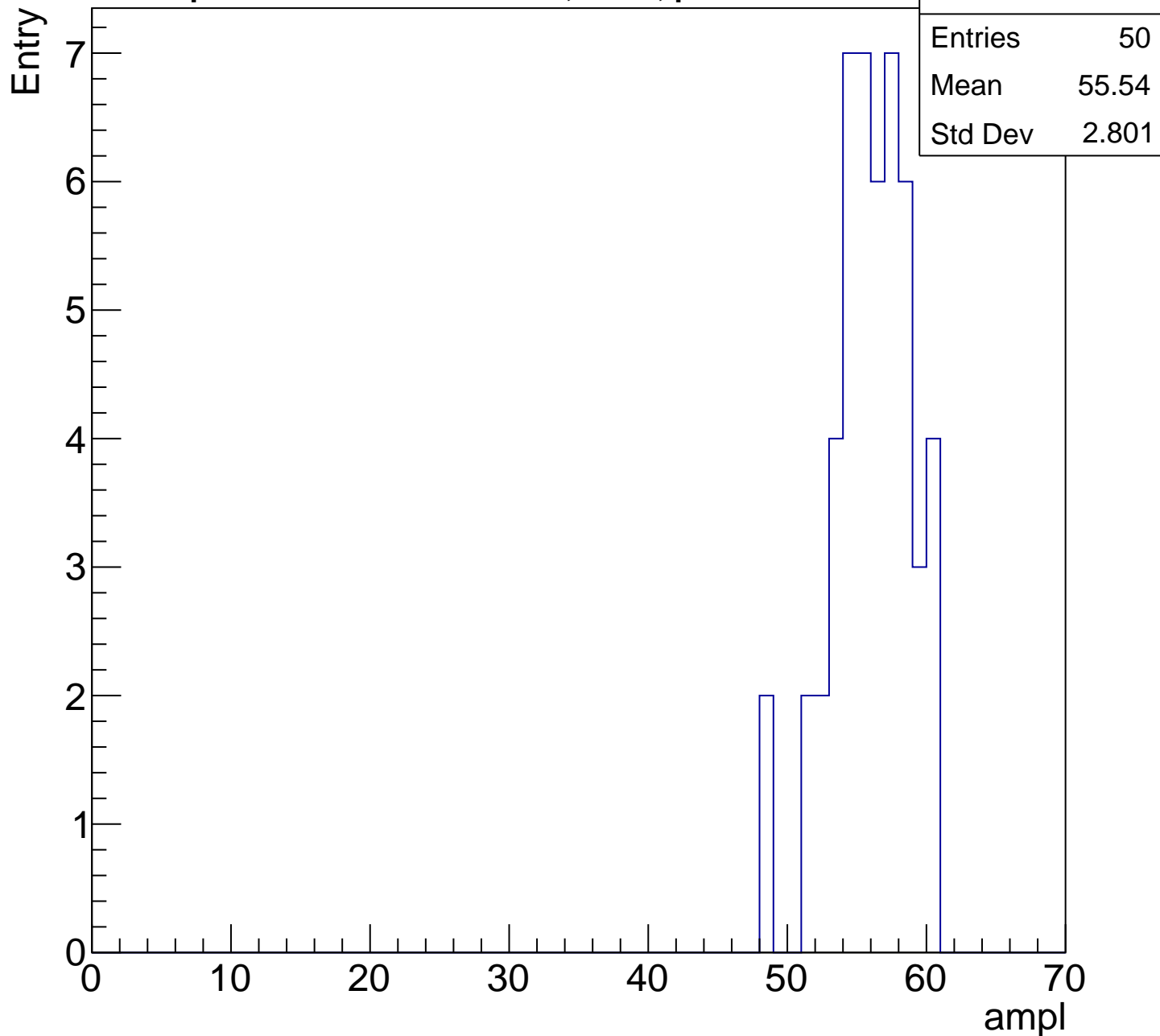
Entries	69
Mean	49.87
Std Dev	3.349

Entry



# B1L000S, U23-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

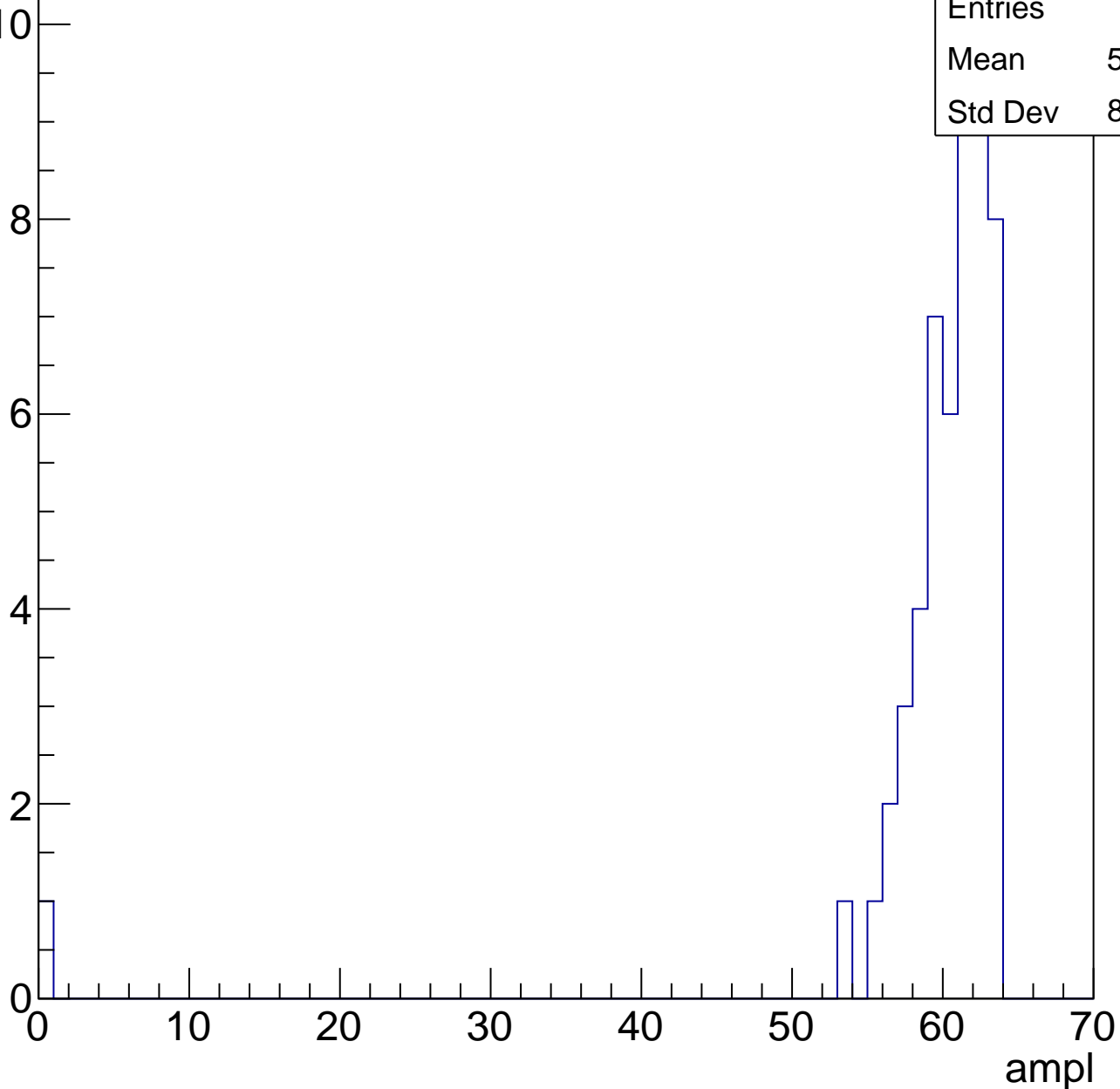


# B1L000S, U23-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	59.02
Std Dev	8.579



# B1L000S, U23-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch40, adc0

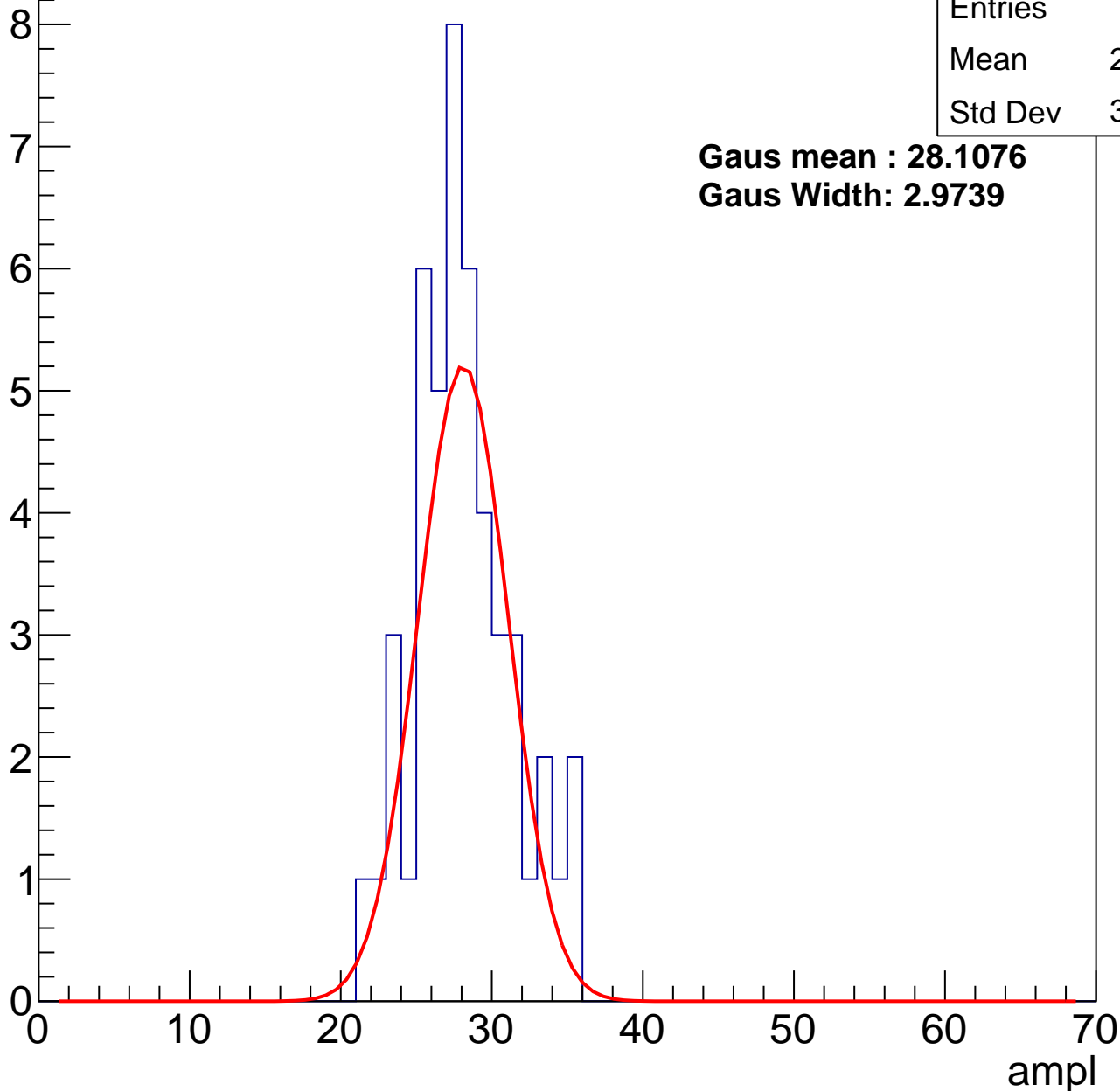
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	27.68
Std Dev	3.249

**Gaus mean : 28.1076**

**Gaus Width: 2.9739**



# B1L000S, U23-ch40, adc1

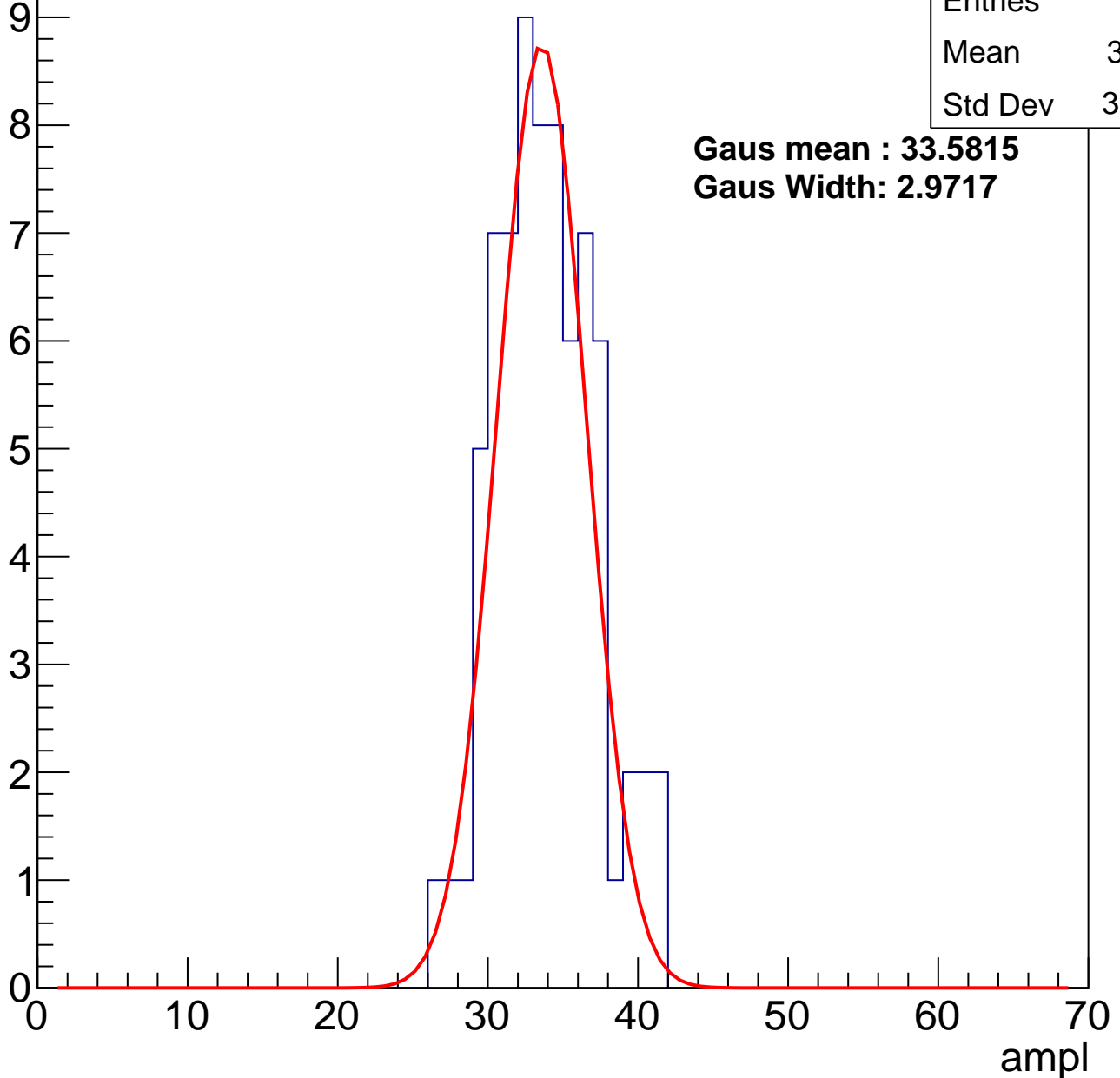
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	33.41
Std Dev	3.293

**Gaus mean : 33.5815**

**Gaus Width: 2.9717**



# B1L000S, U23-ch40, adc2

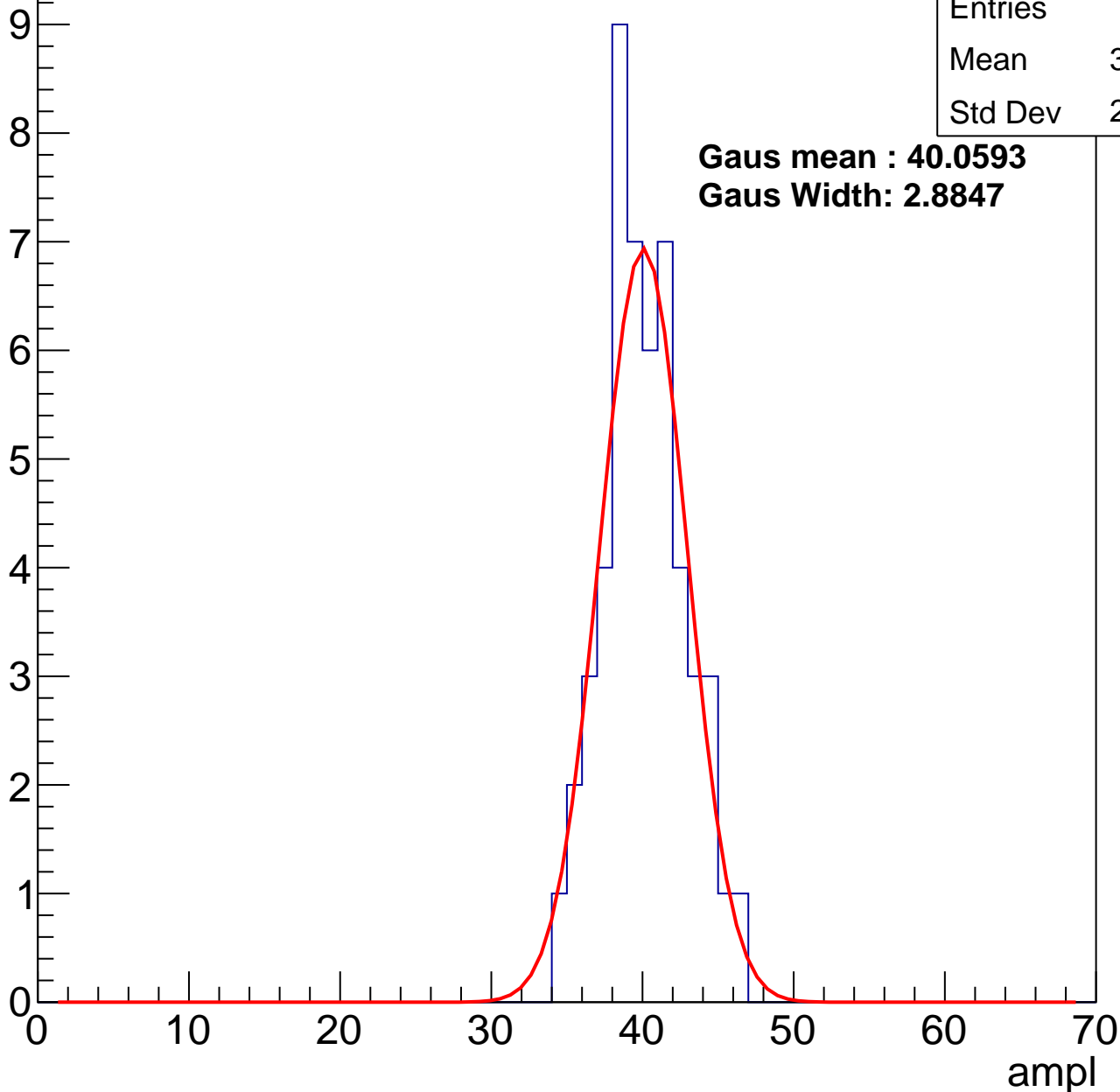
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	39.65
Std Dev	2.678

**Gaus mean : 40.0593**

**Gaus Width: 2.8847**



# B1L000S, U23-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	46.8
Std Dev	3.457

Entry

10

8

6

4

2

0

0

10

20

30

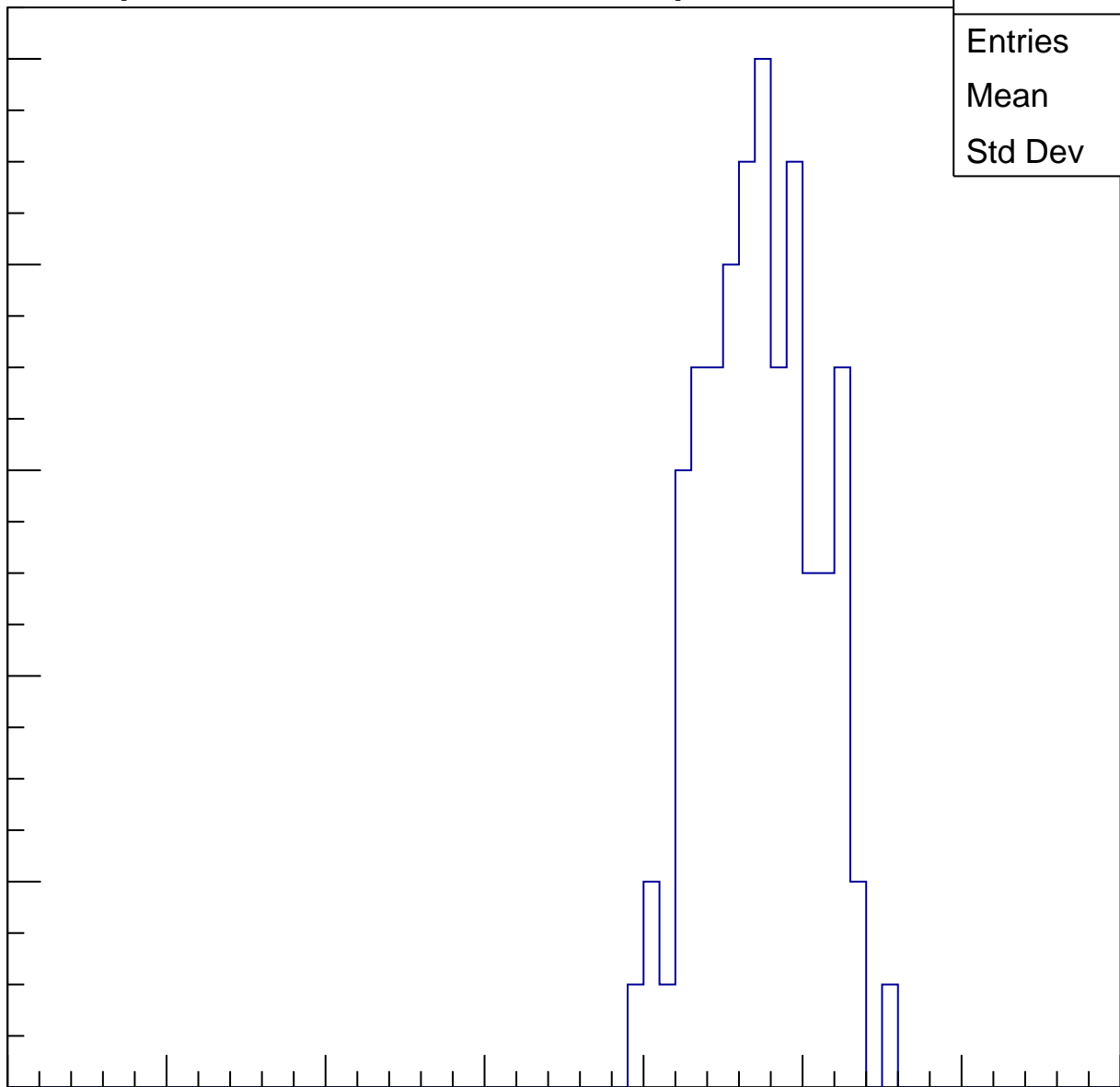
40

50

60

70

ampl

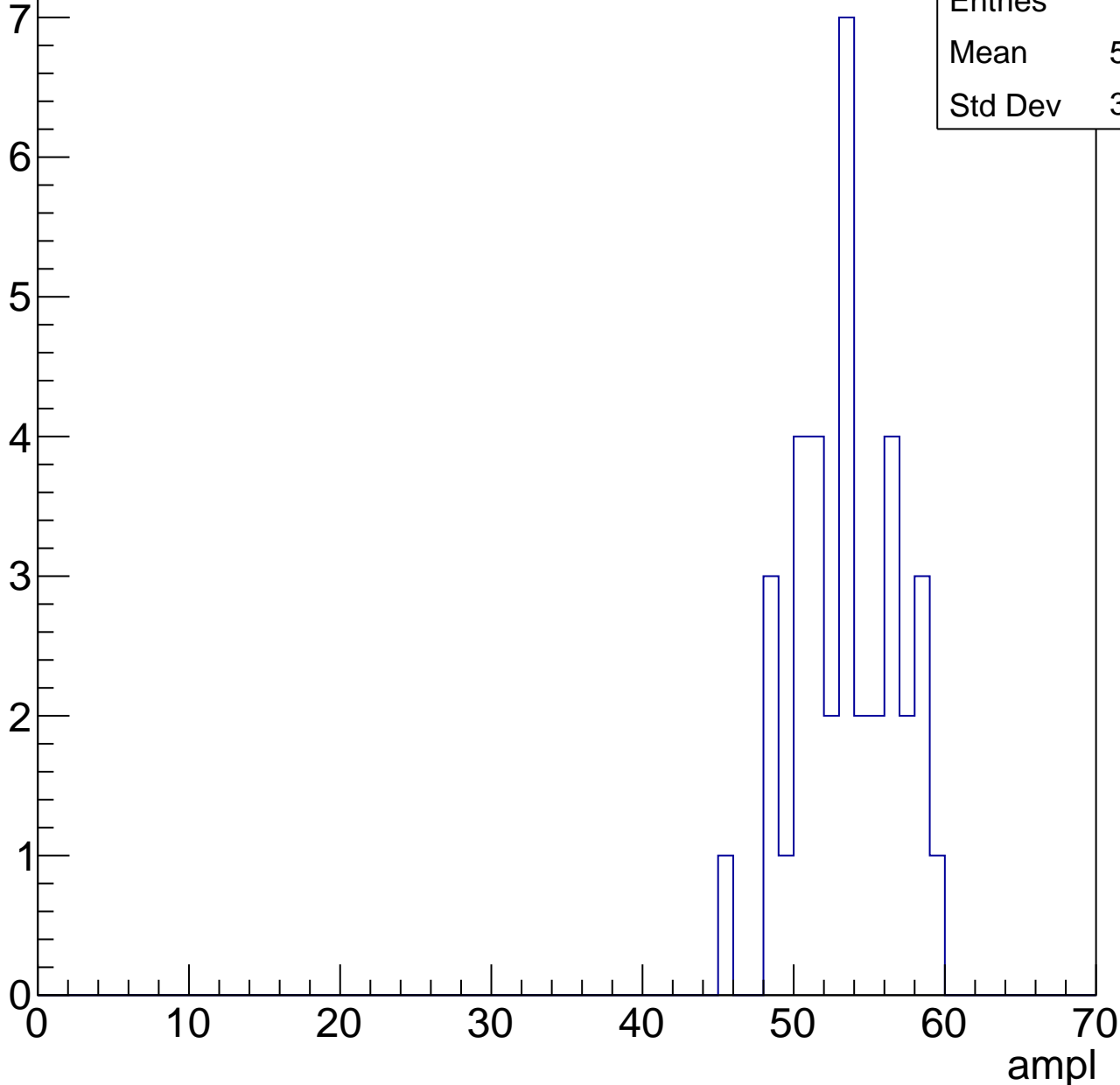


# B1L000S, U23-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

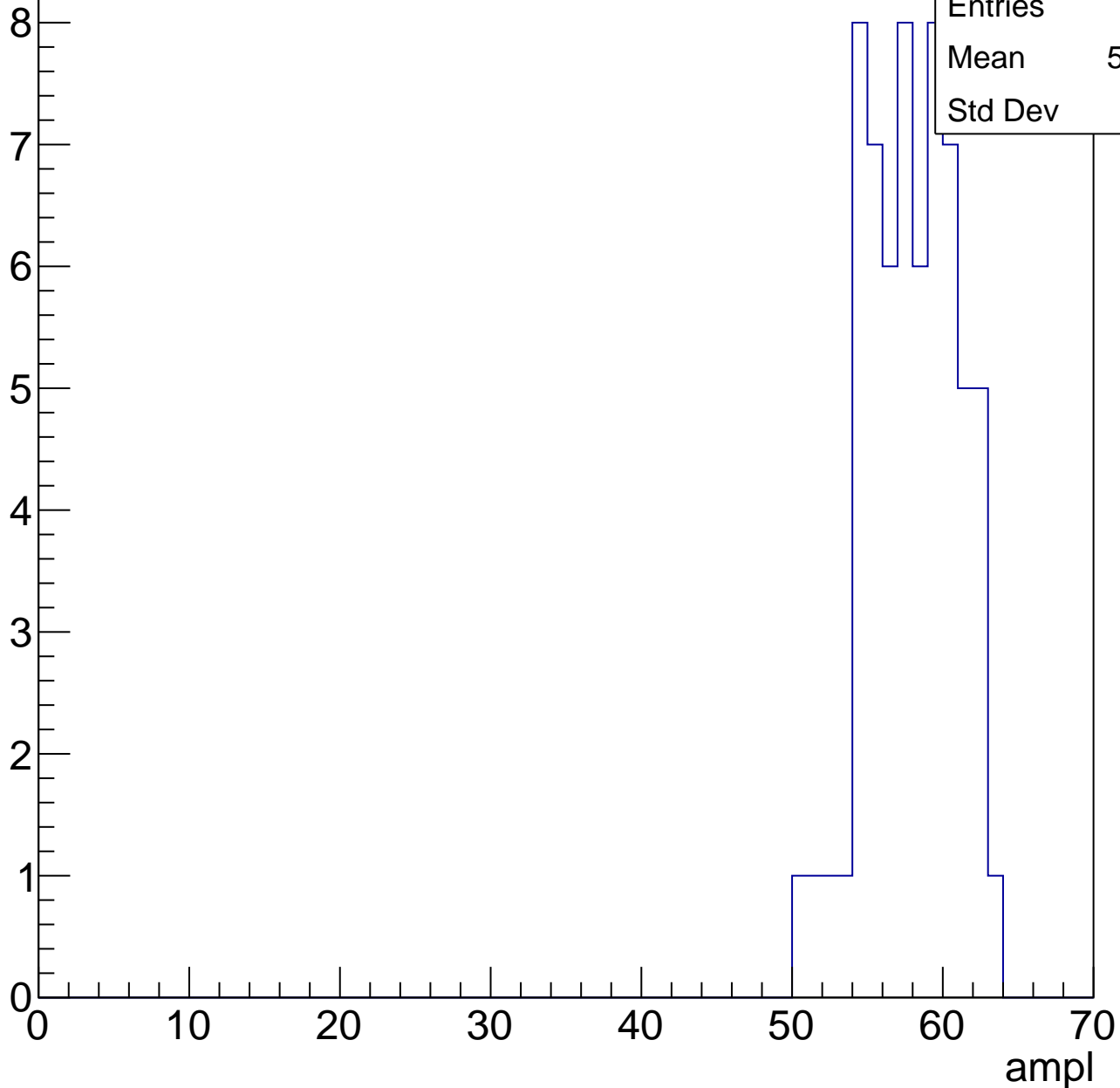
Entries	36
Mean	52.94
Std Dev	3.325



# B1L000S, U23-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

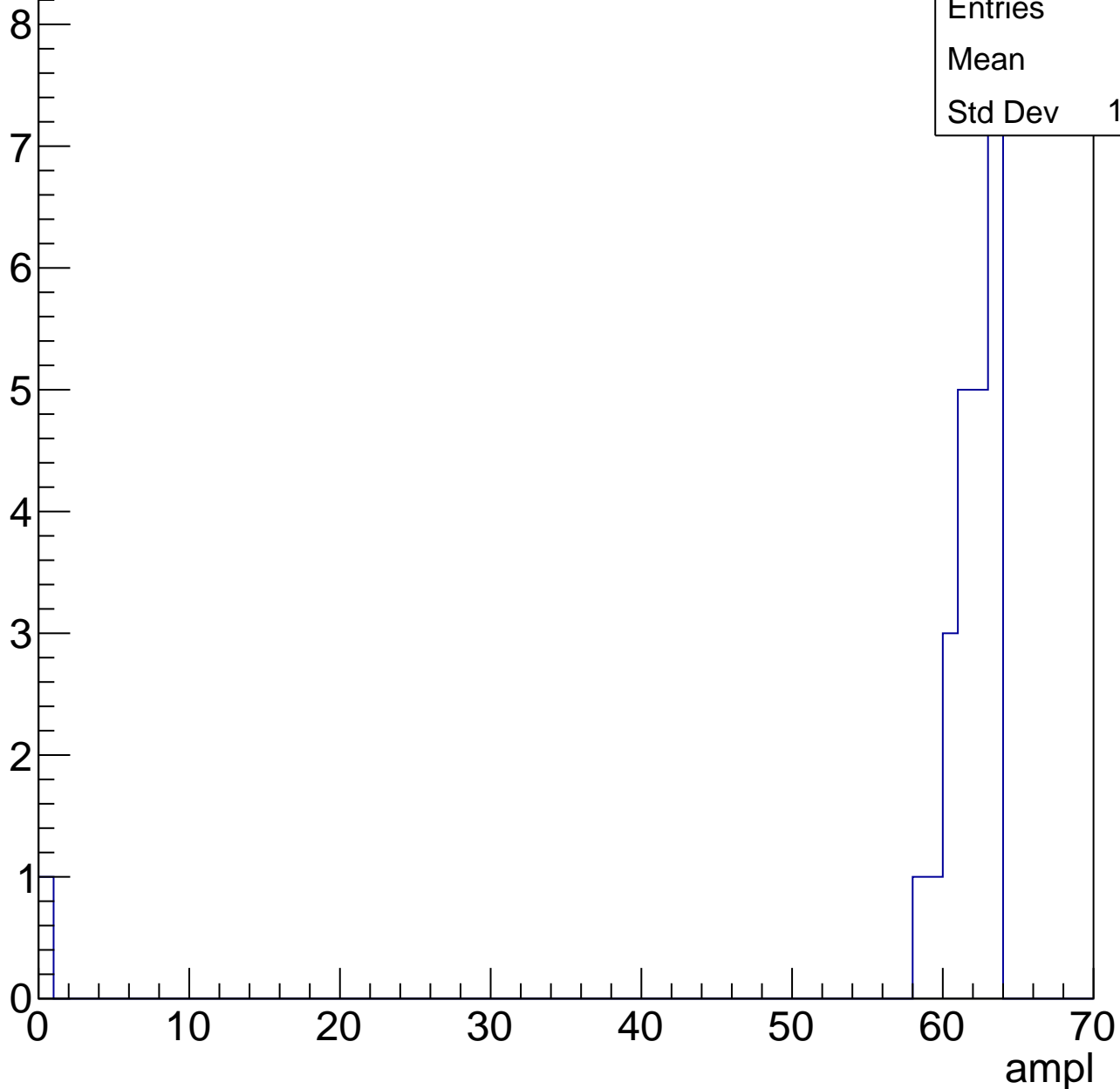


# B1L000S, U23-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	59
Std Dev	12.38





# B1L000S, U23-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U23-ch41, adc0

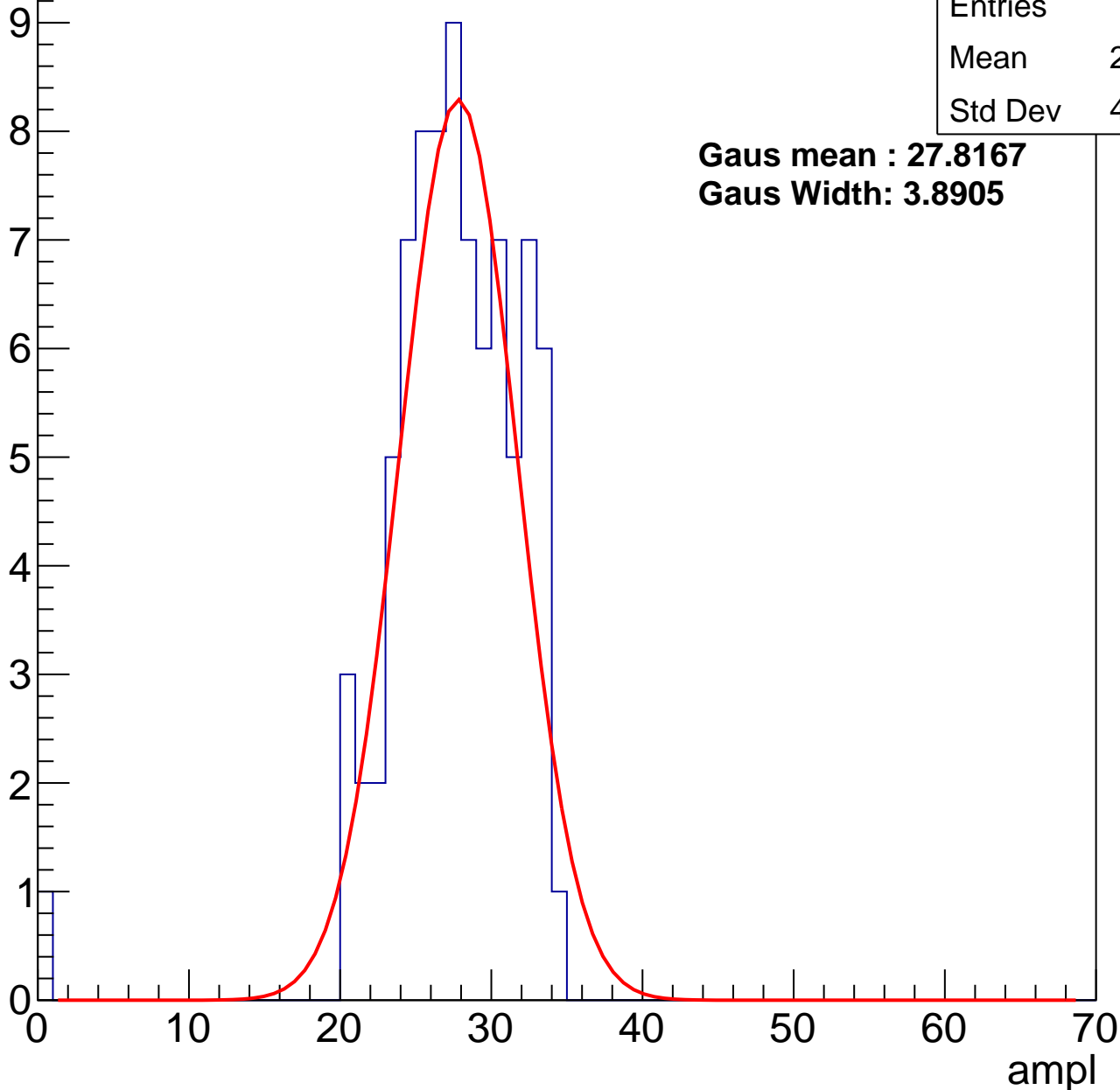
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	84
Mean	27.04
Std Dev	4.623

**Gaus mean : 27.8167**

**Gaus Width: 3.8905**



# B1L000S, U23-ch41, adc1

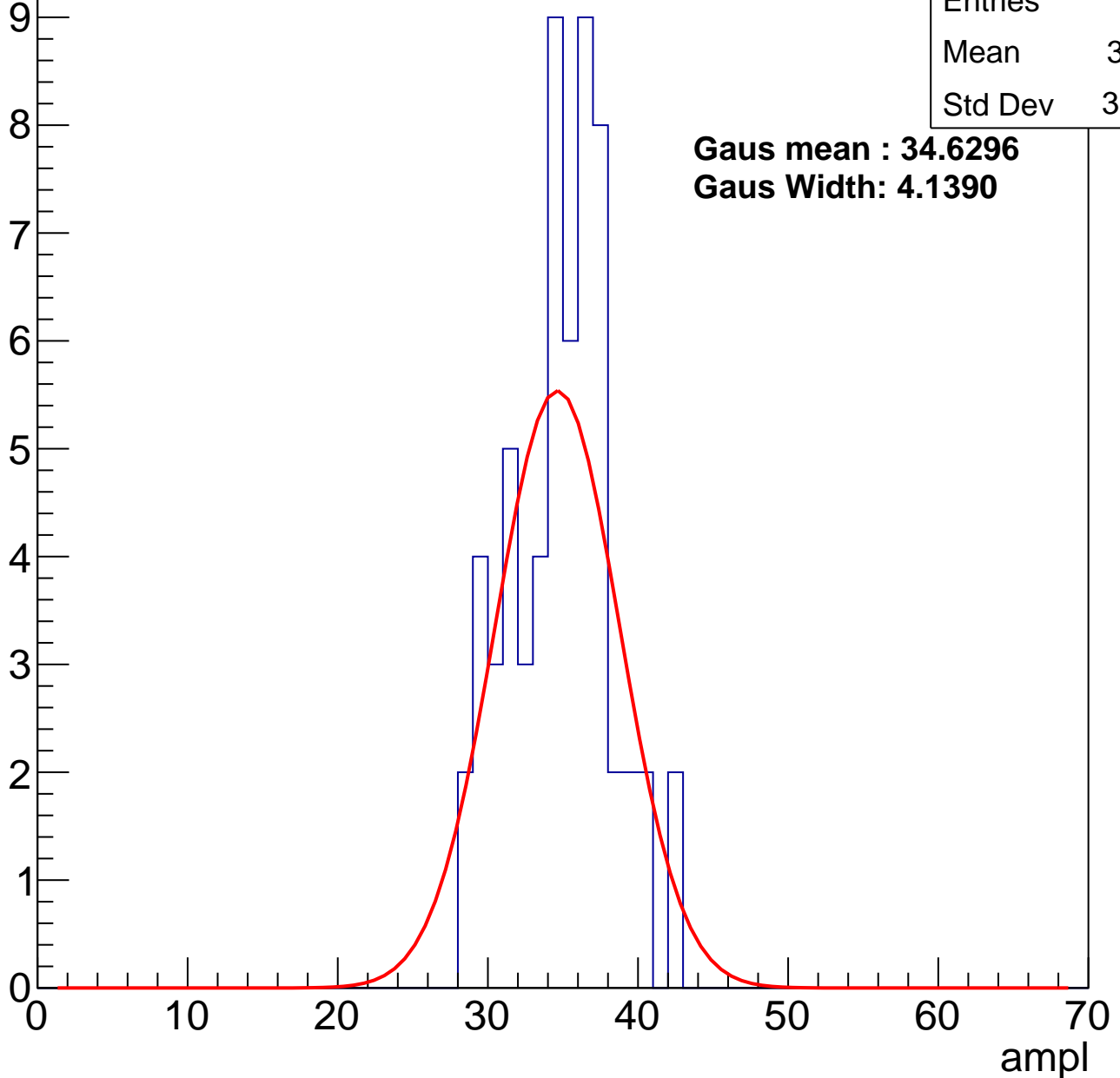
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.41
Std Dev	3.306

**Gaus mean : 34.6296**

**Gaus Width: 4.1390**



# B1L000S, U23-ch41, adc2

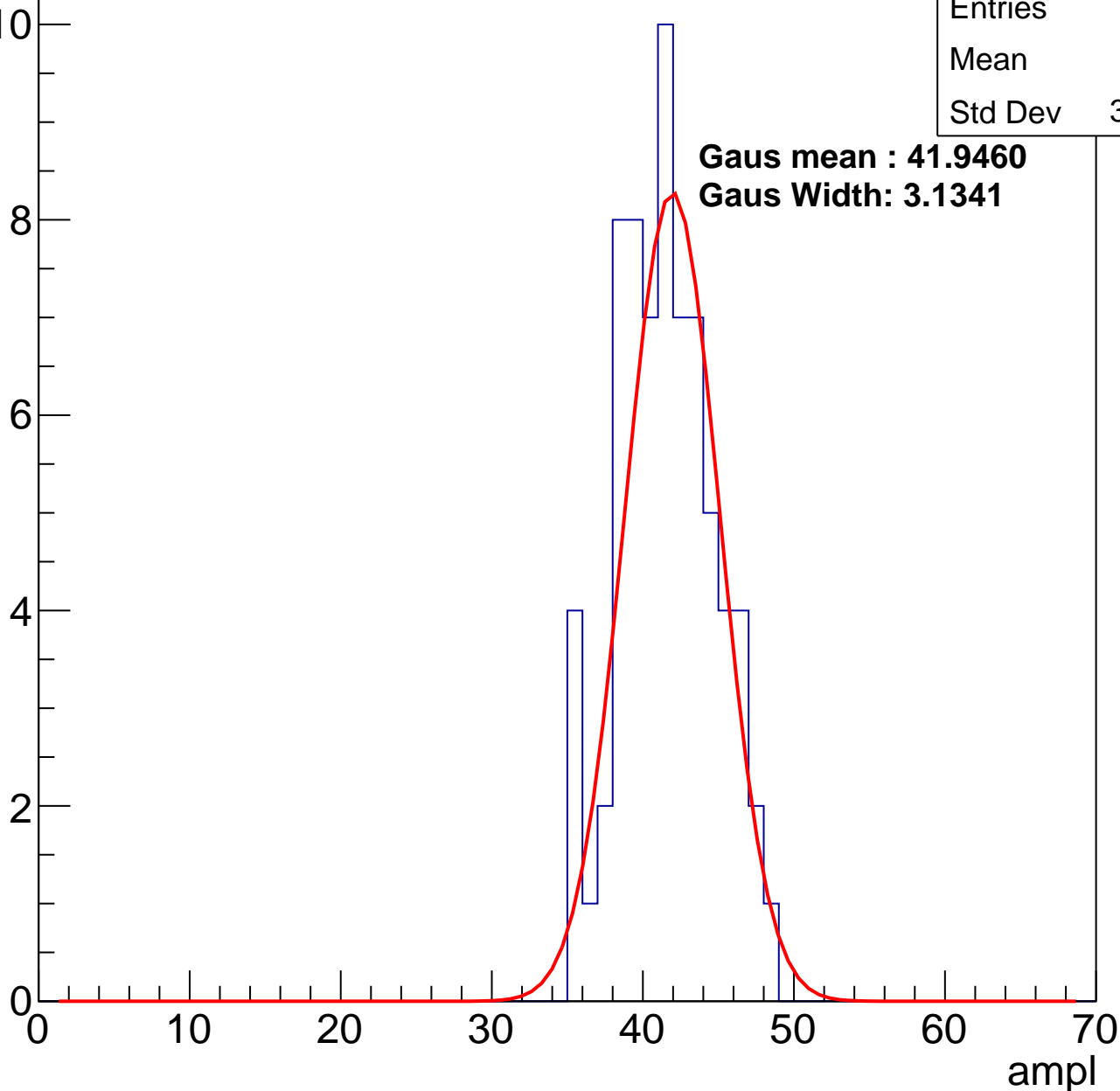
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	41.1
Std Dev	3.108

**Gaus mean : 41.9460**

**Gaus Width: 3.1341**

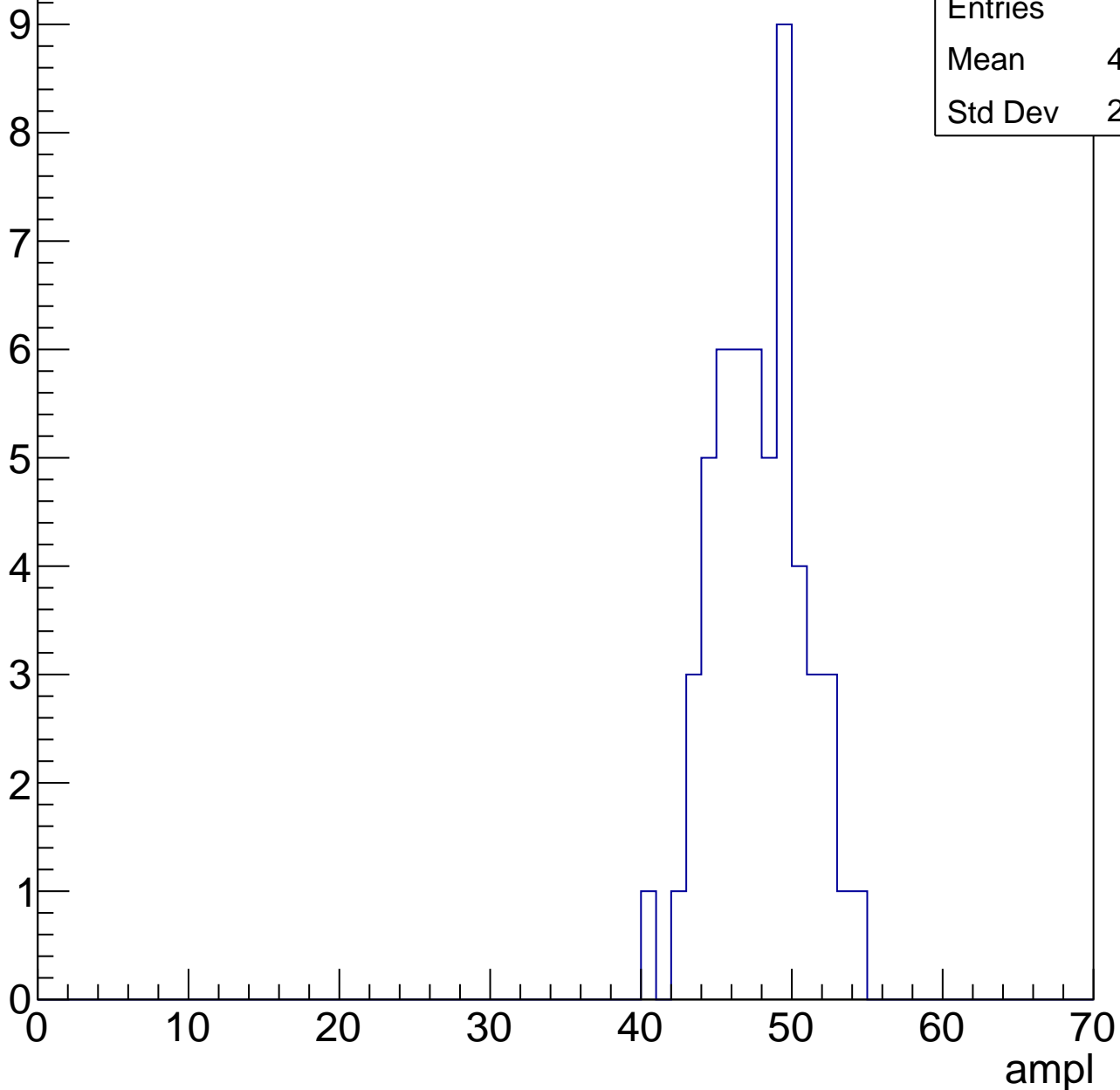


# B1L000S, U23-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	47.33
Std Dev	2.969

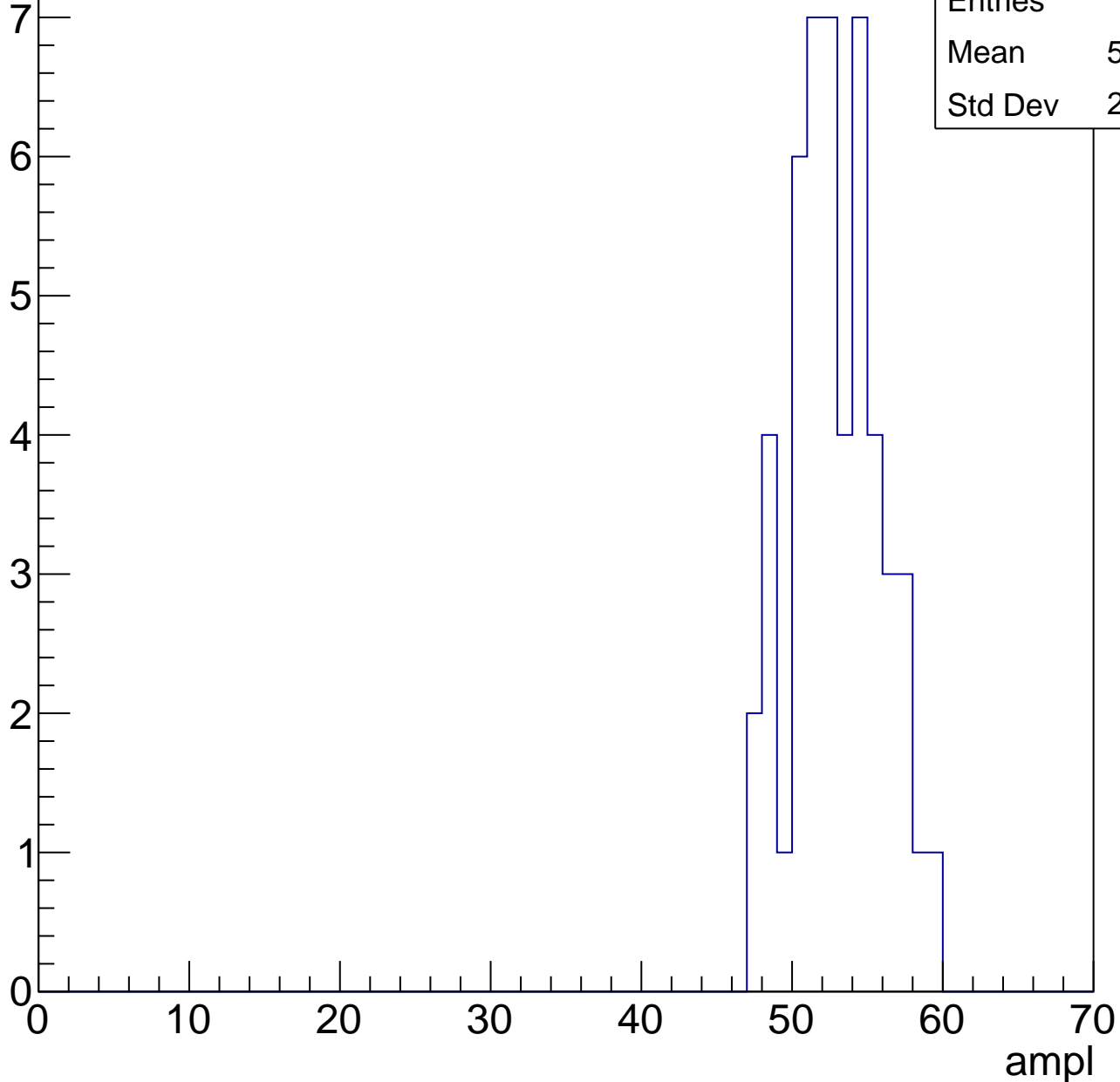


# B1L000S, U23-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

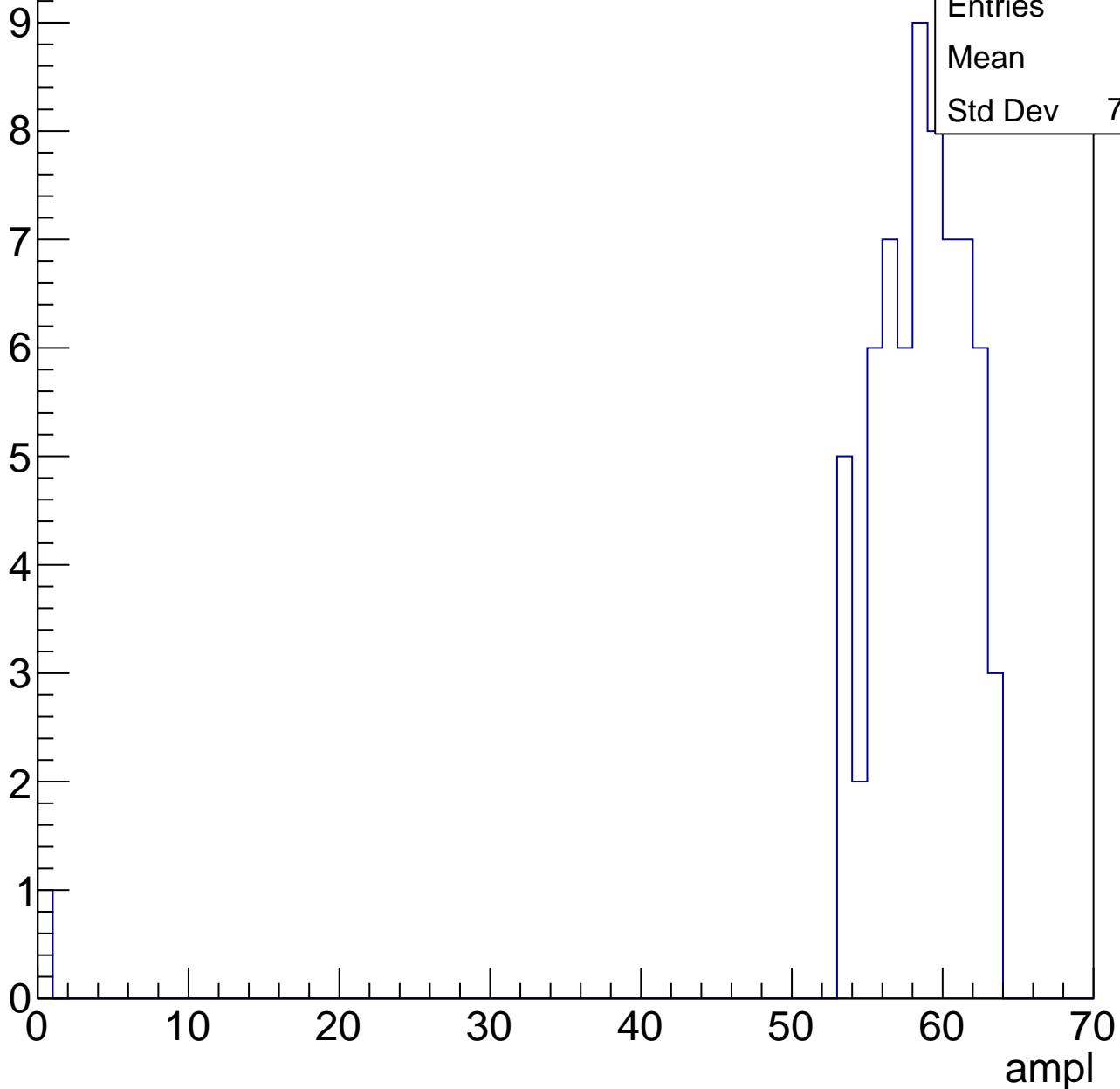
Entries	50
Mean	52.44
Std Dev	2.899



# B1L000S, U23-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

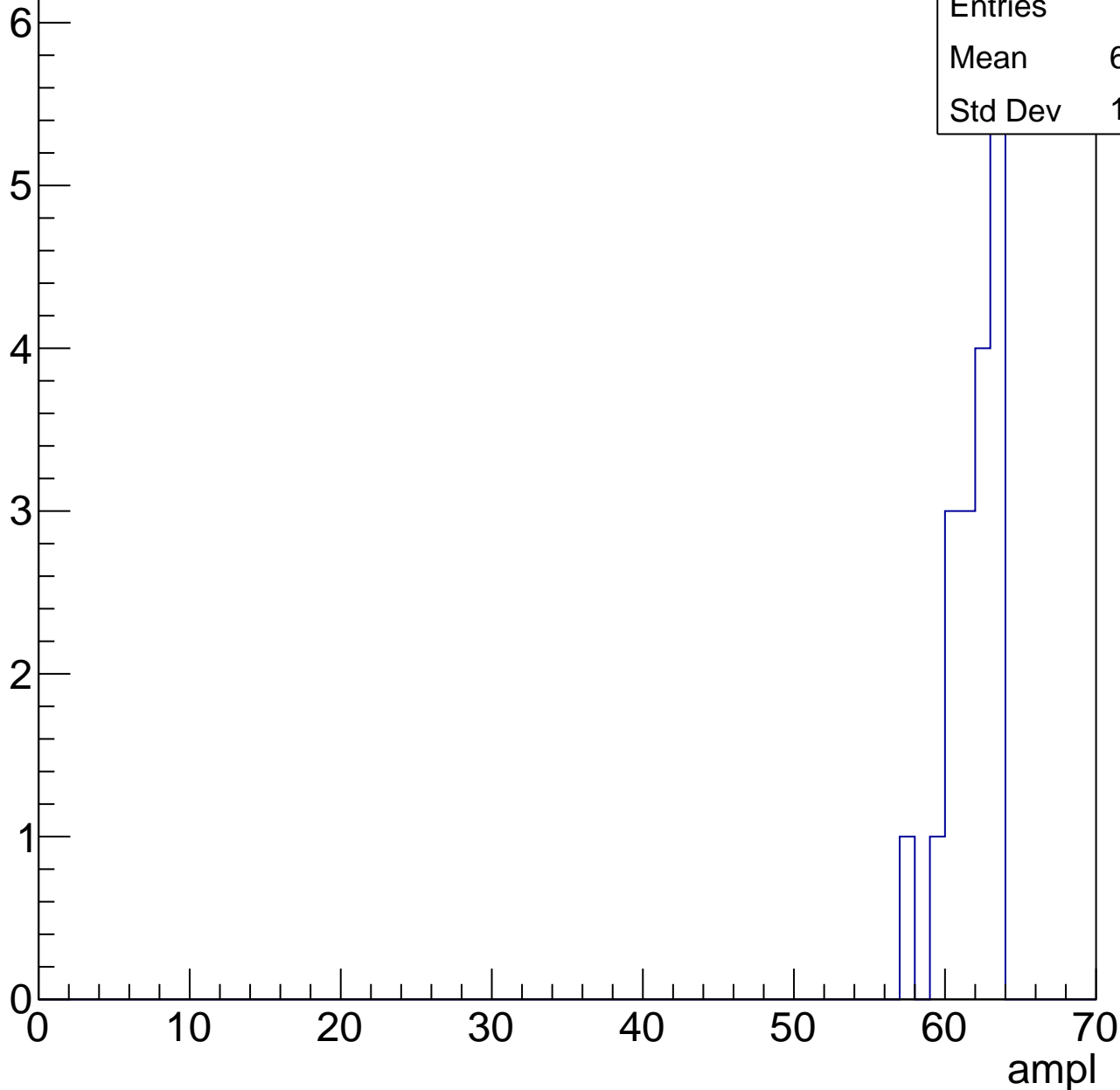


# B1L000S, U23-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.39
Std Dev	1.638





# B1L000S, U23-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U23-ch42, adc0

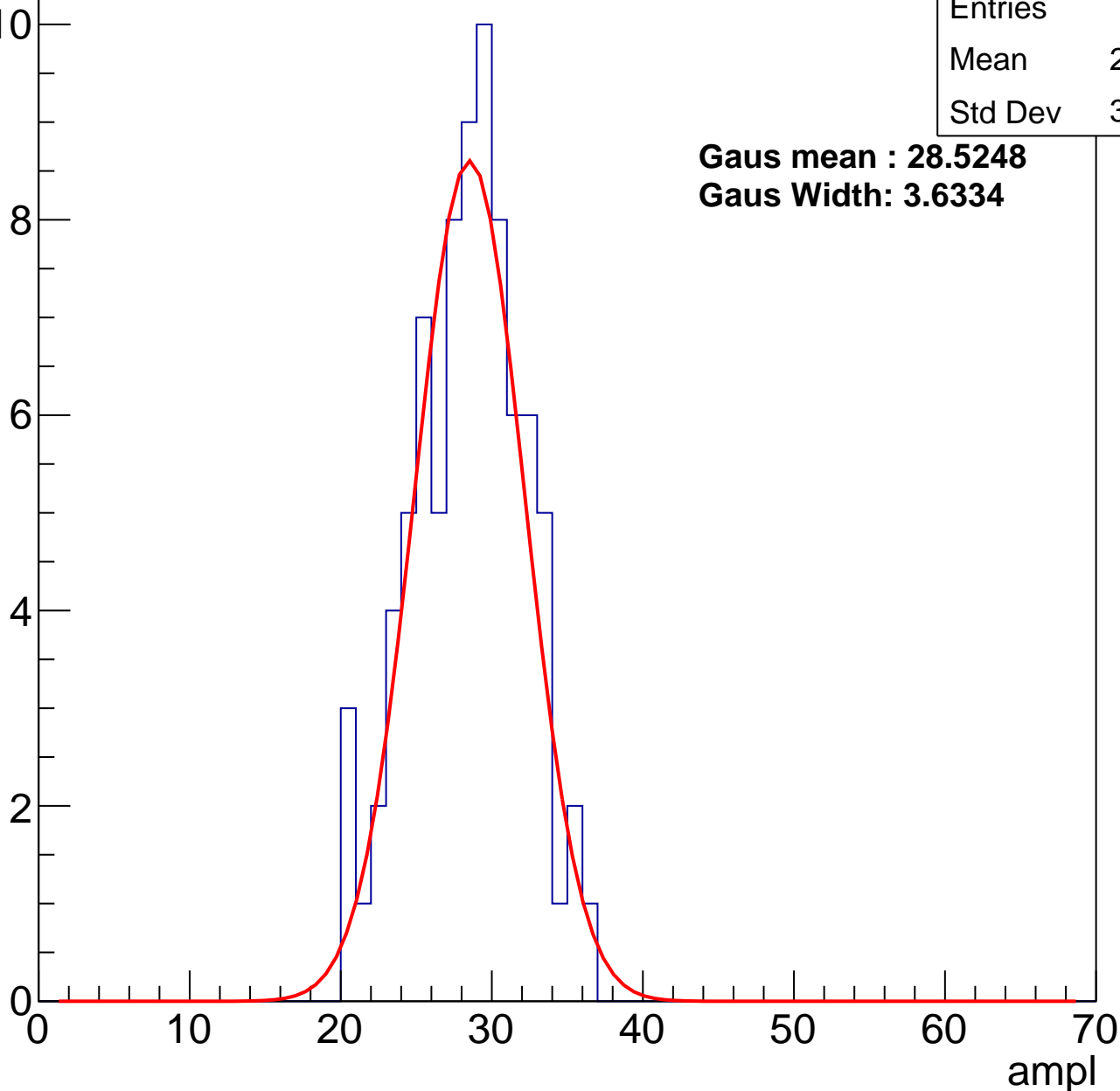
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	27.99
Std Dev	3.639

**Gaus mean : 28.5248**

**Gaus Width: 3.6334**



# B1L000S, U23-ch42, adc1

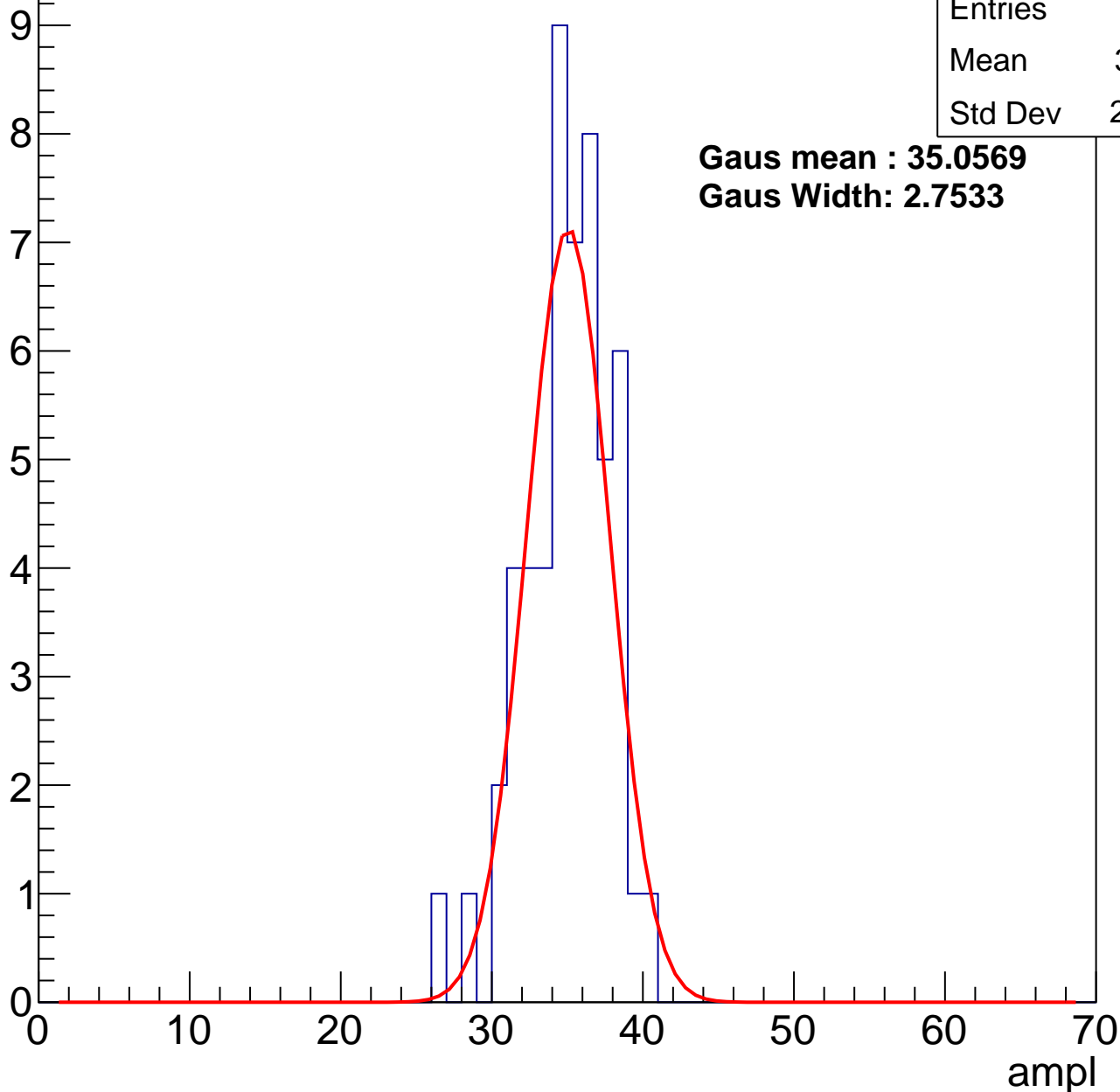
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.51
Std Dev	2.799

**Gaus mean : 35.0569**

**Gaus Width: 2.7533**



# B1L000S, U23-ch42, adc2

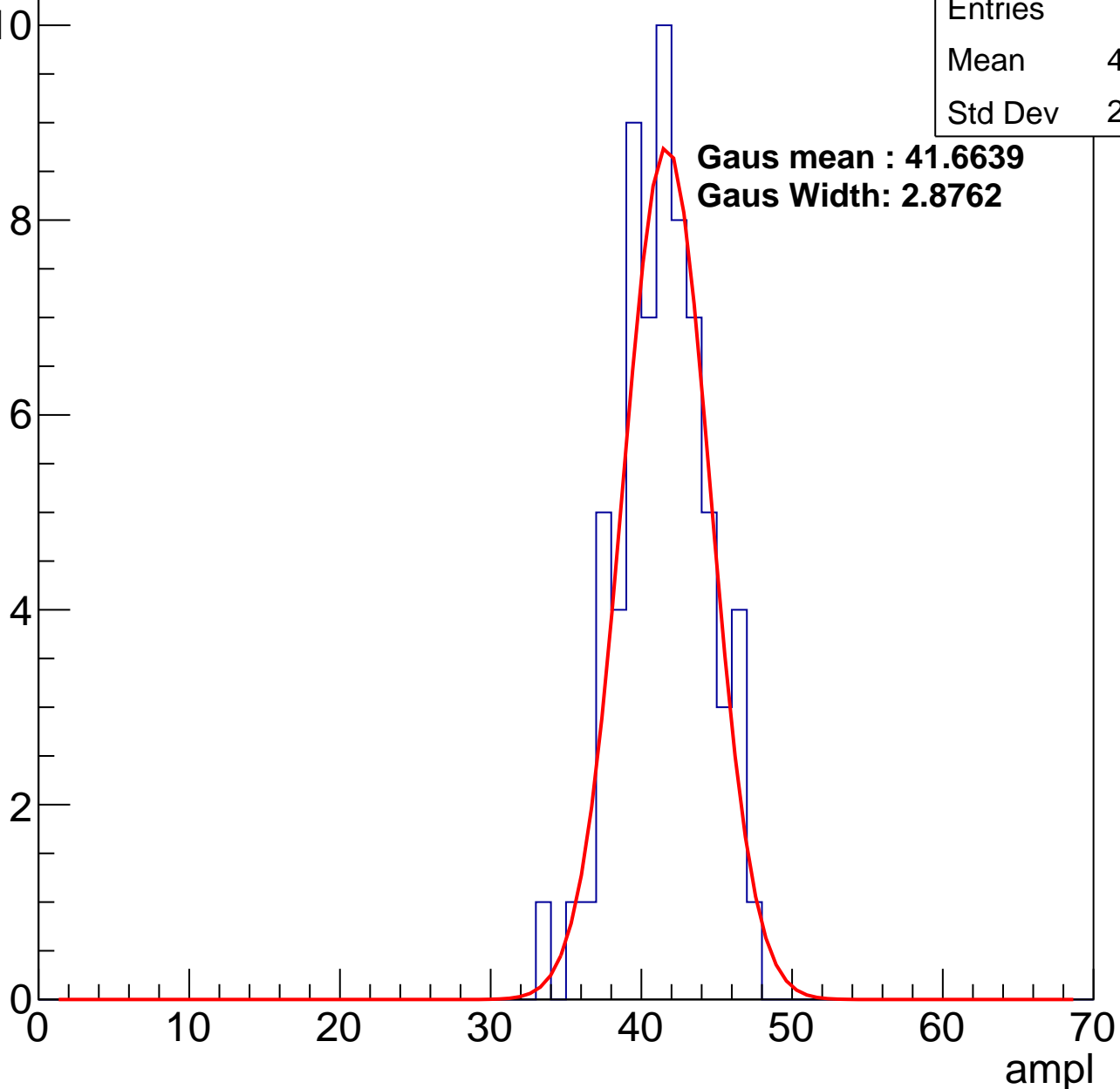
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	40.98
Std Dev	2.884

**Gaus mean : 41.6639**

**Gaus Width: 2.8762**

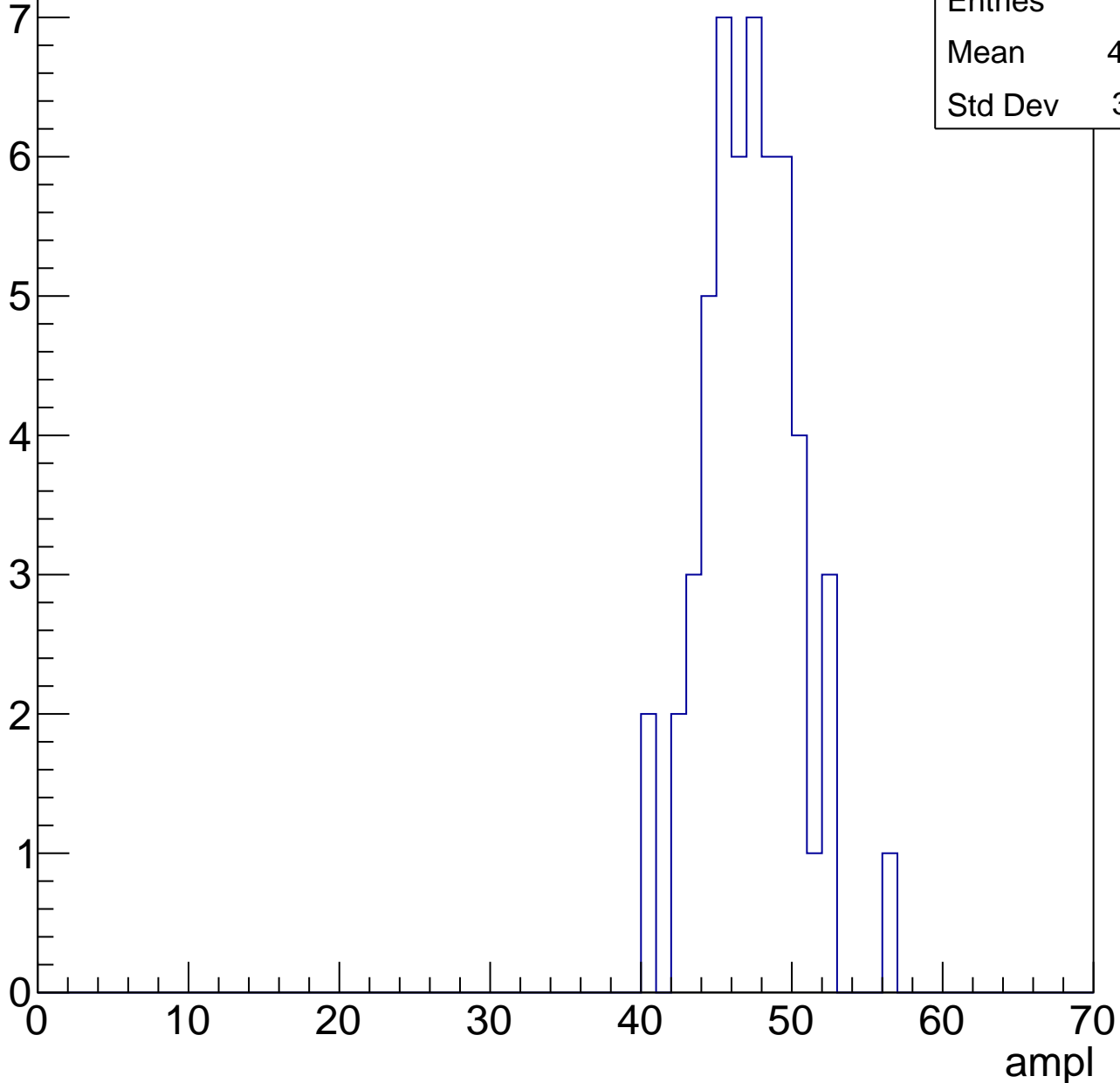


# B1L000S, U23-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

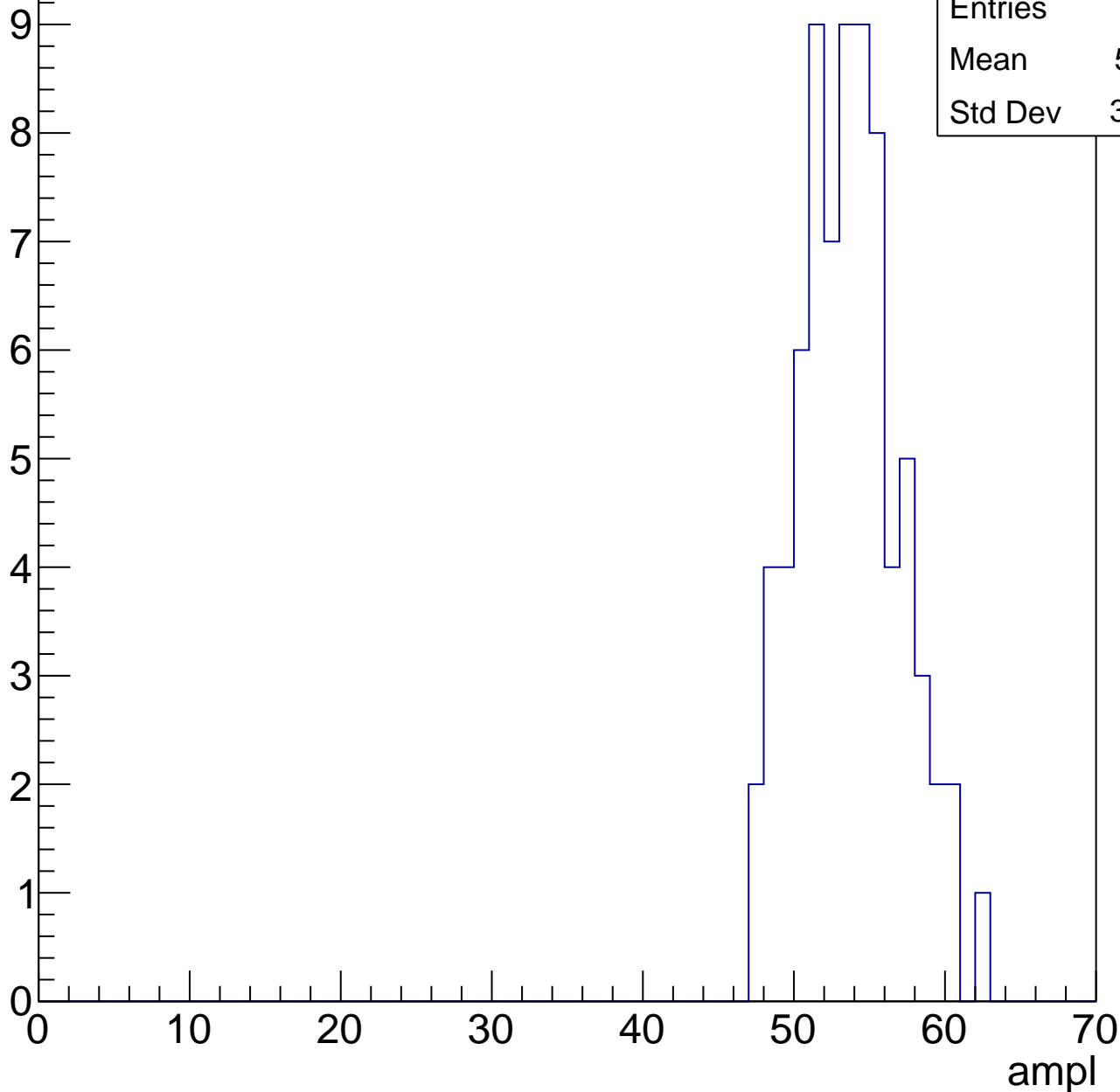
Entries	53
Mean	46.75
Std Dev	3.101



# B1L000S, U23-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	75
Mean	53.21
Std Dev	3.292

# B1L000S, U23-ch42, adc5

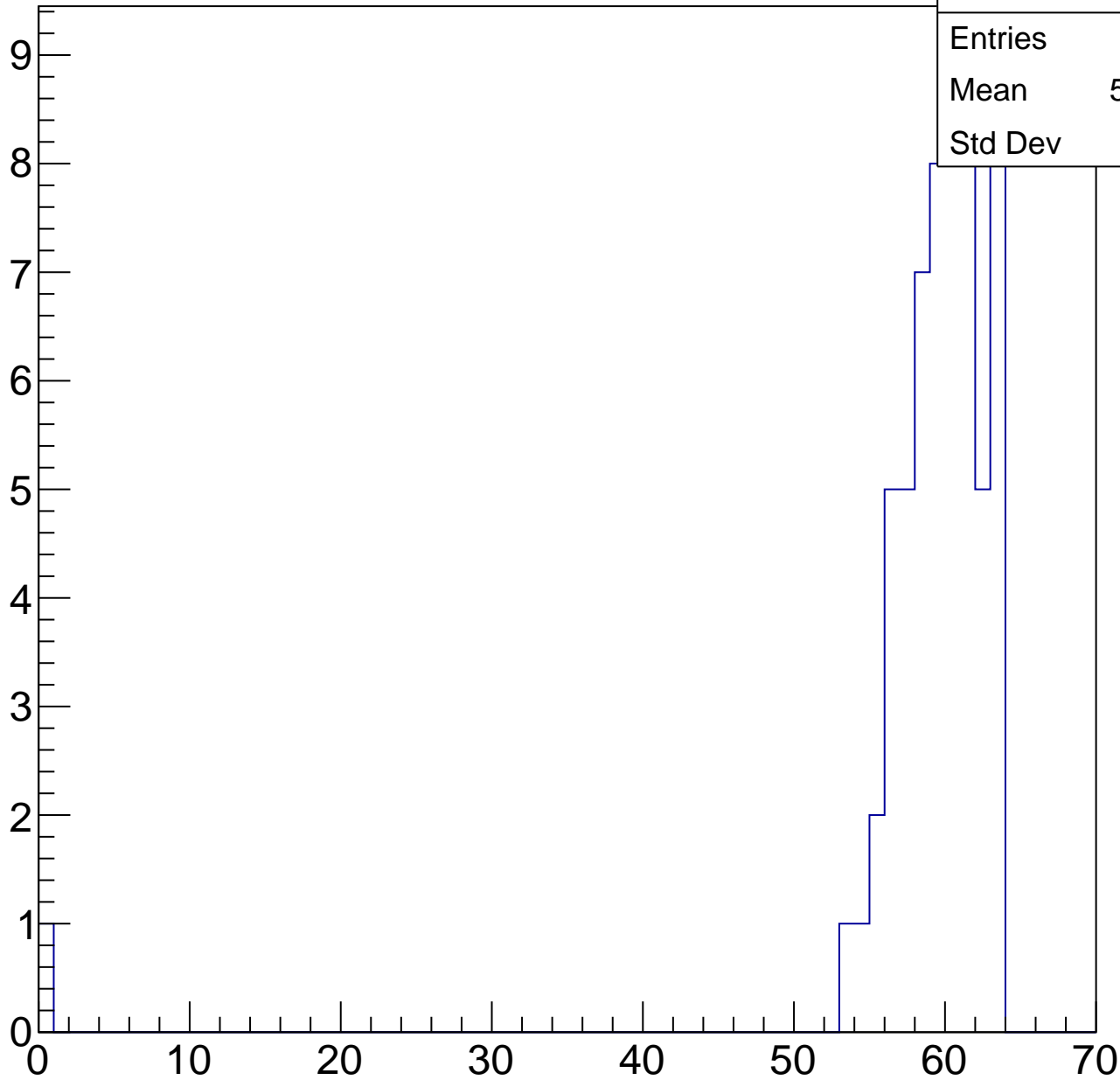
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	58.38
Std Dev	8

ampl



# B1L000S, U23-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	29.45
Std Dev	3.329

**Gaus mean : 30.7139**

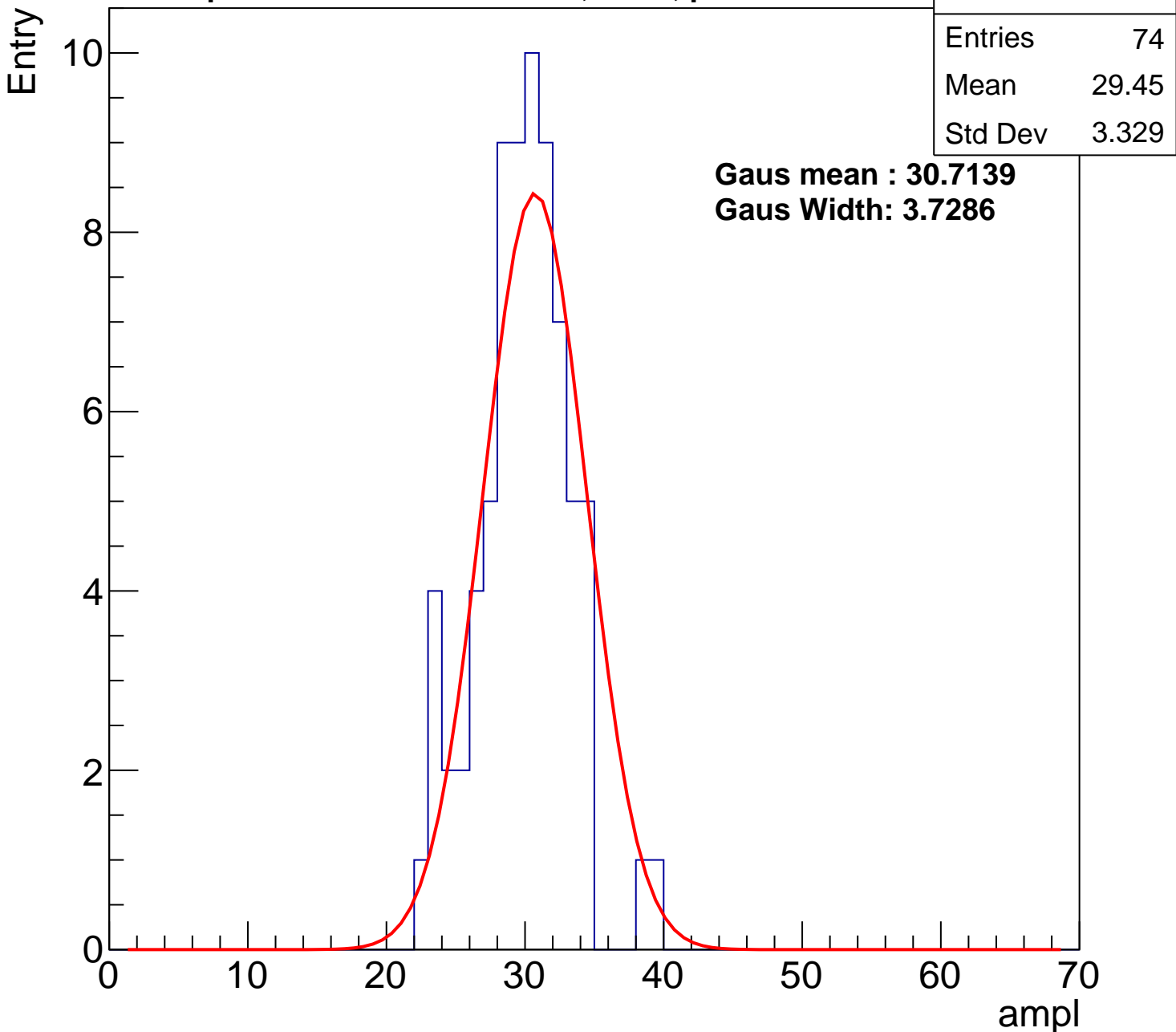
**Gaus Width: 3.7286**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch43, adc1

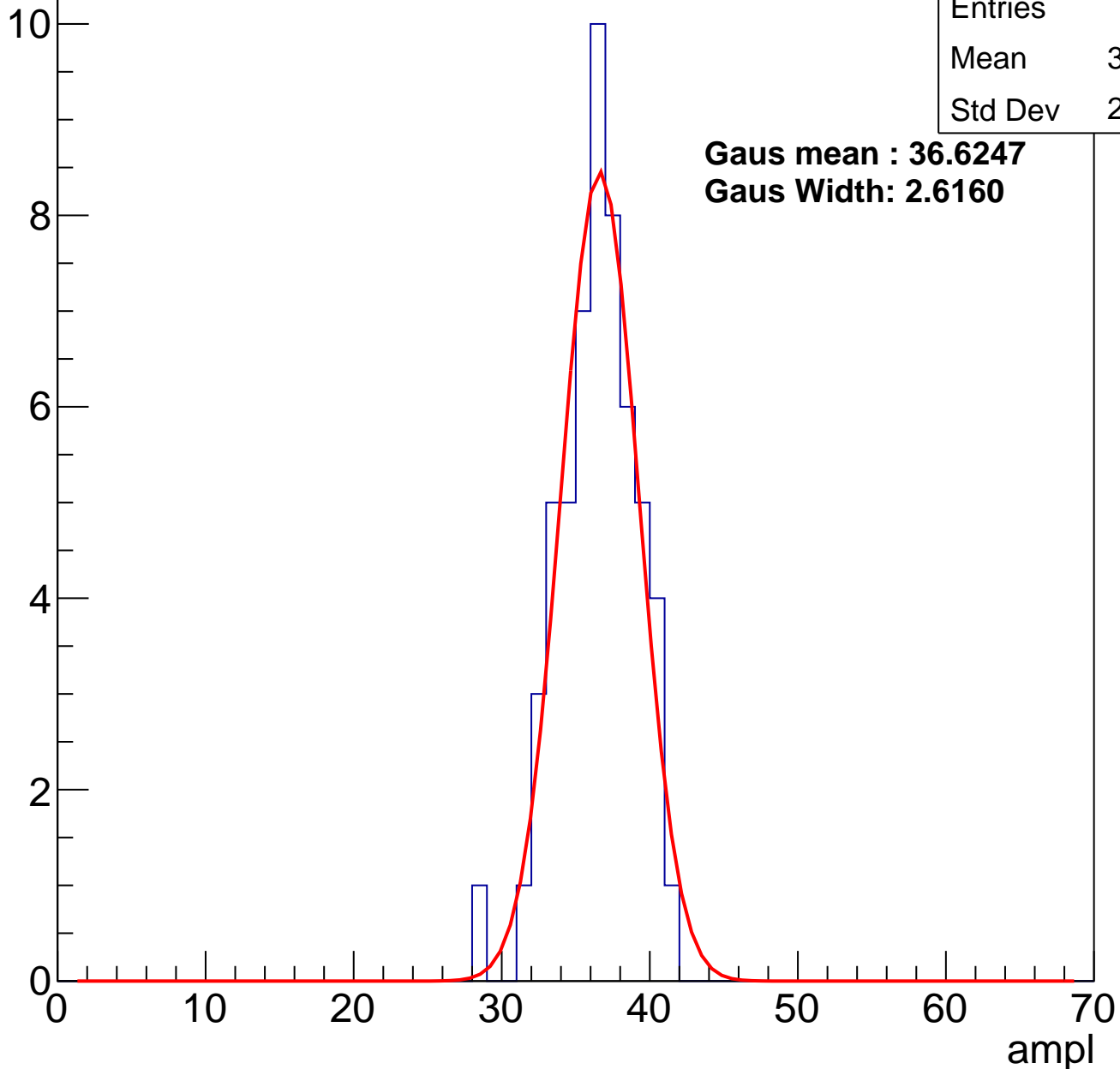
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	35.98
Std Dev	2.588

**Gaus mean : 36.6247**

**Gaus Width: 2.6160**

Entry



# B1L000S, U23-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	42.77
Std Dev	3.696

**Gaus mean : 42.8648**

**Gaus Width: 3.2894**

Entry

10

8

6

4

2

0

0

10

20

30

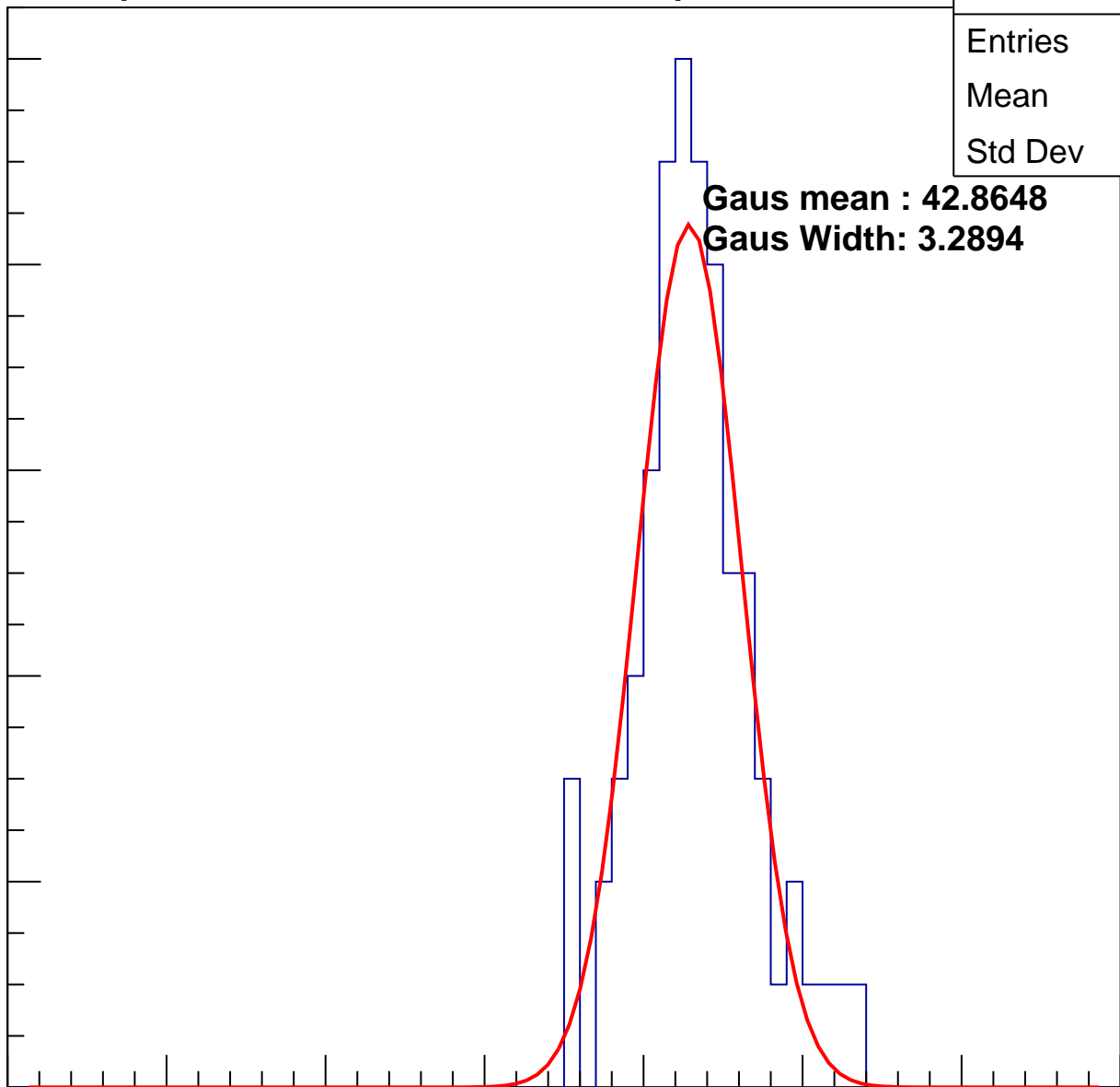
40

50

60

70

ampl

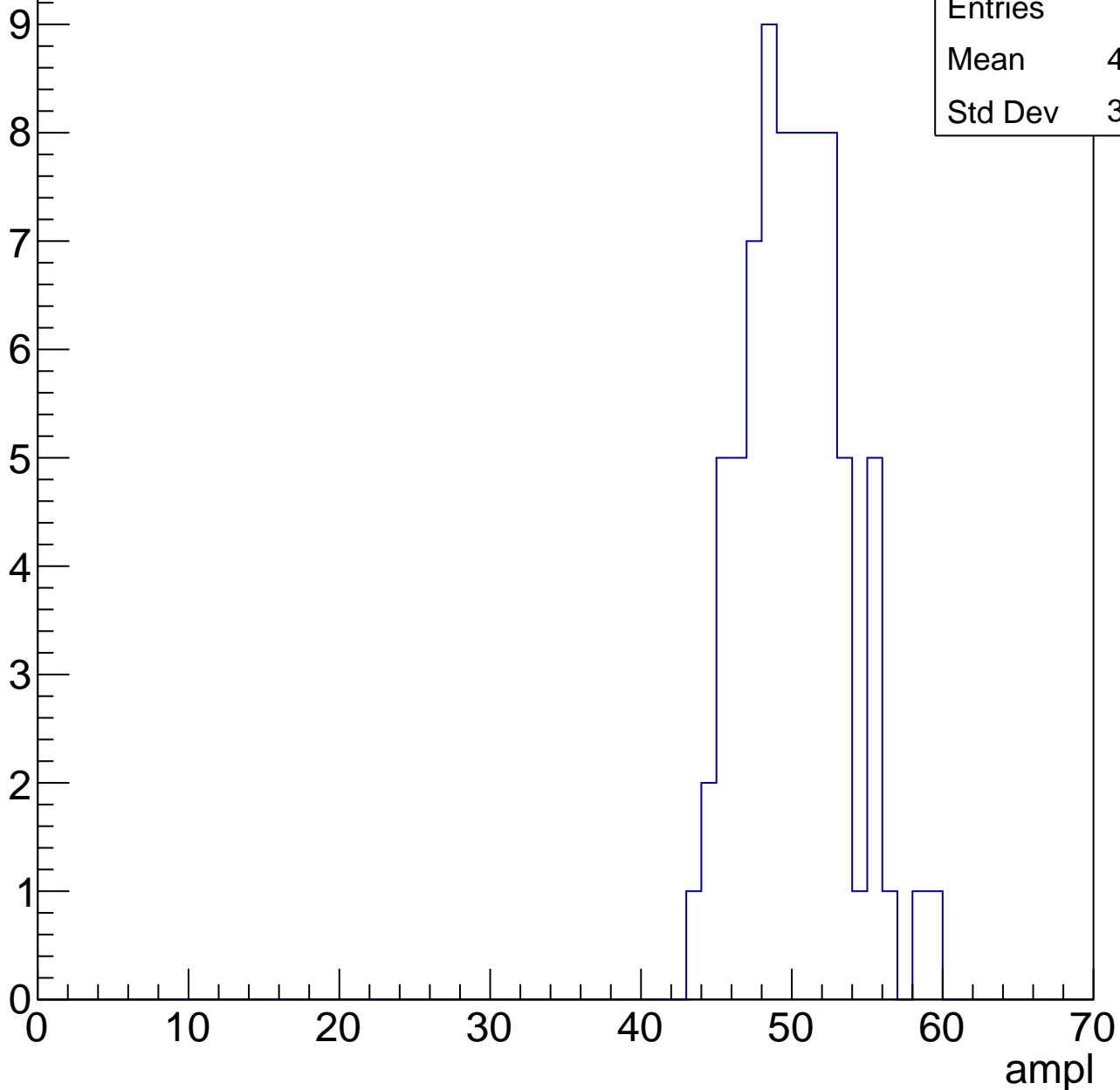


# B1L000S, U23-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	49.73
Std Dev	3.336

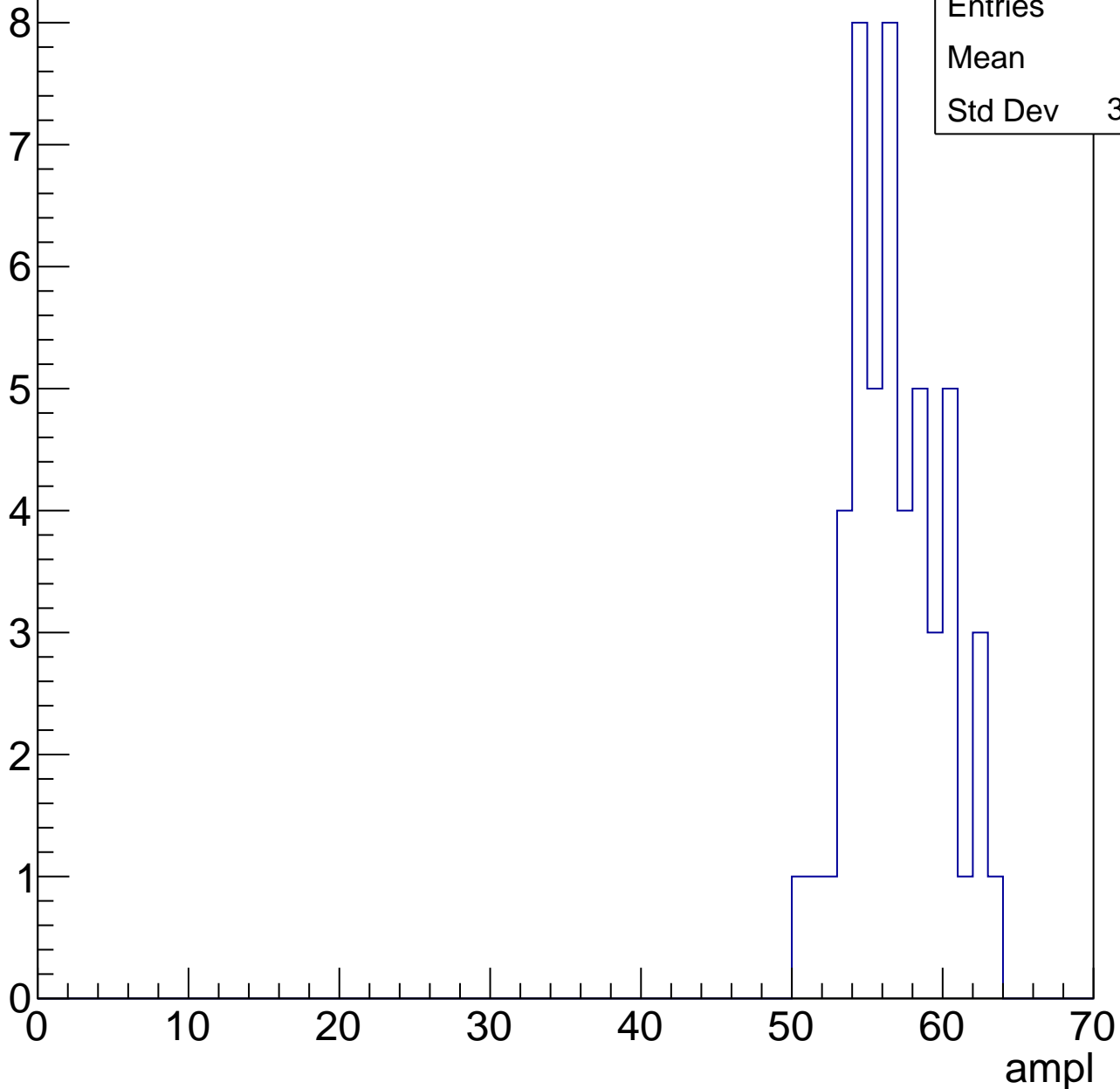


# B1L000S, U23-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	56.5
Std Dev	3.002

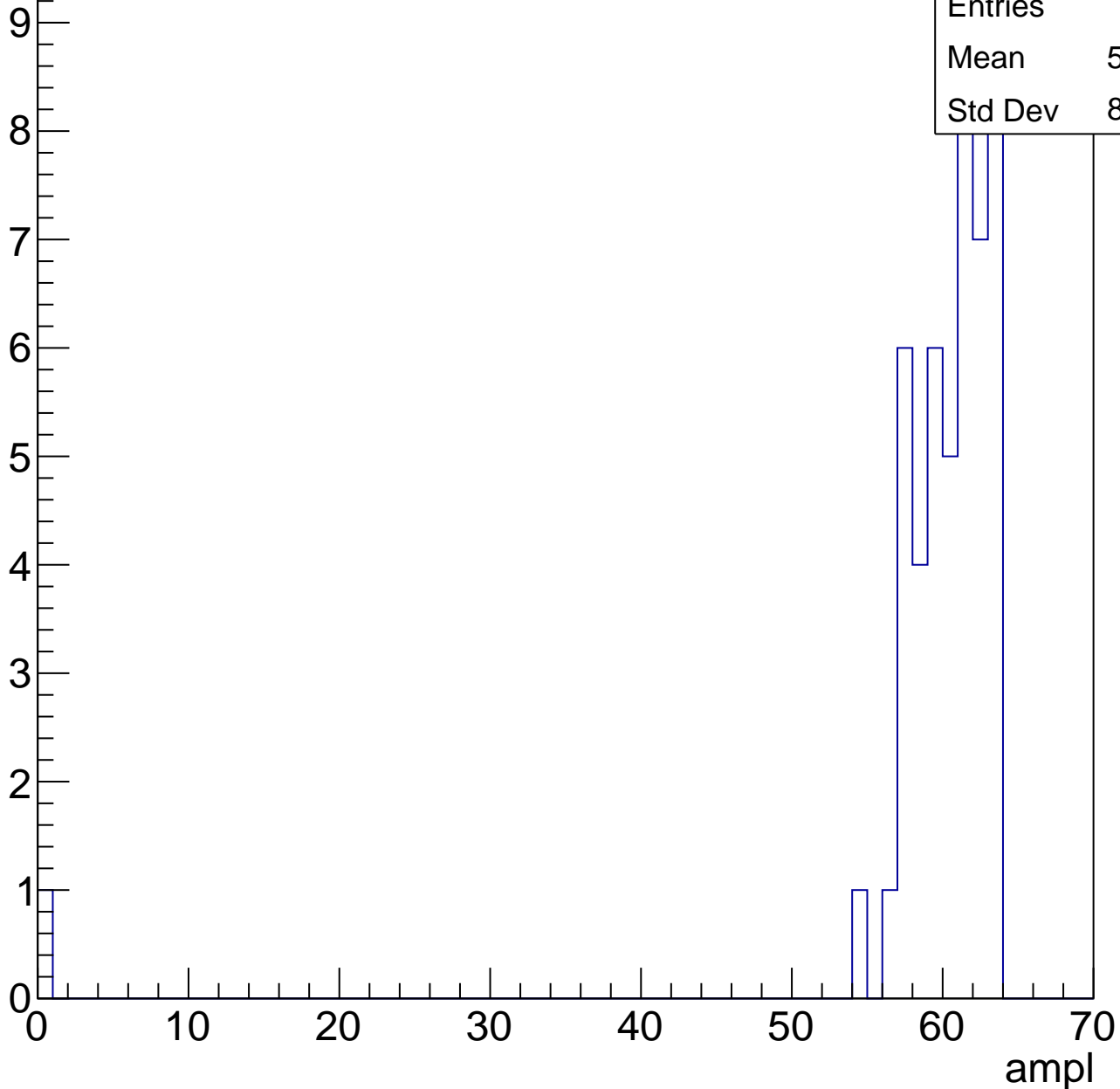


# B1L000S, U23-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	58.94
Std Dev	8.796



# B1L000S, U23-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0



# B1L000S, U23-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch44, adc0

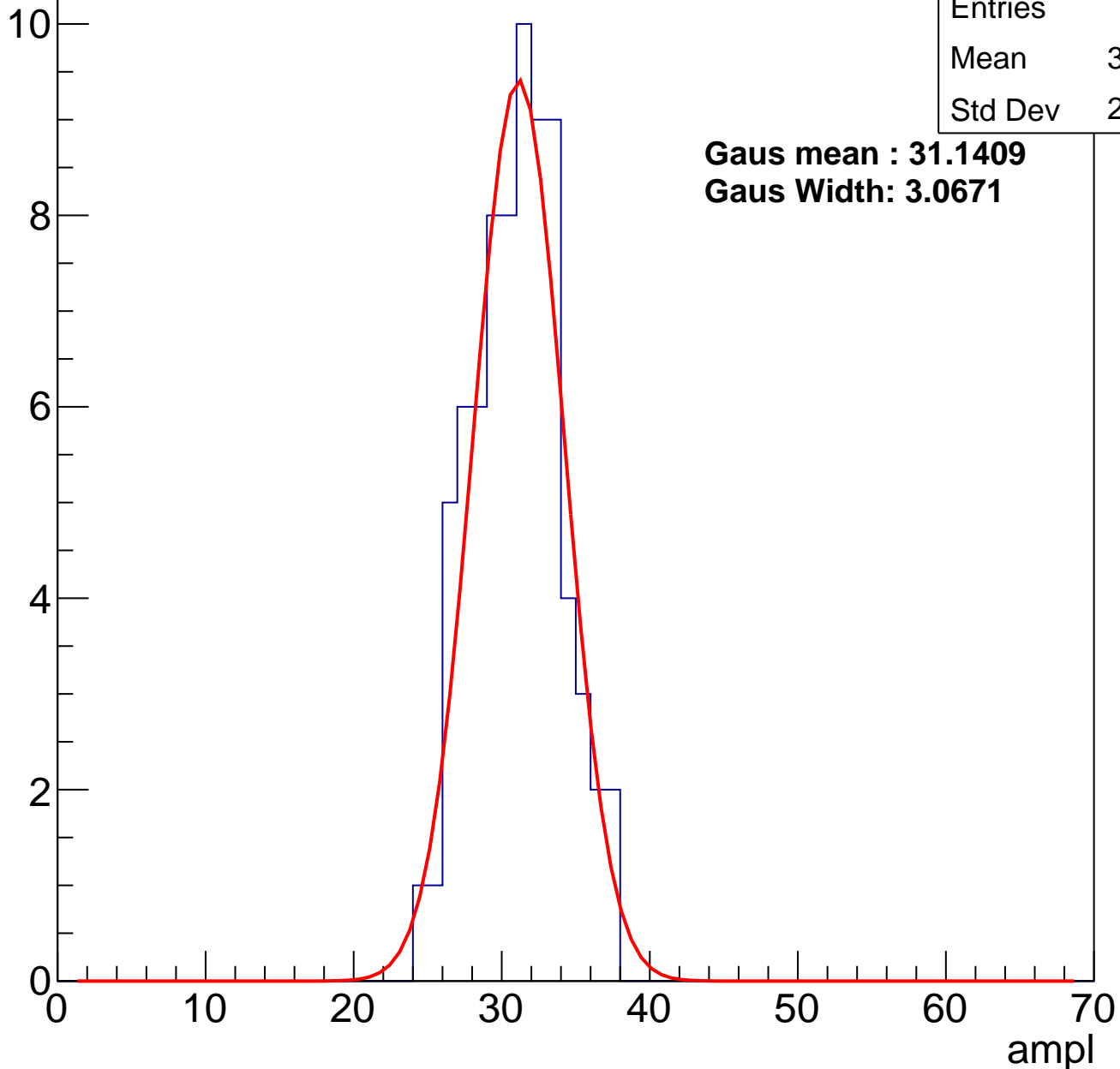
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	30.58
Std Dev	2.936

**Gaus mean : 31.1409**

**Gaus Width: 3.0671**

Entry



# B1L000S, U23-ch44, adc1

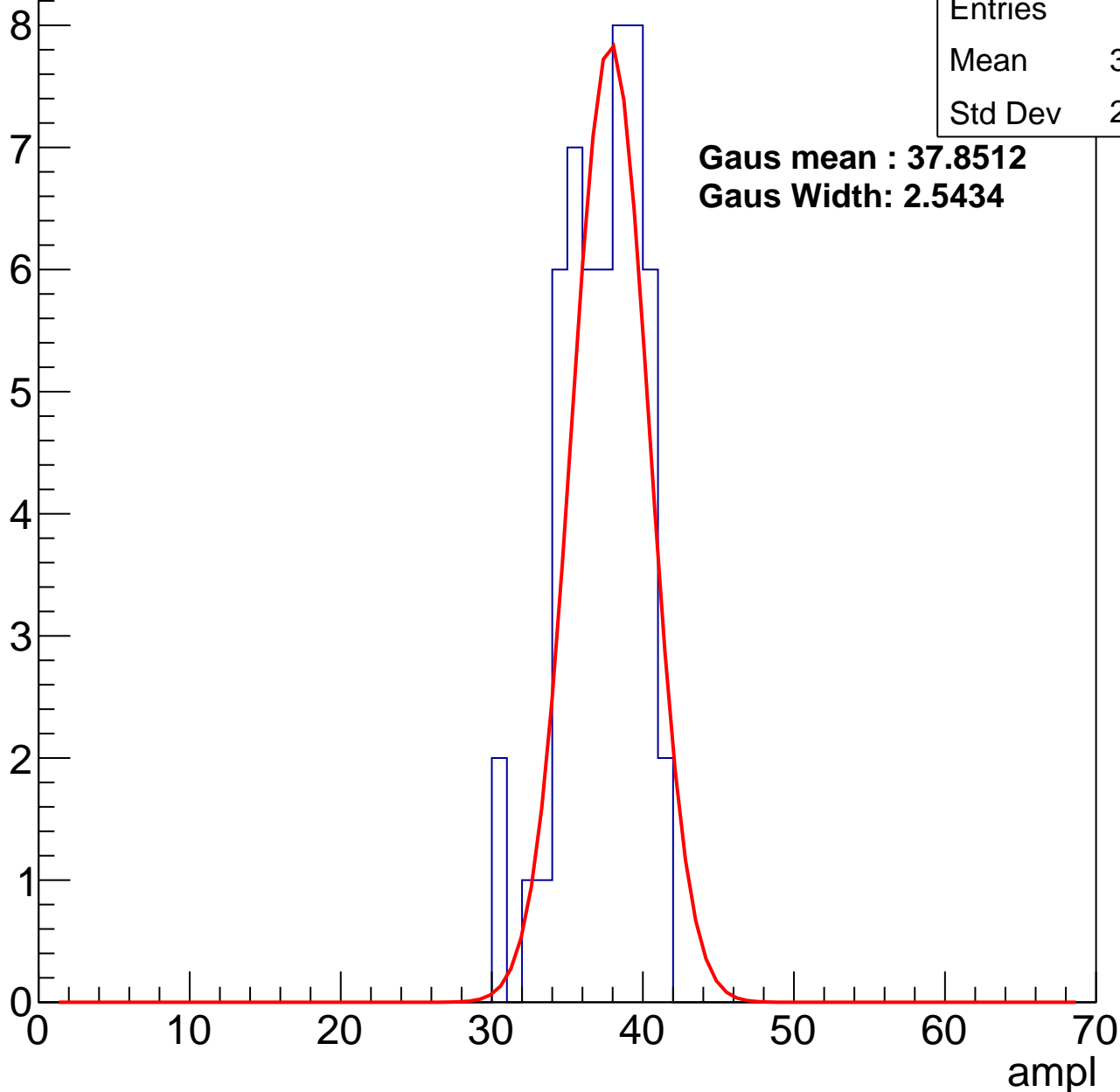
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	36.79
Std Dev	2.572

**Gaus mean : 37.8512**

**Gaus Width: 2.5434**



# B1L000S, U23-ch44, adc2

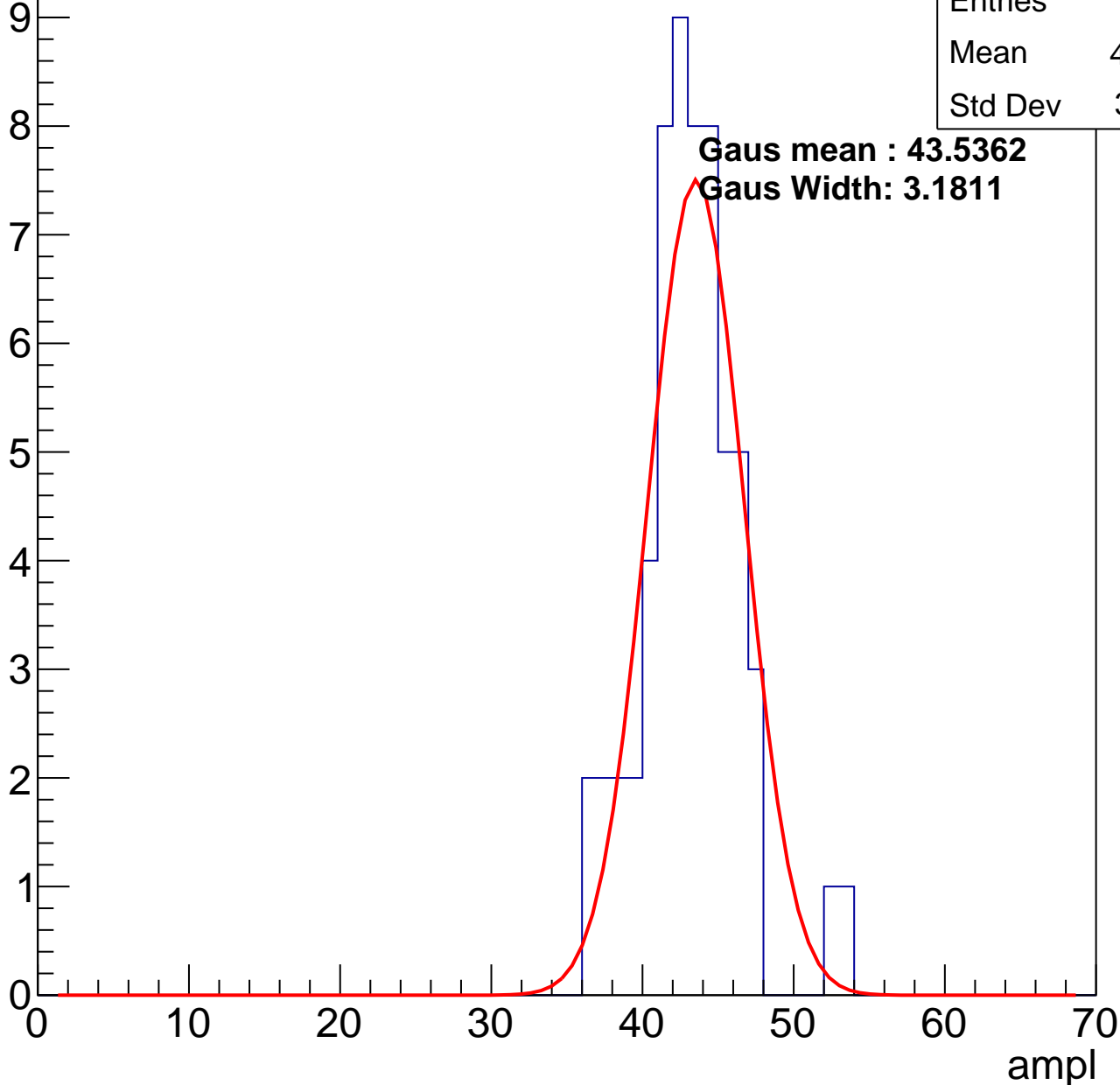
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.72
Std Dev	3.231

**Gaus mean : 43.5362**

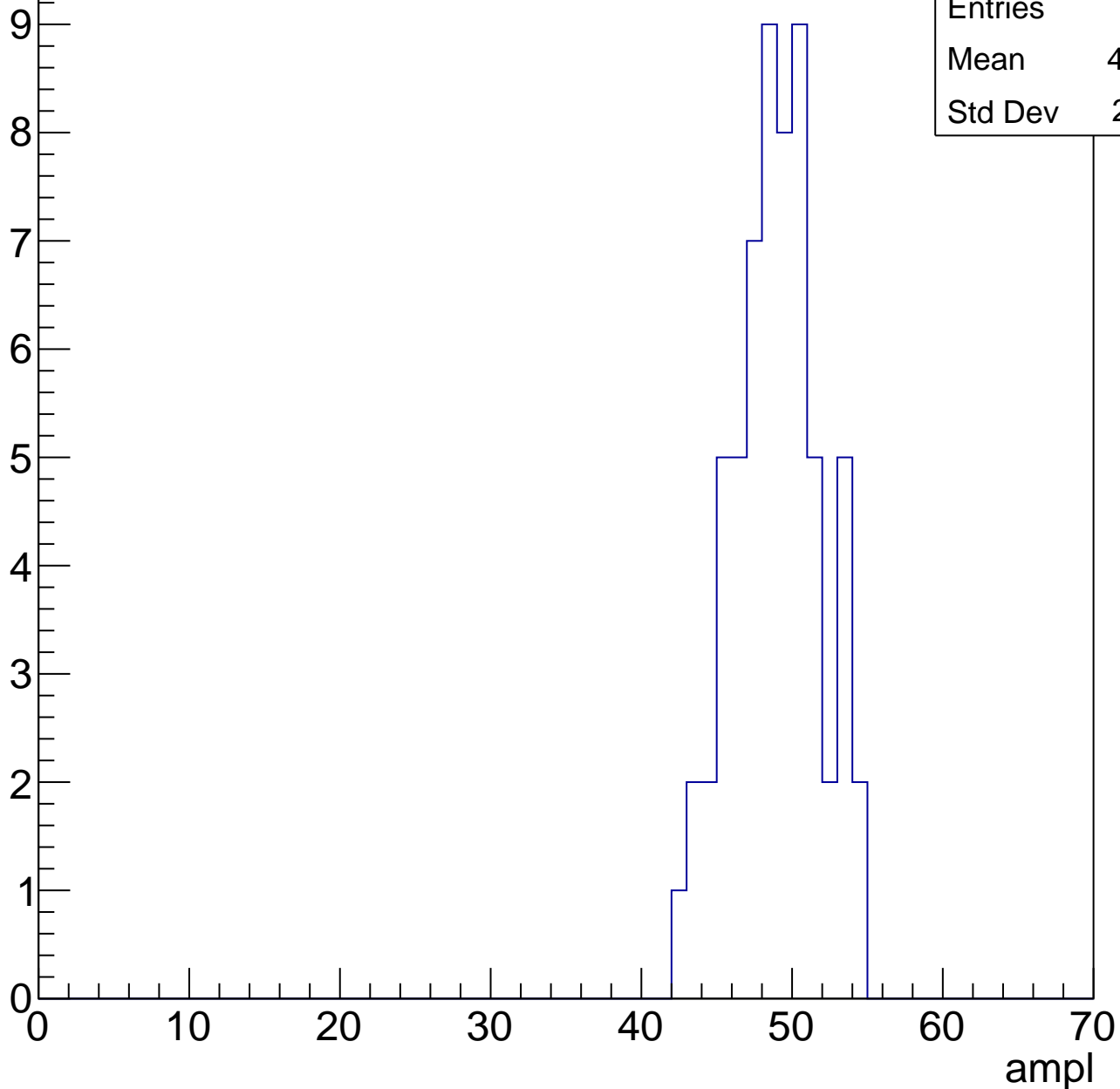
**Gaus Width: 3.1811**



# B1L000S, U23-ch44, adc3

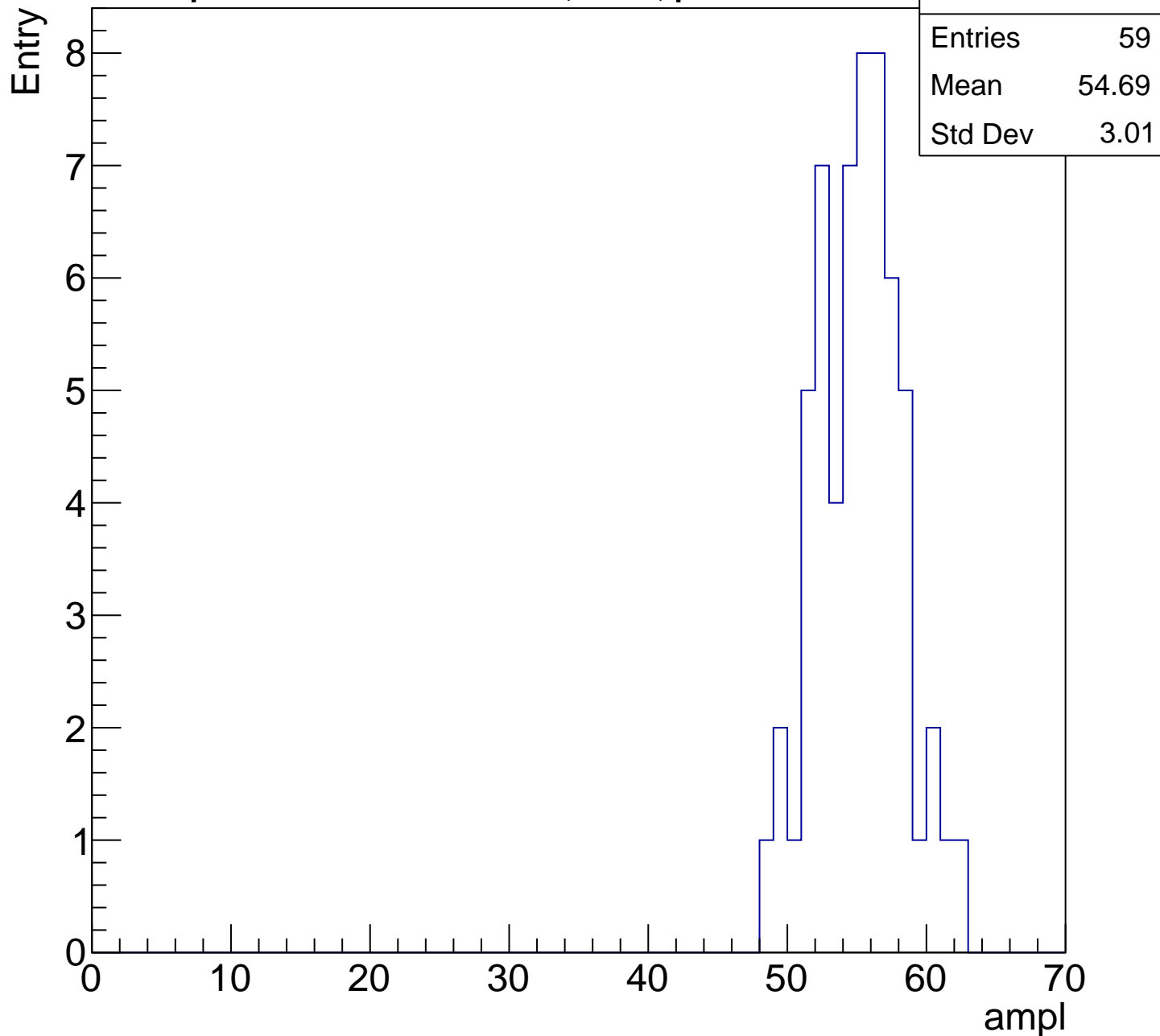
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch44, adc4

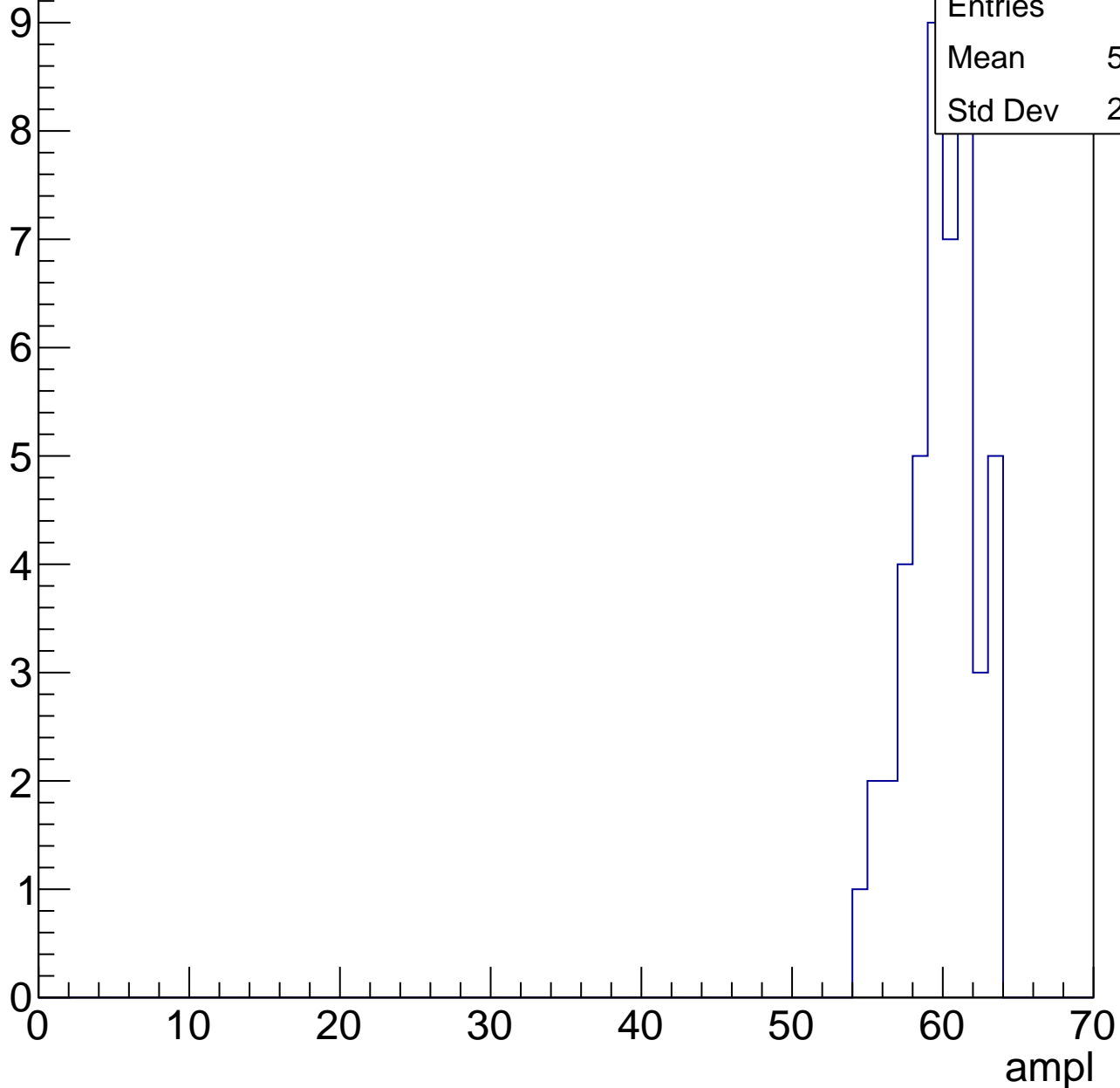
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

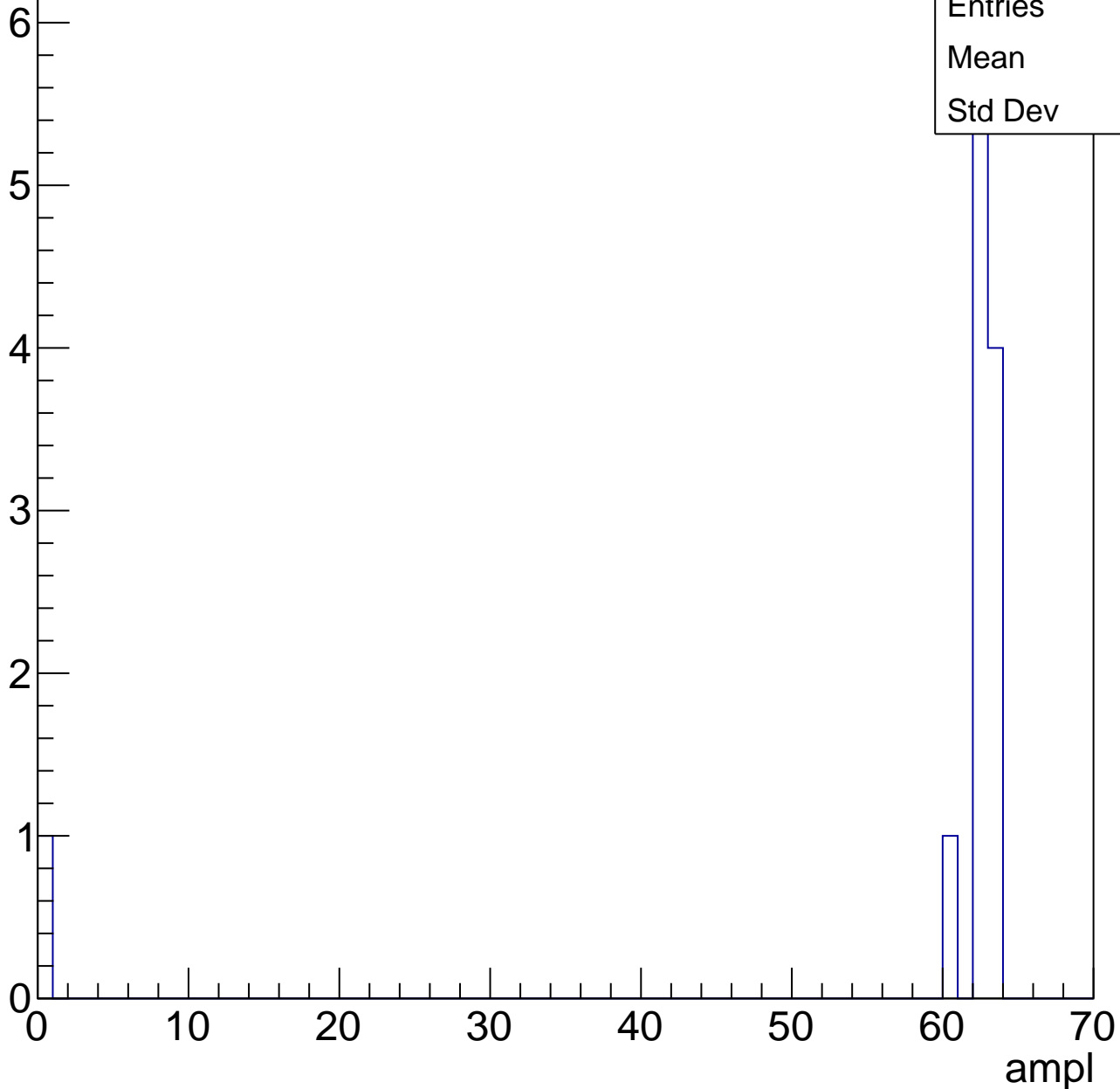


# B1L000S, U23-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	57
Std Dev	17.2





# B1L000S, U23-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	43
Std Dev	20

ampl

# B1L000S, U23-ch45, adc0

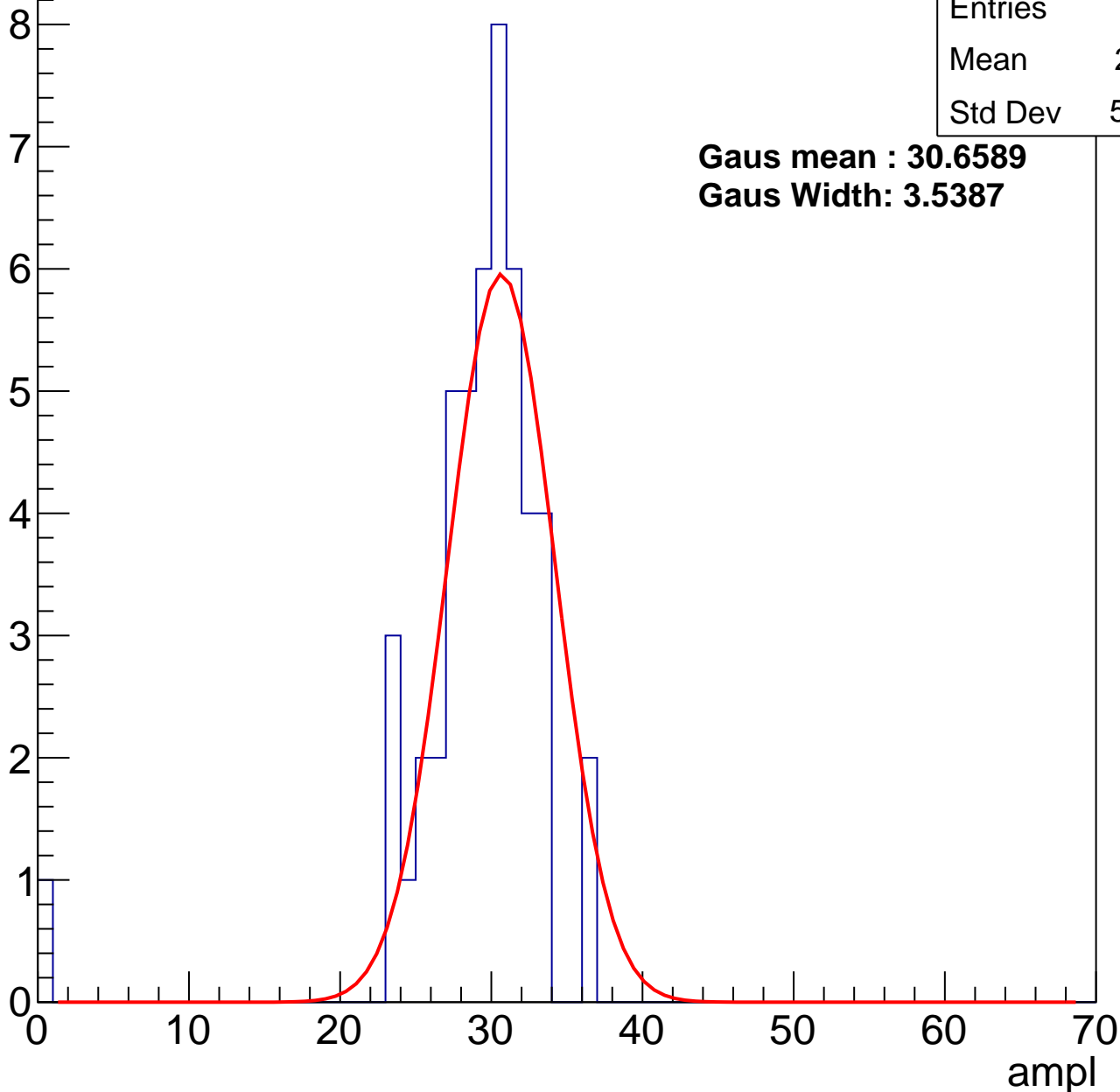
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	28.61
Std Dev	5.094

**Gaus mean : 30.6589**

**Gaus Width: 3.5387**



# B1L000S, U23-ch45, adc1

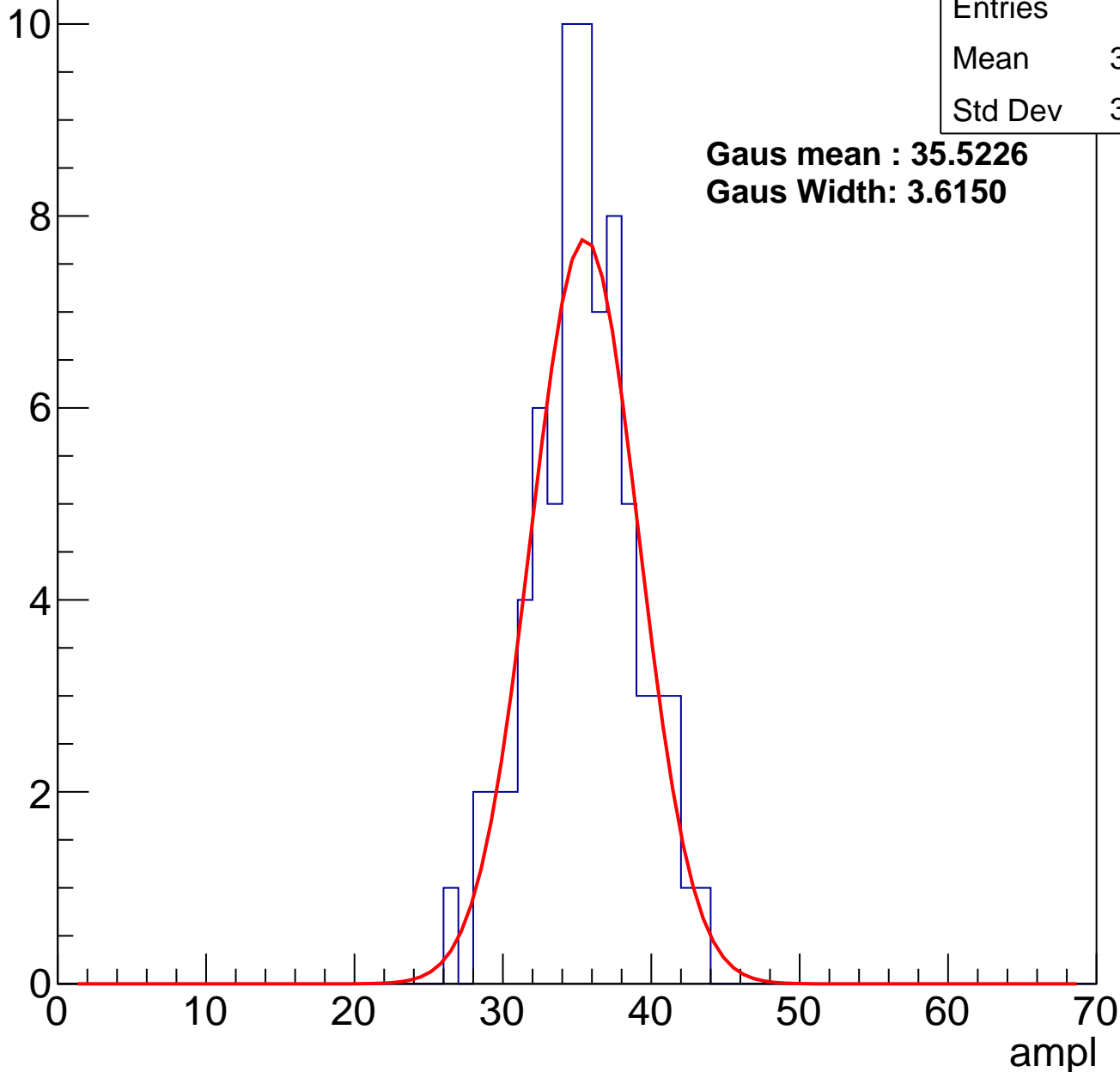
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	34.99
Std Dev	3.466

**Gaus mean : 35.5226**

**Gaus Width: 3.6150**

Entry



# B1L000S, U23-ch45, adc2

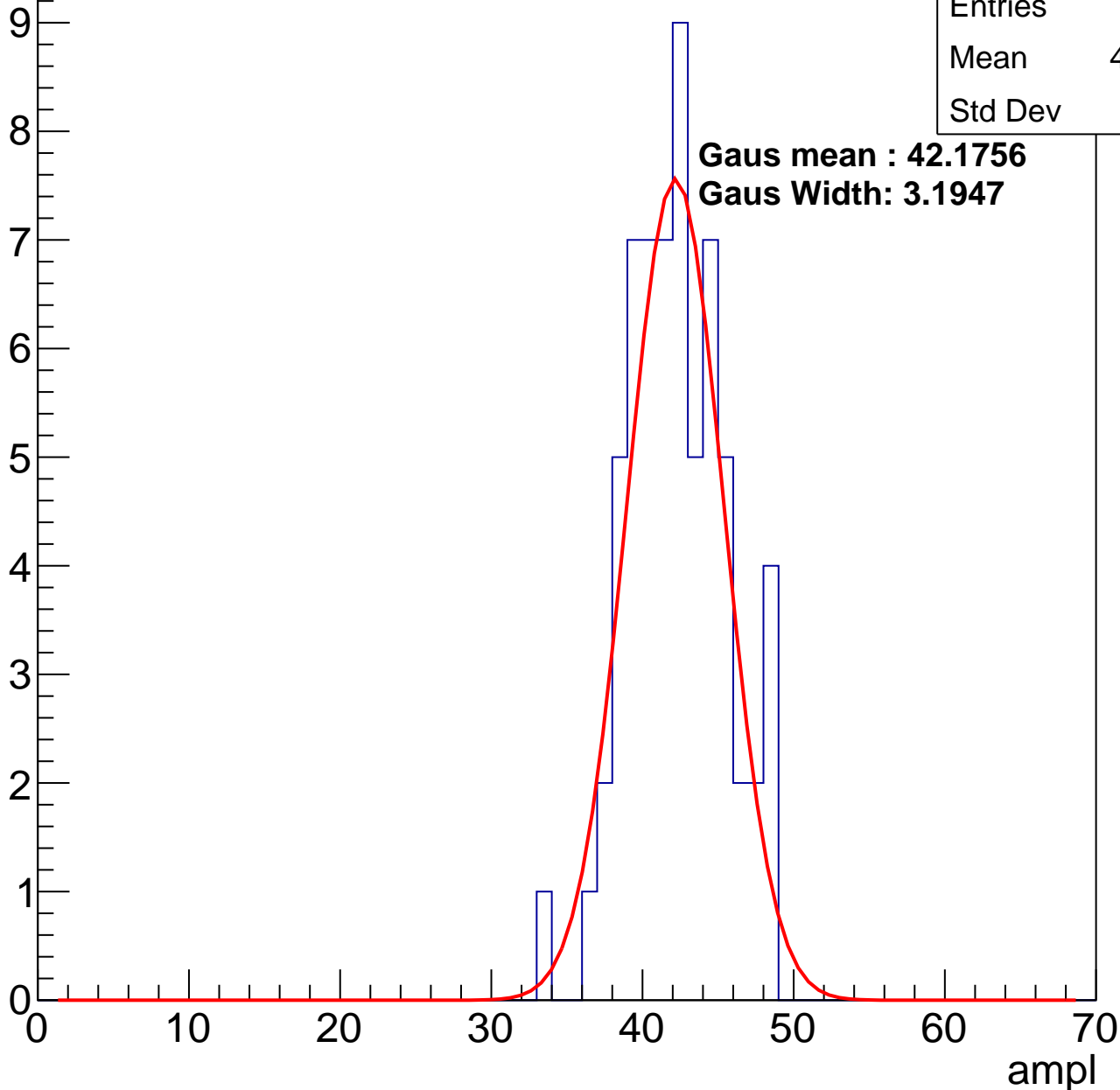
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.83
Std Dev	3.18

**Gaus mean : 42.1756**

**Gaus Width: 3.1947**



# B1L000S, U23-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	48.68
Std Dev	3.24

Entry

10

8

6

4

2

0

0

10

20

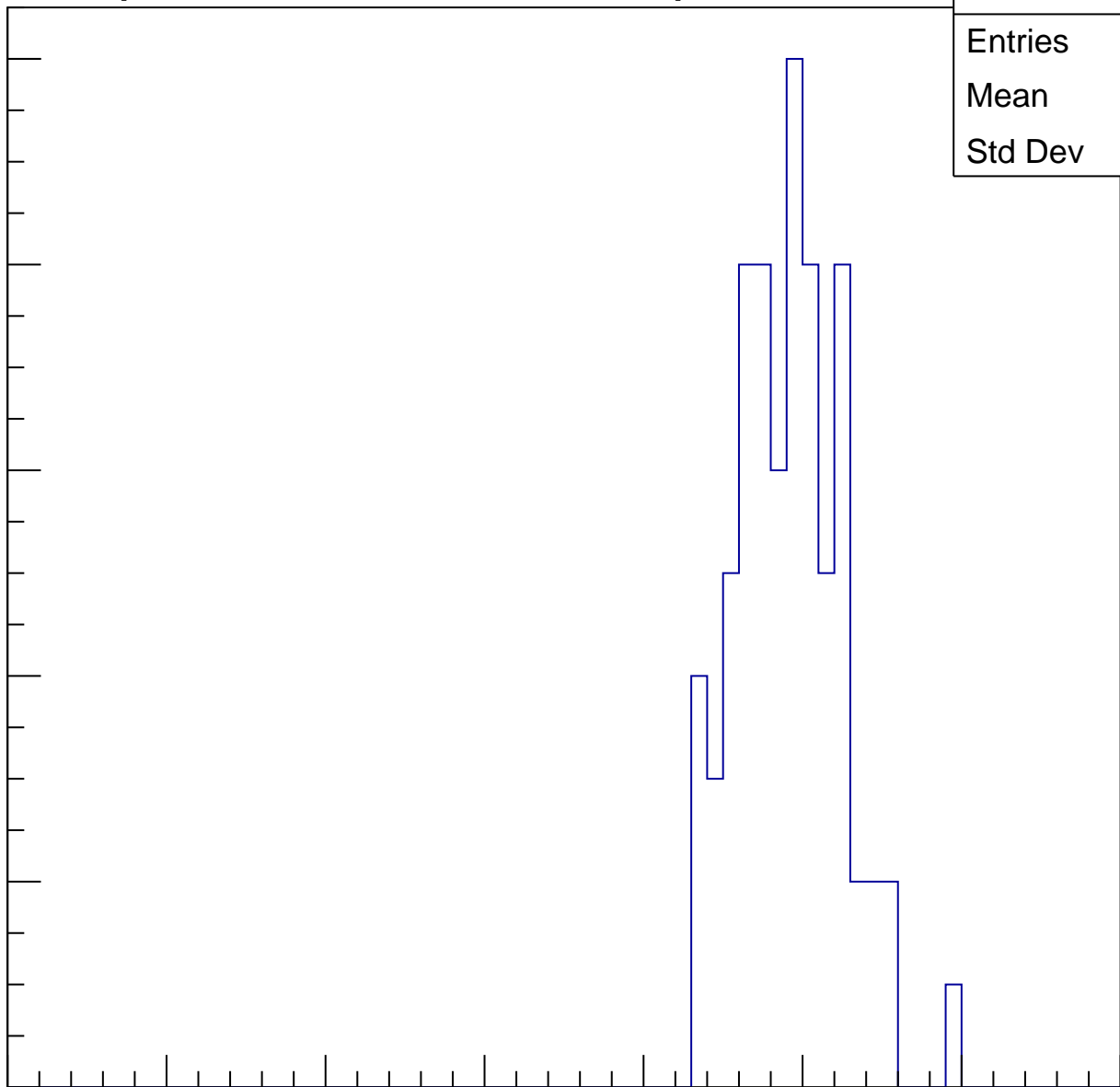
30

40

50

60

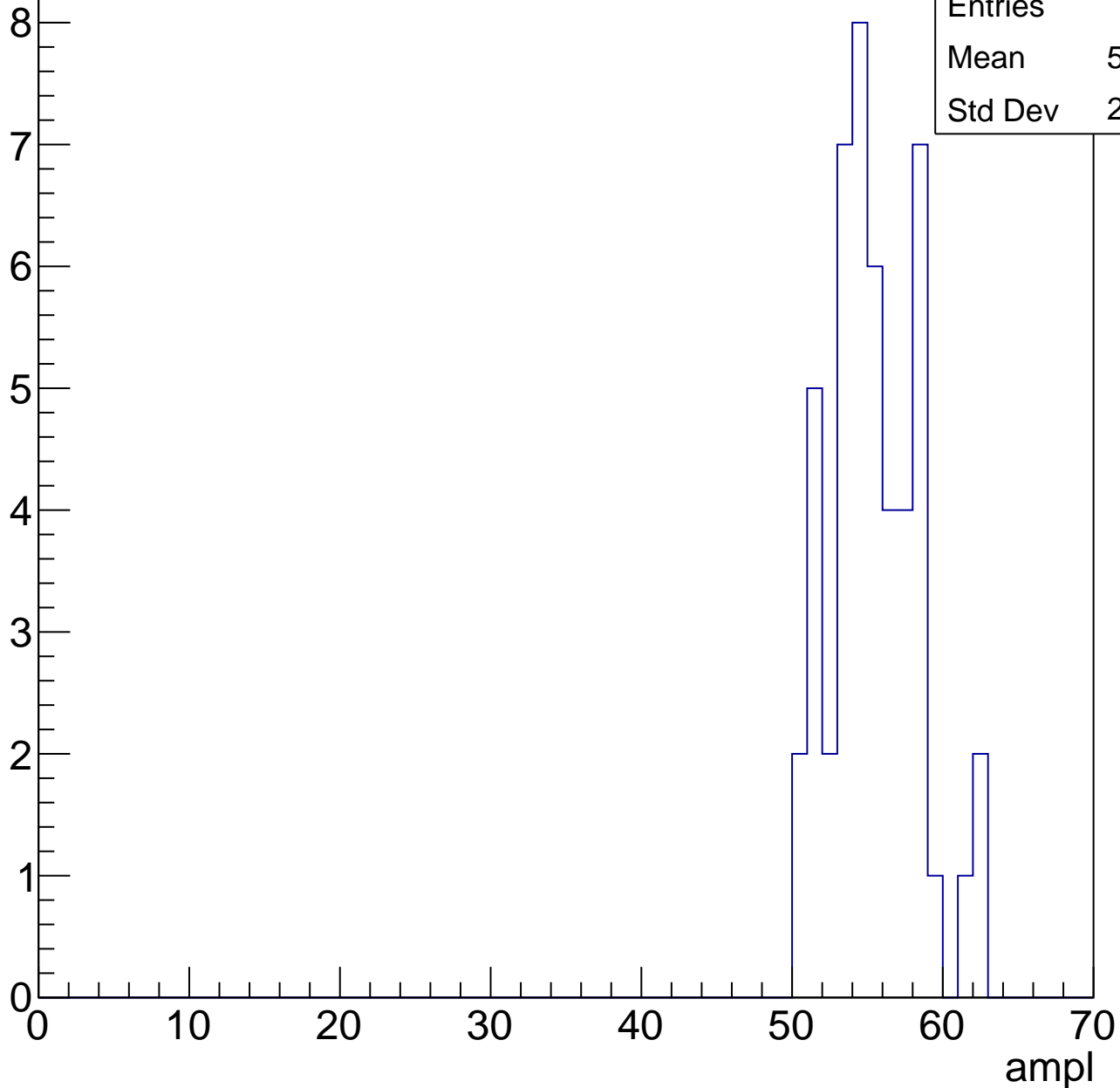
ampl



# B1L000S, U23-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

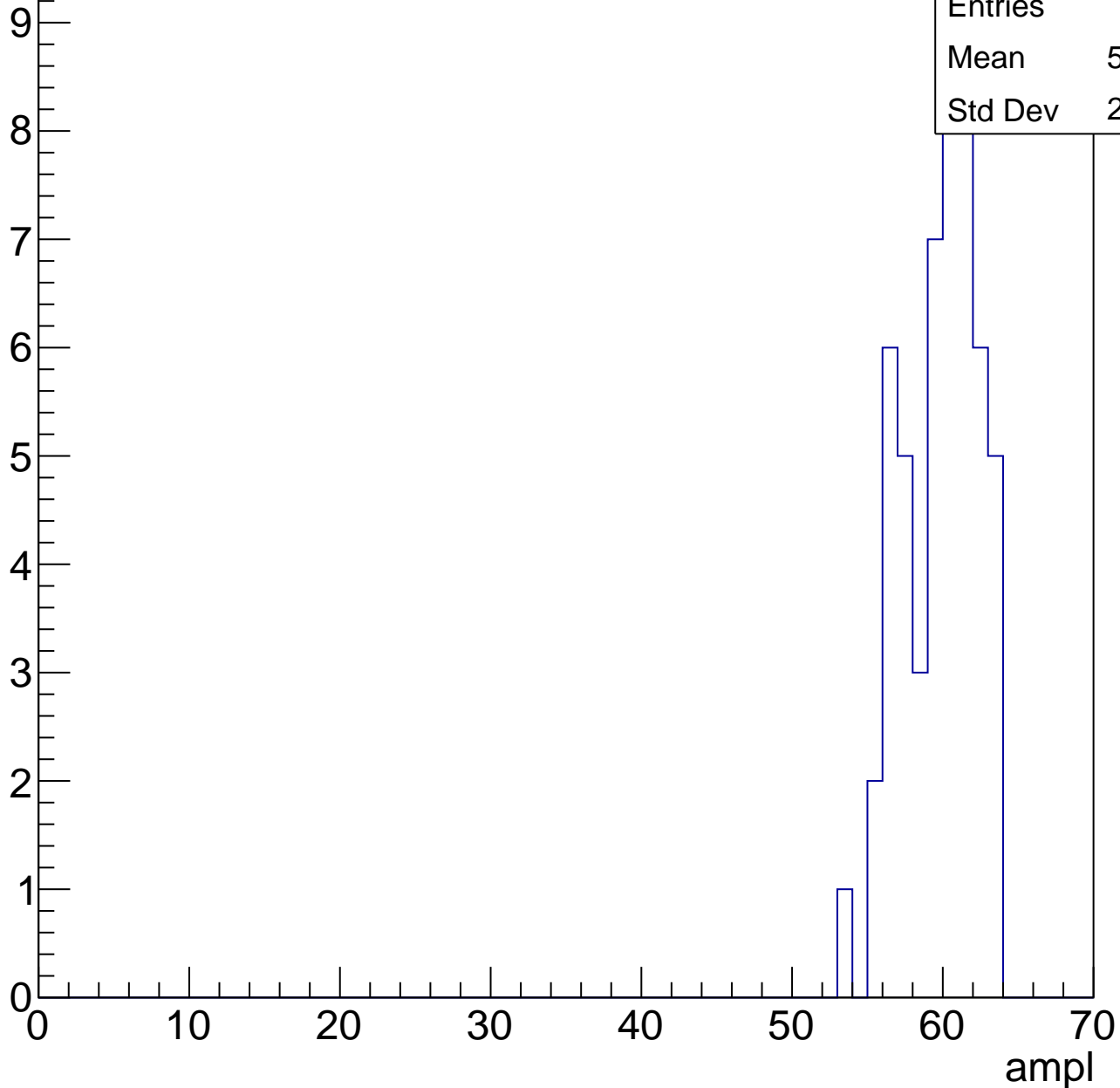
Entry



# B1L000S, U23-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

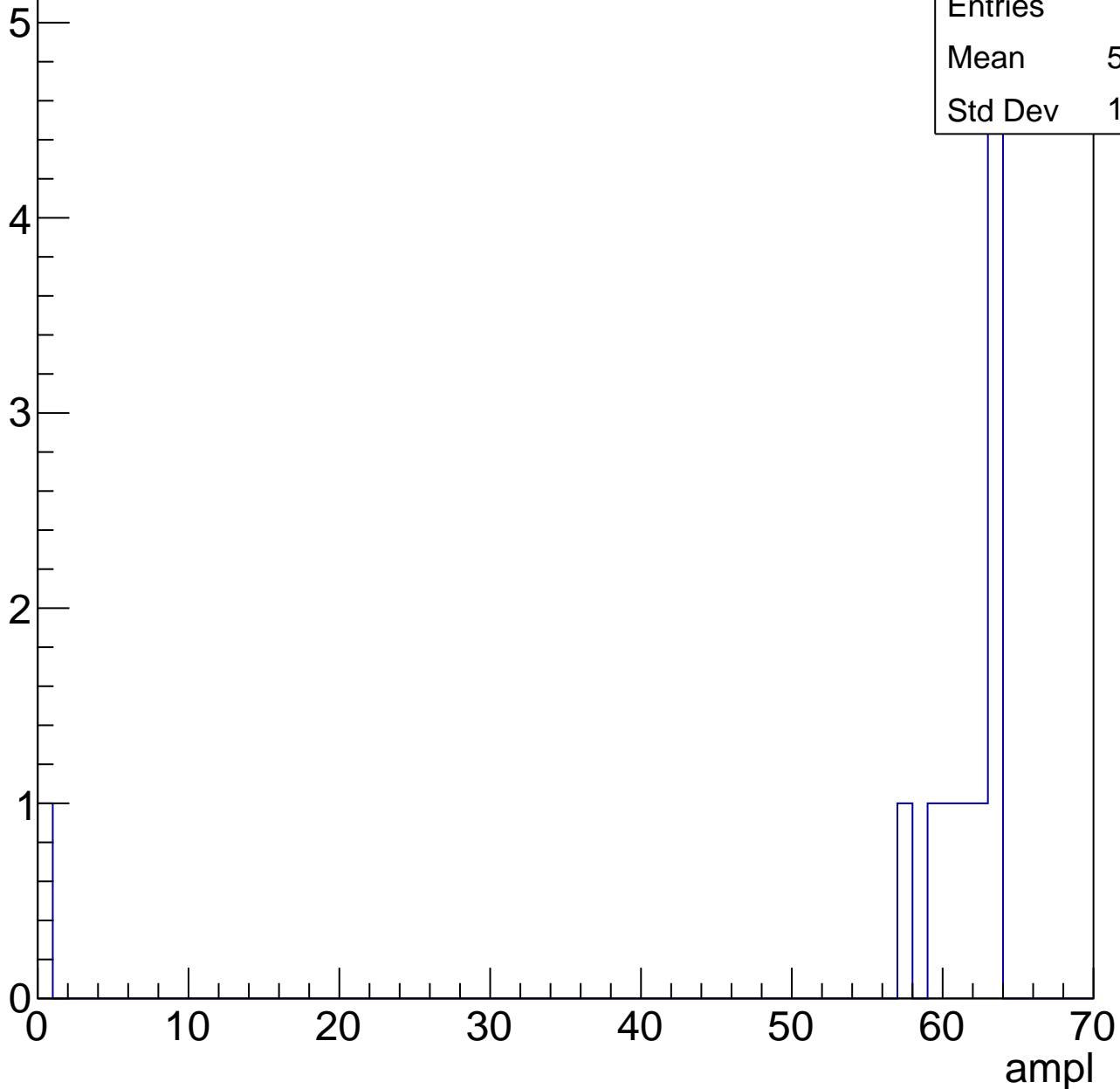


# B1L000S, U23-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	55.82
Std Dev	17.75

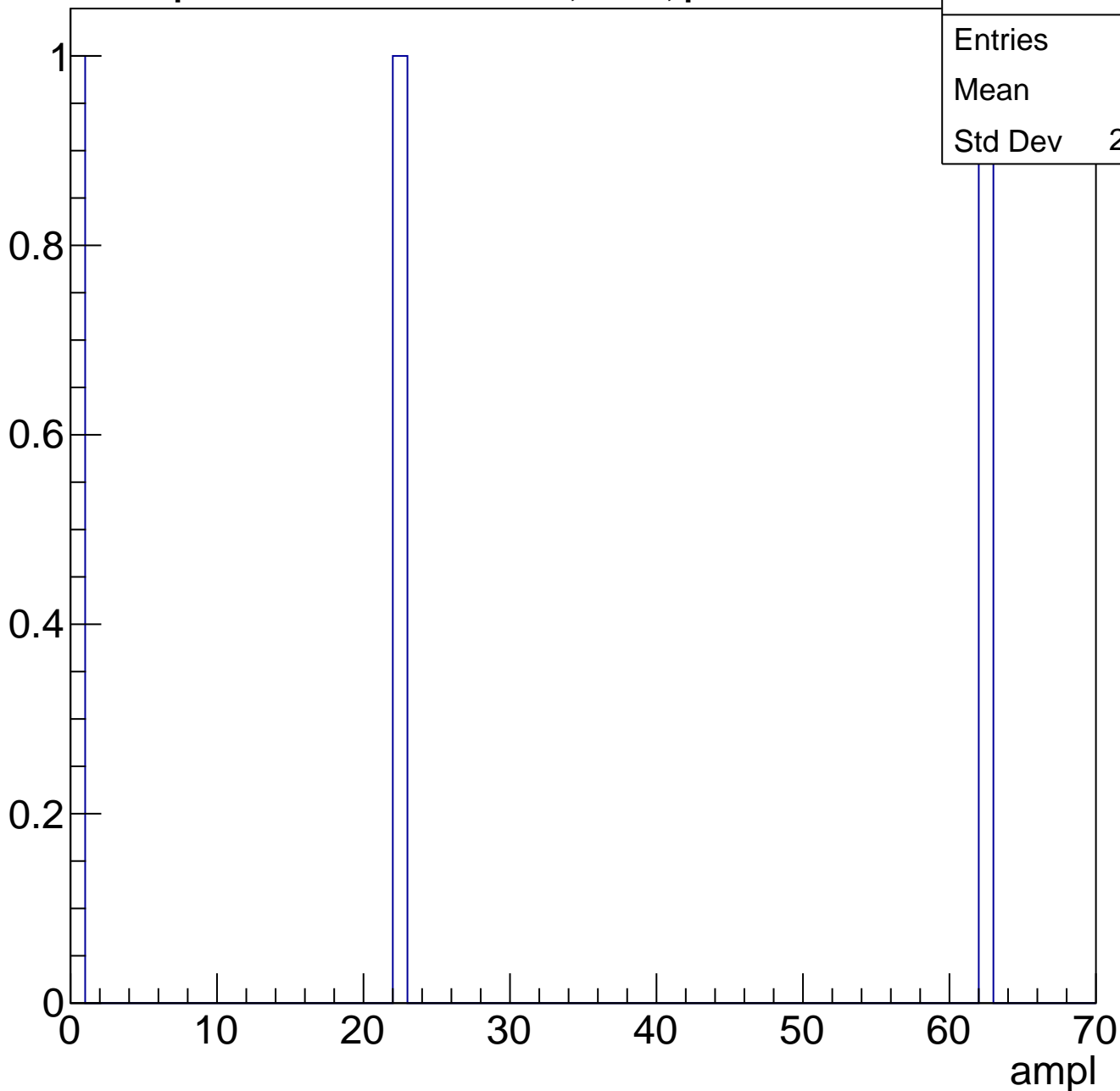




# B1L000S, U23-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28
Std Dev	25.66

# B1L000S, U23-ch46, adc0

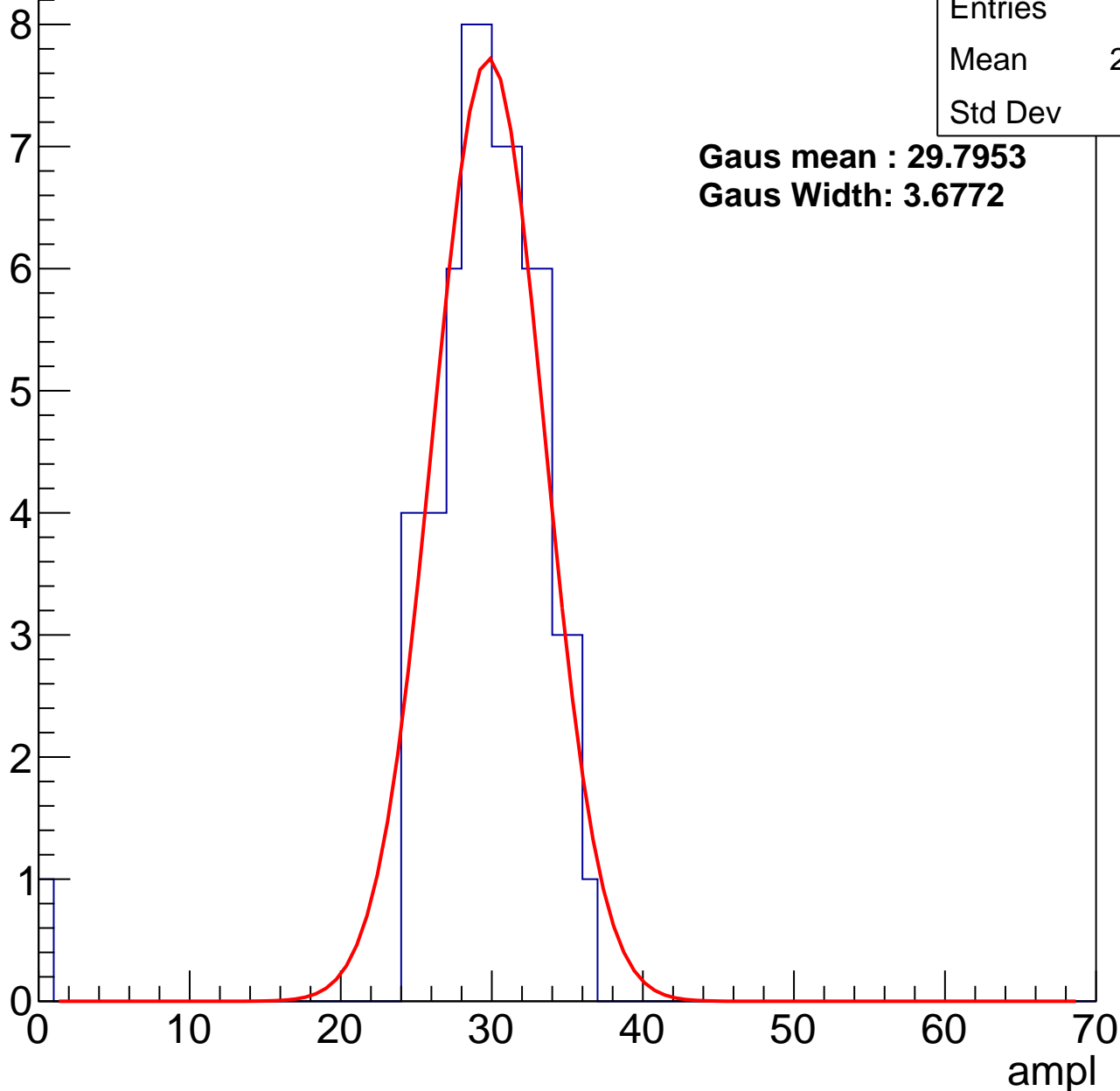
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.09
Std Dev	4.68

**Gaus mean : 29.7953**

**Gaus Width: 3.6772**



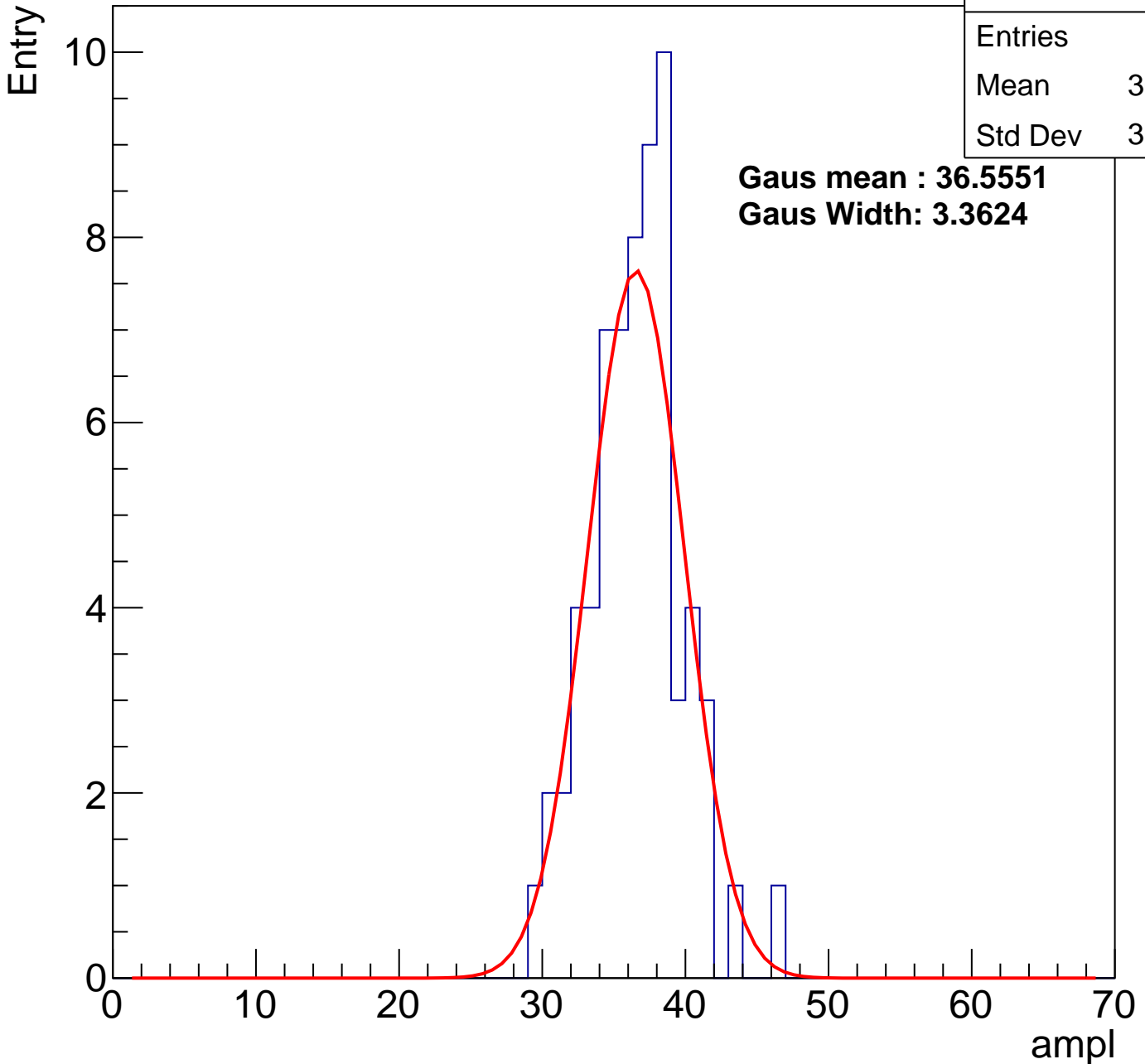
# B1L000S, U23-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	36.12
Std Dev	3.184

**Gaus mean : 36.5551**

**Gaus Width: 3.3624**



# B1L000S, U23-ch46, adc2

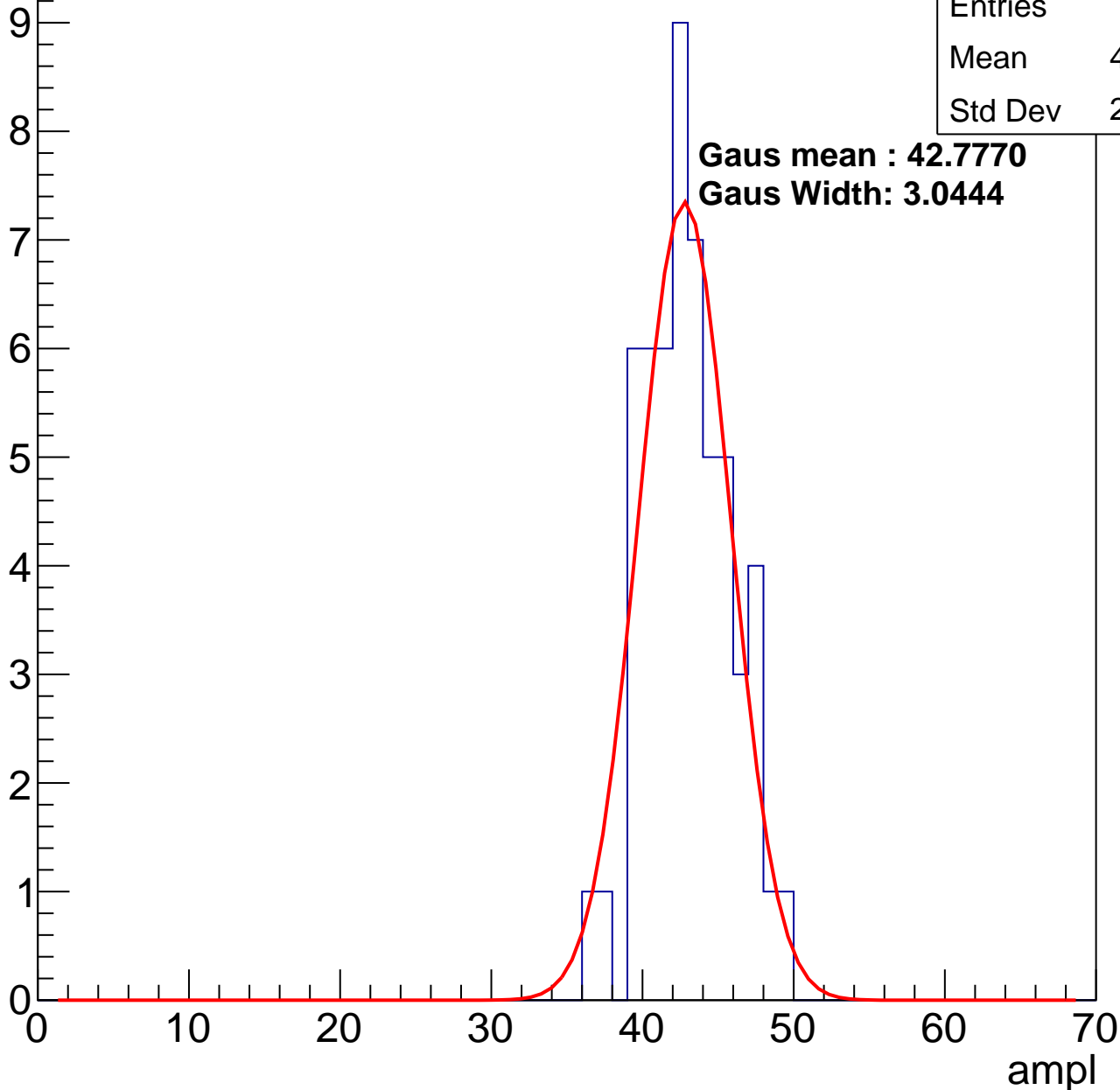
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.55
Std Dev	2.808

**Gaus mean : 42.7770**

**Gaus Width: 3.0444**

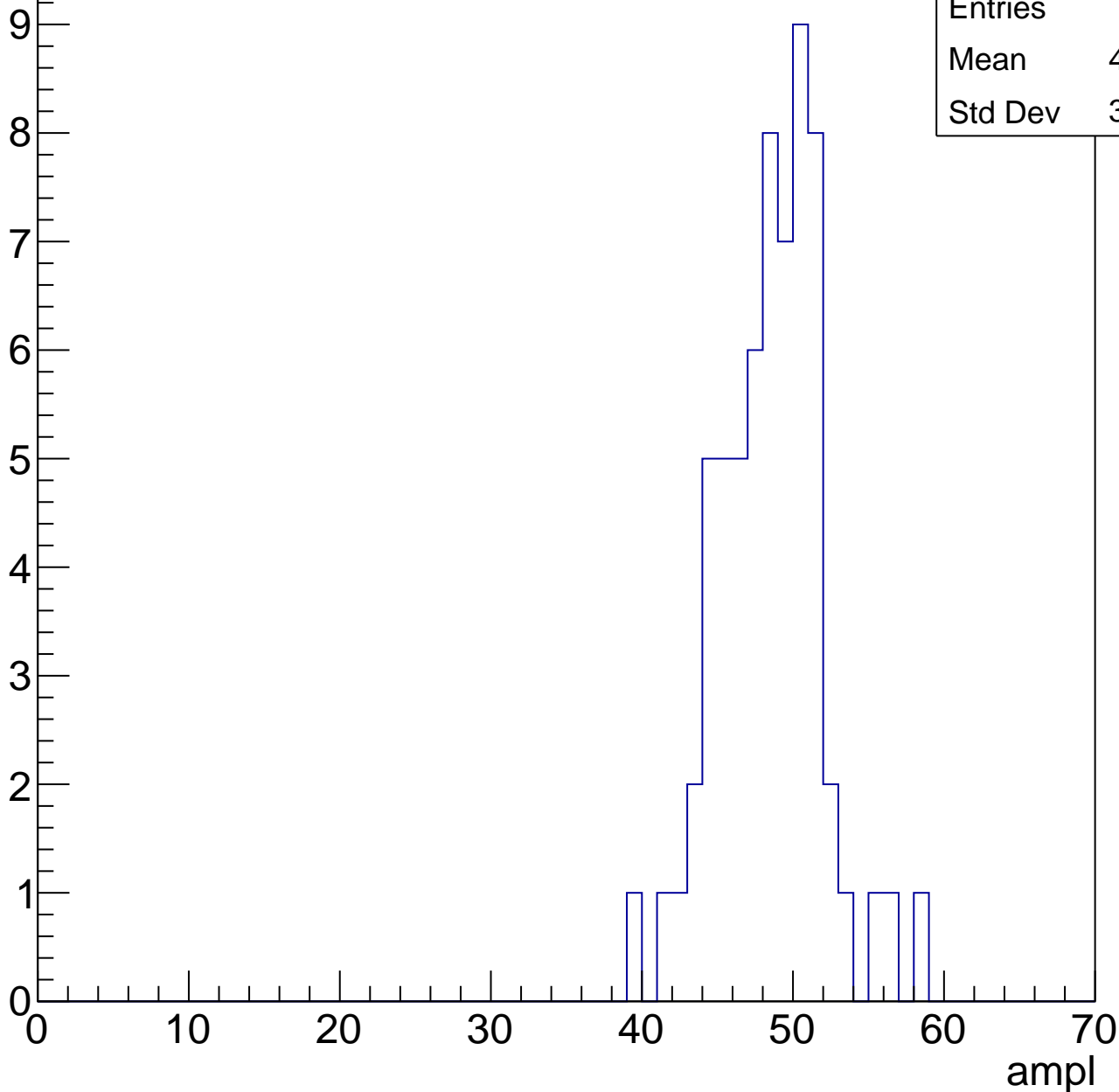


# B1L000S, U23-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.06
Std Dev	3.427

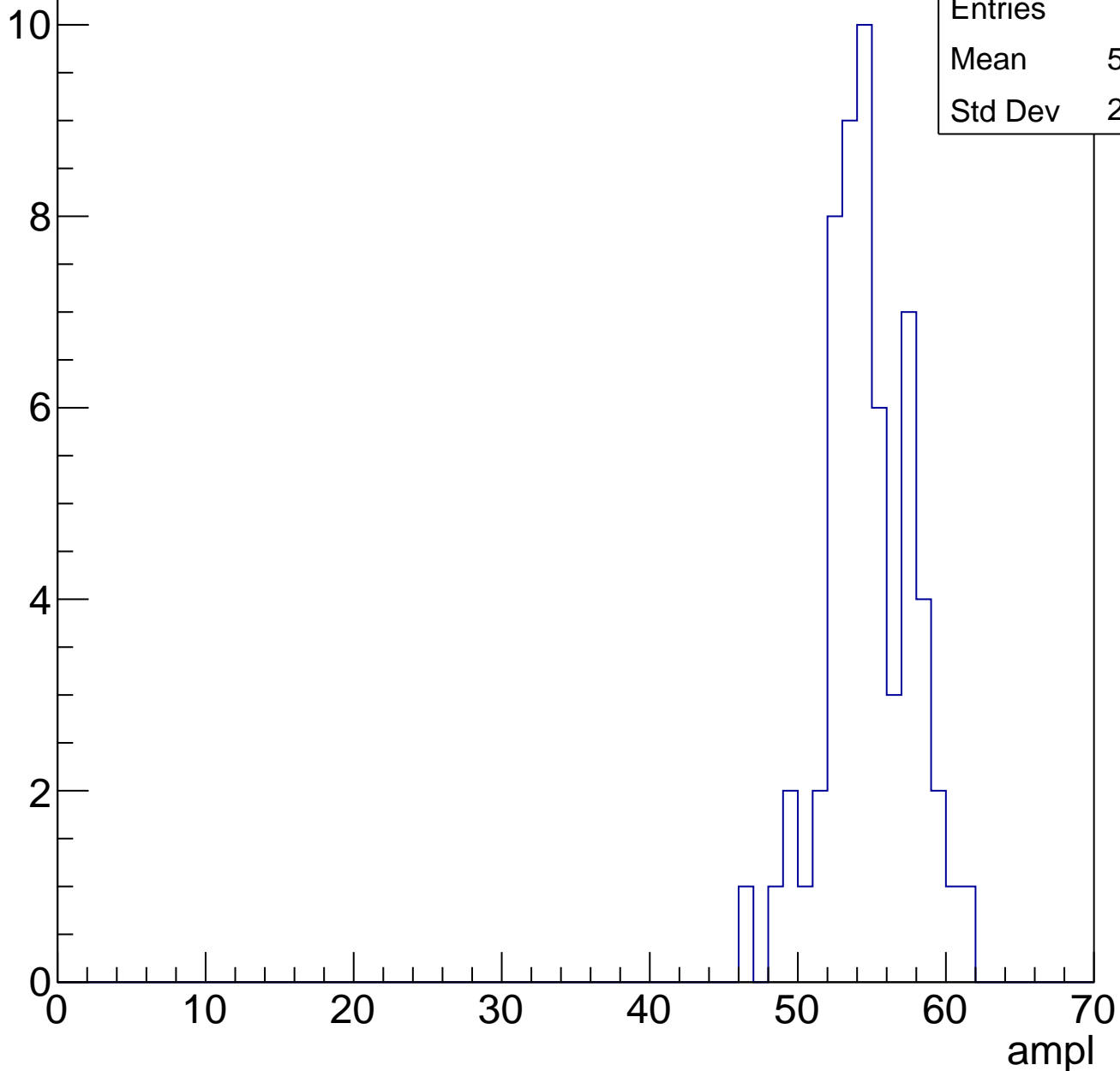


# B1L000S, U23-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	54.22
Std Dev	2.942

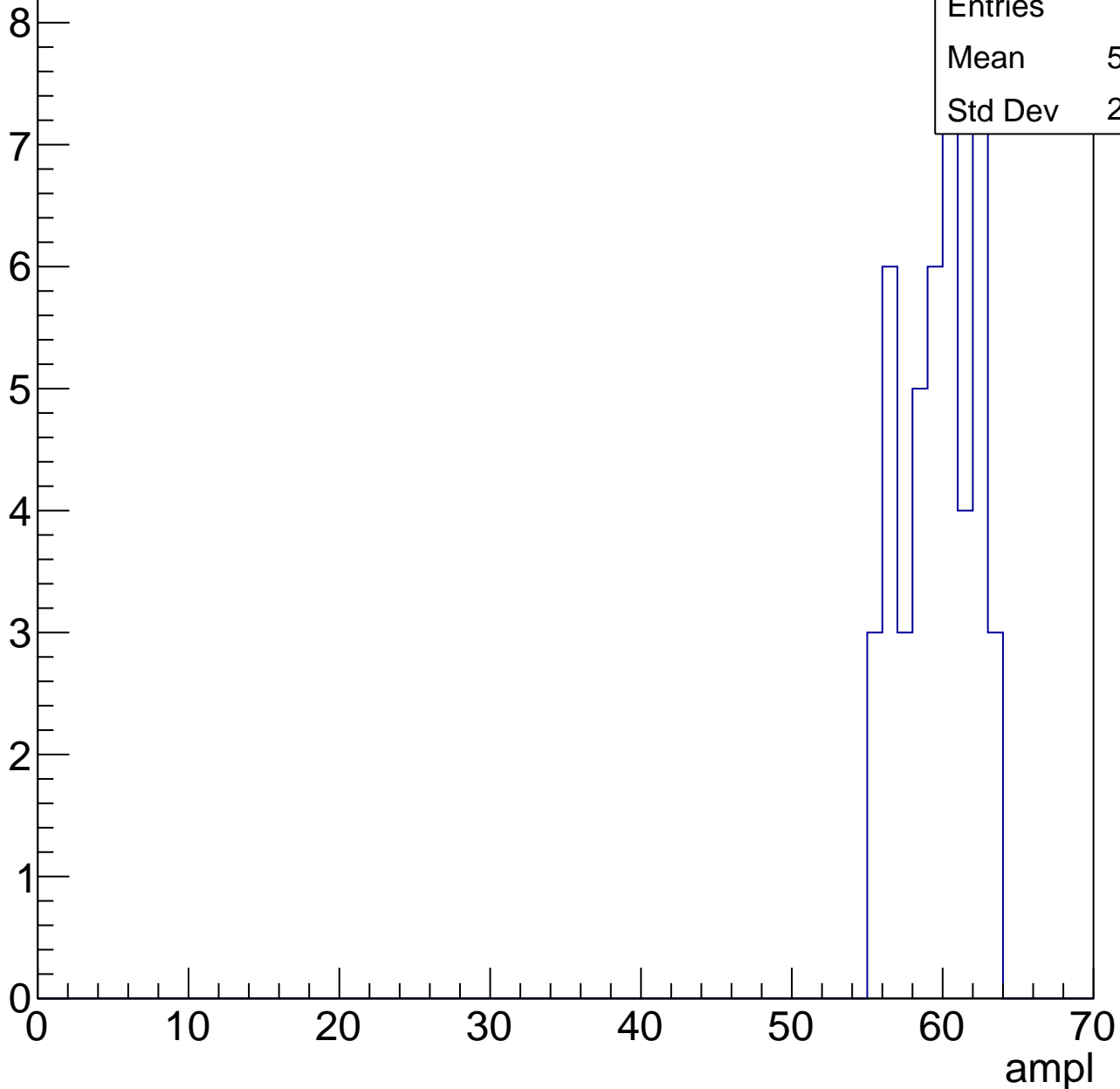


# B1L000S, U23-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	59.24
Std Dev	2.379

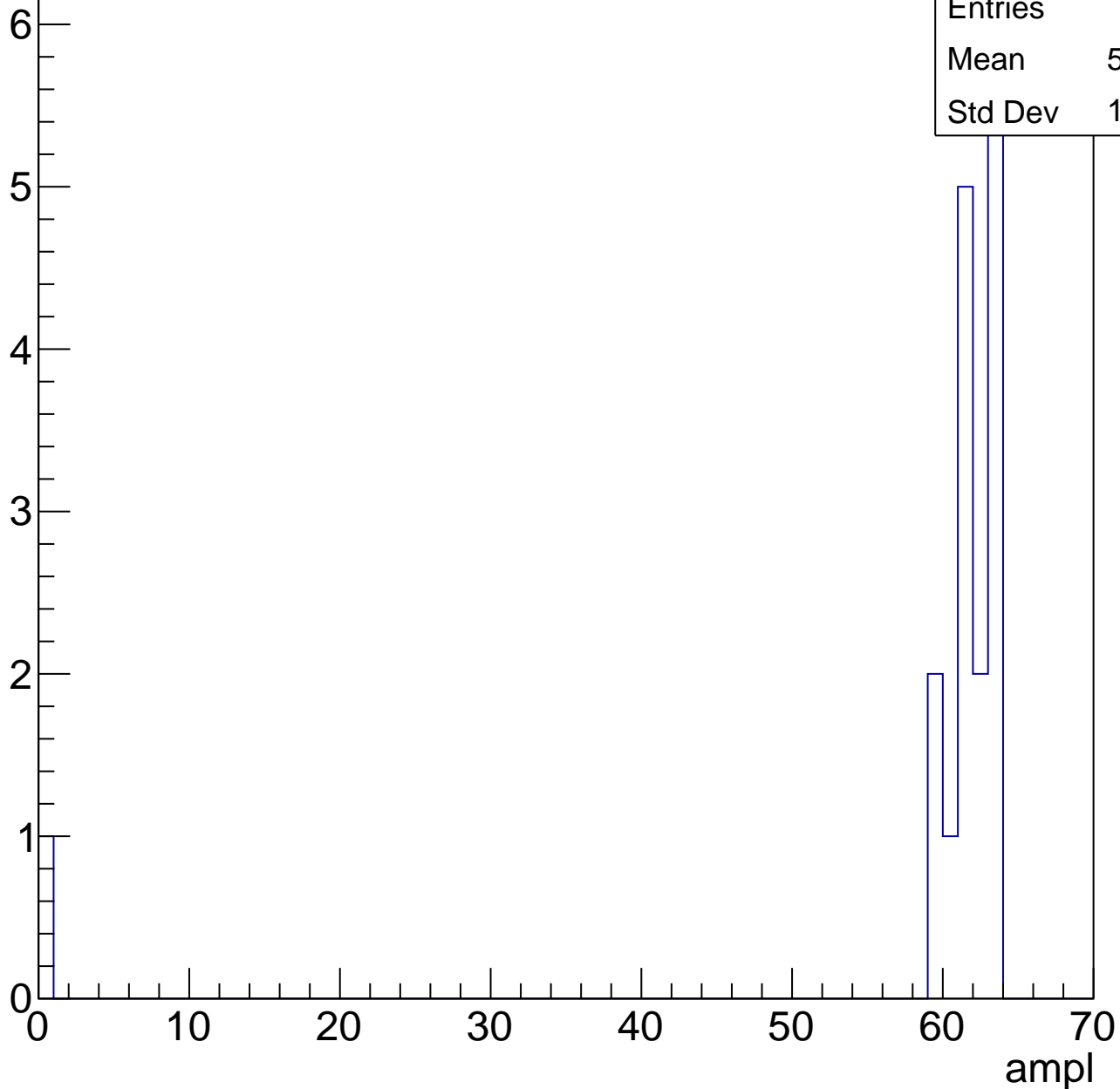


# B1L000S, U23-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.94
Std Dev	14.55

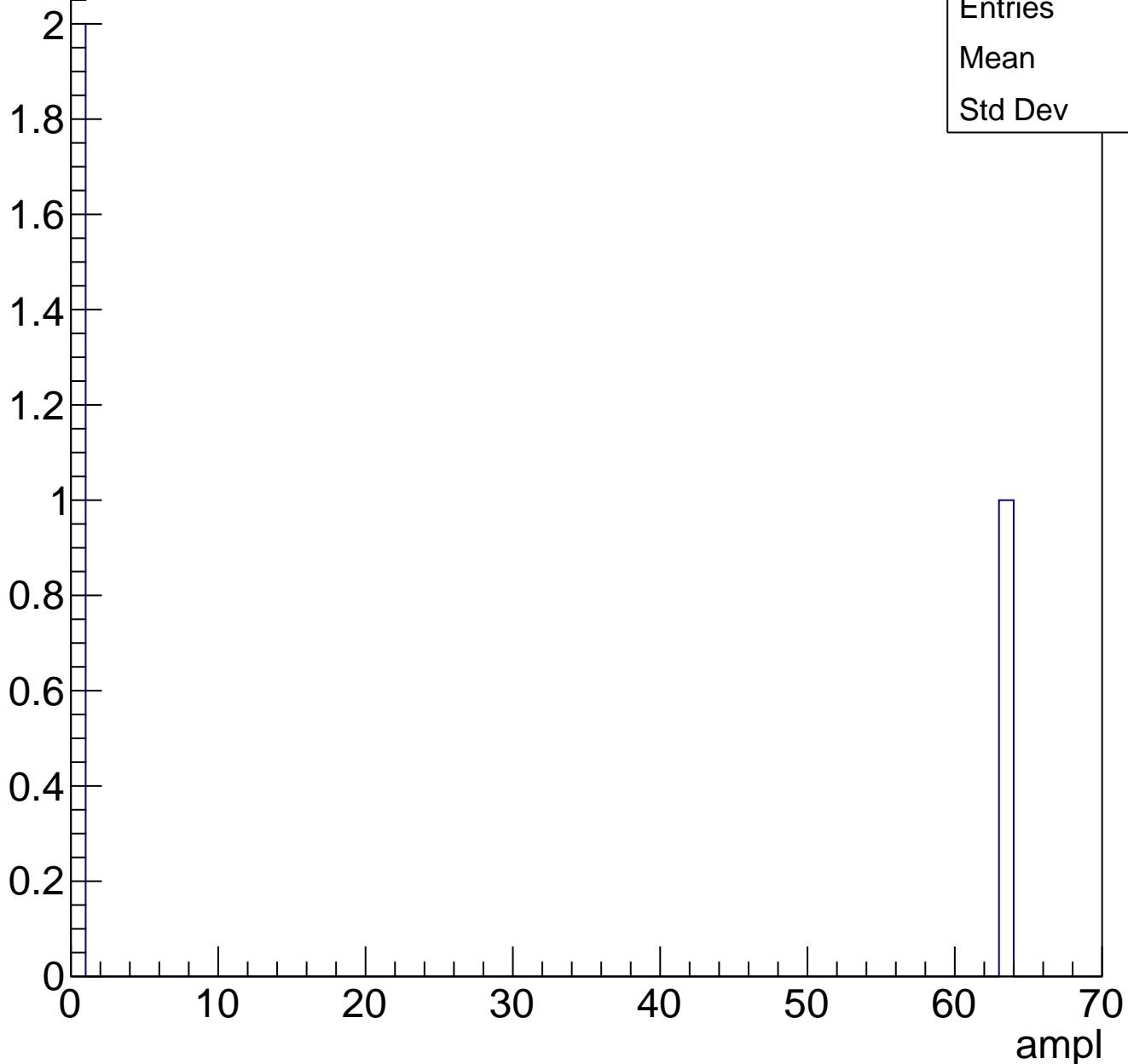




# B1L000S, U23-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U23-ch47, adc0

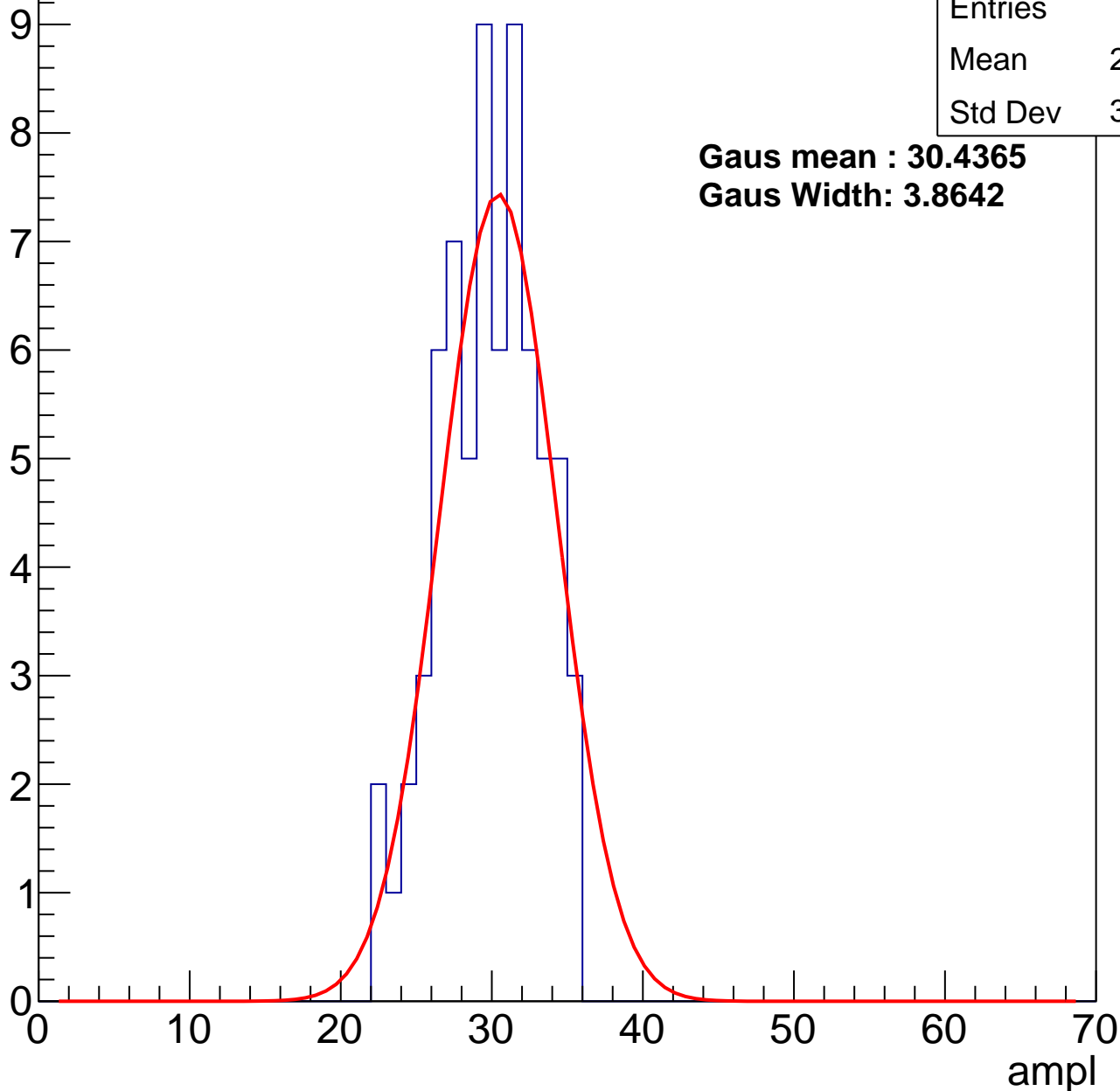
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.38
Std Dev	3.226

**Gaus mean : 30.4365**

**Gaus Width: 3.8642**



# B1L000S, U23-ch47, adc1

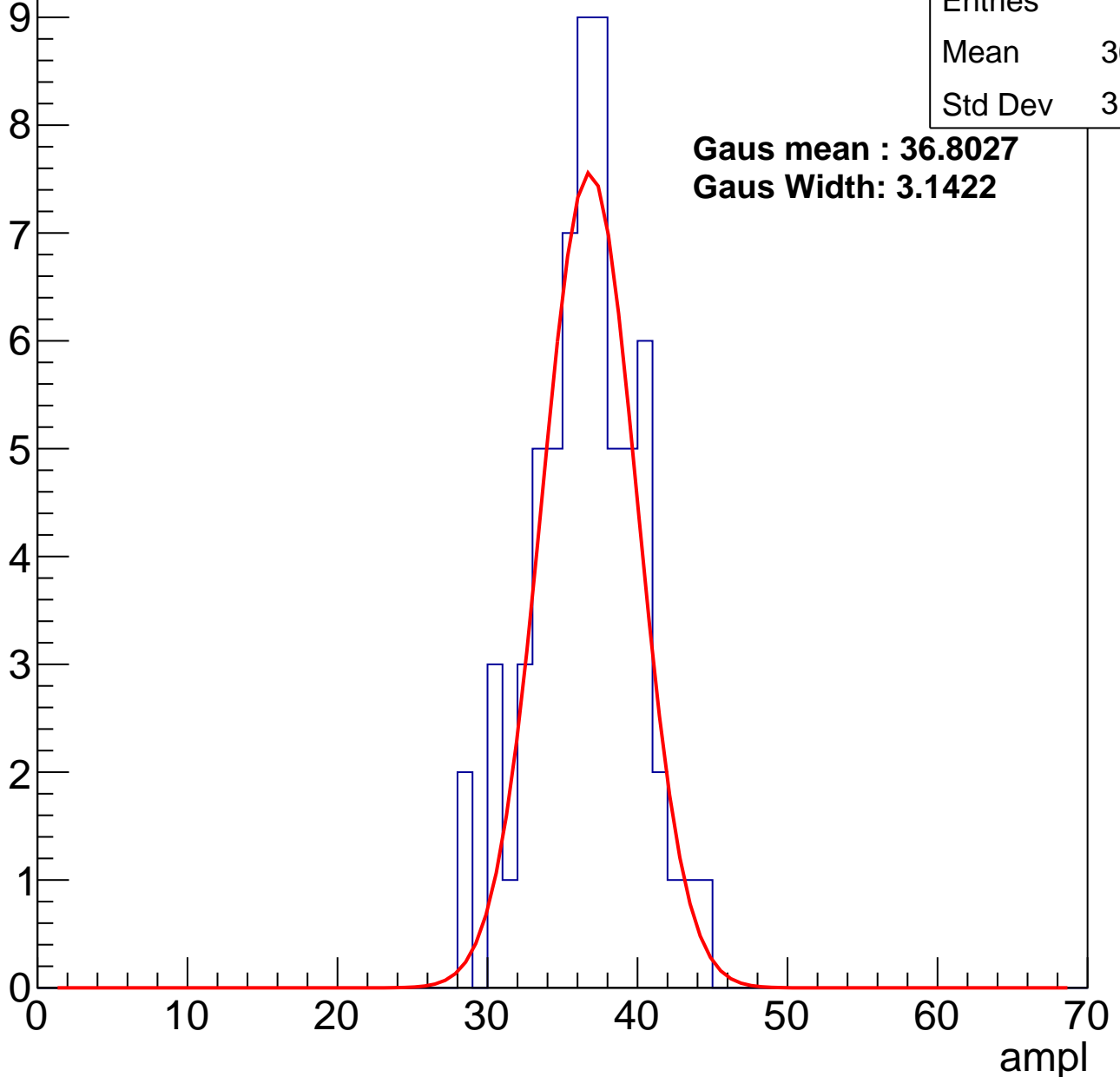
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	36.09
Std Dev	3.396

**Gaus mean : 36.8027**

**Gaus Width: 3.1422**



# B1L000S, U23-ch47, adc2

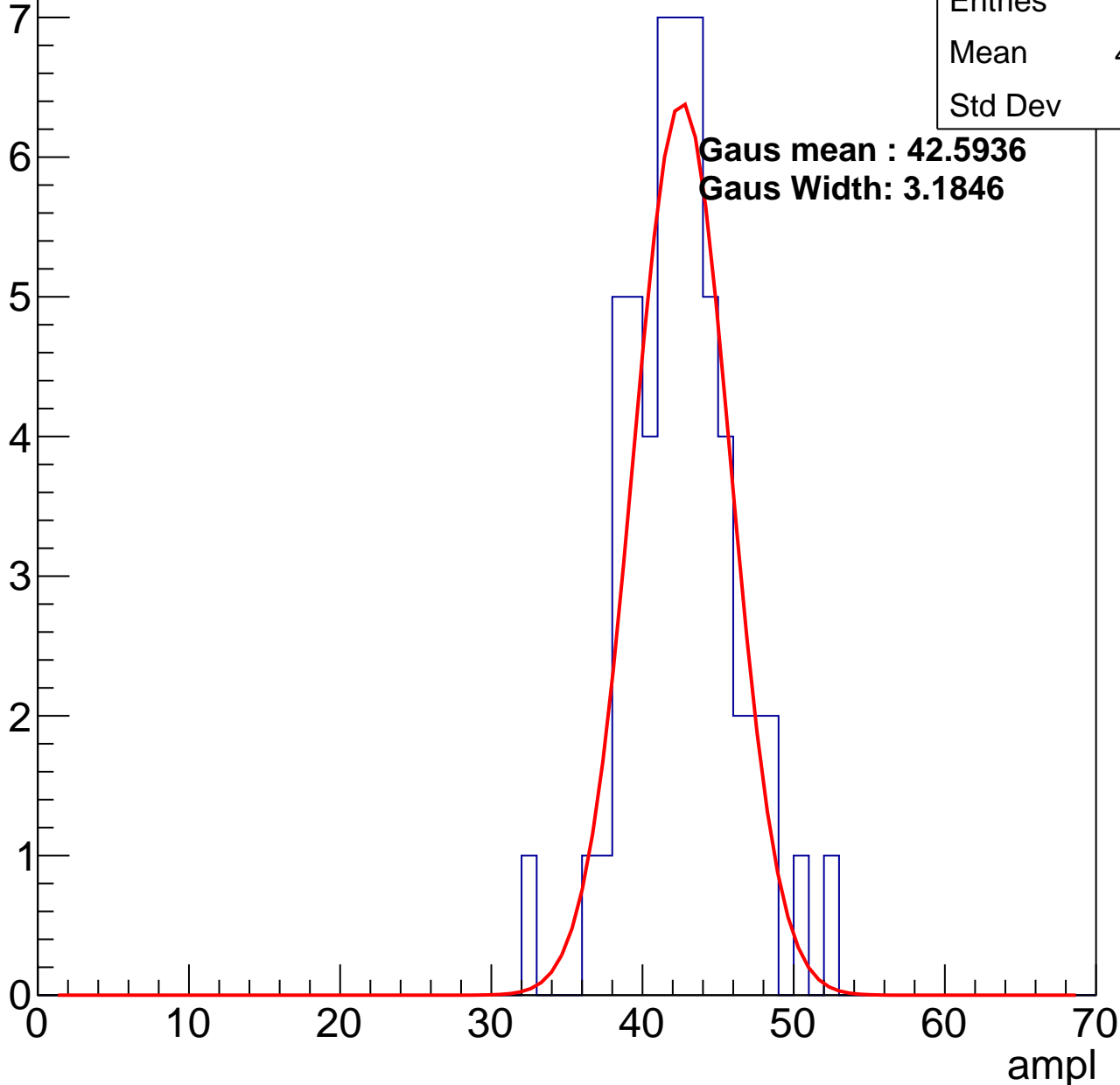
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.11
Std Dev	3.54

**Gaus mean : 42.5936**

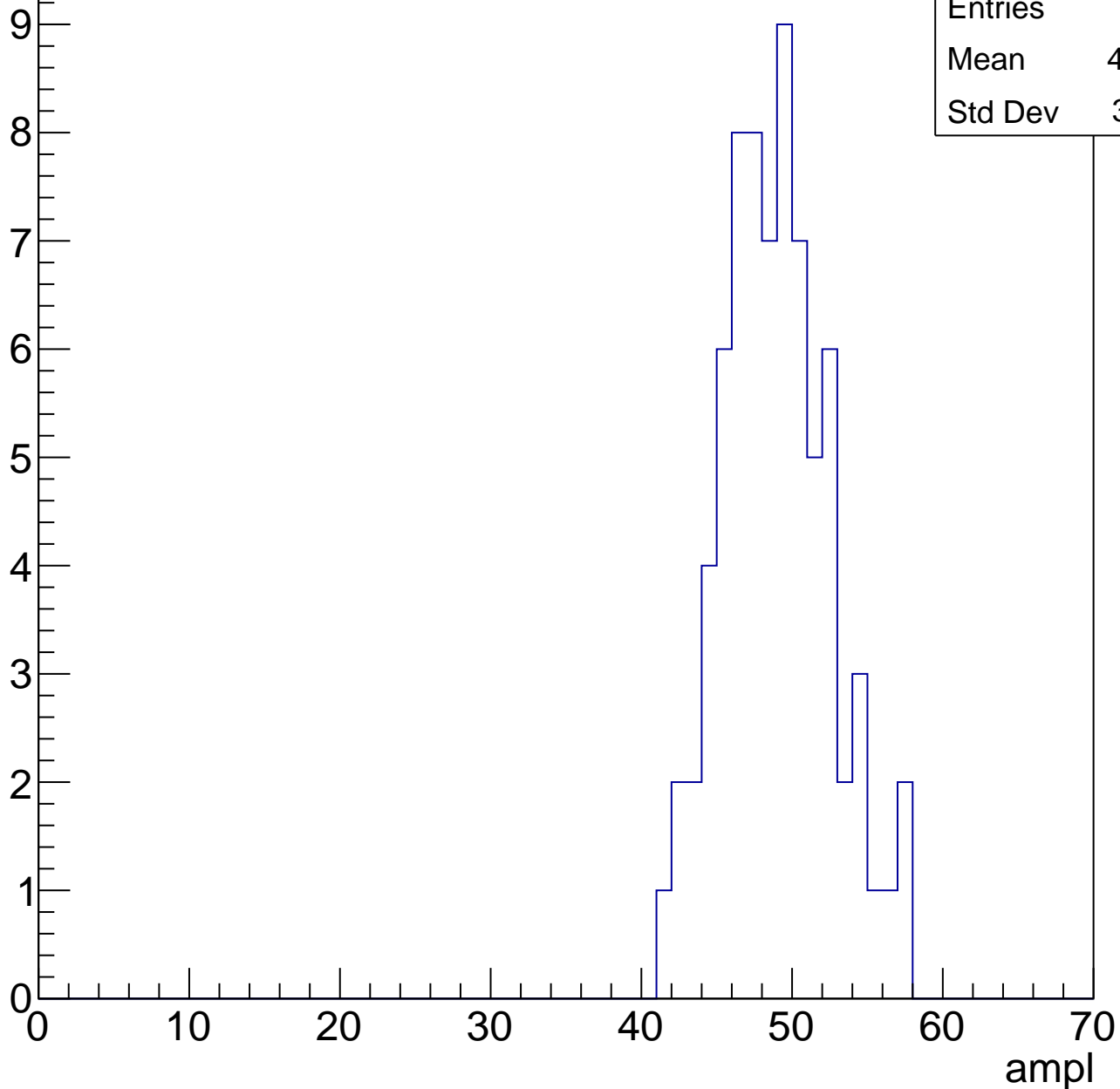
**Gaus Width: 3.1846**



# B1L000S, U23-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



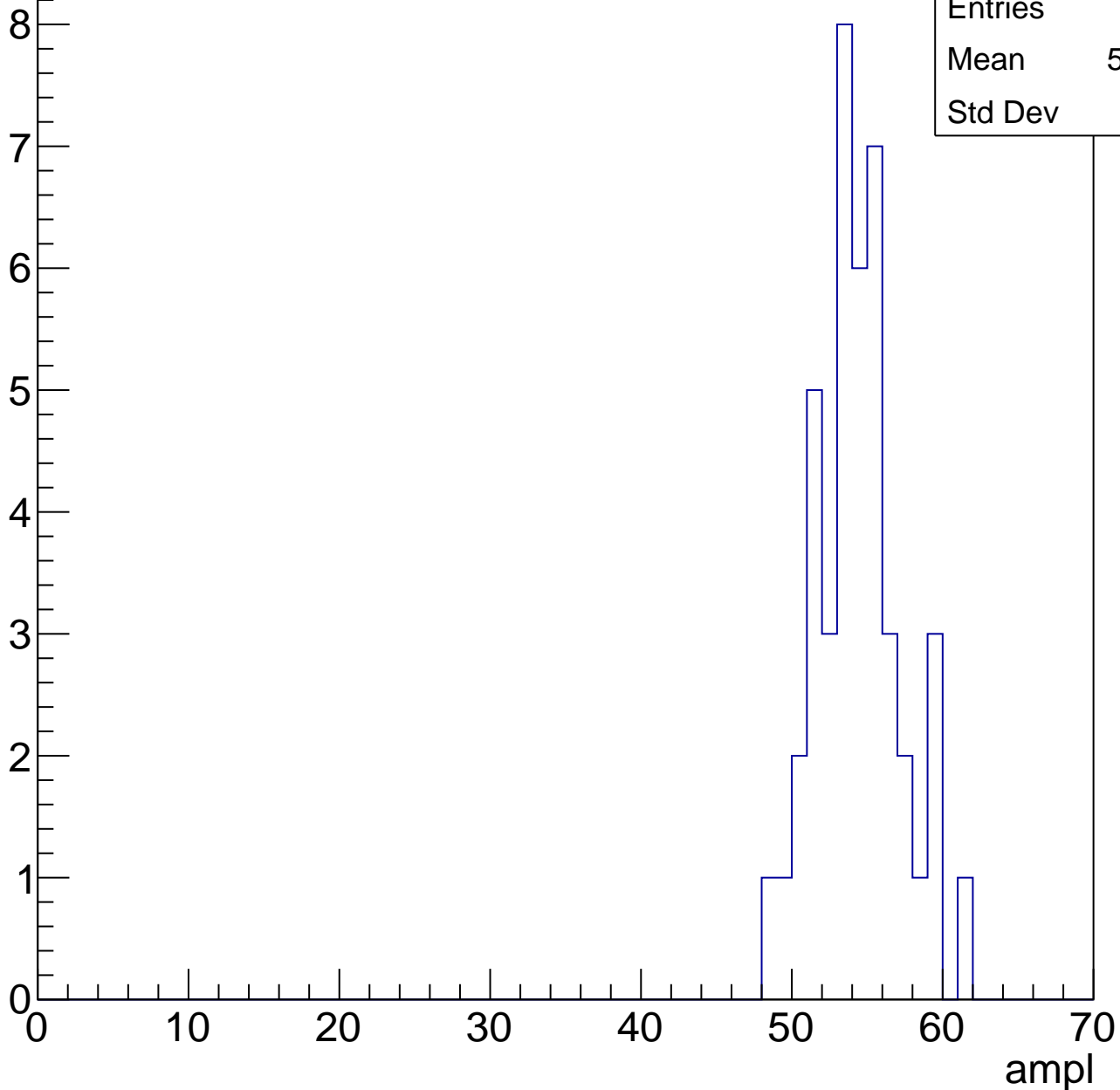
Entries	74
Mean	48.49
Std Dev	3.531

# B1L000S, U23-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	53.93
Std Dev	2.79



# B1L000S, U23-ch47, adc5

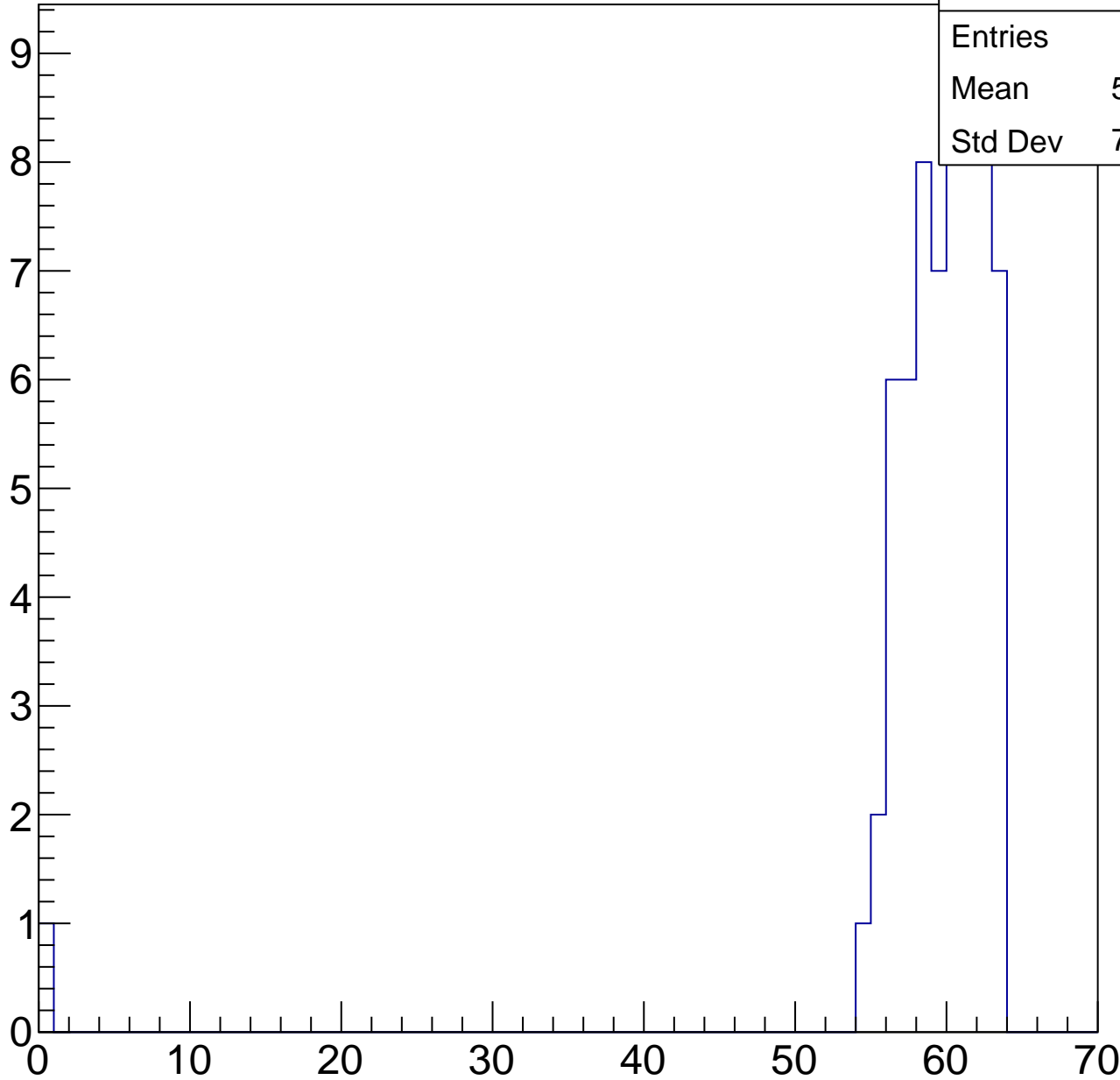
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.48
Std Dev	7.799

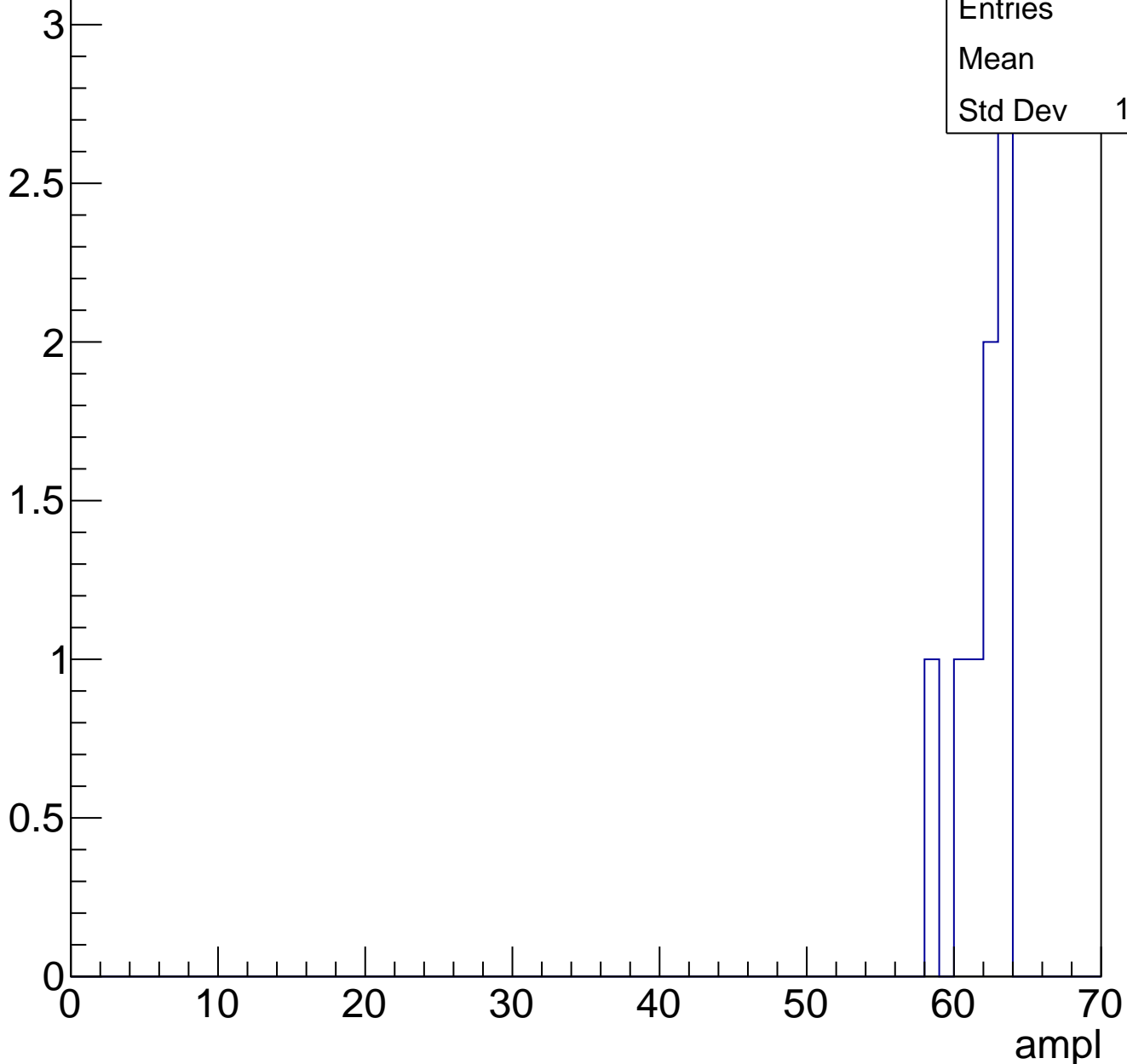
ampl



# B1L000S, U23-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch48, adc0

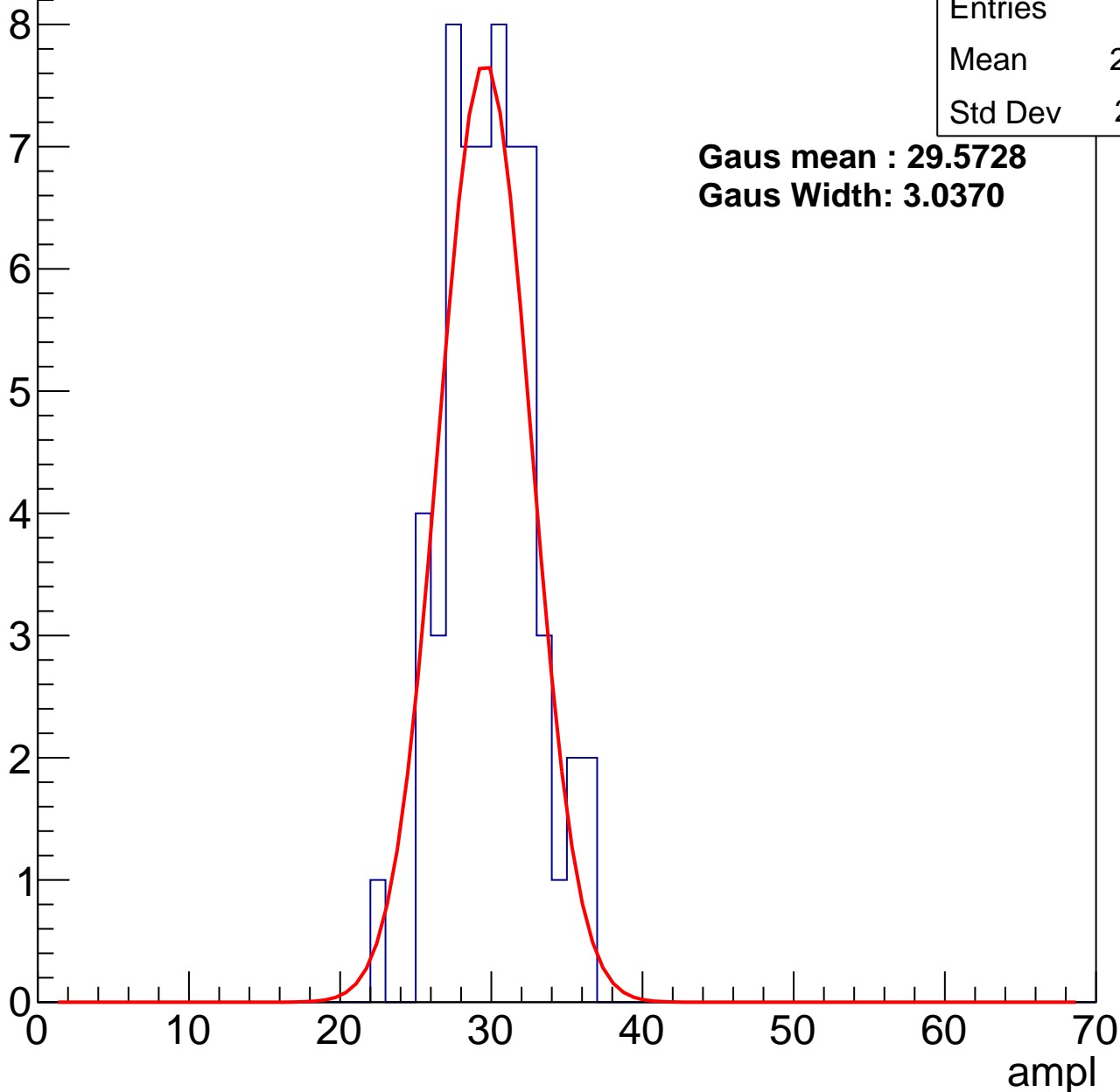
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	29.52
Std Dev	2.901

**Gaus mean : 29.5728**

**Gaus Width: 3.0370**



# B1L000S, U23-ch48, adc1

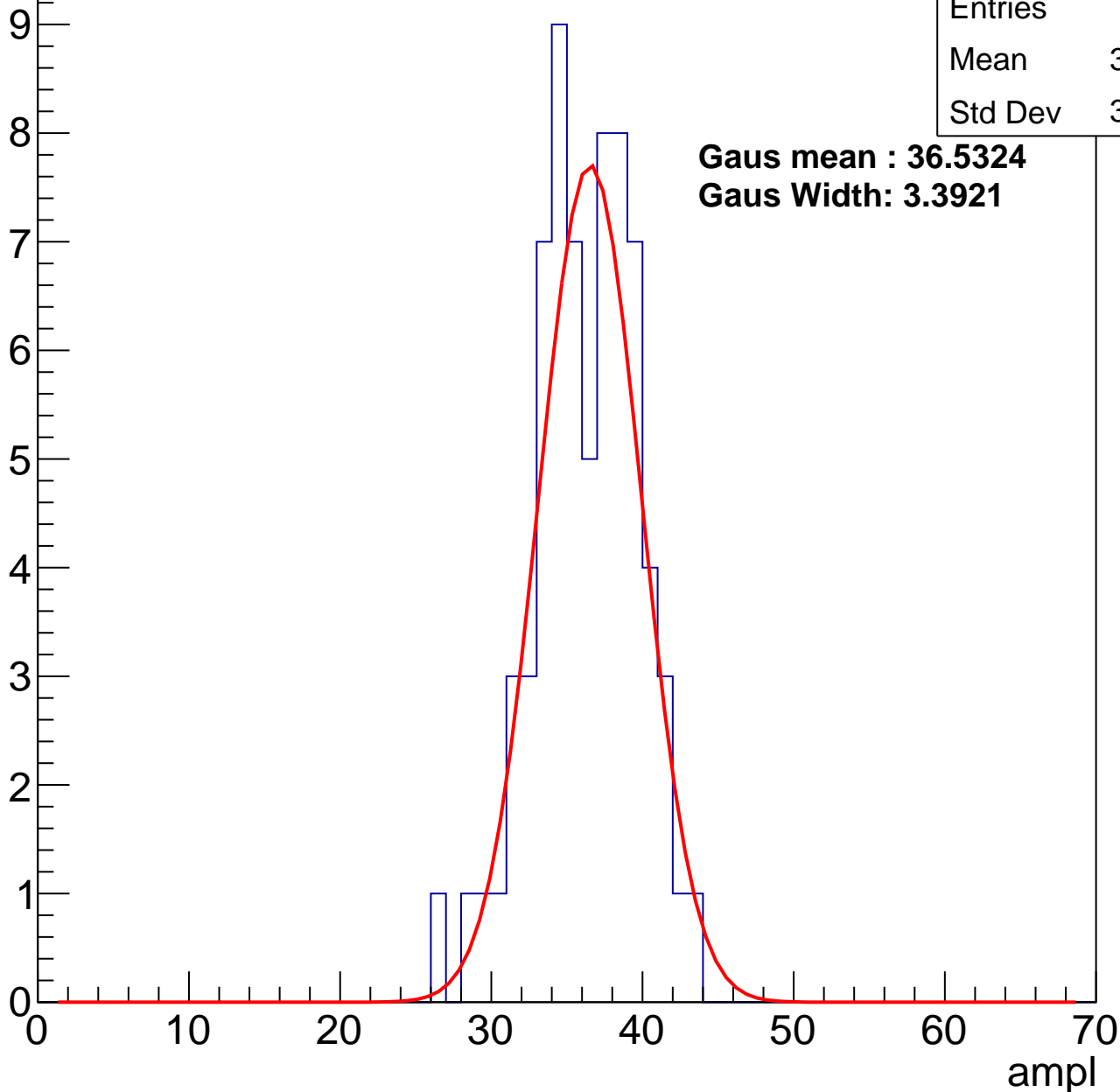
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	35.79
Std Dev	3.384

**Gaus mean : 36.5324**

**Gaus Width: 3.3921**



# B1L000S, U23-ch48, adc2

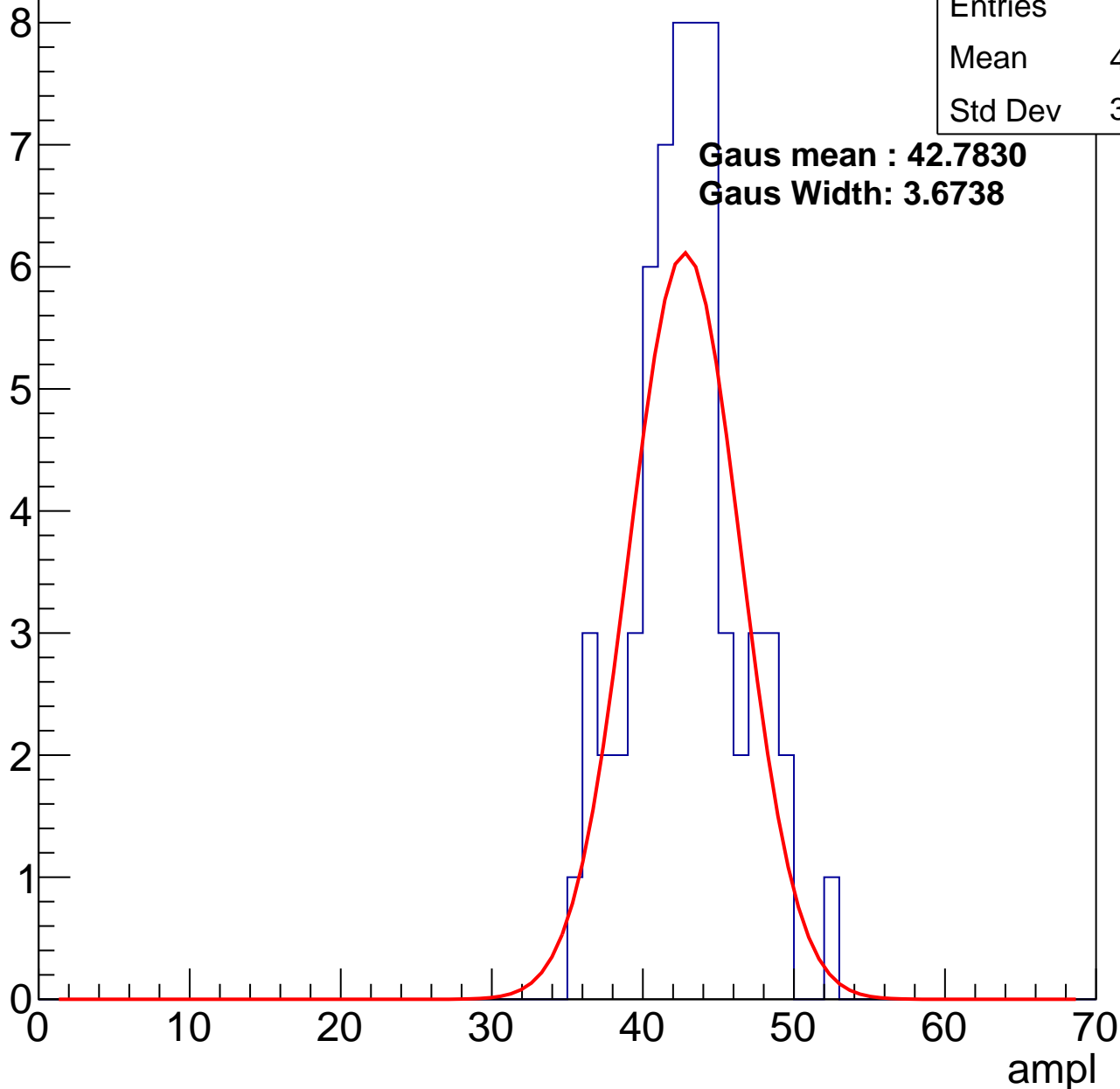
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.44
Std Dev	3.513

**Gaus mean : 42.7830**

**Gaus Width: 3.6738**

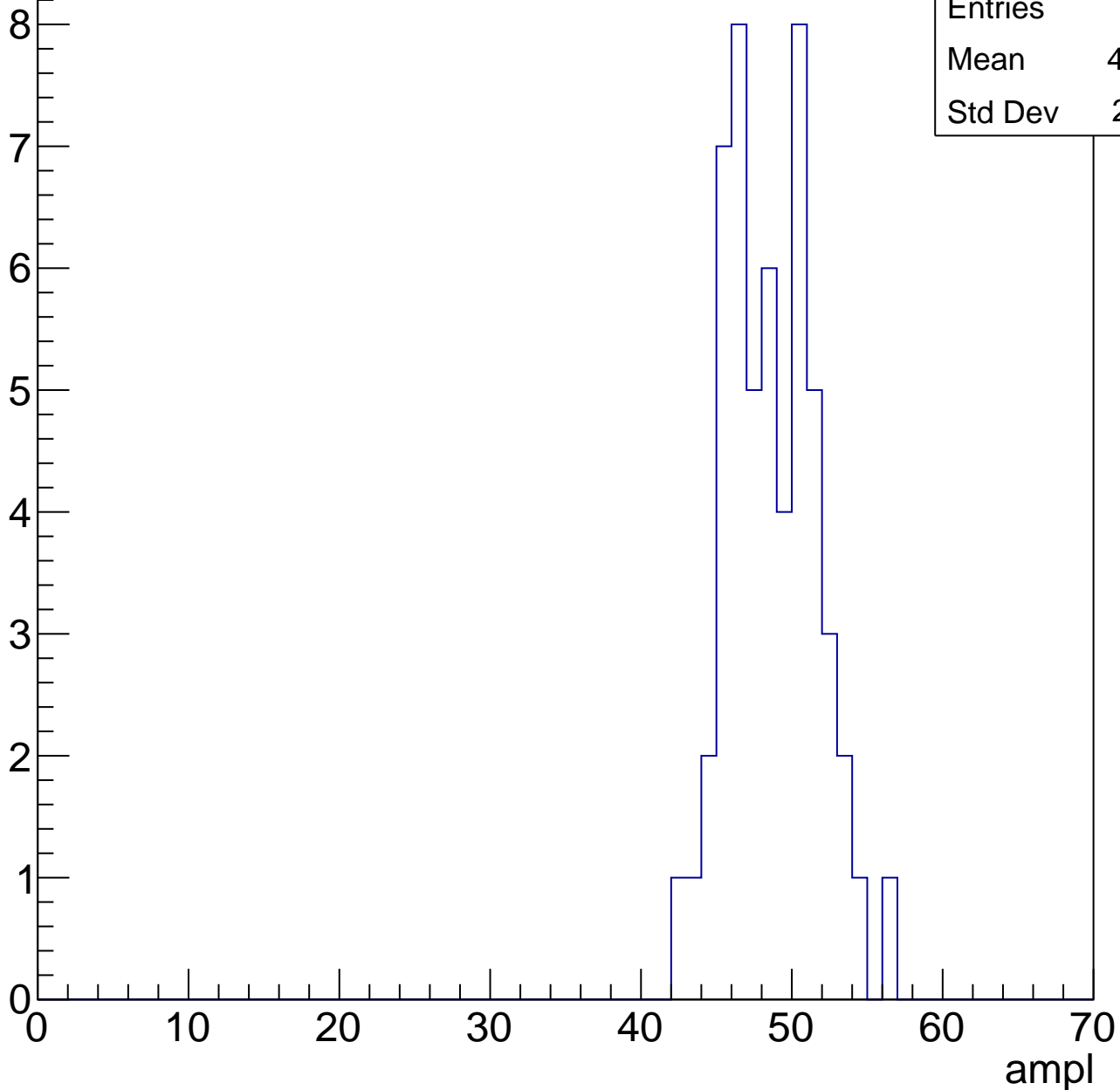


# B1L000S, U23-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	48.19
Std Dev	2.951

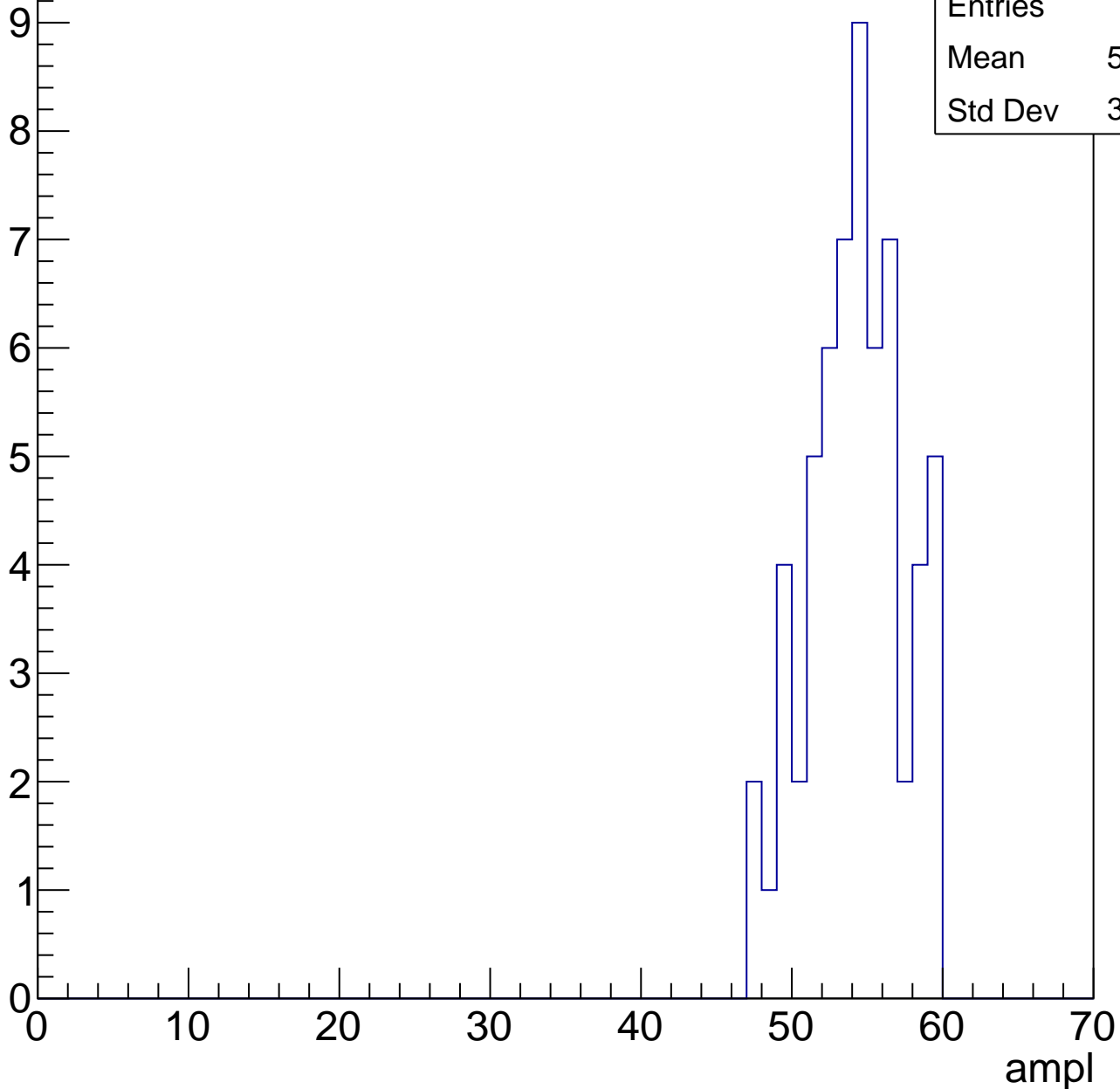


# B1L000S, U23-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	53.75
Std Dev	3.107

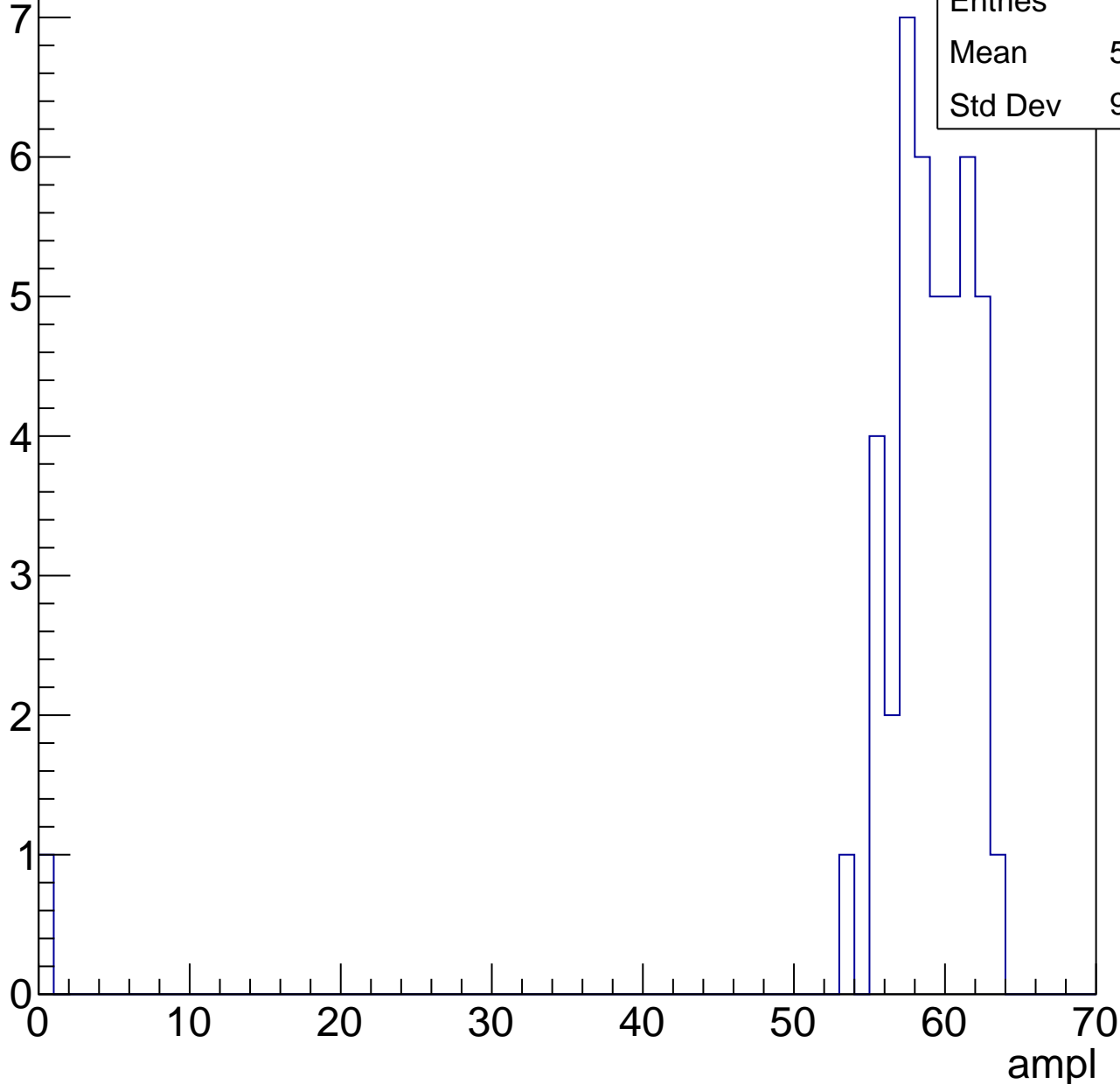


# B1L000S, U23-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	57.35
Std Dev	9.157

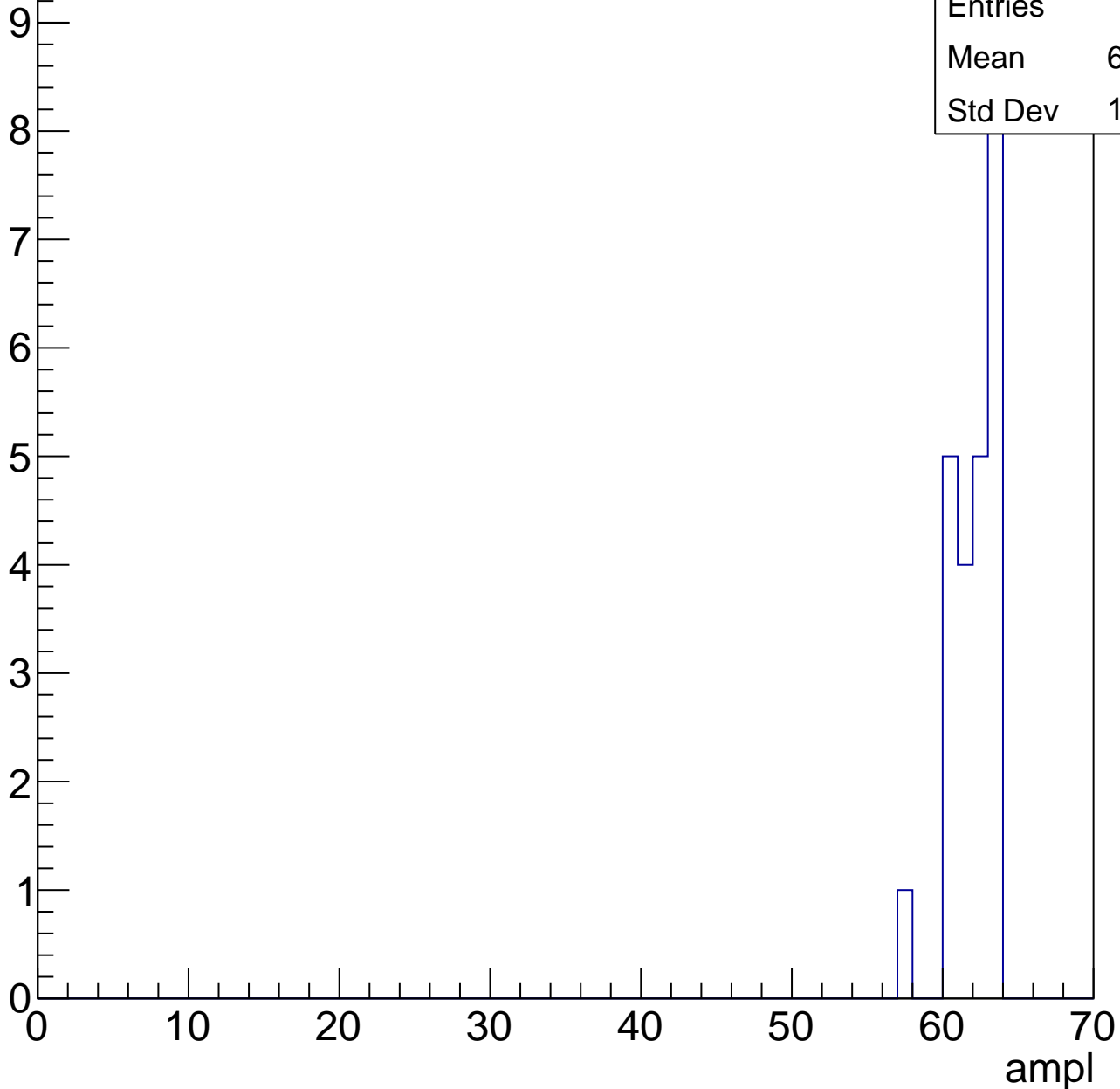


# B1L000S, U23-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	61.58
Std Dev	1.498





# B1L000S, U23-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch49, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	27.85
Std Dev	2.971

**Gaus mean : 28.5143**

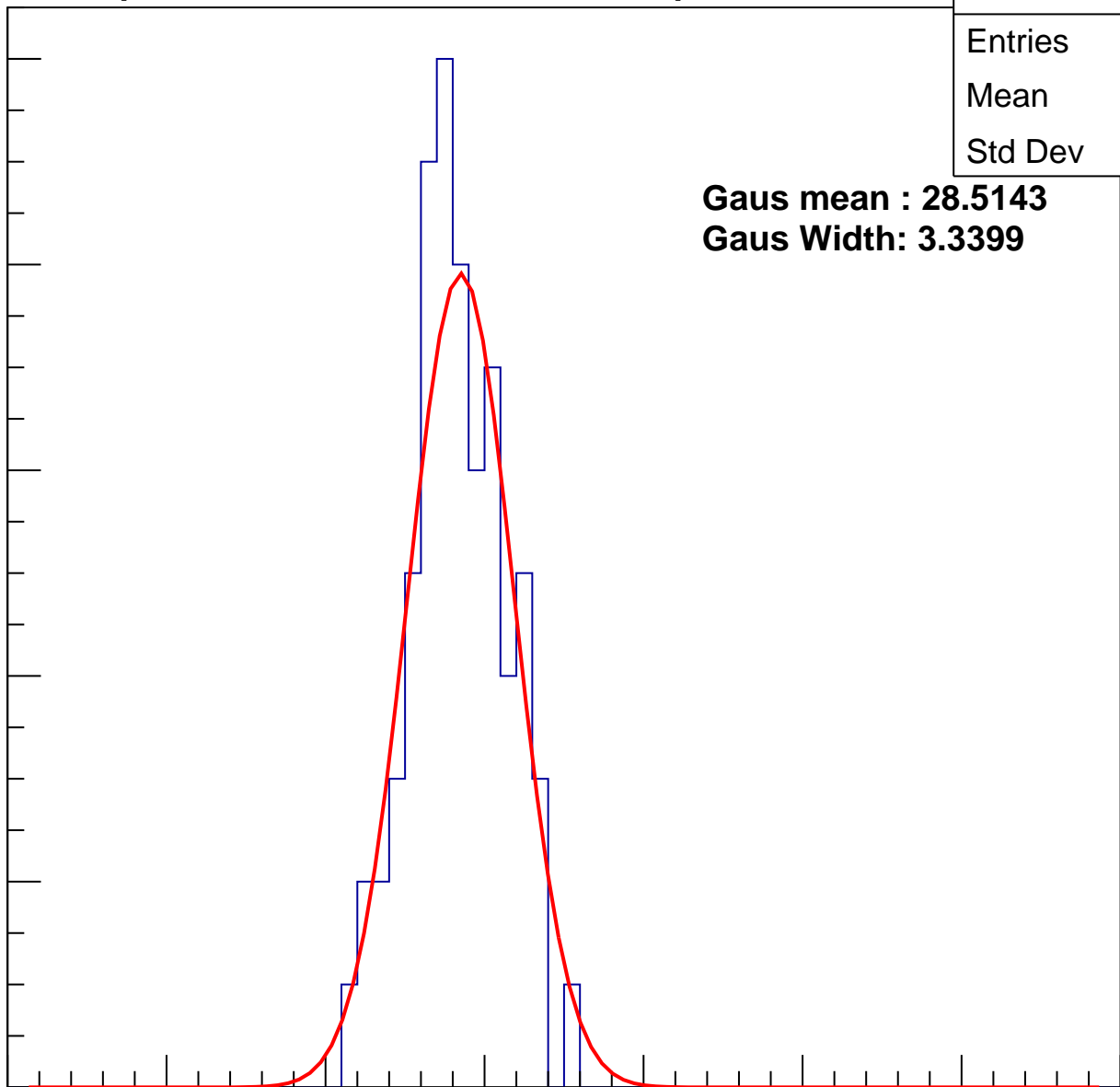
**Gaus Width: 3.3399**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch49, adc1

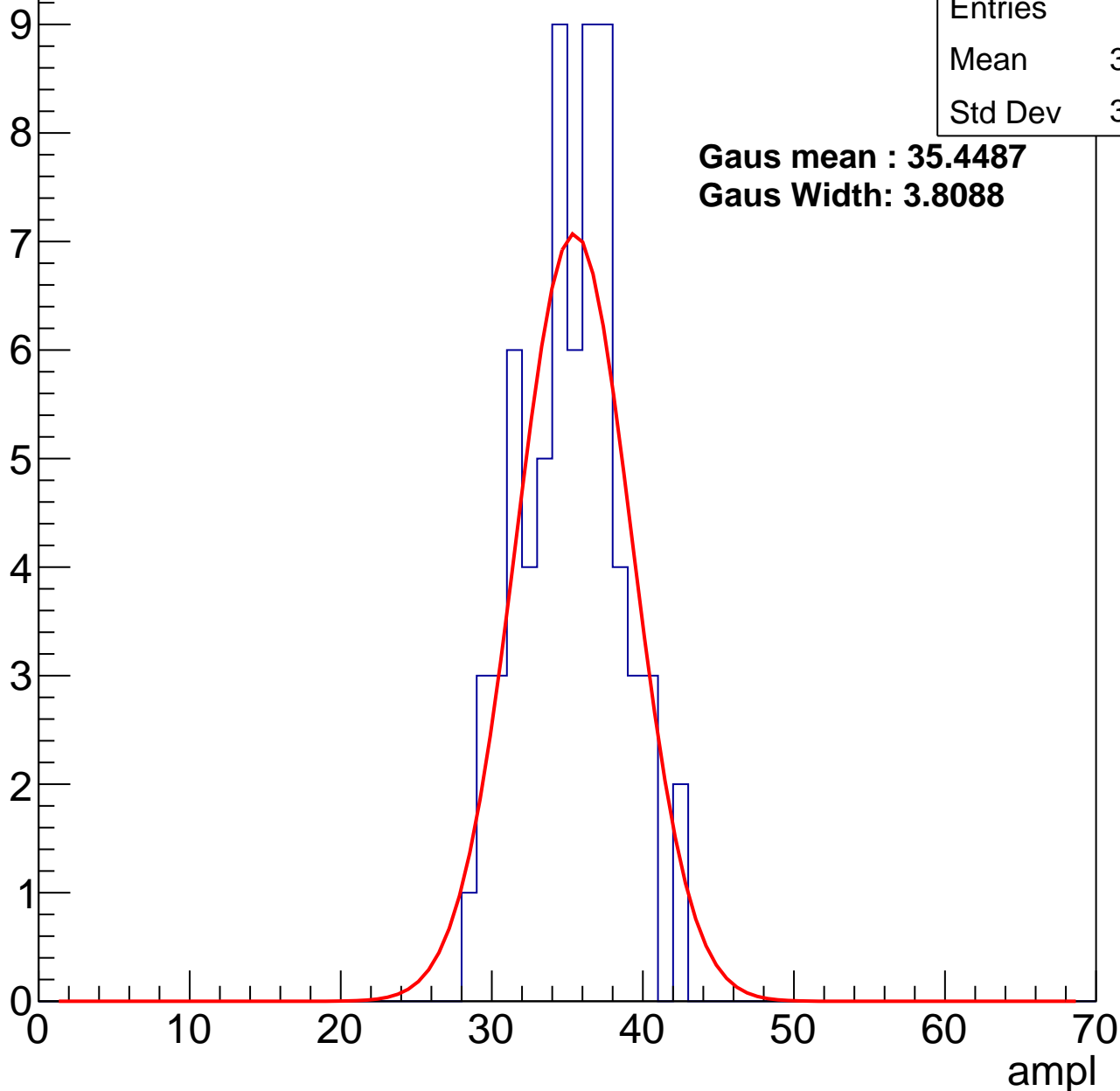
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.78
Std Dev	3.213

**Gaus mean : 35.4487**

**Gaus Width: 3.8088**



# B1L000S, U23-ch49, adc2

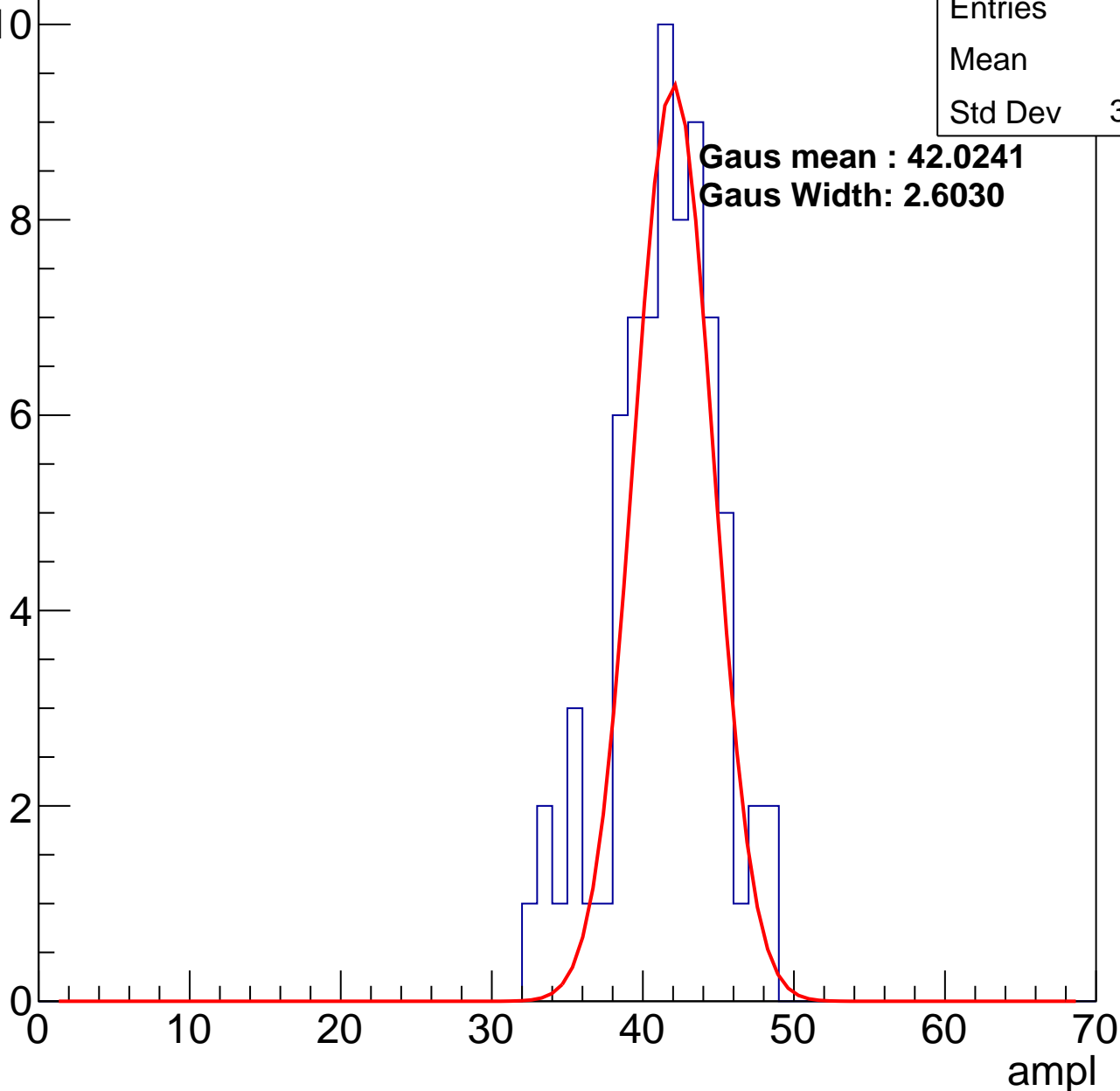
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	41
Std Dev	3.468

**Gaus mean : 42.0241**

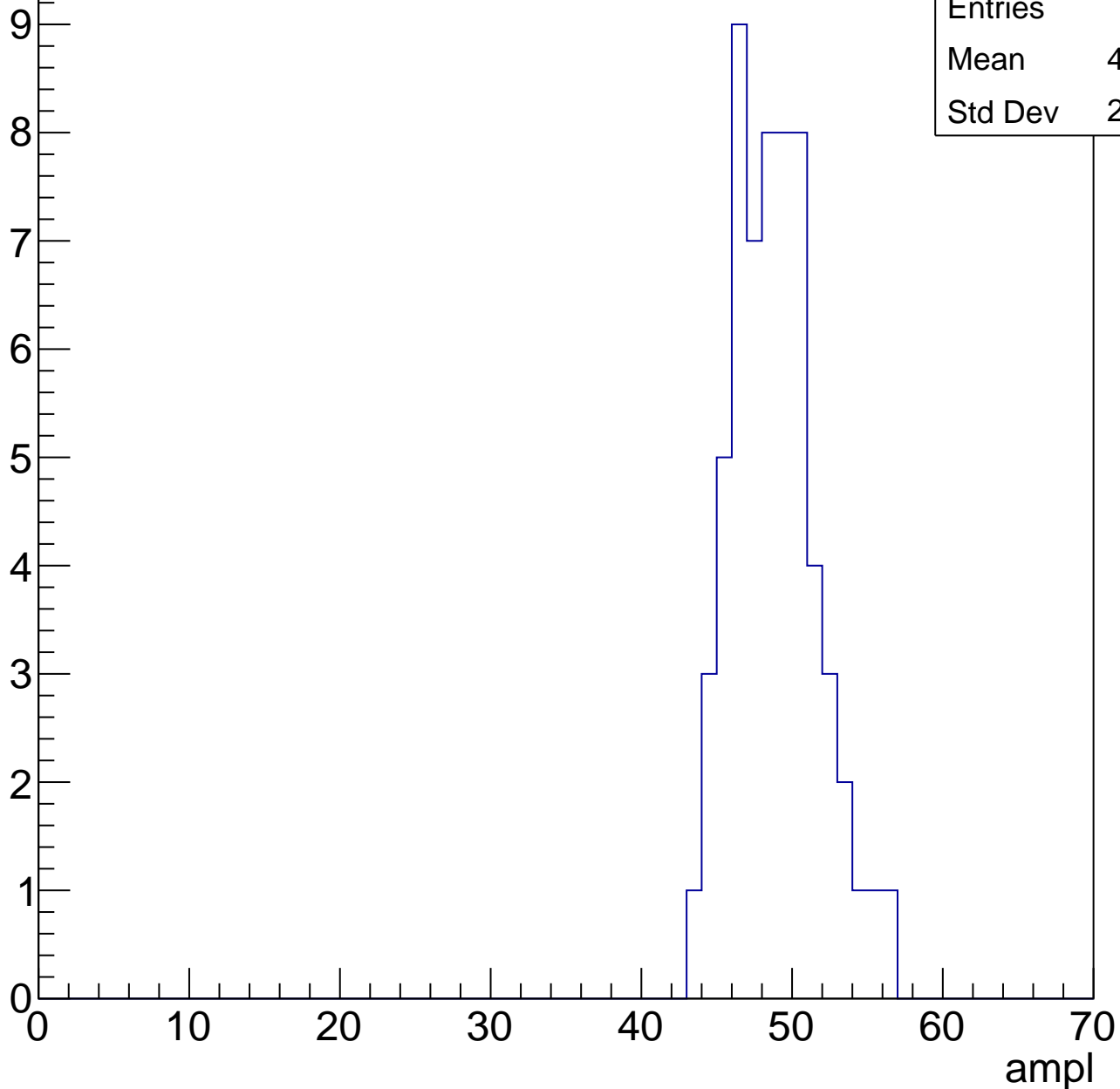
**Gaus Width: 2.6030**



# B1L000S, U23-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



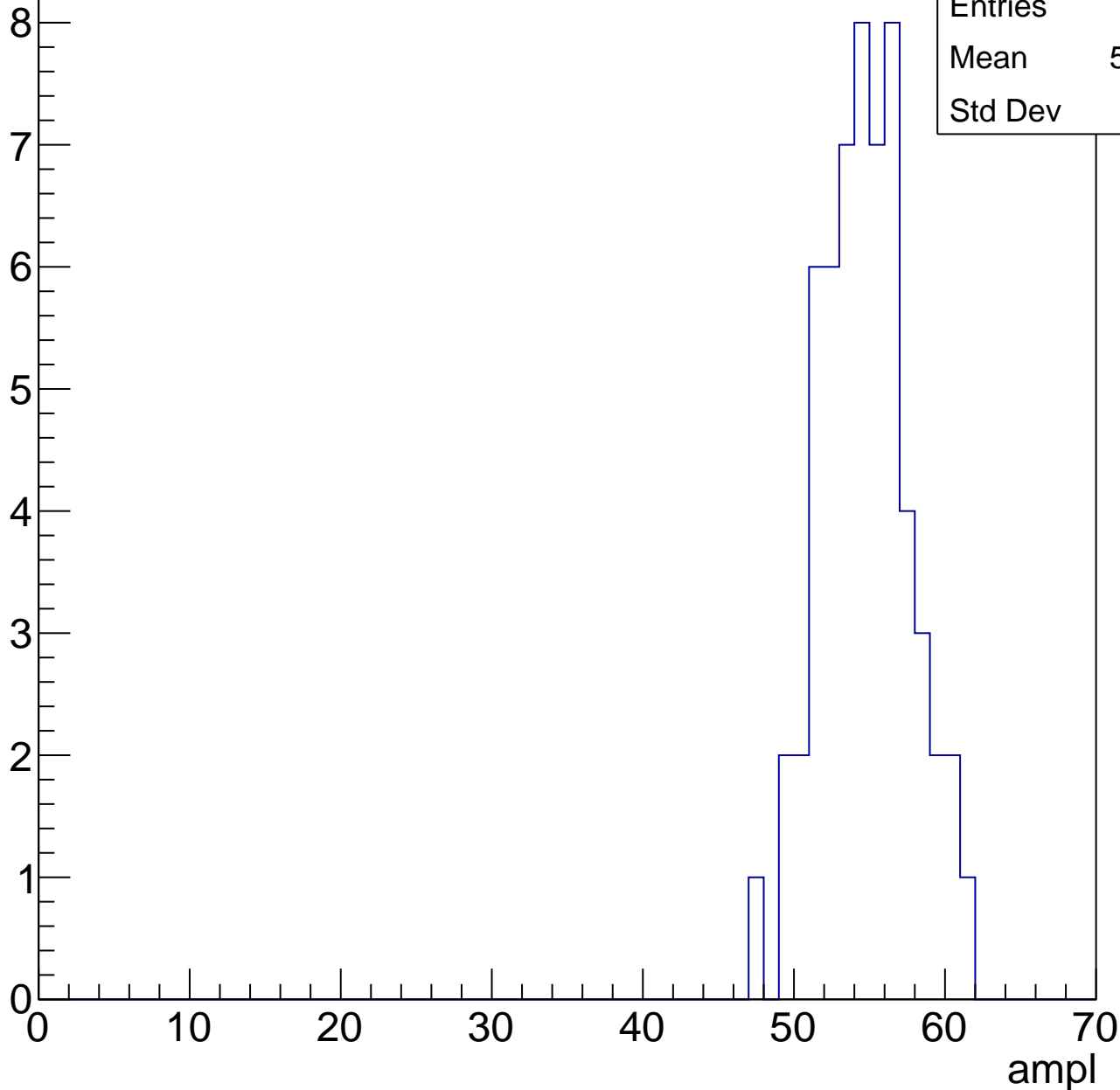
Entries	61
Mean	48.36
Std Dev	2.794

# B1L000S, U23-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

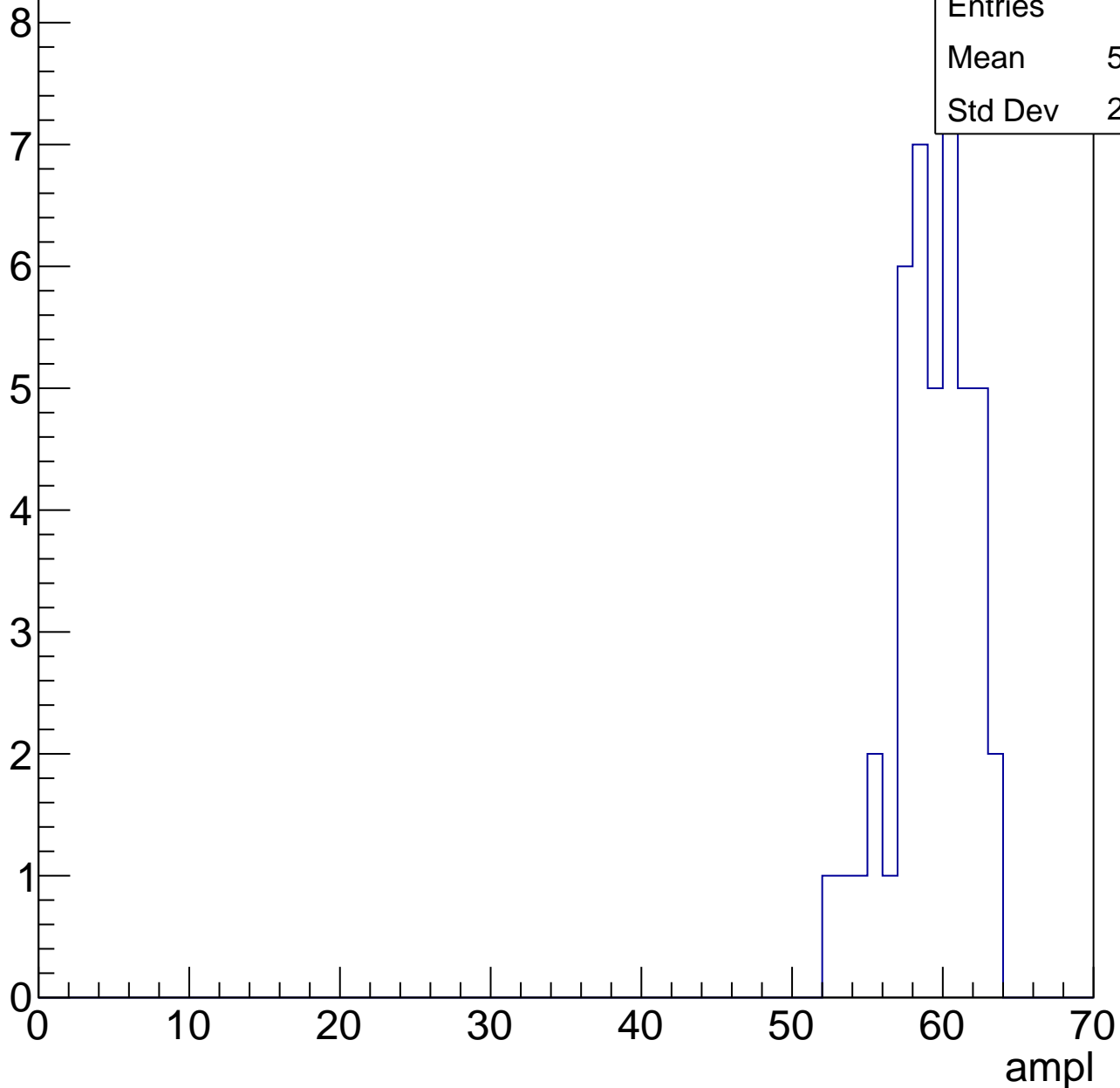
Entries	59
Mean	54.24
Std Dev	2.93



# B1L000S, U23-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

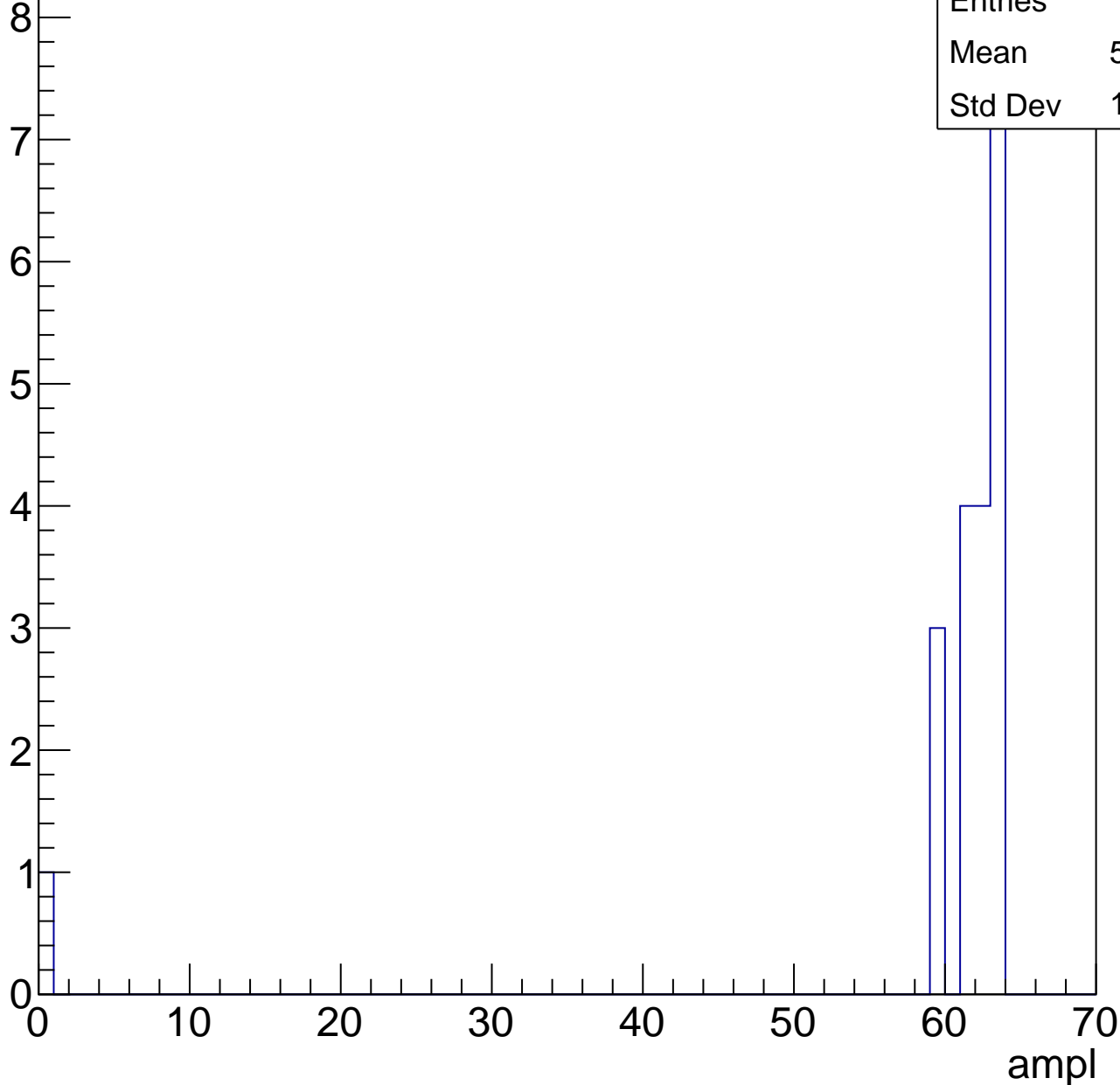


# B1L000S, U23-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.65
Std Dev	13.53





# B1L000S, U23-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch50, adc0

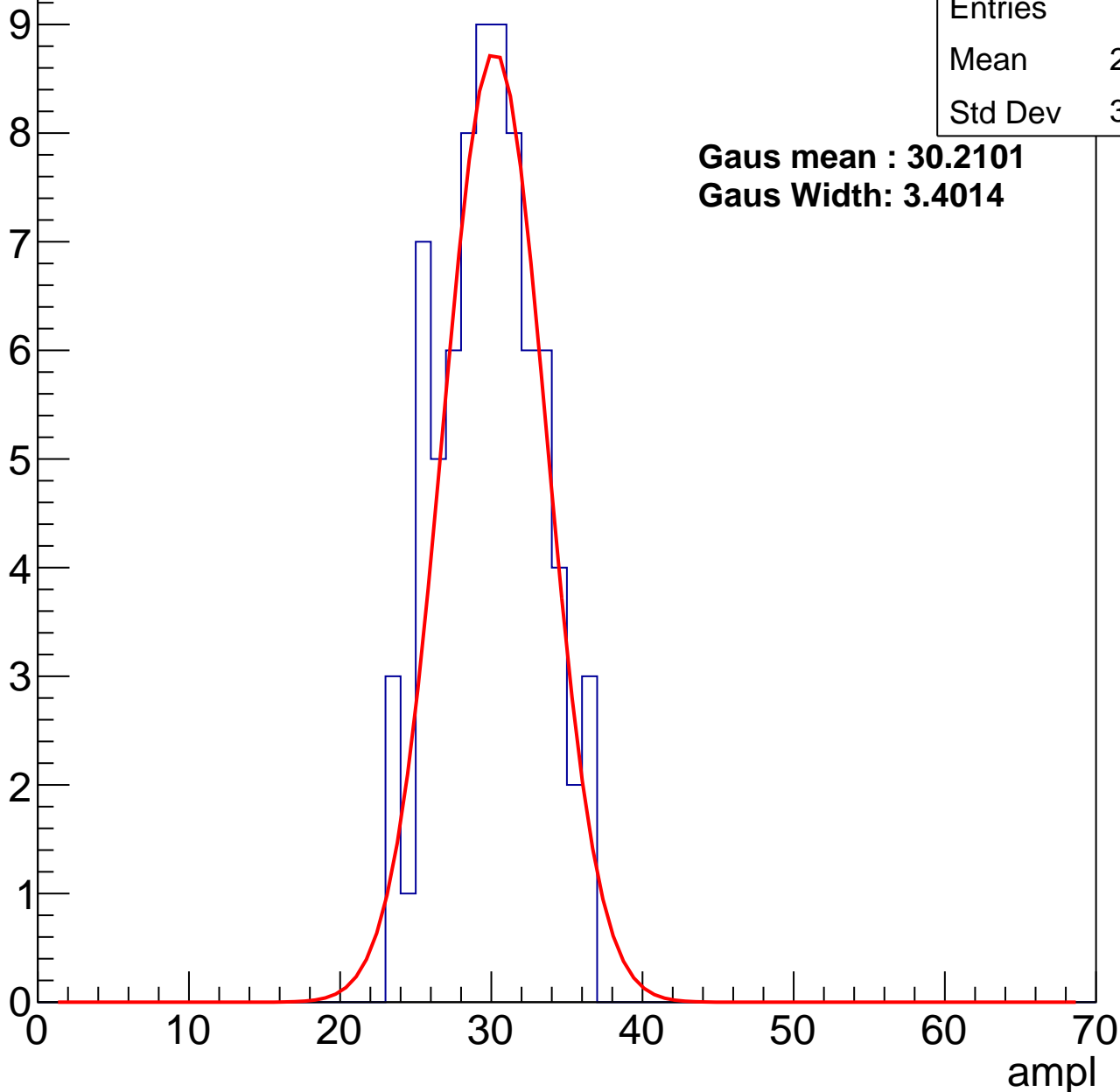
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	29.44
Std Dev	3.257

**Gaus mean : 30.2101**

**Gaus Width: 3.4014**



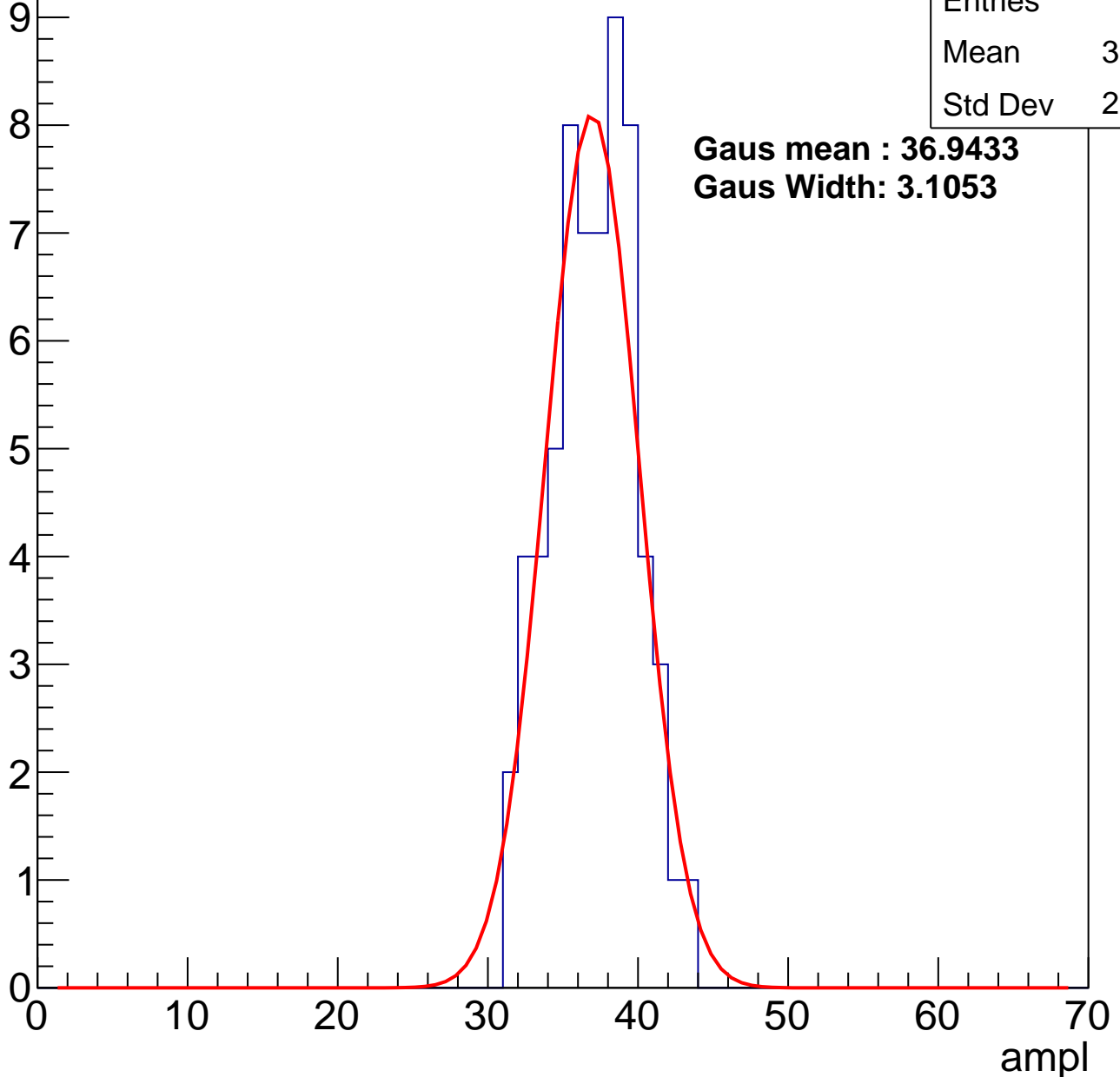
# B1L000S, U23-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	36.59
Std Dev	2.804

**Gaus mean : 36.9433**  
**Gaus Width: 3.1053**



# B1L000S, U23-ch50, adc2

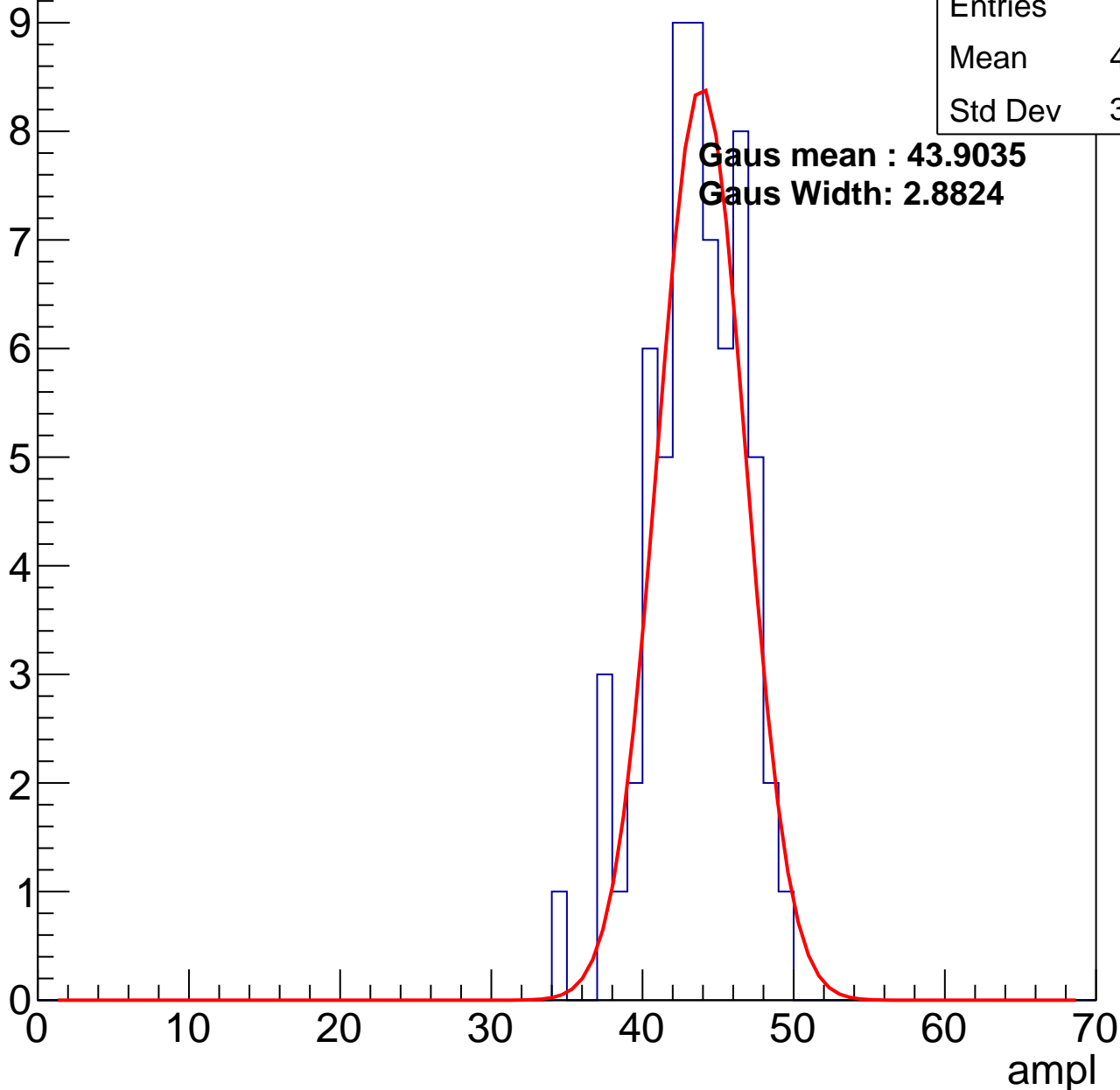
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.03
Std Dev	3.033

**Gaus mean : 43.9035**

**Gaus Width: 2.8824**

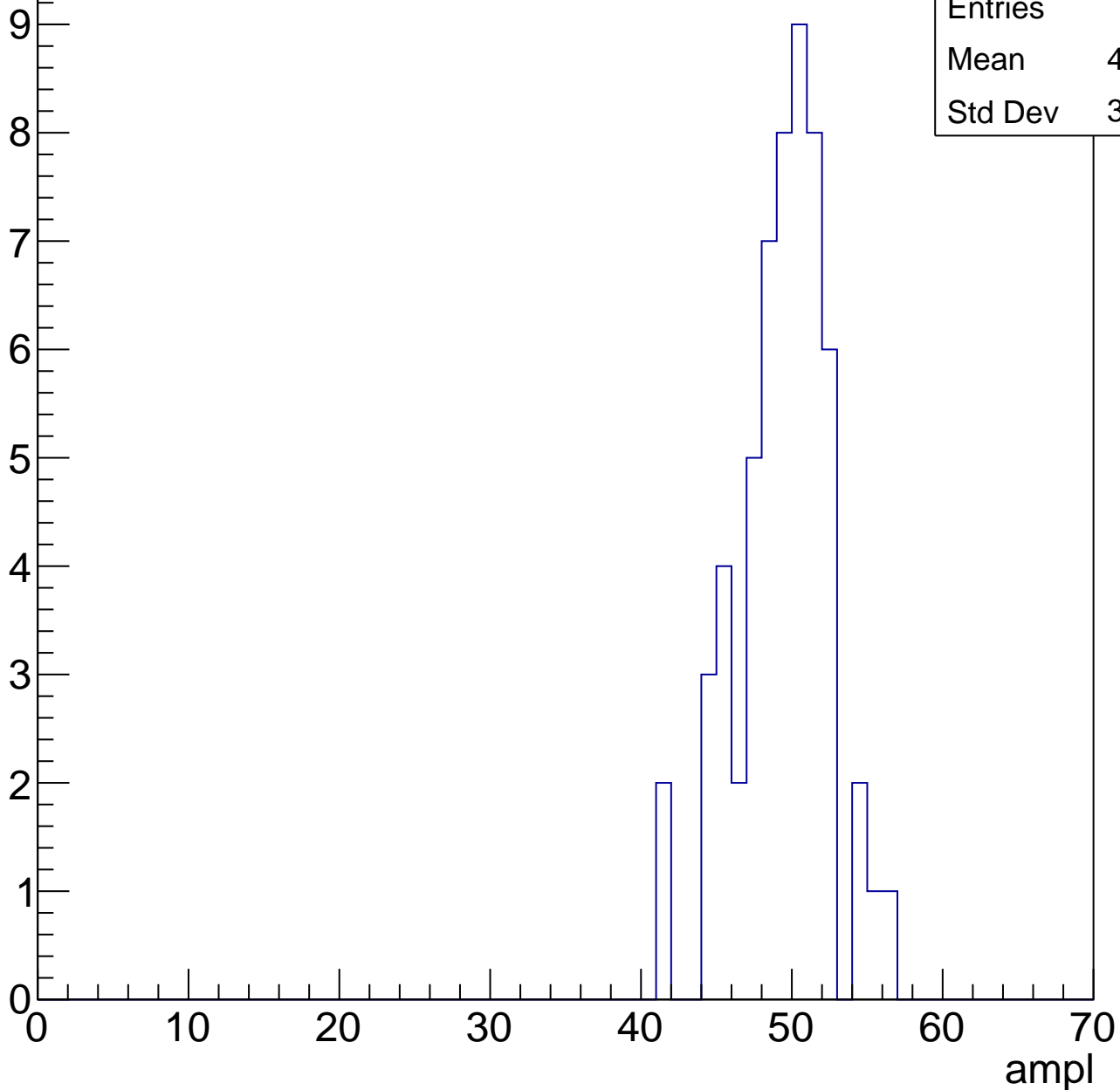


# B1L000S, U23-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.93
Std Dev	3.056



# B1L000S, U23-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	52
Mean	54.9
Std Dev	2.803

Entry

10

8

6

4

2

0

0

10

20

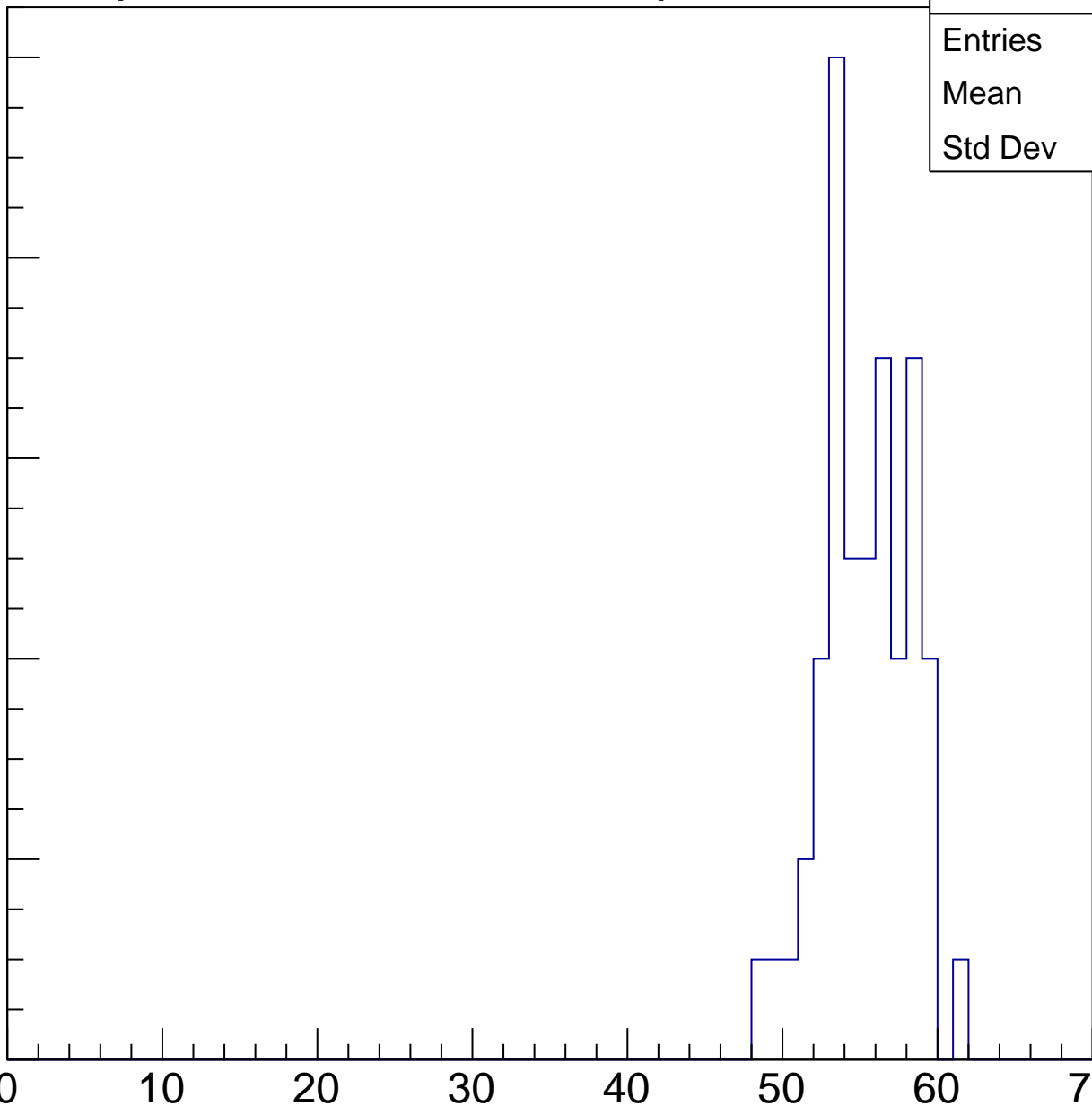
30

40

50

60

ampl



# B1L000S, U23-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

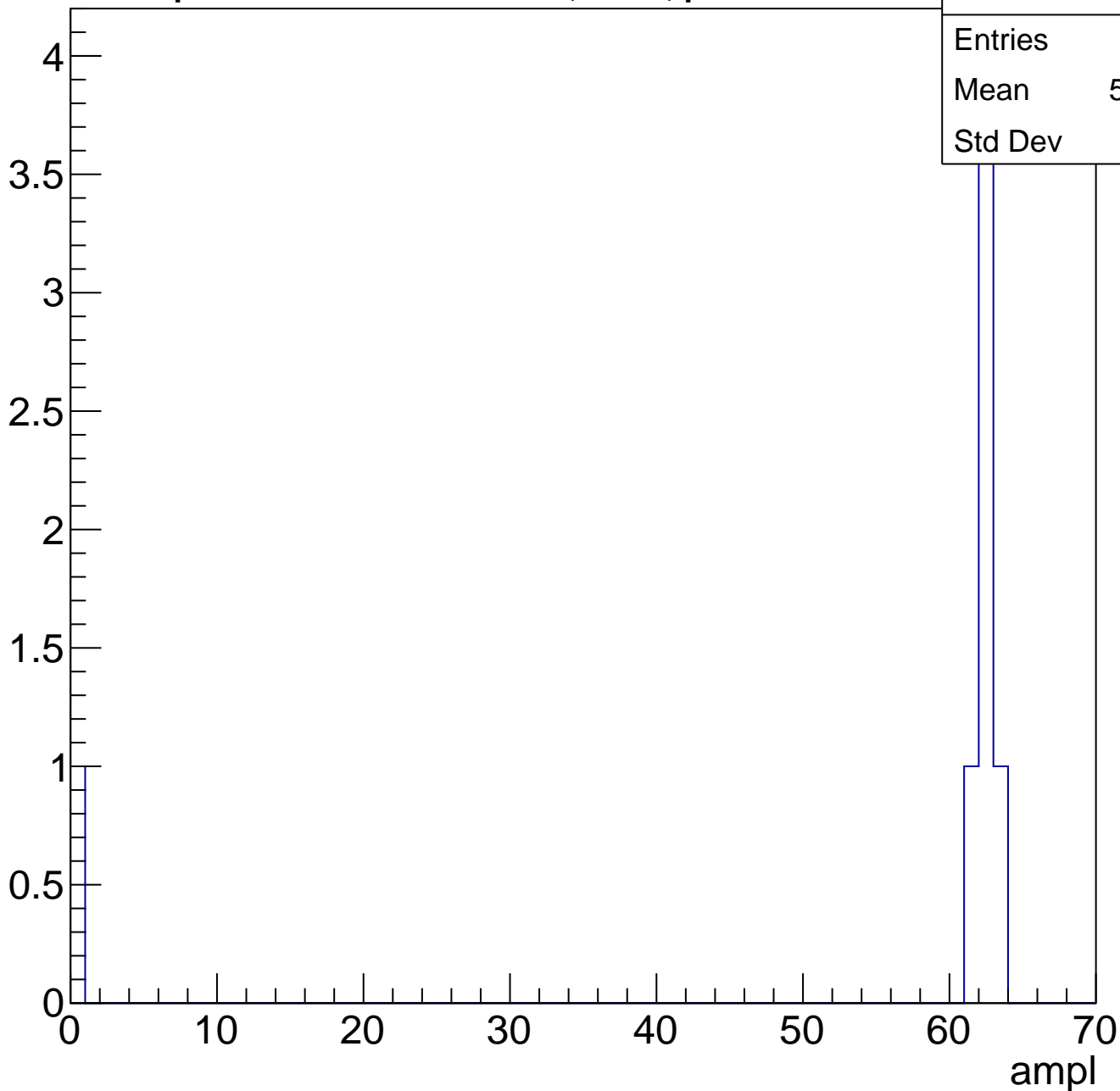
ampl

Entries	57
Mean	59.49
Std Dev	2.683

# B1L000S, U23-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

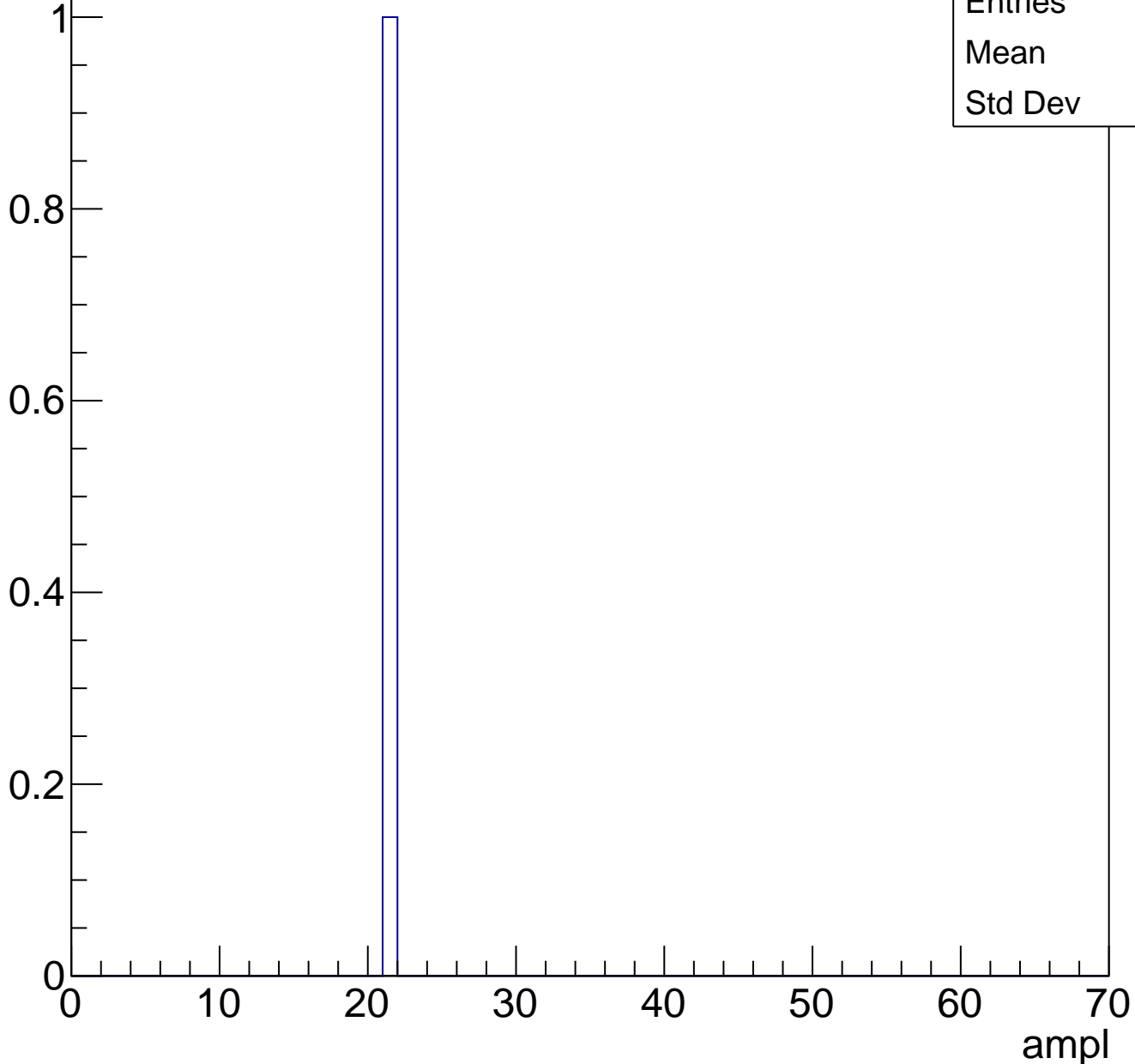




# B1L000S, U23-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	21
Std Dev	0

# B1L000S, U23-ch51, adc0

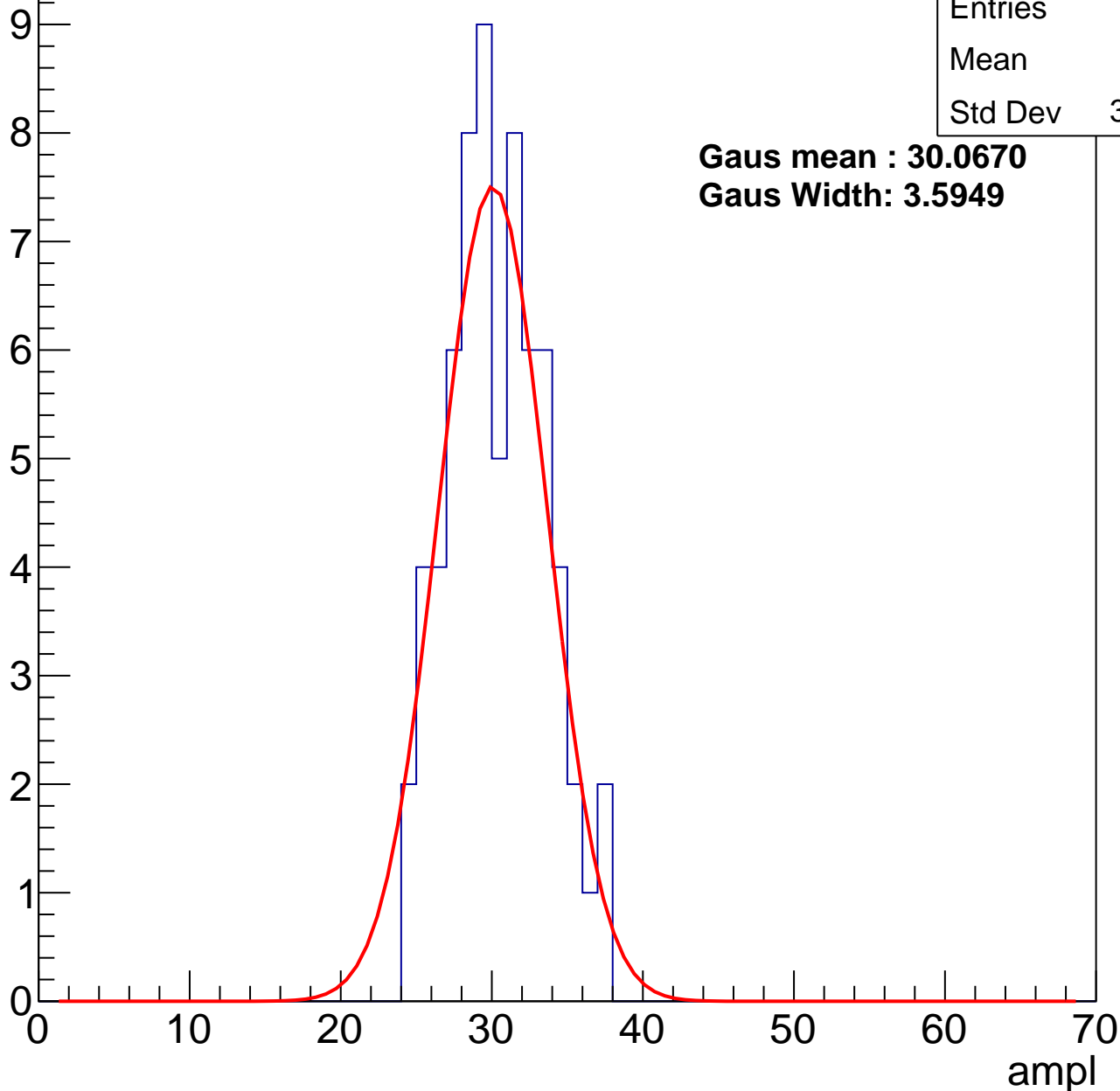
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.9
Std Dev	3.149

**Gaus mean : 30.0670**

**Gaus Width: 3.5949**



# B1L000S, U23-ch51, adc1

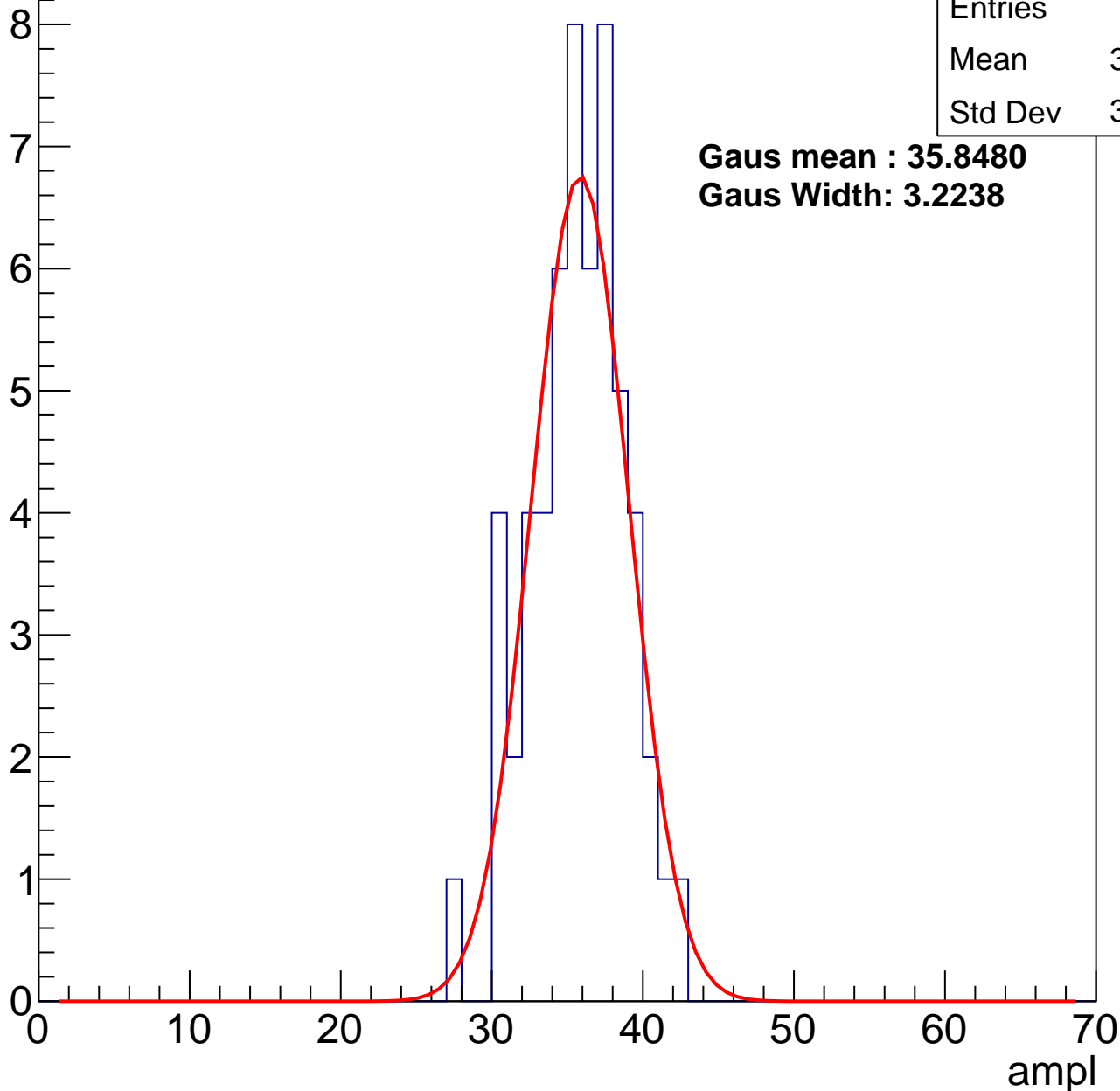
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.25
Std Dev	3.084

**Gaus mean : 35.8480**

**Gaus Width: 3.2238**



# B1L000S, U23-ch51, adc2

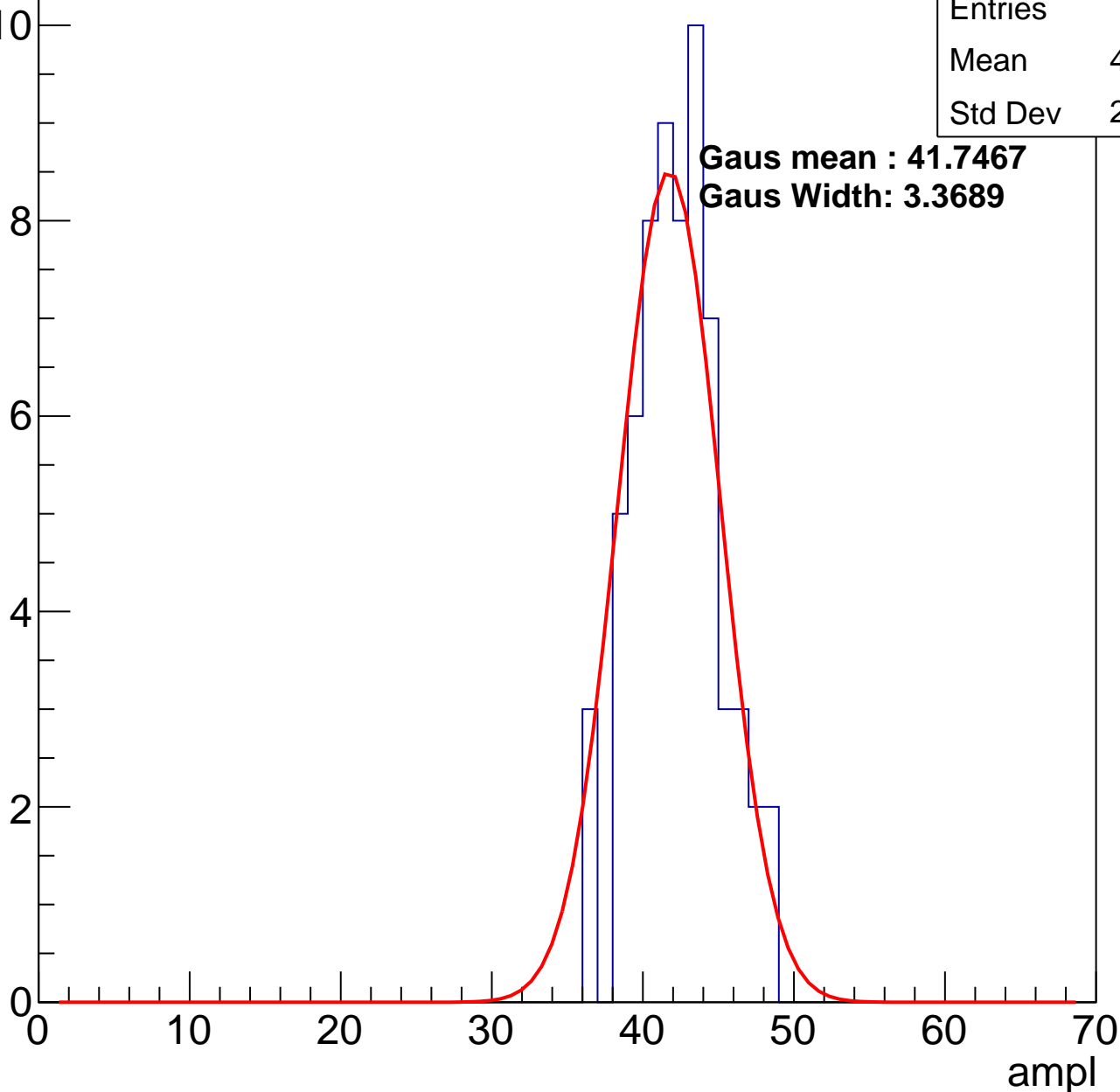
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	41.79
Std Dev	2.793

**Gaus mean : 41.7467**

**Gaus Width: 3.3689**

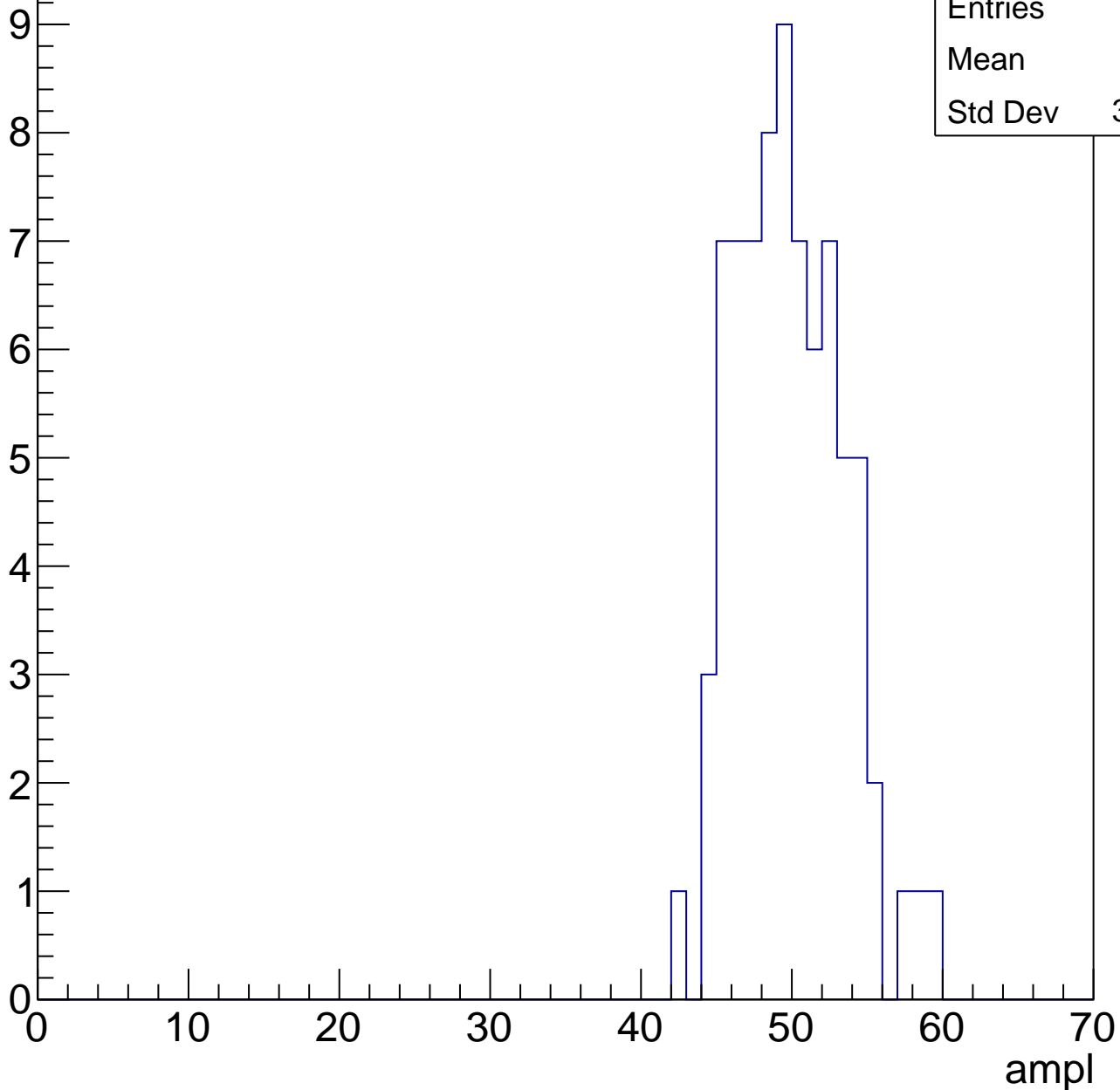


# B1L000S, U23-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

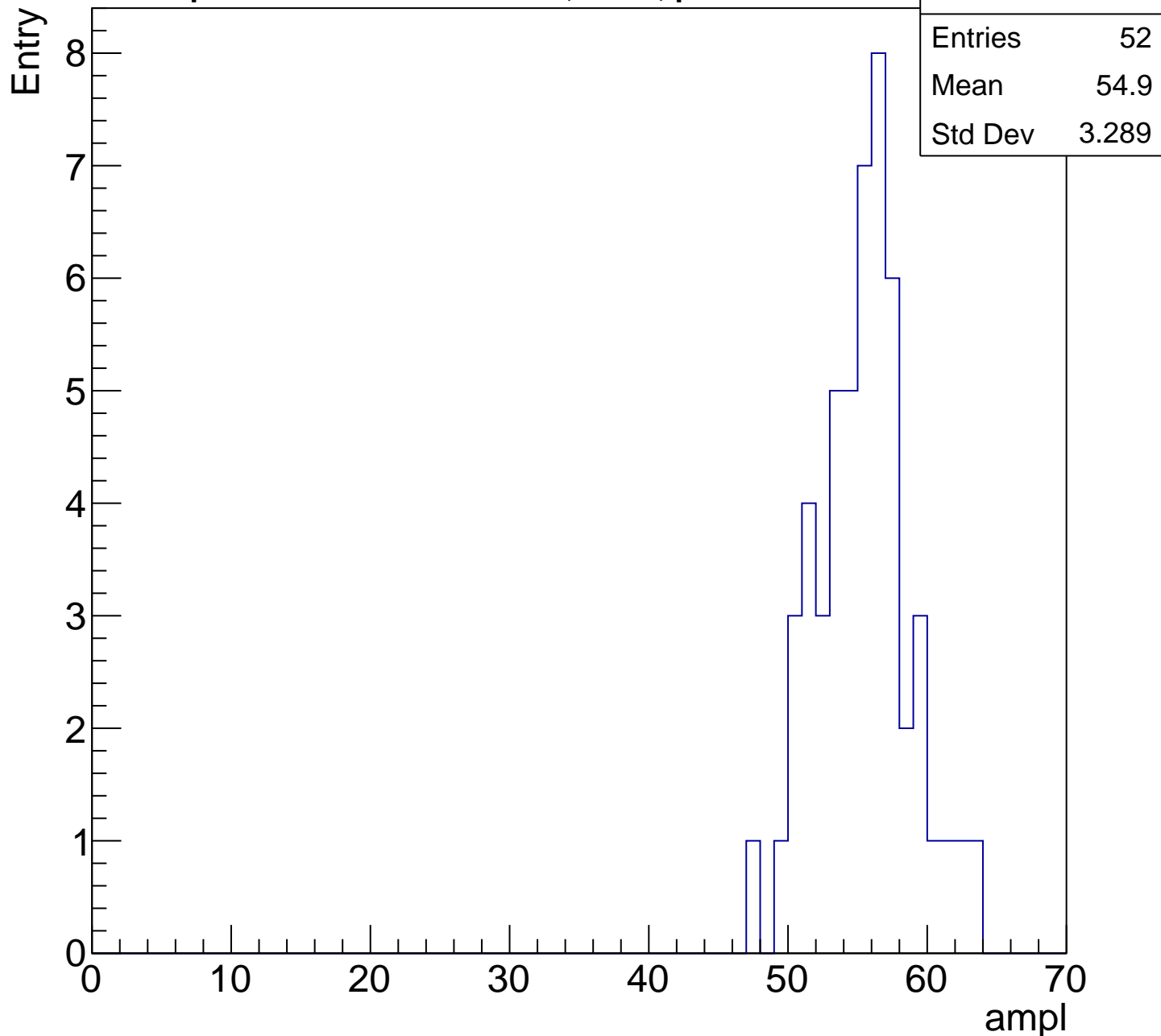
Entry

Entries	77
Mean	49.4
Std Dev	3.491



# B1L000S, U23-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch51, adc5

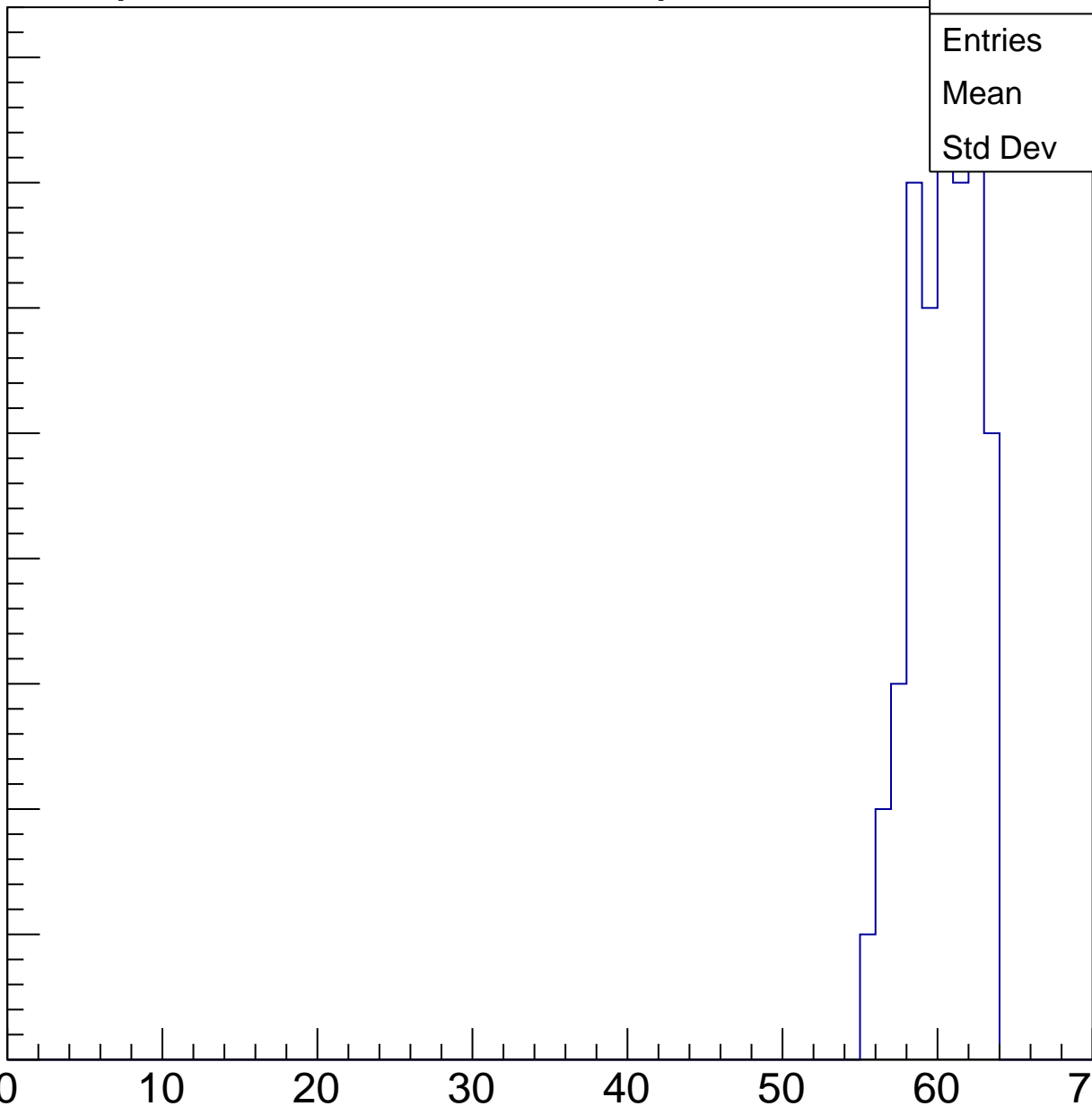
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	59.91
Std Dev	2.071

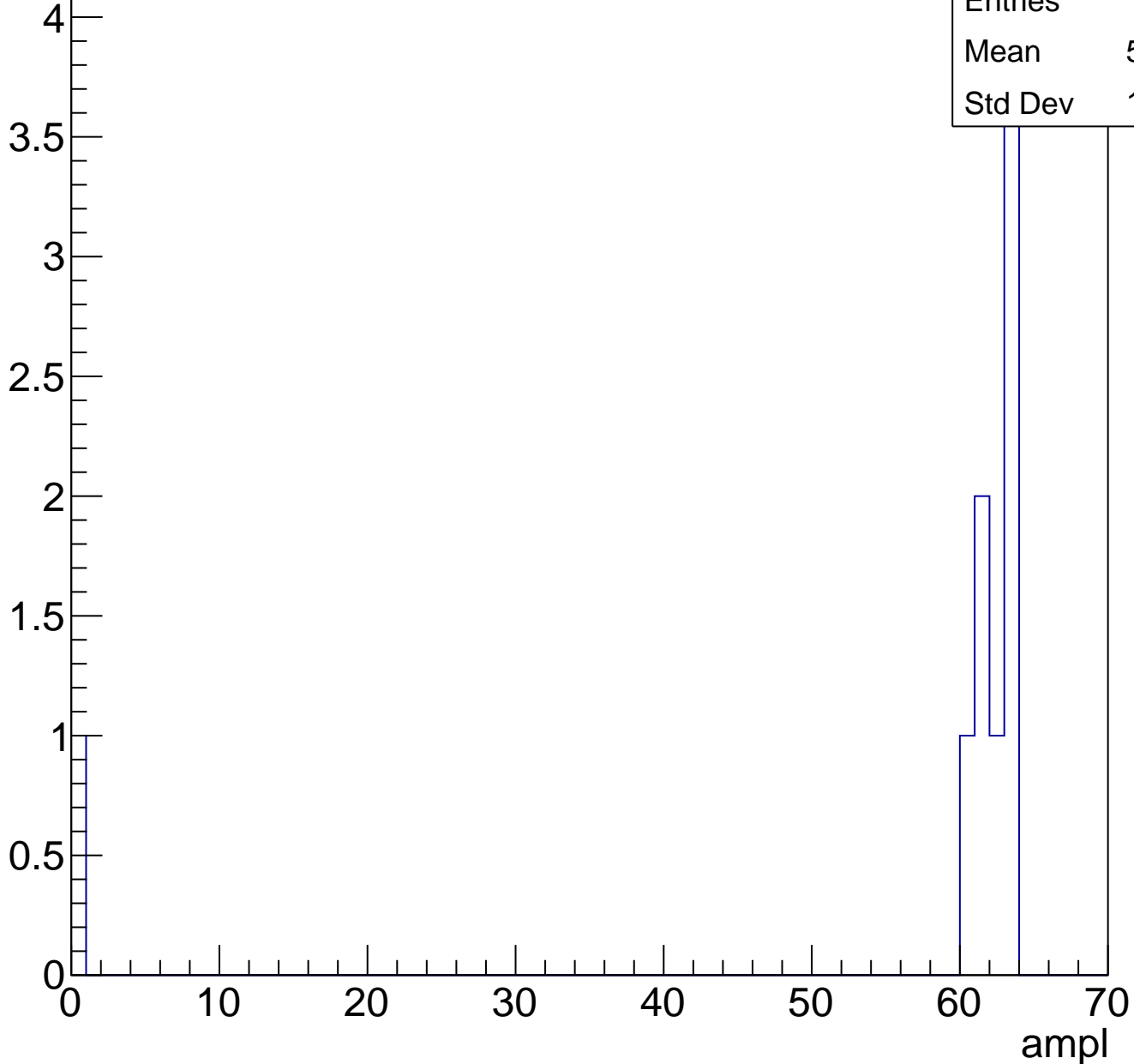
ampl



# B1L000S, U23-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch52, adc0

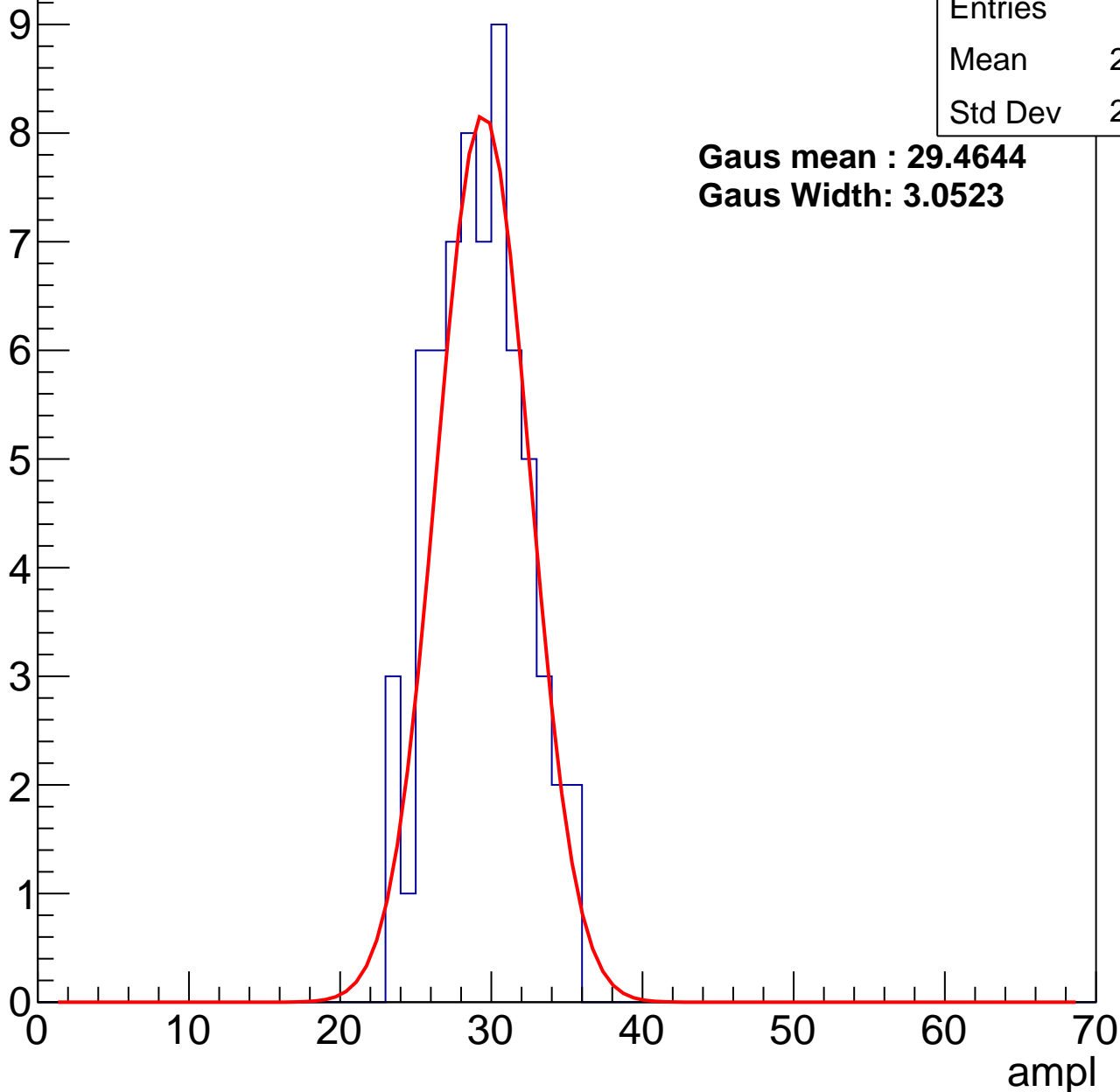
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.74
Std Dev	2.942

**Gaus mean : 29.4644**

**Gaus Width: 3.0523**



# B1L000S, U23-ch52, adc1

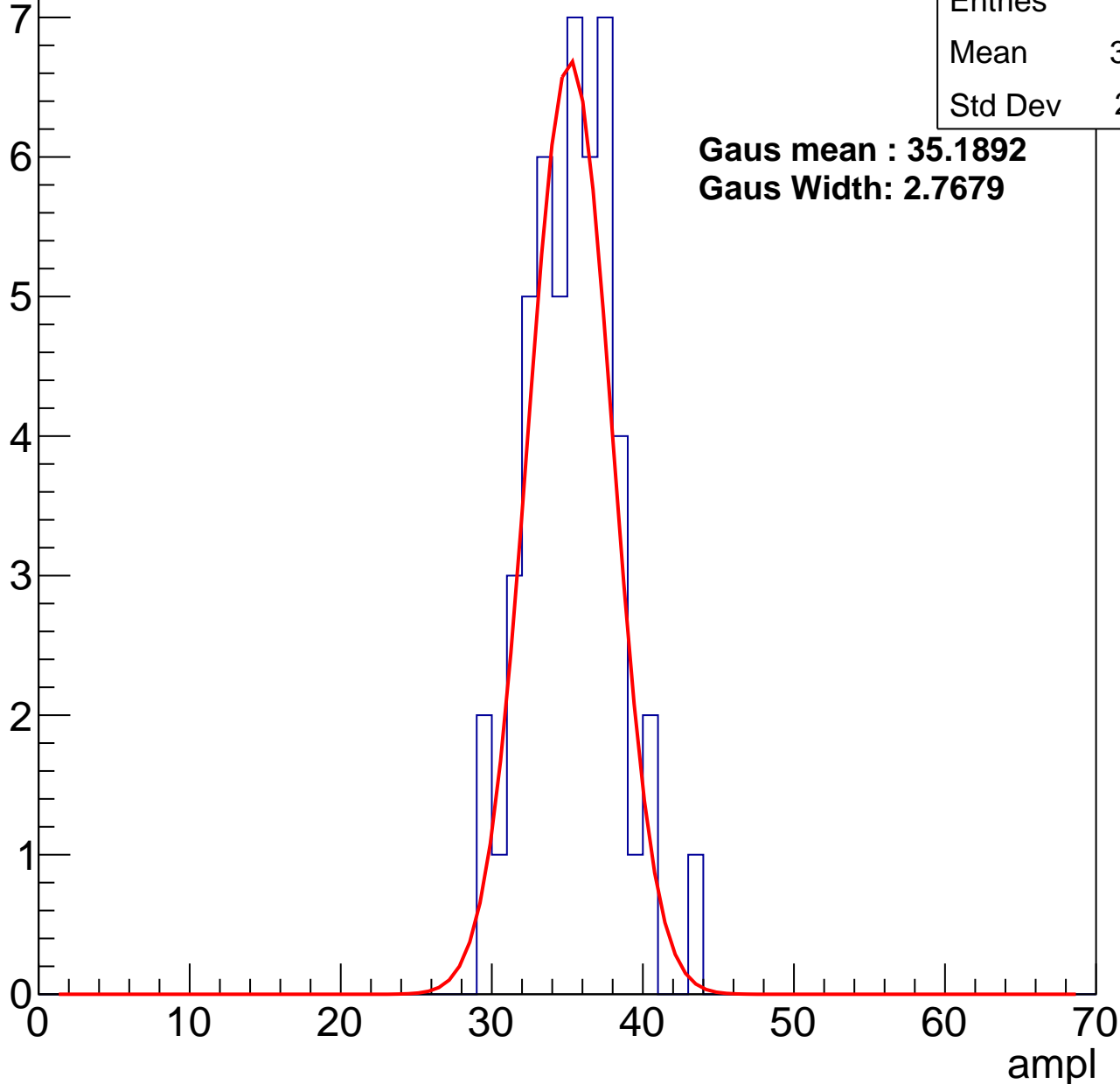
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	34.86
Std Dev	2.891

**Gaus mean : 35.1892**

**Gaus Width: 2.7679**



# B1L000S, U23-ch52, adc2

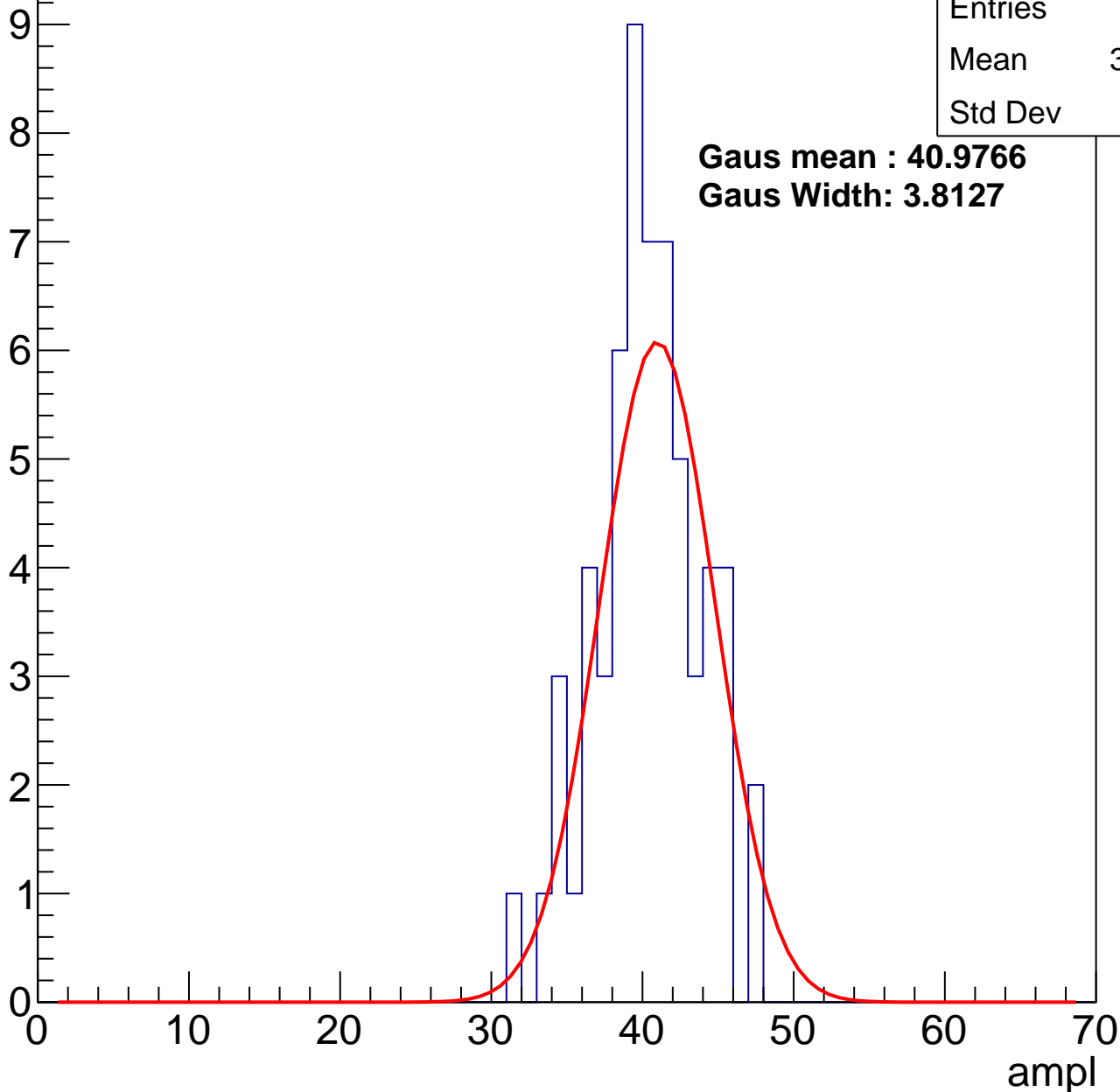
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	39.85
Std Dev	3.42

**Gaus mean : 40.9766**

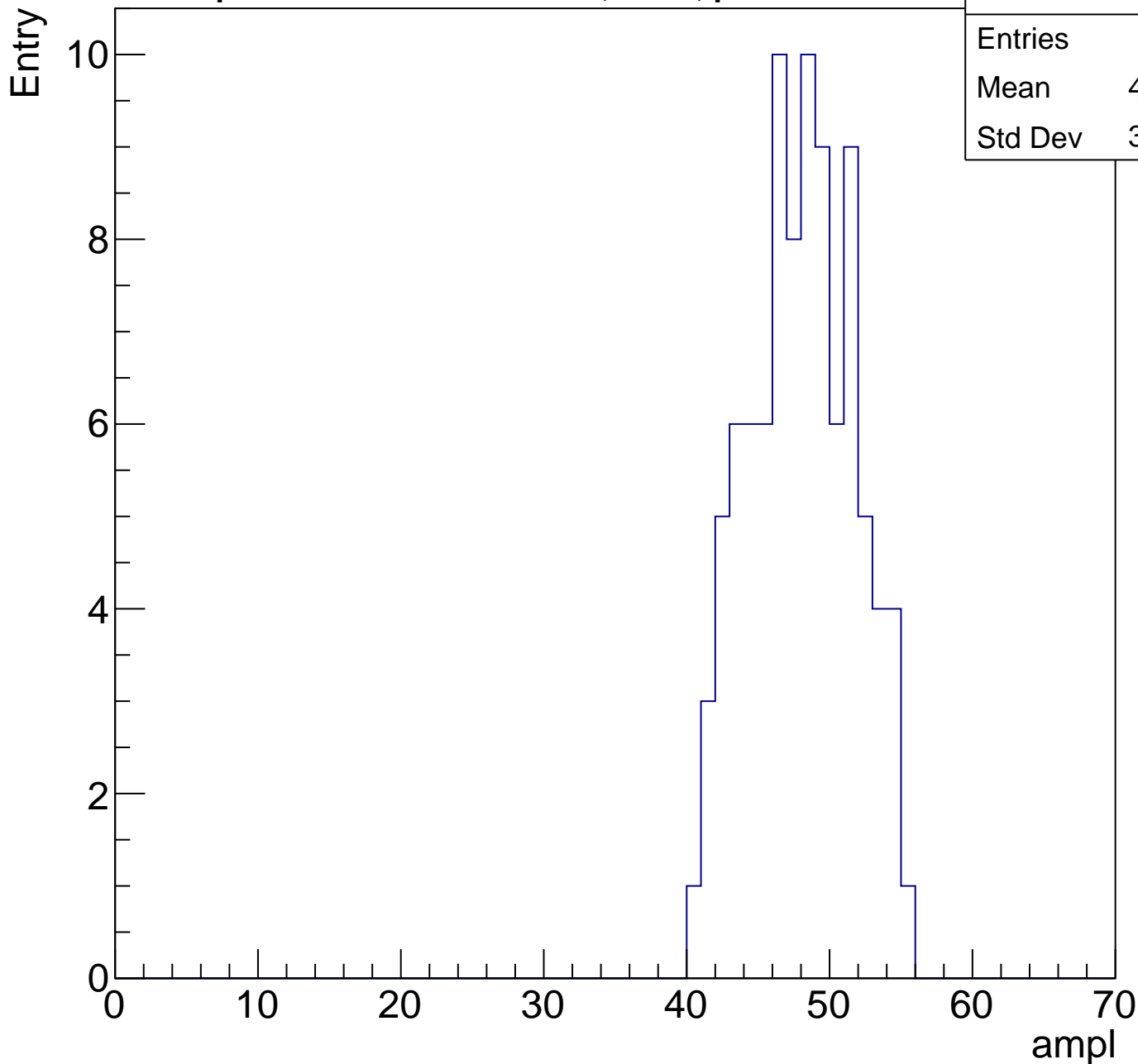
**Gaus Width: 3.8127**



# B1L000S, U23-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	93
Mean	47.57
Std Dev	3.605

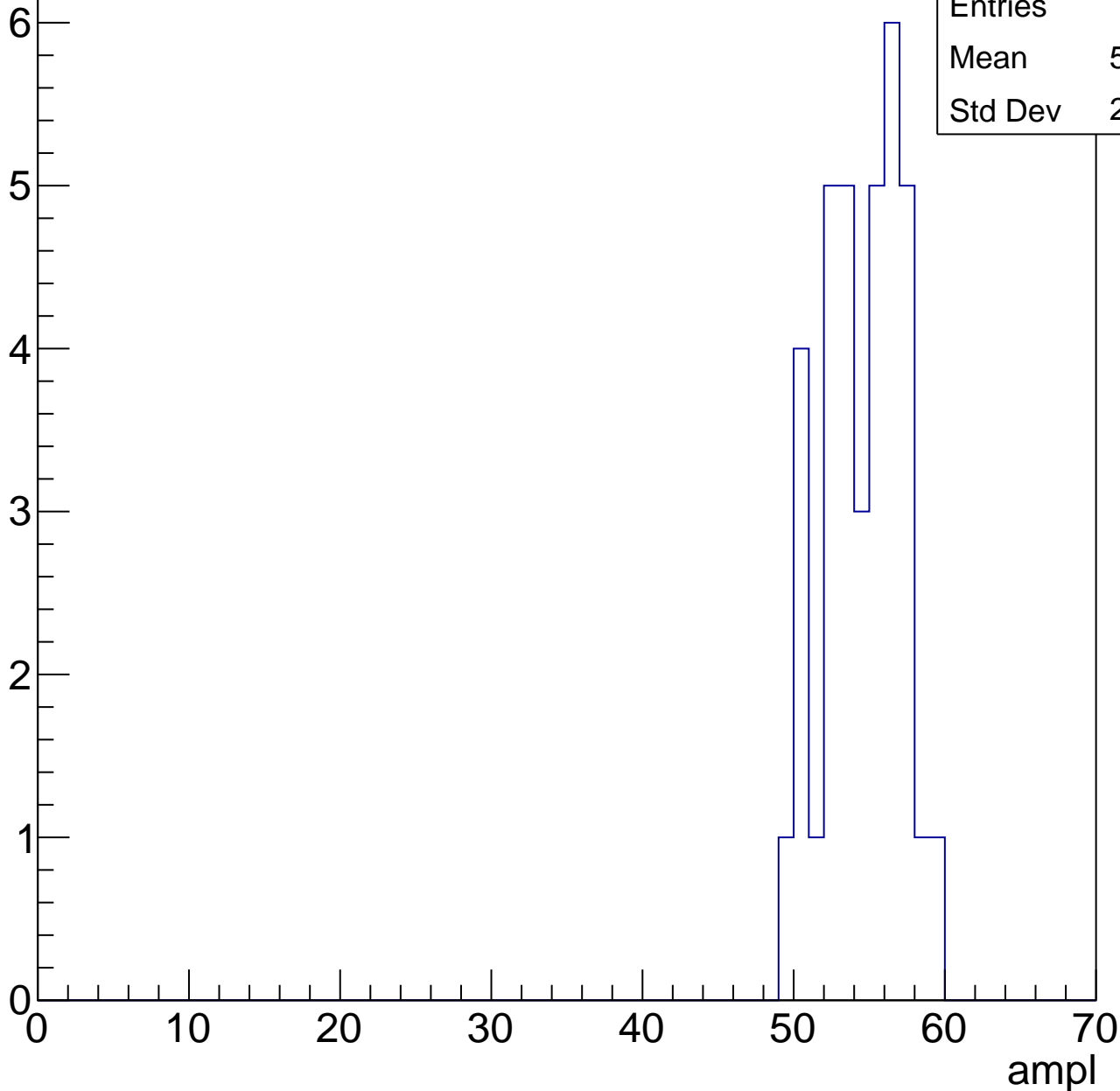


# B1L000S, U23-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	37
Mean	54.05
Std Dev	2.536



# B1L000S, U23-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 65

Mean 58.15

Std Dev 7.719

8

6

4

2

0

0

10

20

30

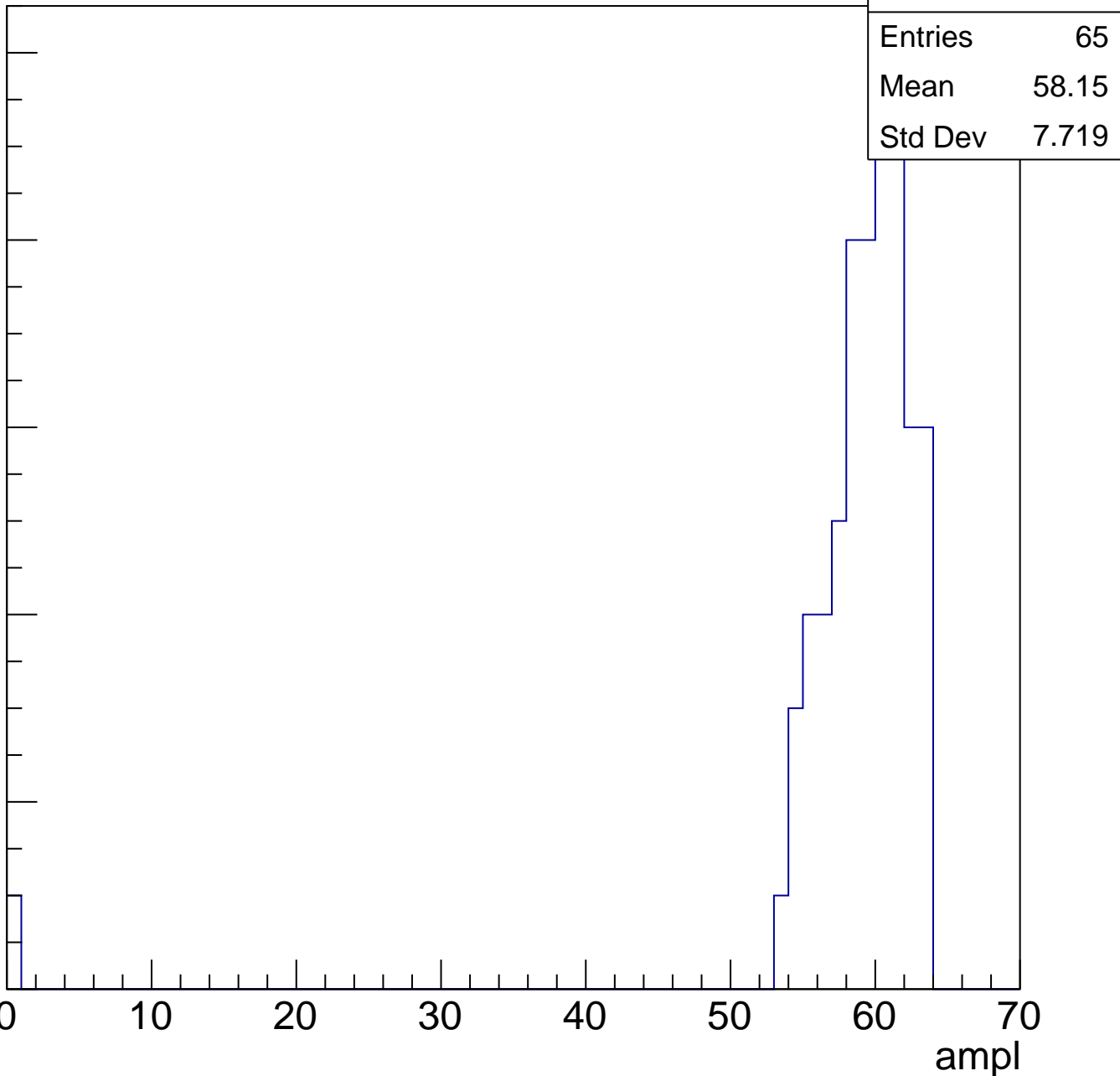
40

50

60

70

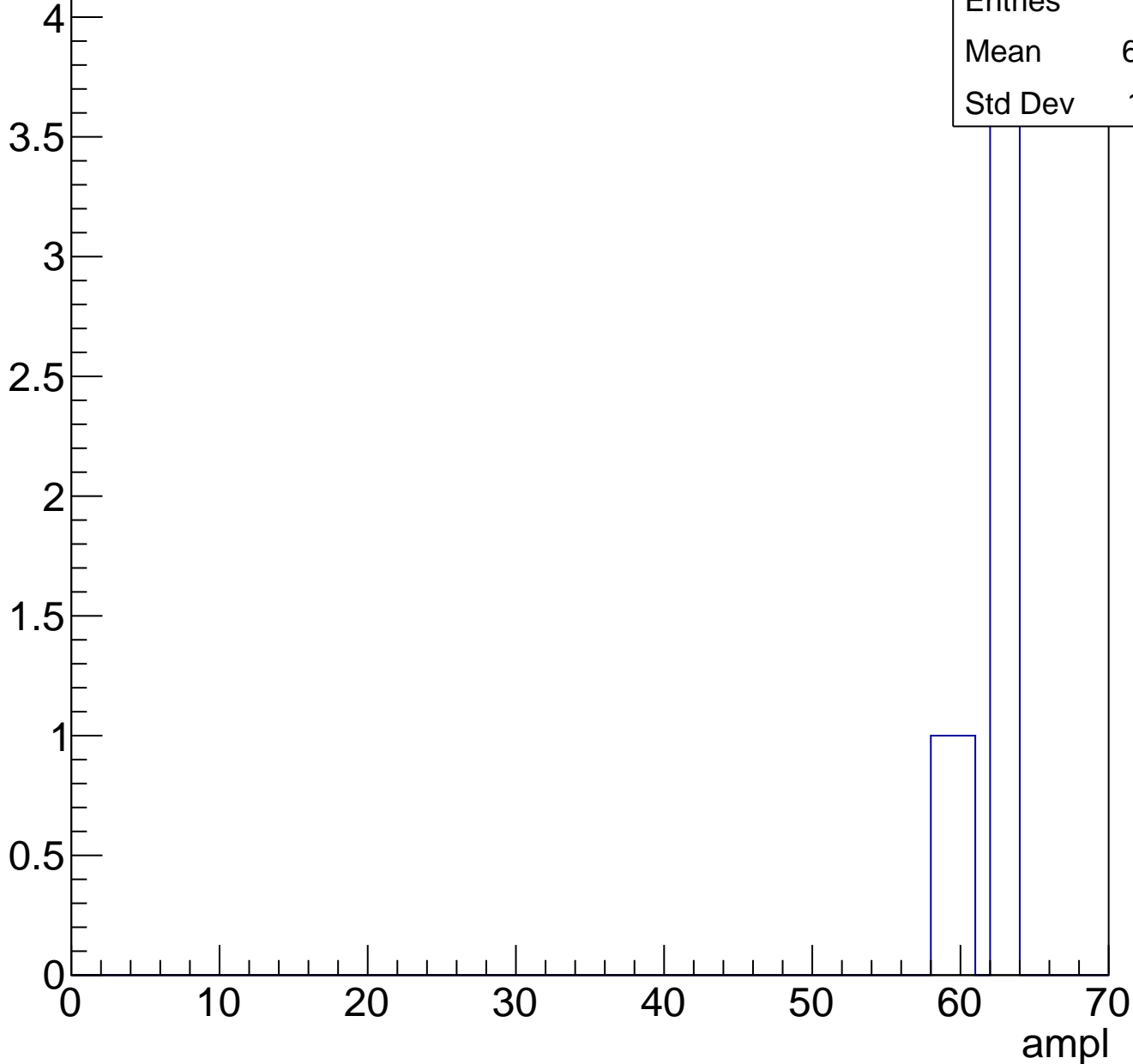
ampl



# B1L000S, U23-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch53, adc0

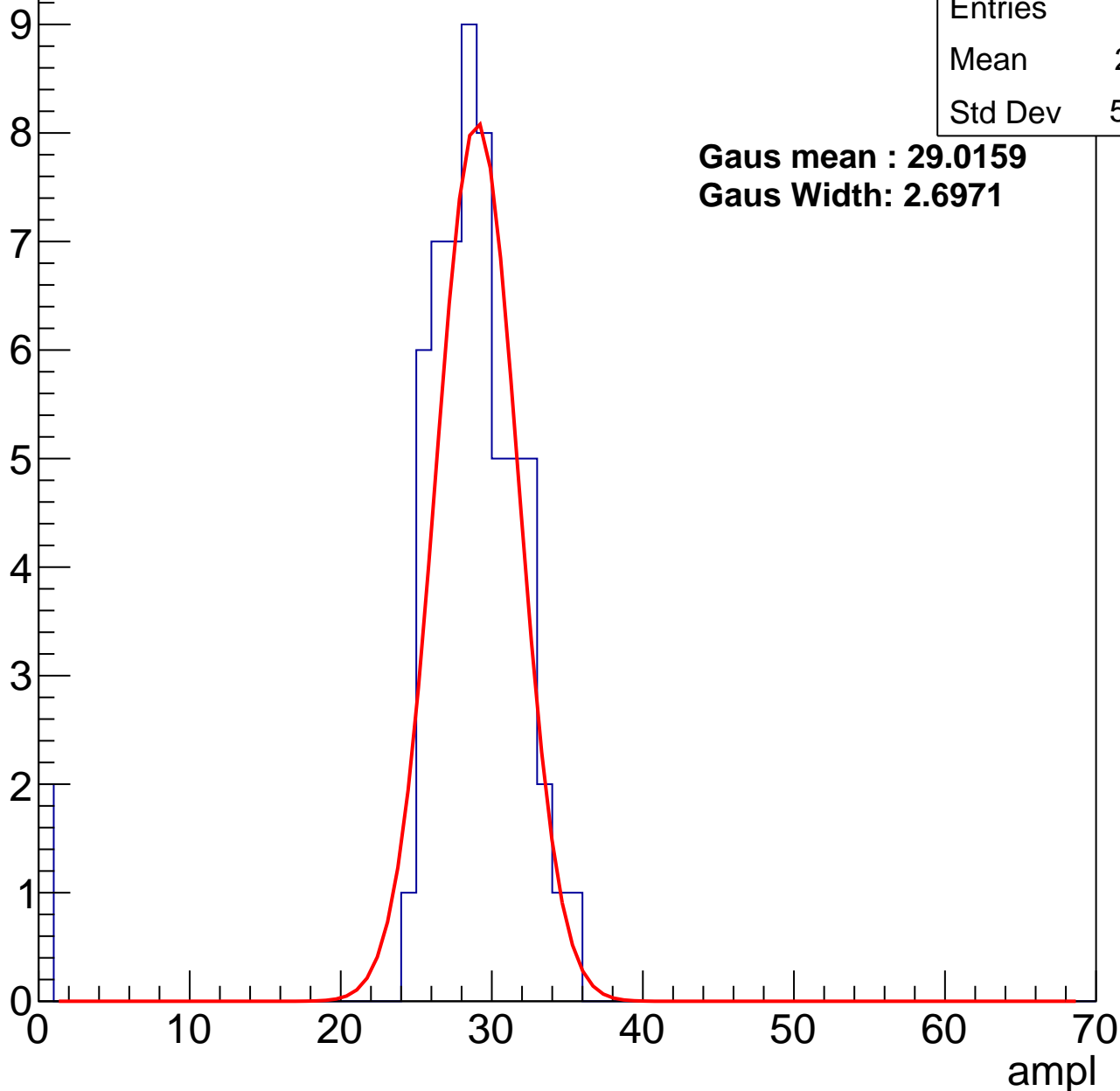
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.61
Std Dev	5.752

**Gaus mean : 29.0159**

**Gaus Width: 2.6971**



# B1L000S, U23-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	35.07
Std Dev	3.242

**Gaus mean : 35.4962**

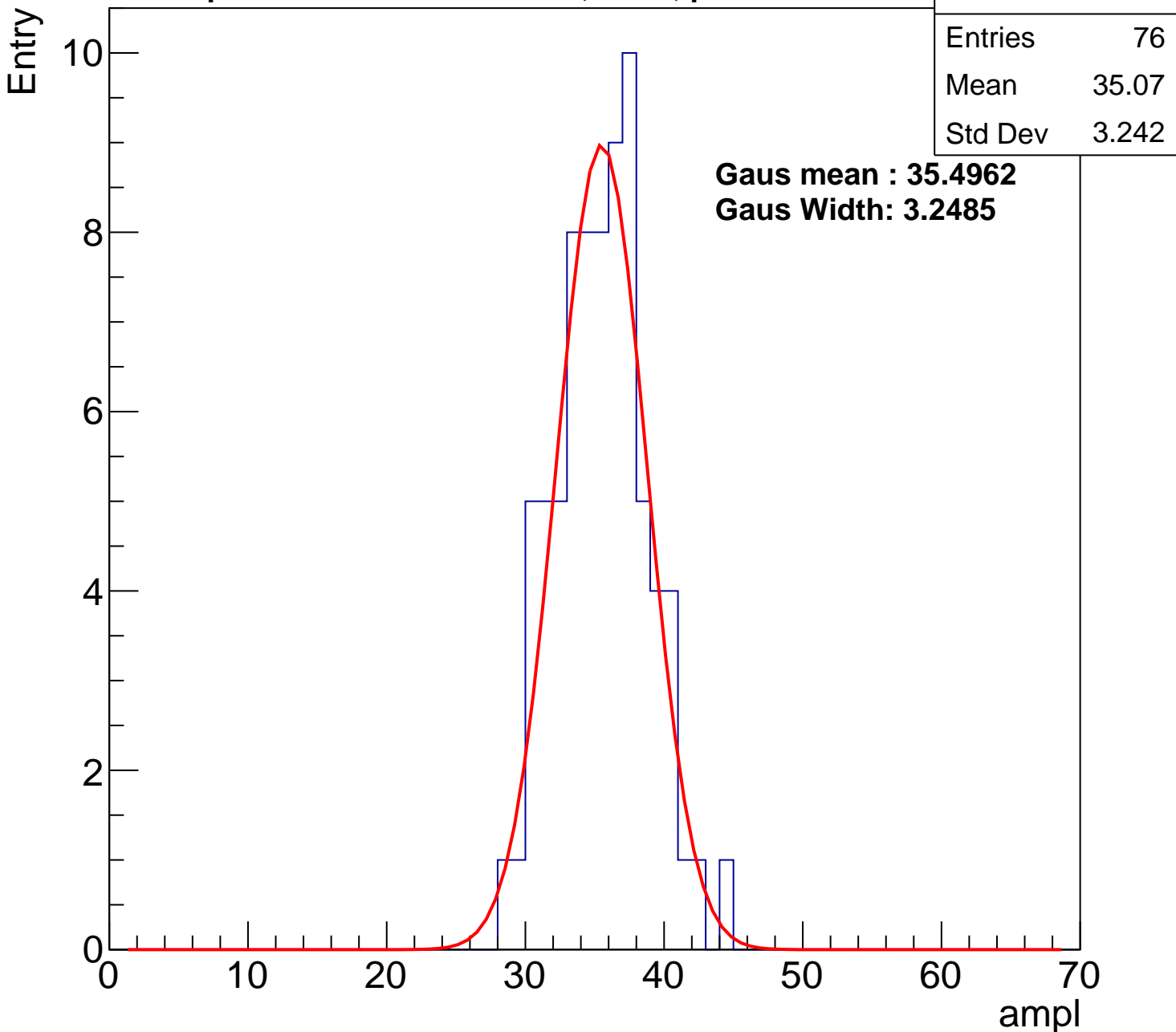
**Gaus Width: 3.2485**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch53, adc2

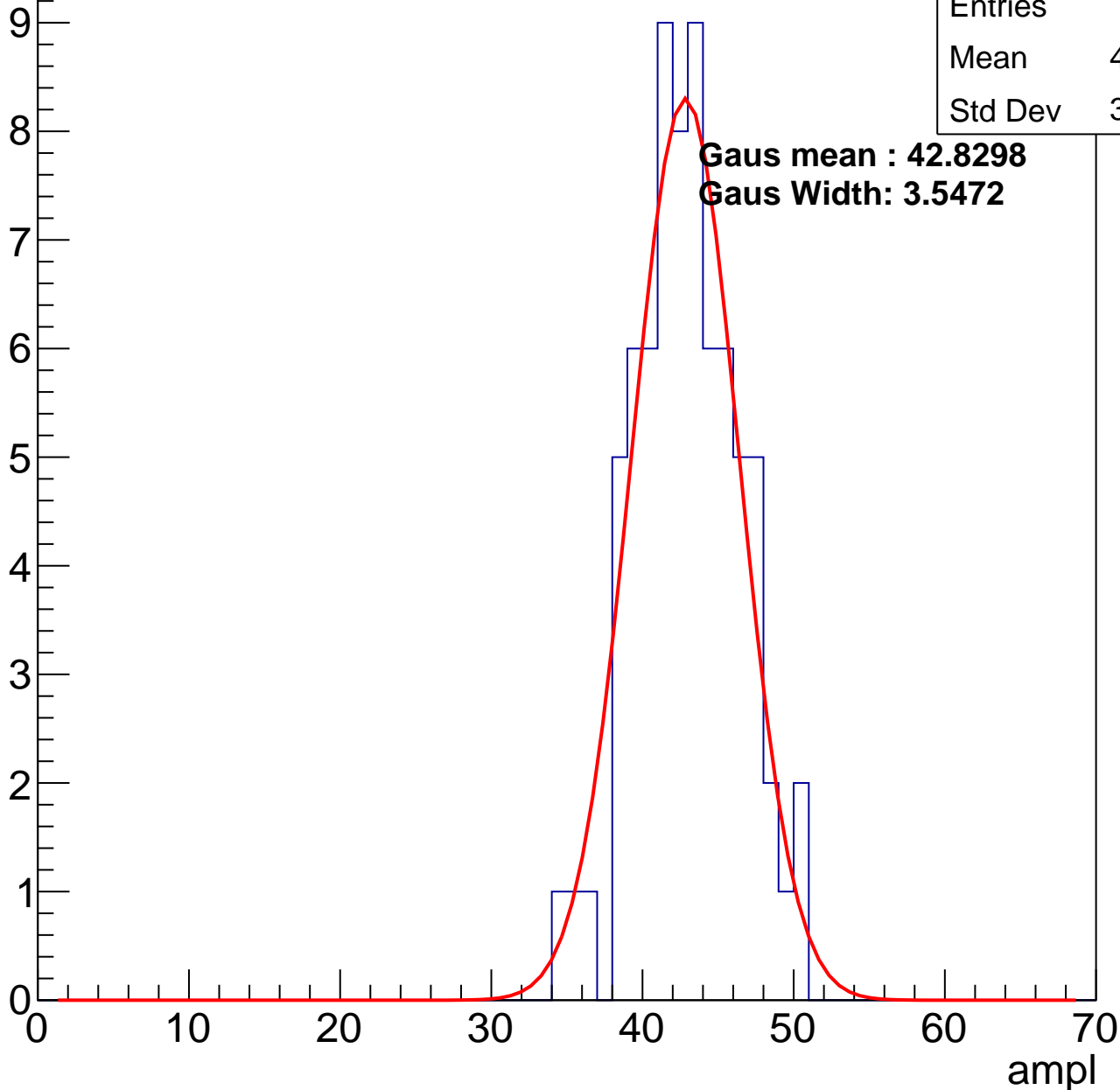
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	42.53
Std Dev	3.384

**Gaus mean : 42.8298**

**Gaus Width: 3.5472**

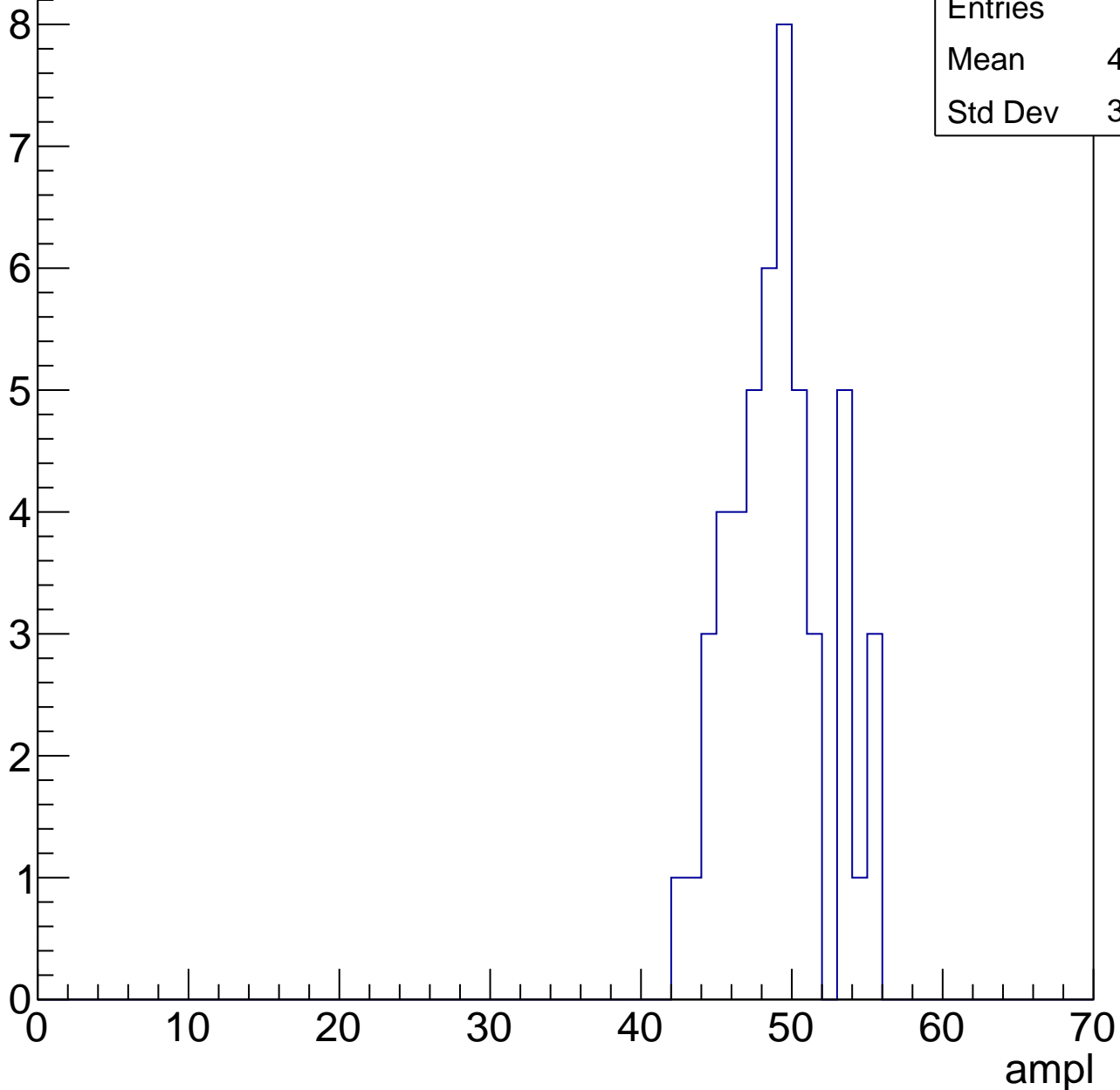


# B1L000S, U23-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	48.63
Std Dev	3.224

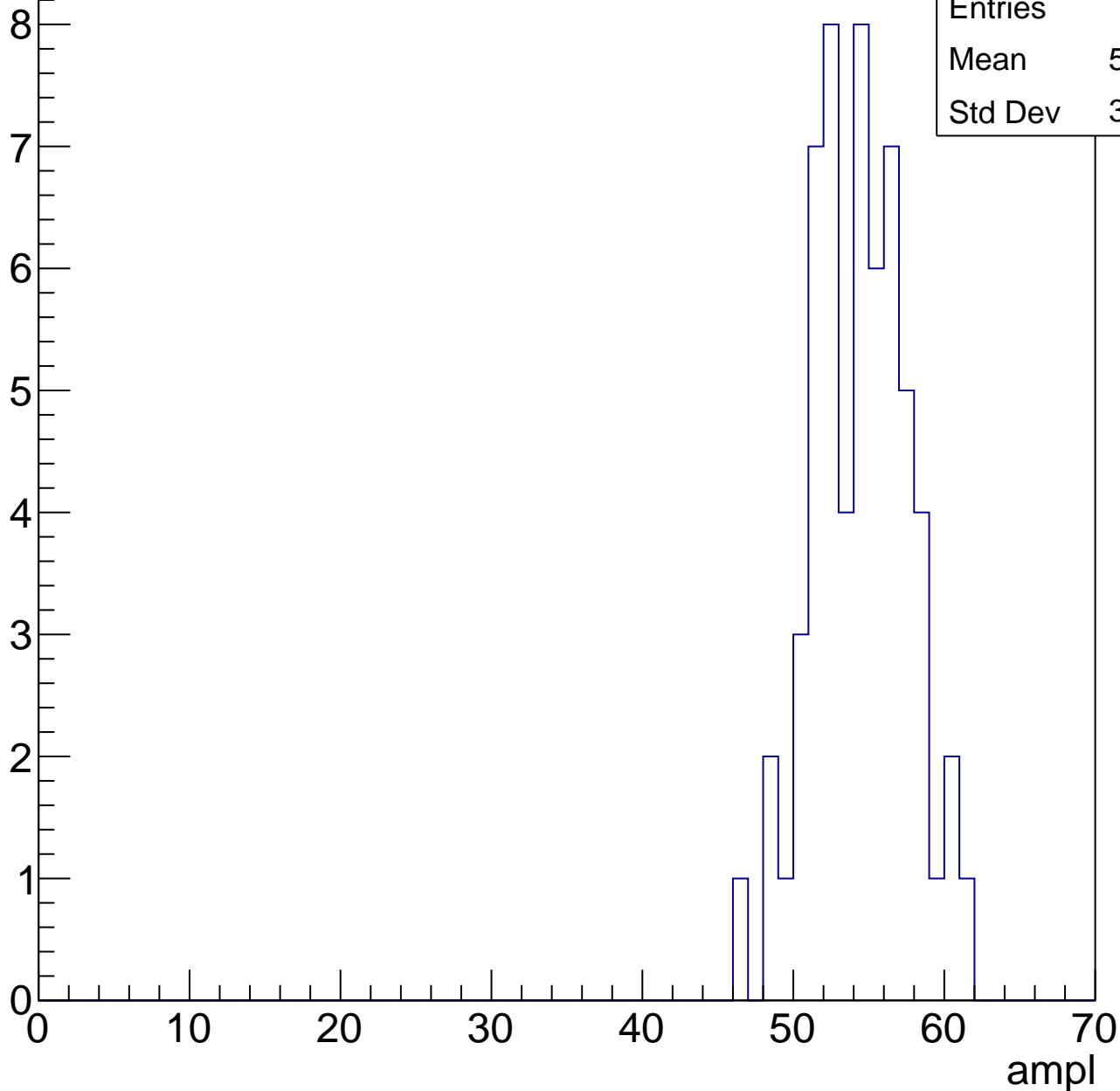


# B1L000S, U23-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

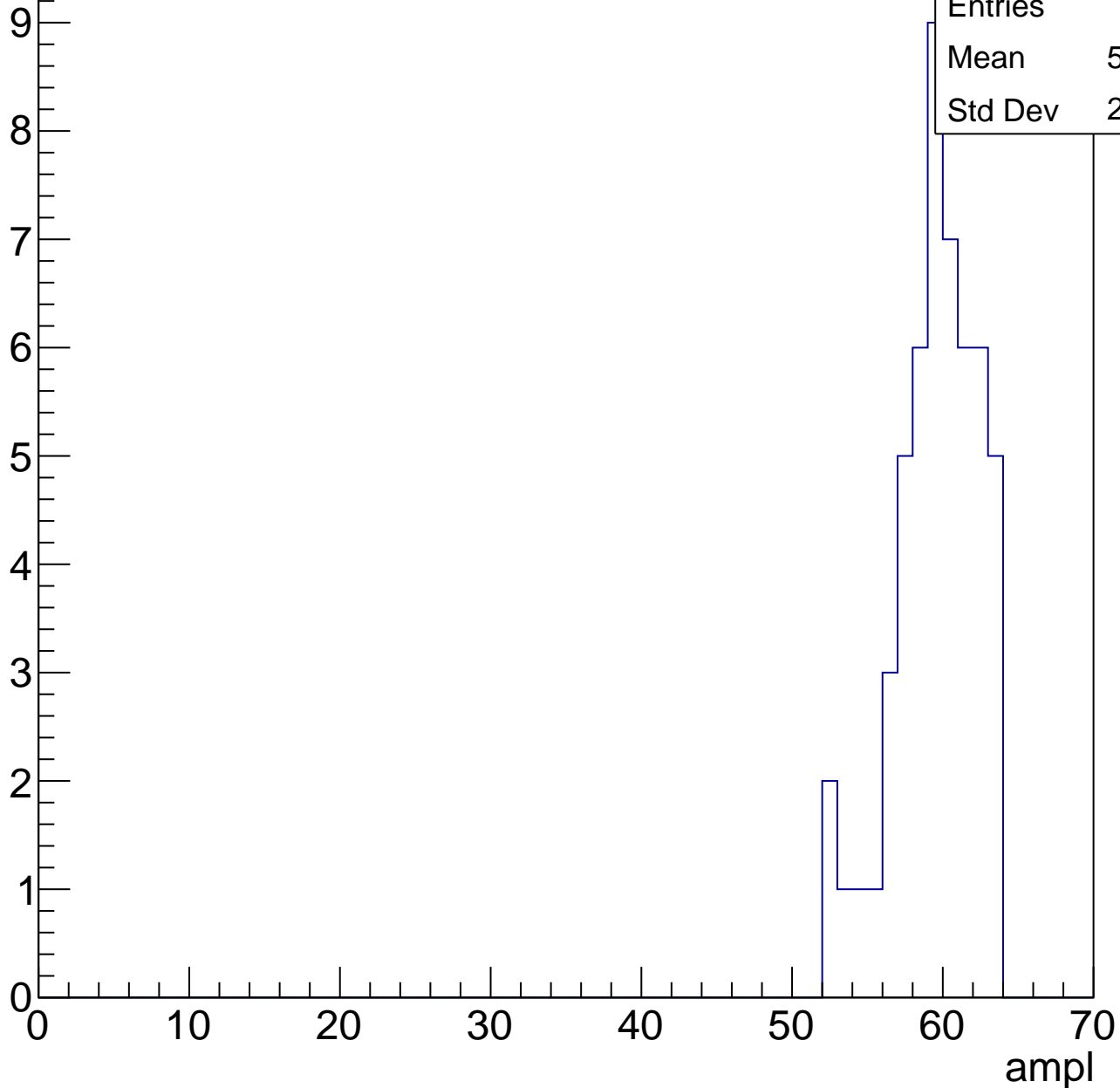
Entries	60
Mean	53.95
Std Dev	3.154



# B1L000S, U23-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



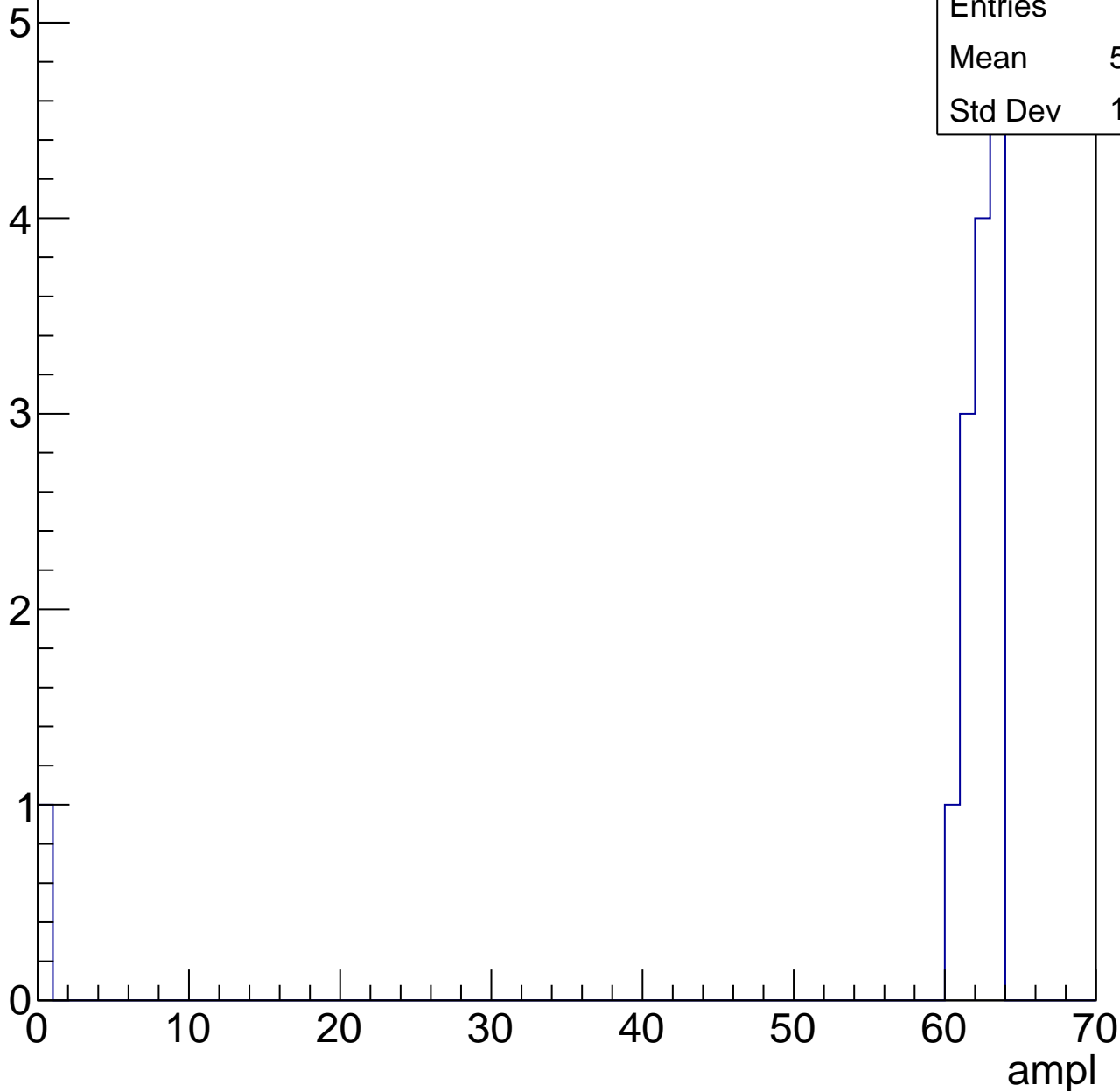
Entries	52
Mean	59.06
Std Dev	2.749

# B1L000S, U23-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.57
Std Dev	15.99





# B1L000S, U23-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U23-ch54, adc0

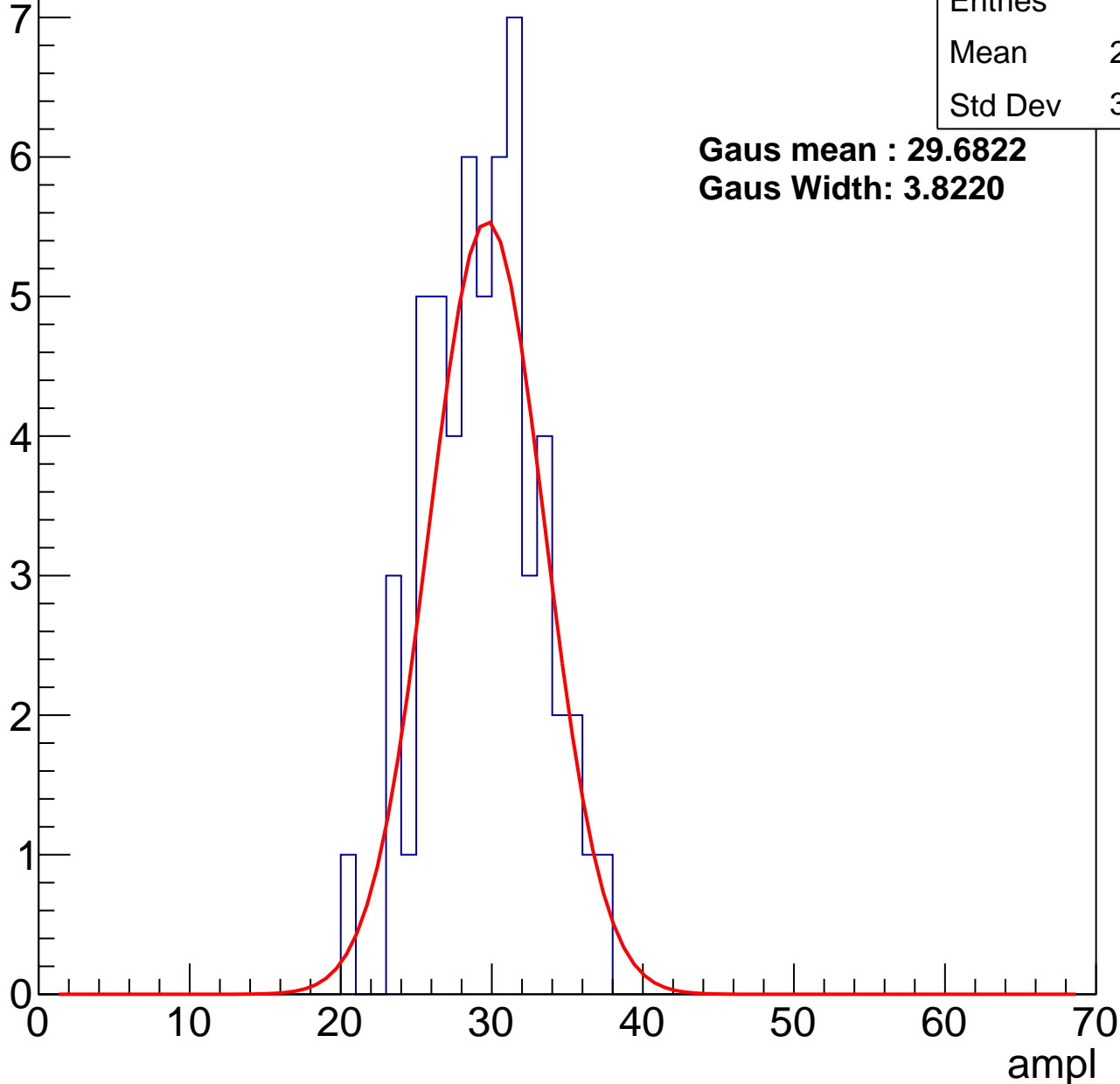
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	29.02
Std Dev	3.588

**Gaus mean : 29.6822**

**Gaus Width: 3.8220**



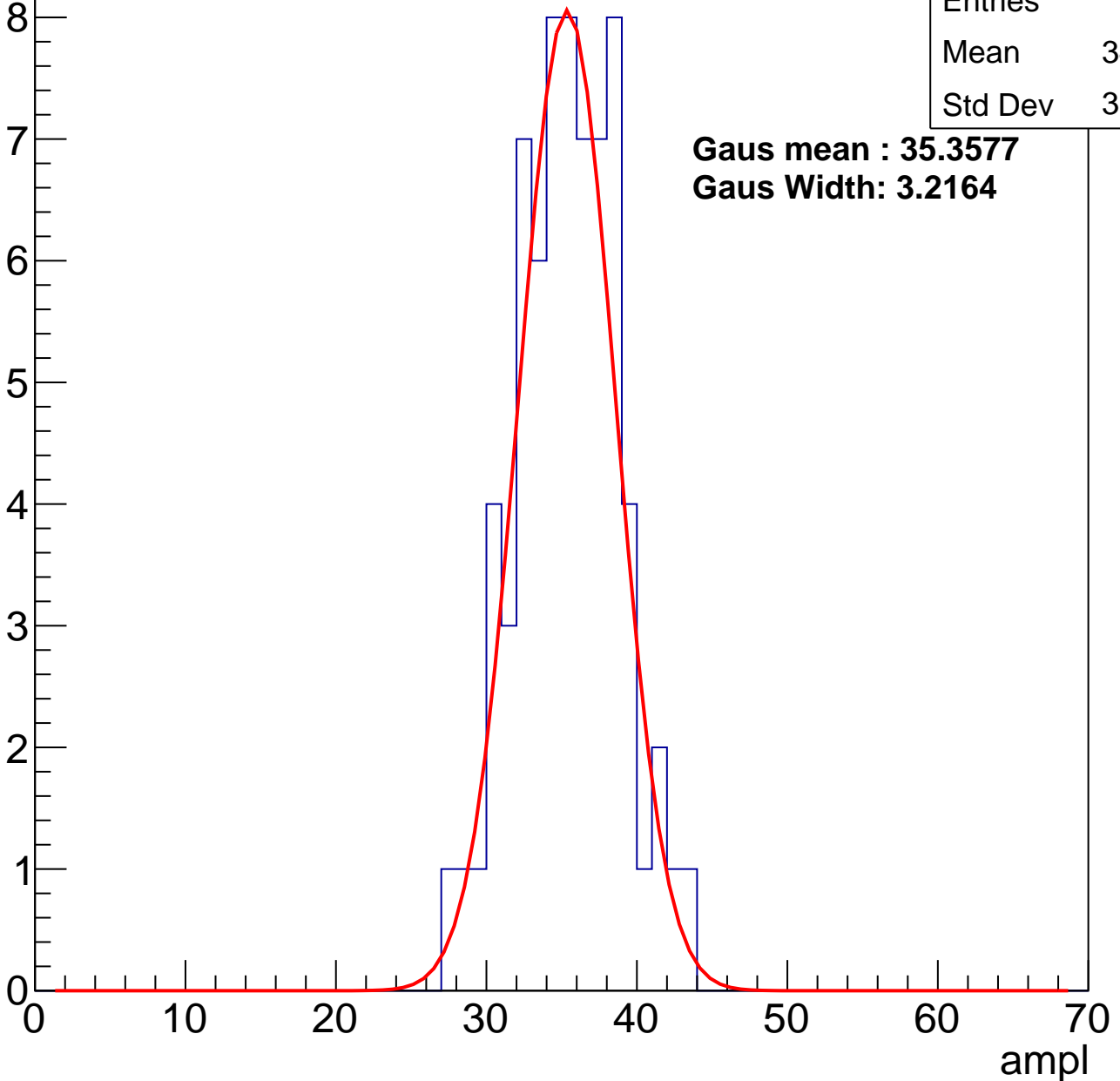
# B1L000S, U23-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	34.99
Std Dev	3.323

**Gaus mean : 35.3577**  
**Gaus Width: 3.2164**



# B1L000S, U23-ch54, adc2

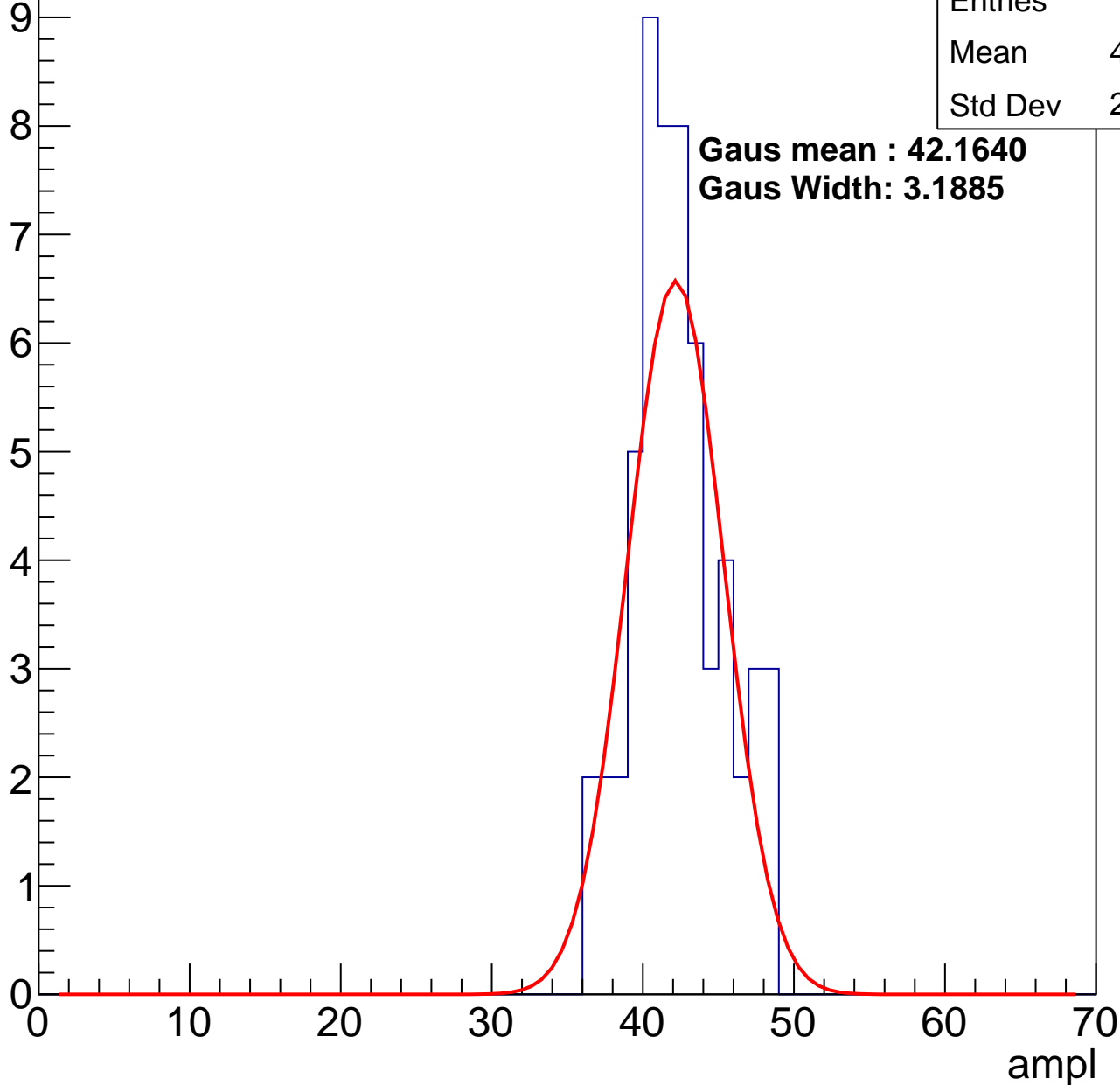
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.89
Std Dev	2.995

**Gaus mean : 42.1640**

**Gaus Width: 3.1885**

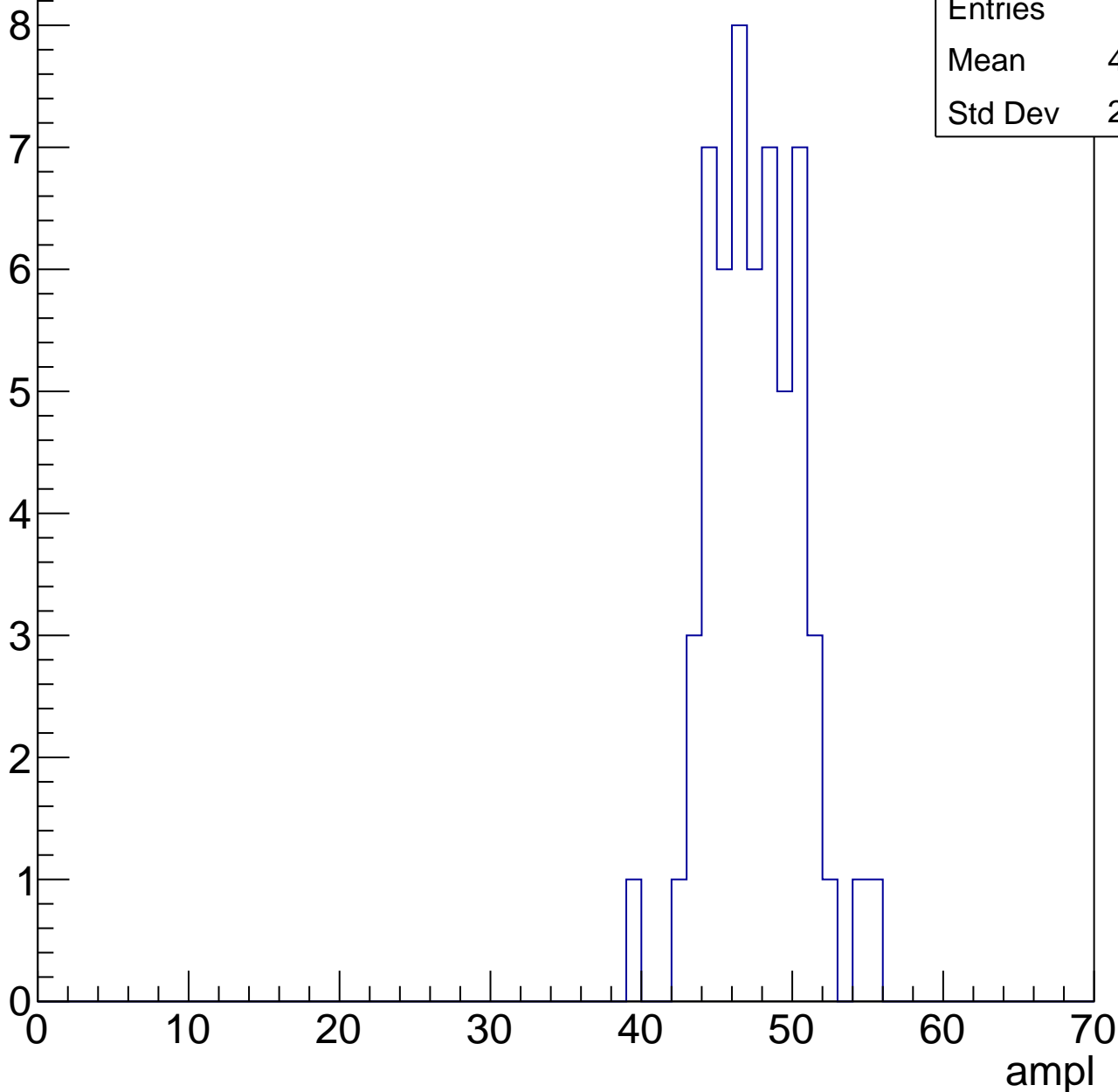


# B1L000S, U23-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

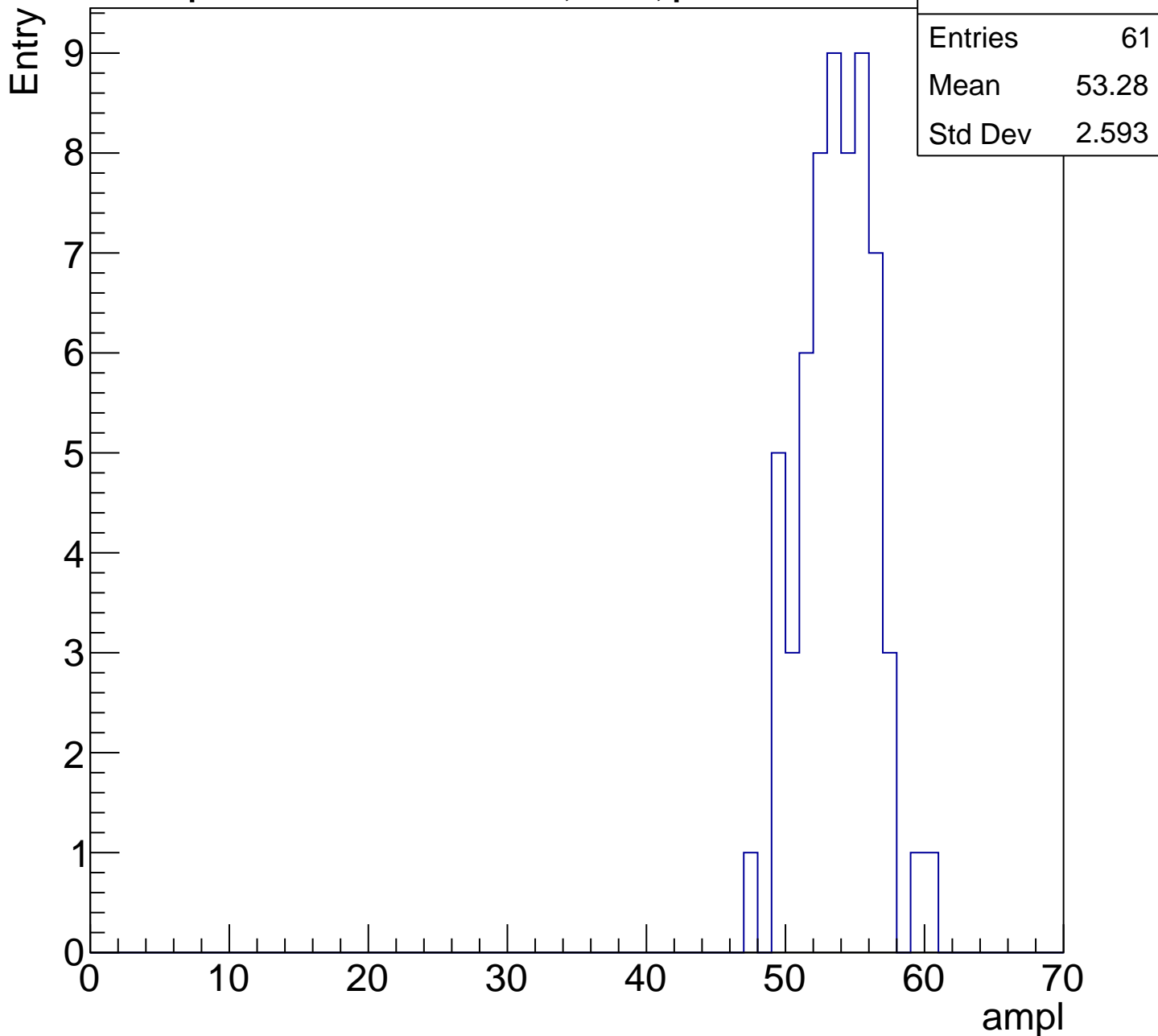
Entry

Entries	57
Mean	47.07
Std Dev	2.985



# B1L000S, U23-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 61

Mean 59.08

Std Dev 2.62

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

Entry

calib\_packv5\_042523\_0143.root, FC#3, port B3

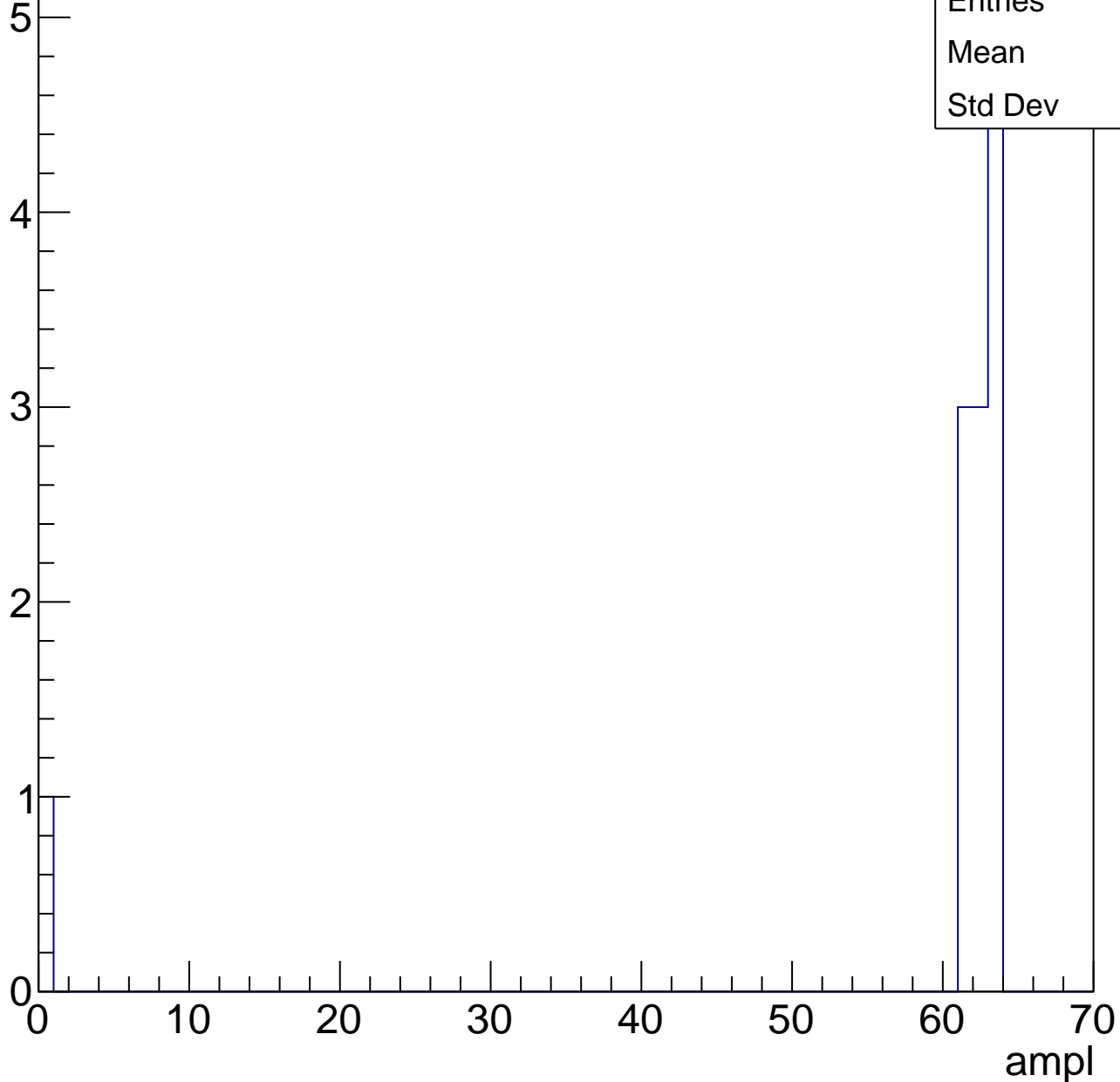
# B1L000S, U23-ch54, adc5

# B1L000S, U23-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	57
Std Dev	17.2





# B1L000S, U23-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	8.5
Std Dev	8.5

# B1L000S, U23-ch55, adc0

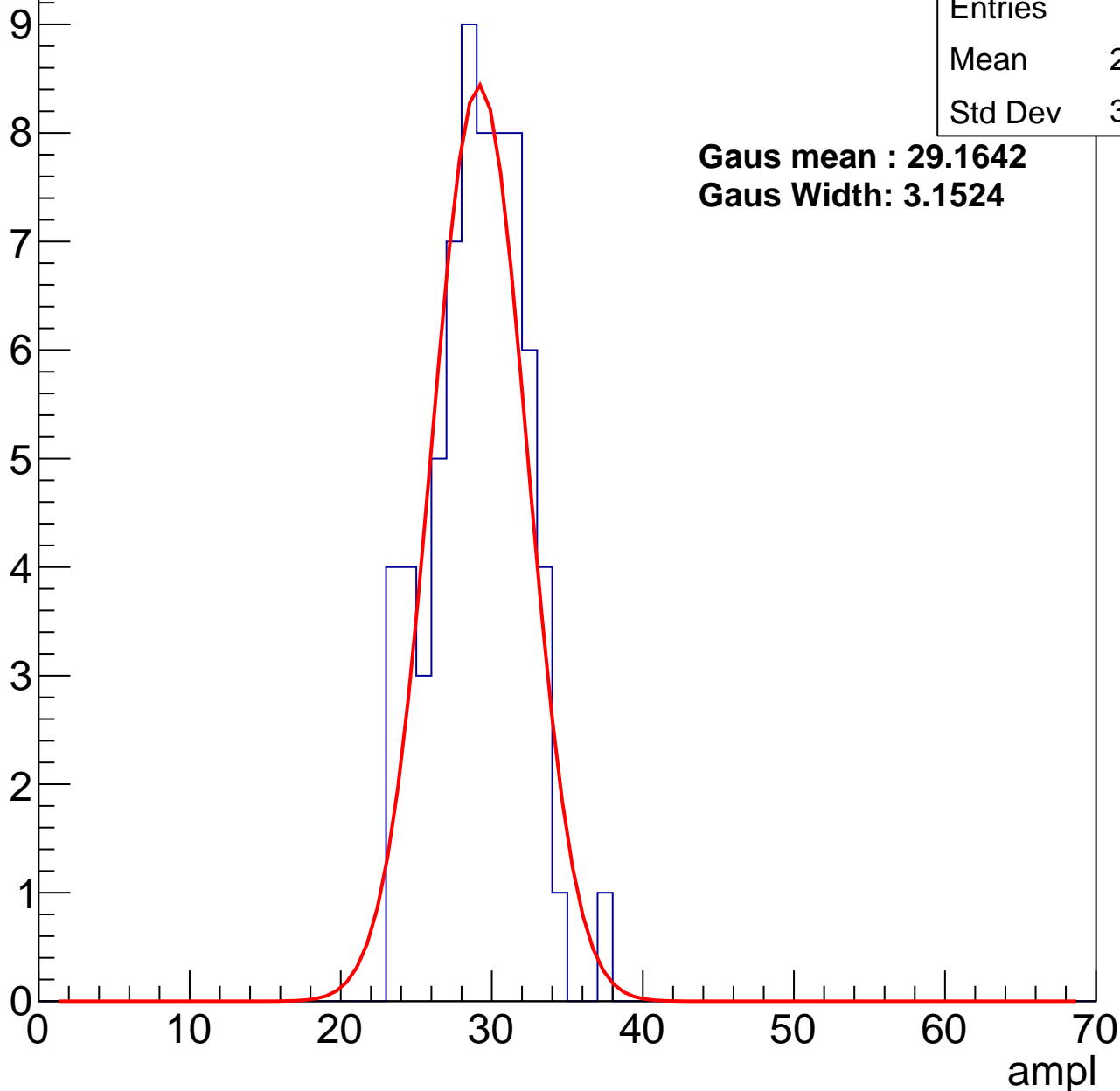
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.66
Std Dev	3.003

**Gaus mean : 29.1642**

**Gaus Width: 3.1524**



# B1L000S, U23-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	35.64
Std Dev	3.381

**Gaus mean : 36.0533**

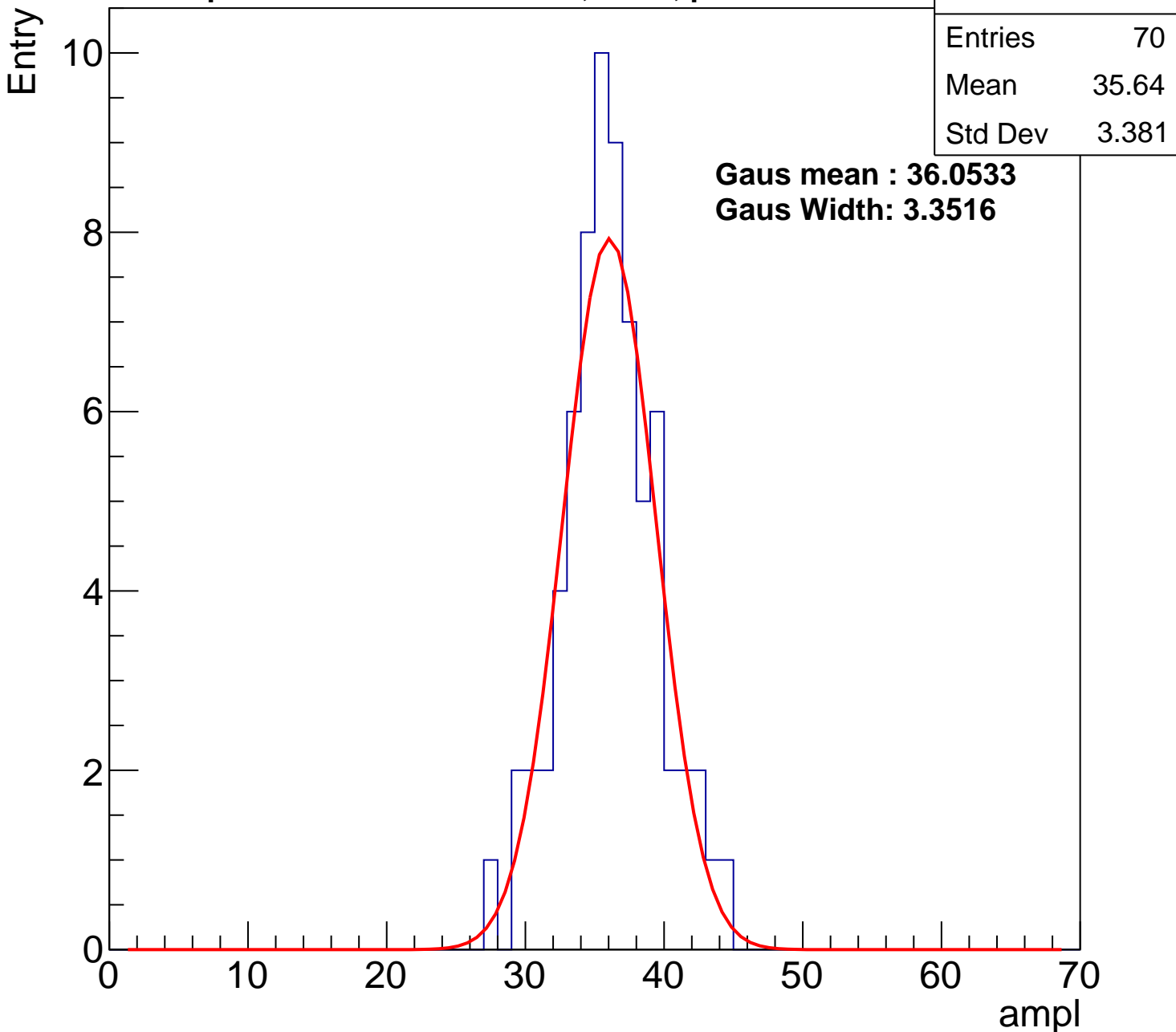
**Gaus Width: 3.3516**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch55, adc2

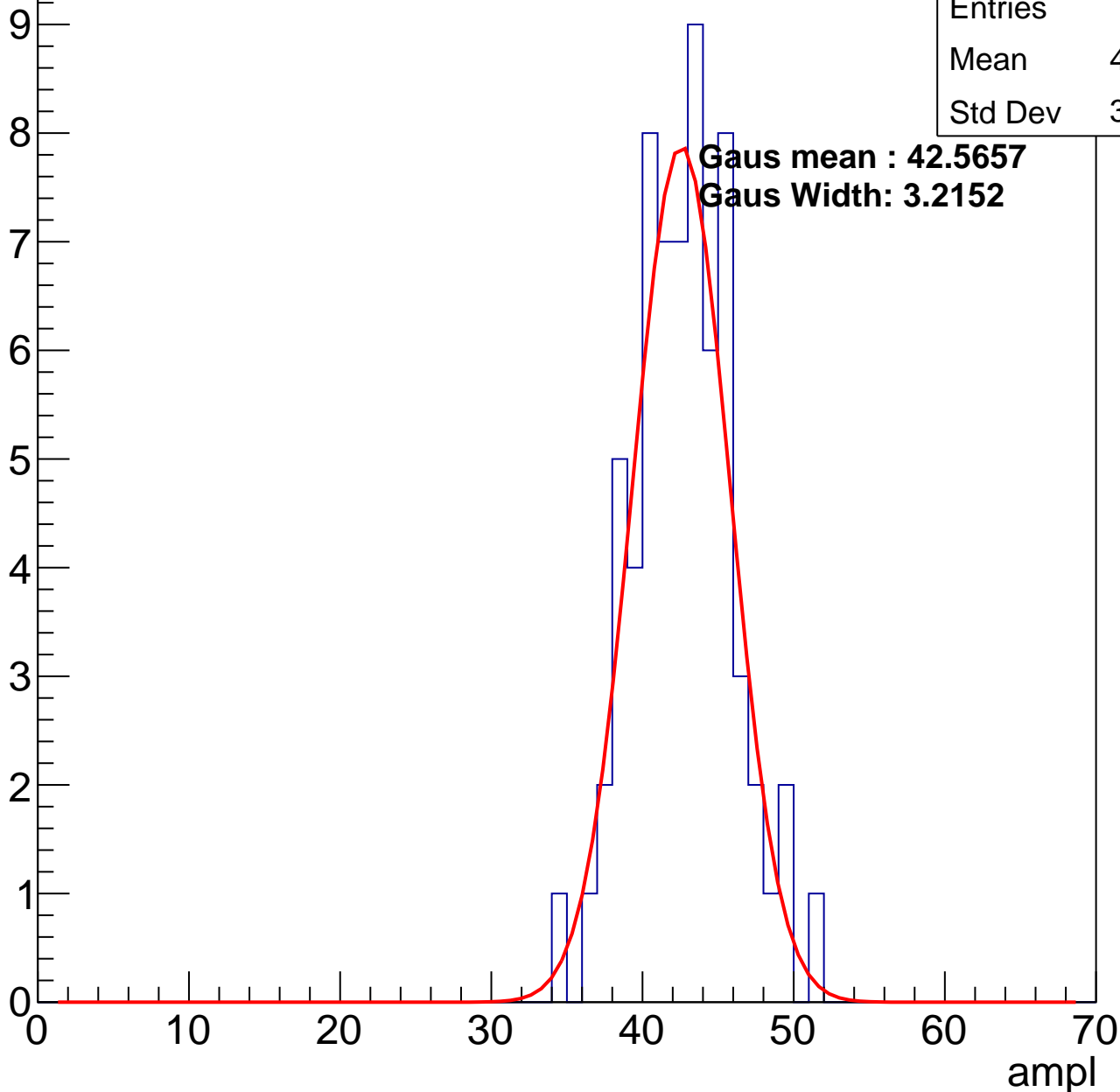
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.25
Std Dev	3.275

**Gaus mean : 42.5657**

**Gaus Width: 3.2152**

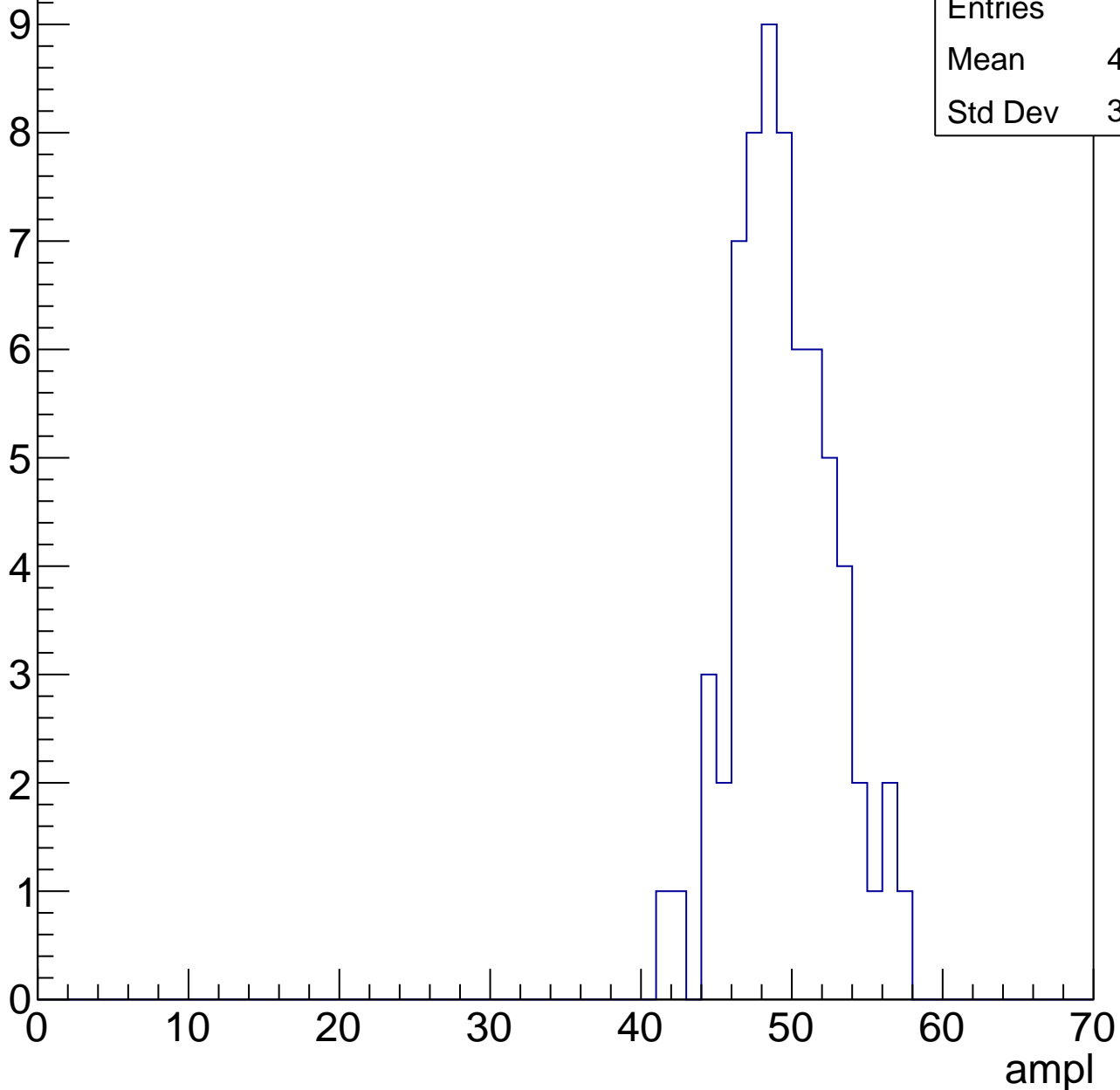


# B1L000S, U23-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	49.05
Std Dev	3.282

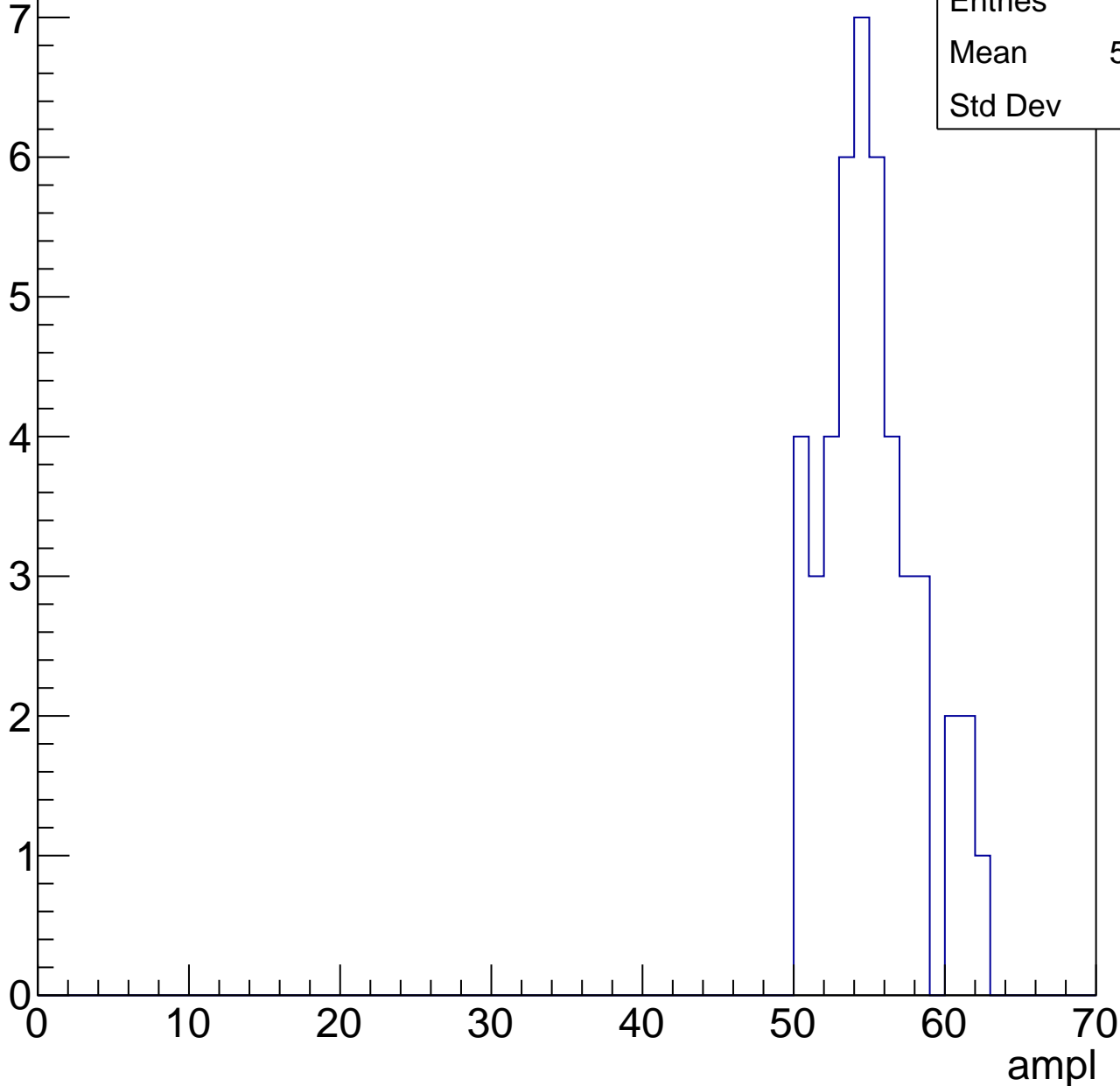


# B1L000S, U23-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	54.67
Std Dev	3.07

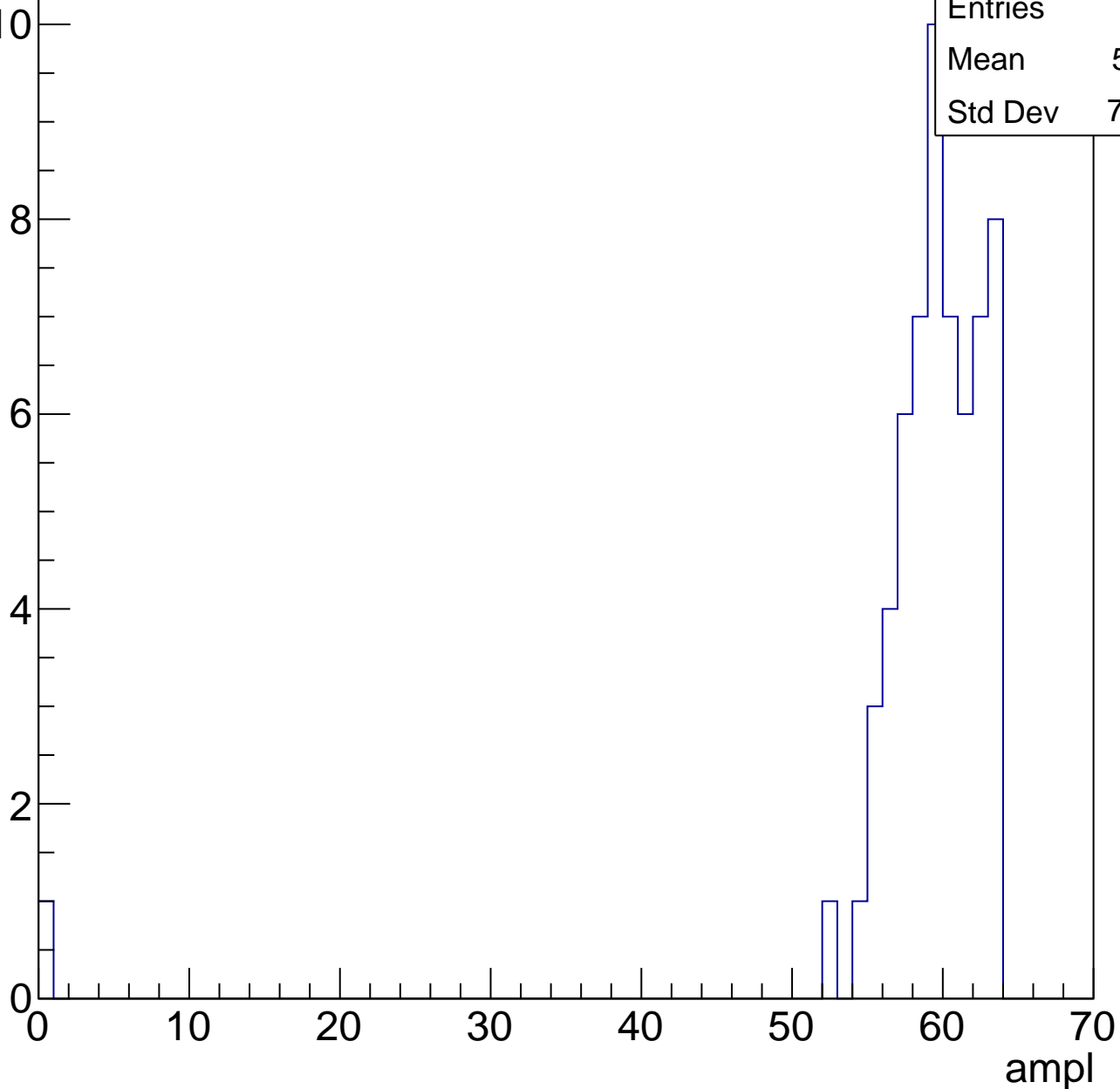


# B1L000S, U23-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.31
Std Dev	7.958



# B1L000S, U23-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L000S, U23-ch56, adc0

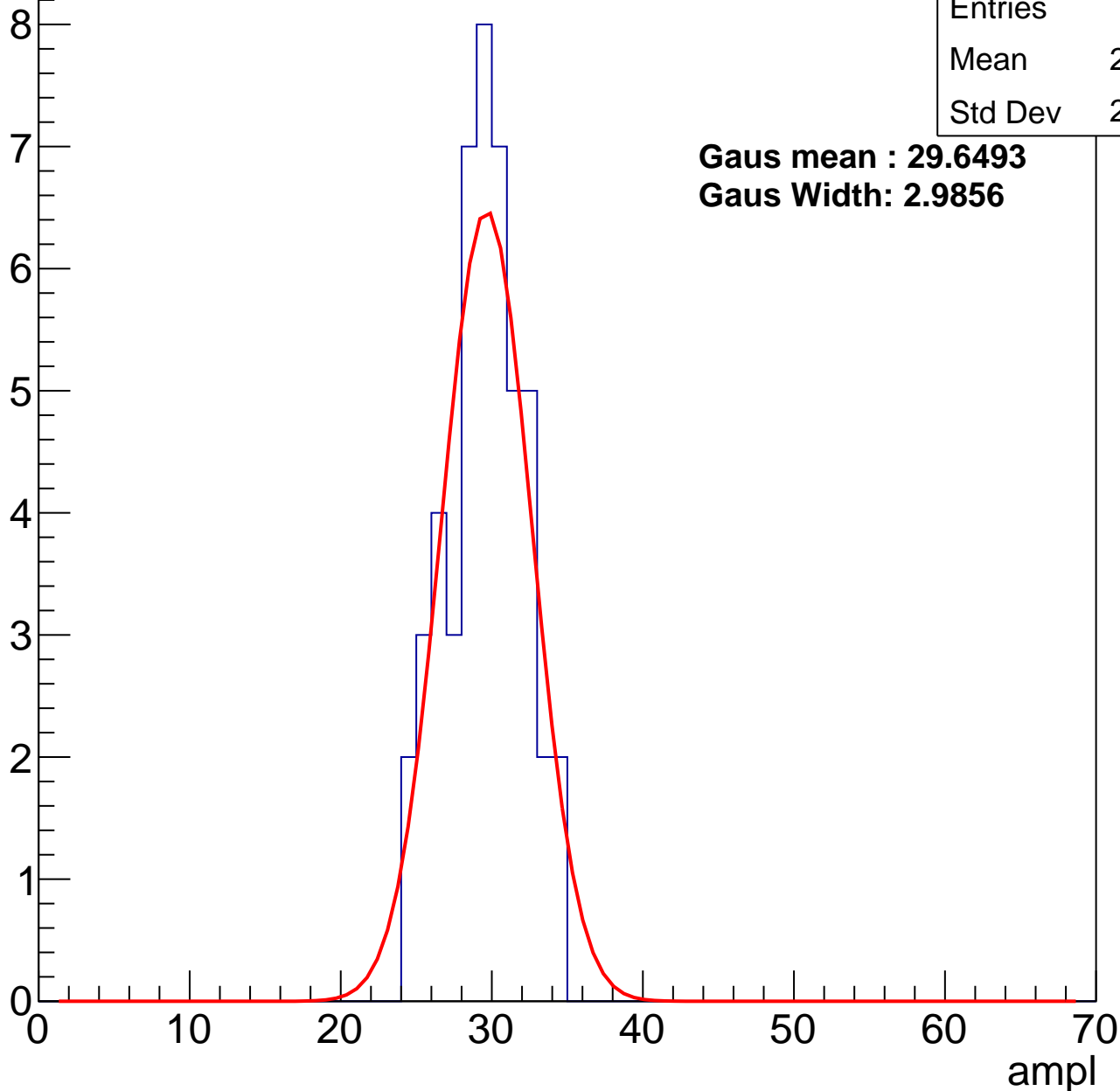
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	29.06
Std Dev	2.528

**Gaus mean : 29.6493**

**Gaus Width: 2.9856**



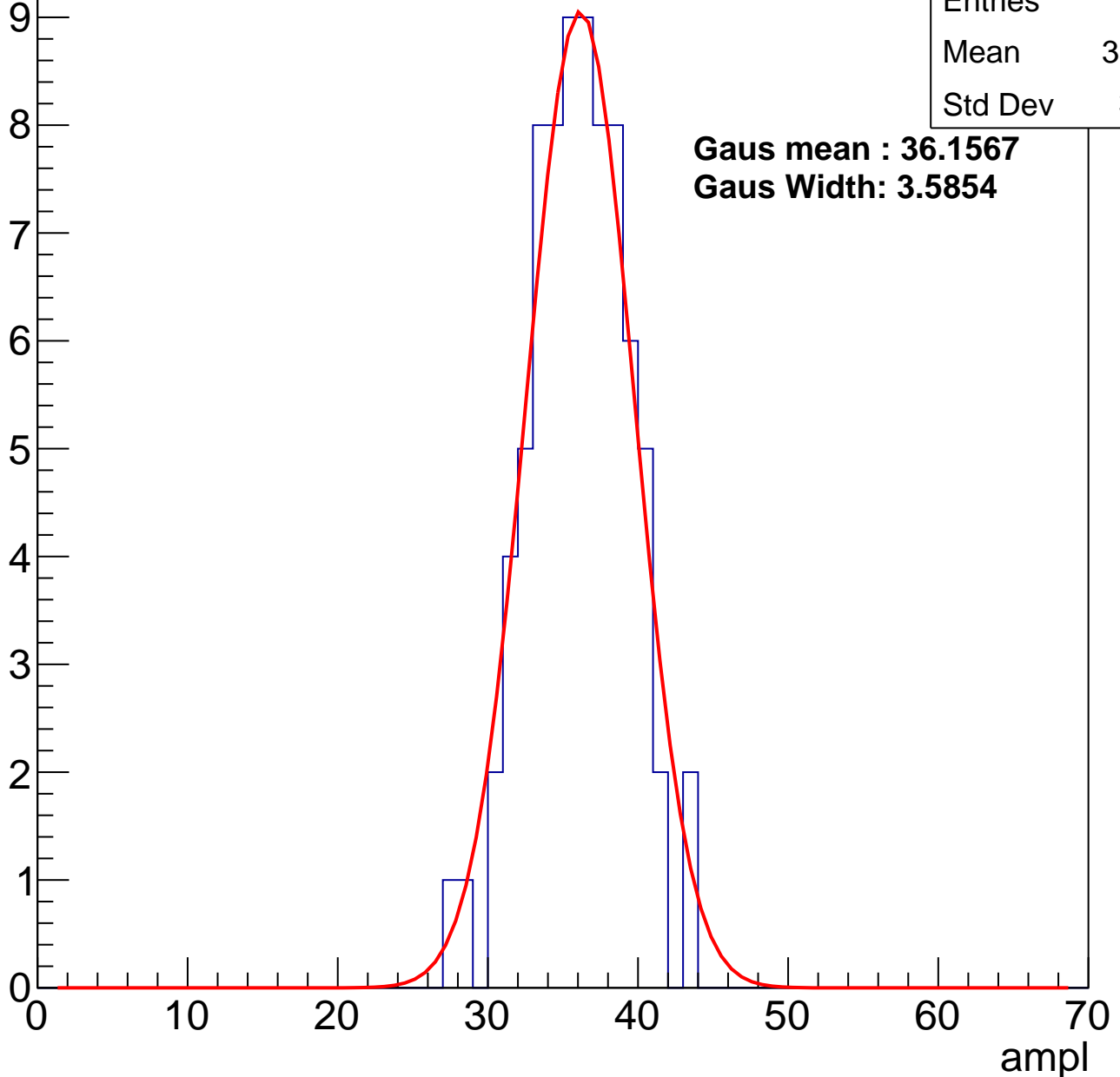
# B1L000S, U23-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	35.59
Std Dev	3.24

**Gaus mean : 36.1567**  
**Gaus Width: 3.5854**



# B1L000S, U23-ch56, adc2

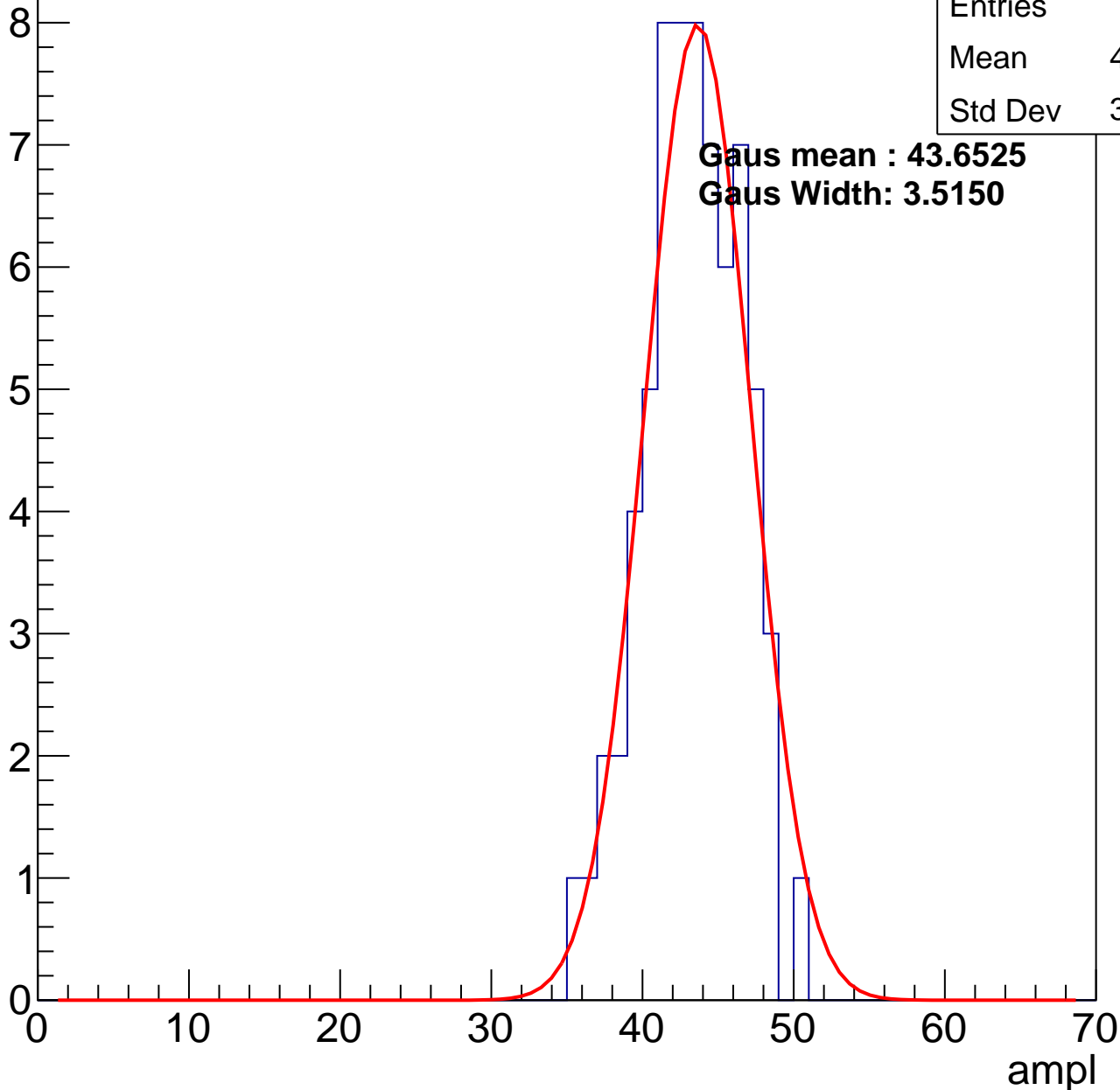
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.85
Std Dev	3.164

**Gaus mean : 43.6525**

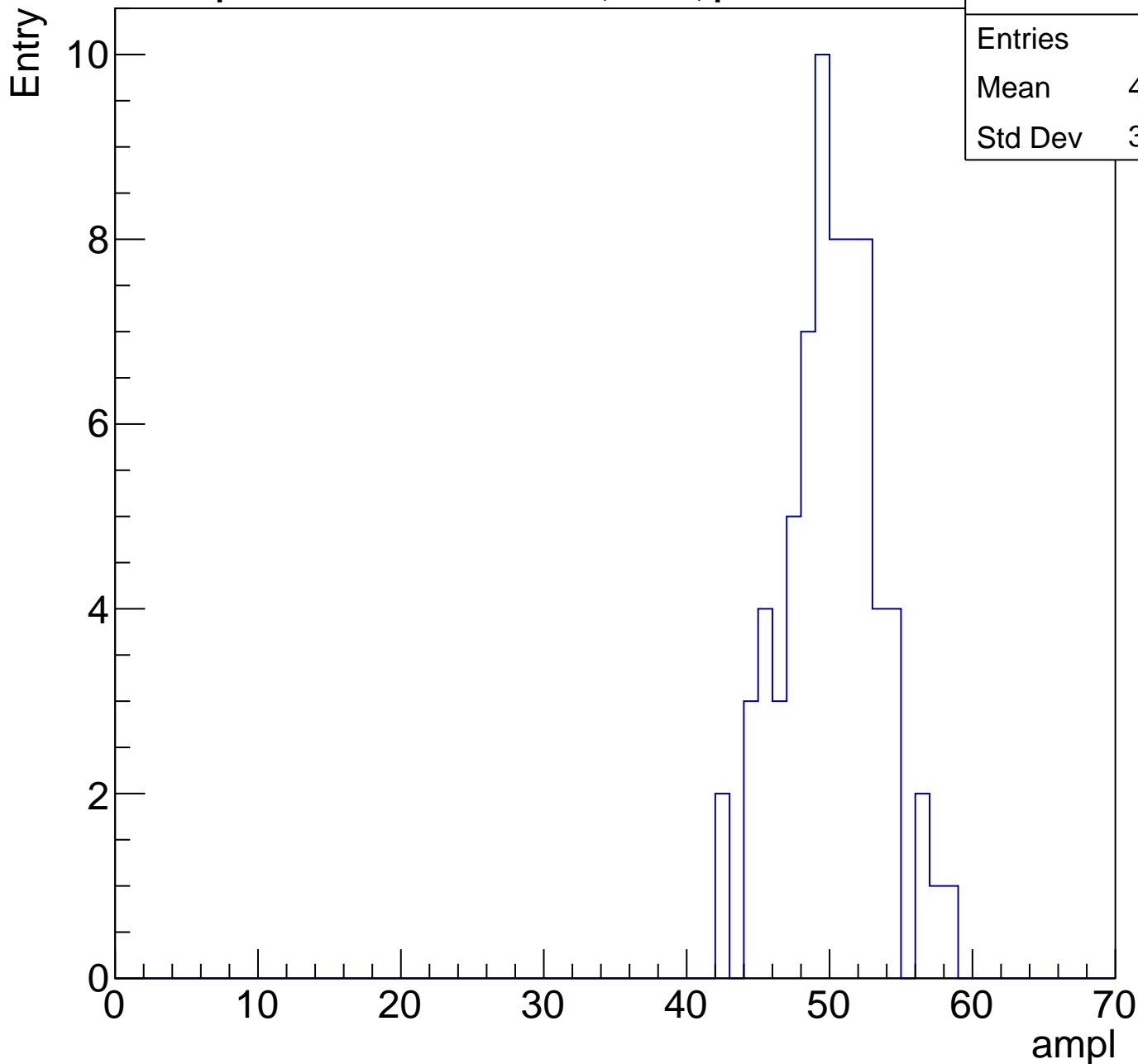
**Gaus Width: 3.5150**



# B1L000S, U23-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	49.63
Std Dev	3.343

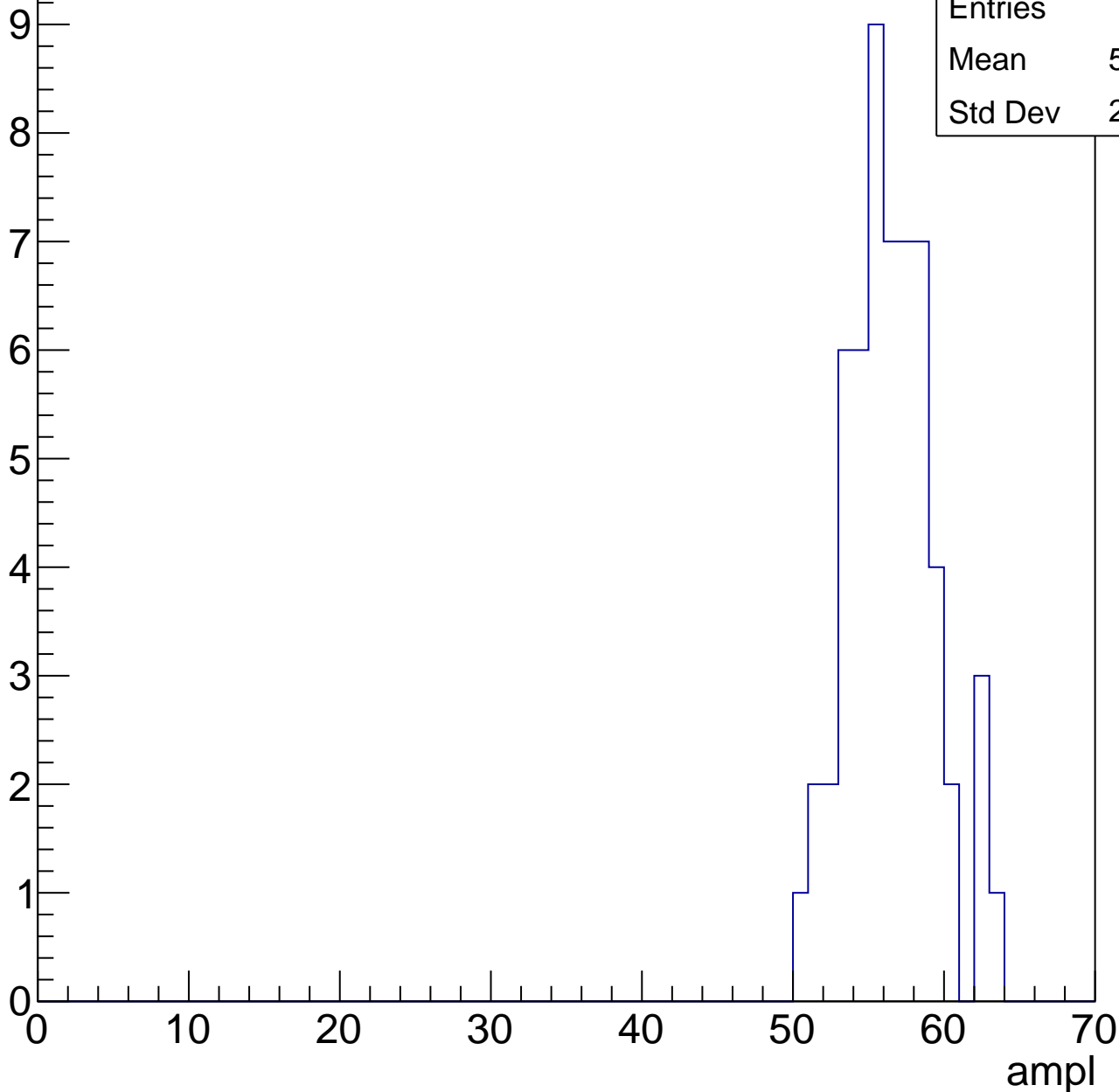


# B1L000S, U23-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	56.05
Std Dev	2.856

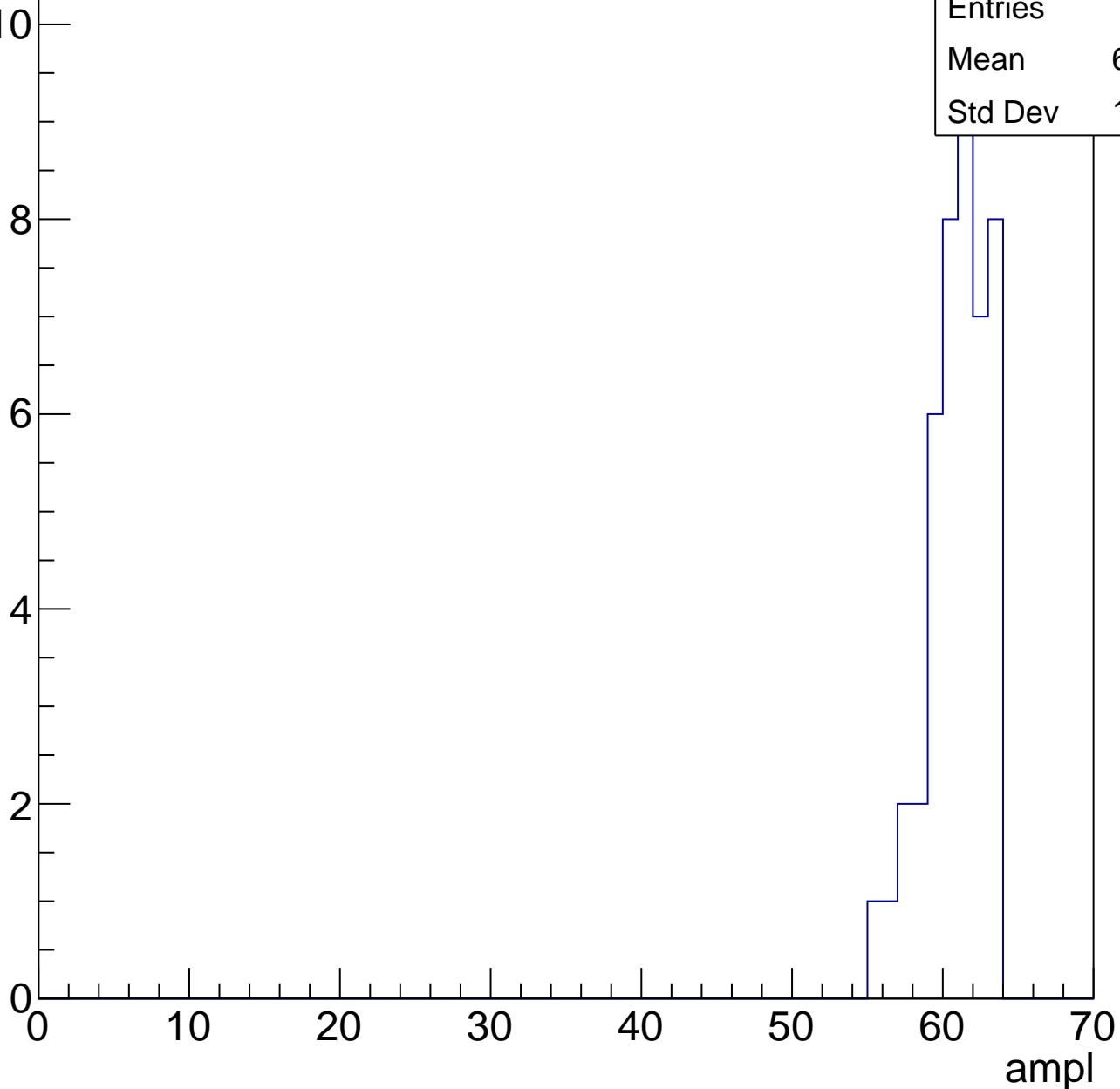


# B1L000S, U23-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

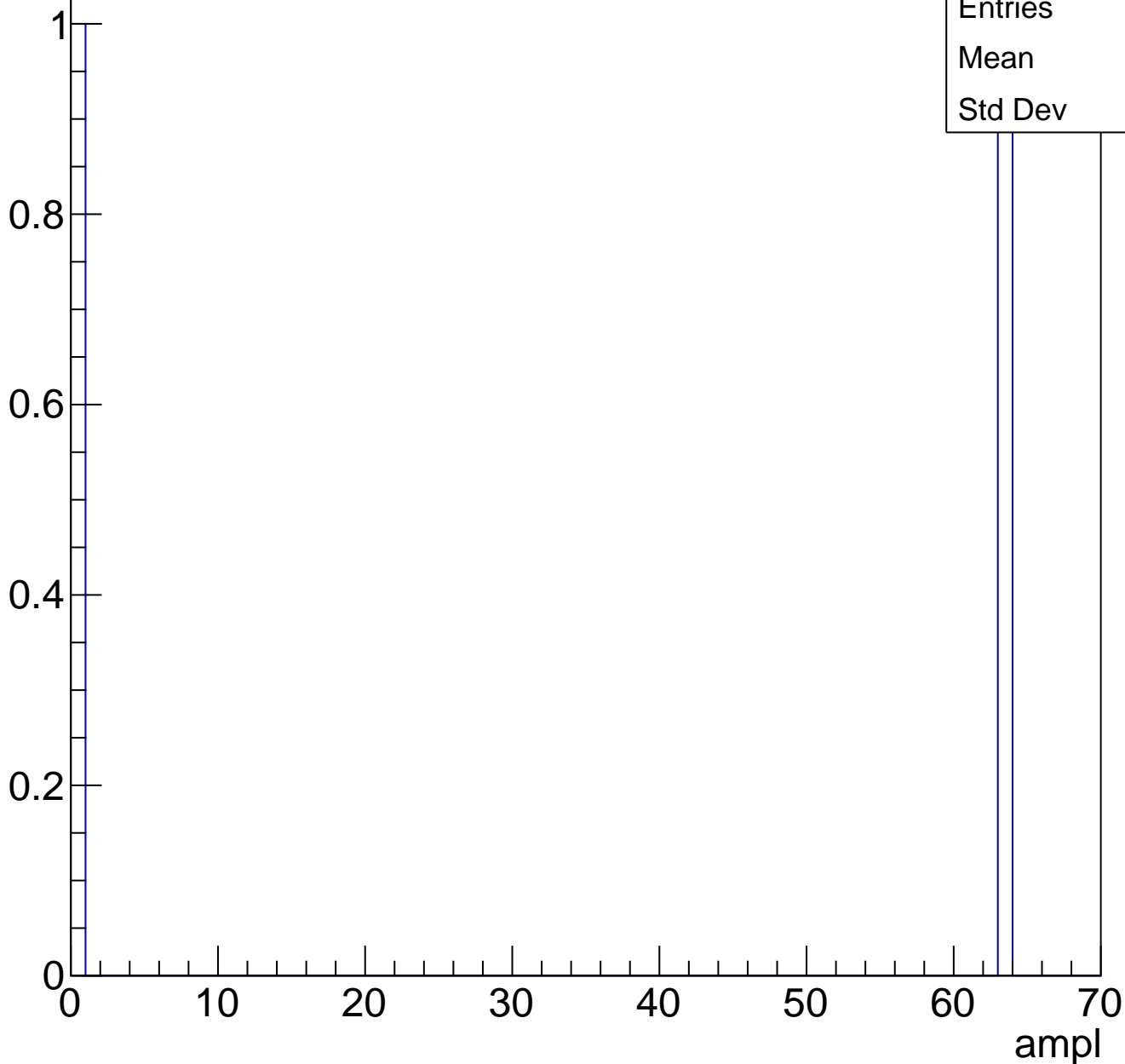
Entries	45
Mean	60.51
Std Dev	1.951



# B1L000S, U23-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	27.7
Std Dev	5.649

**Gaus mean : 28.9516**

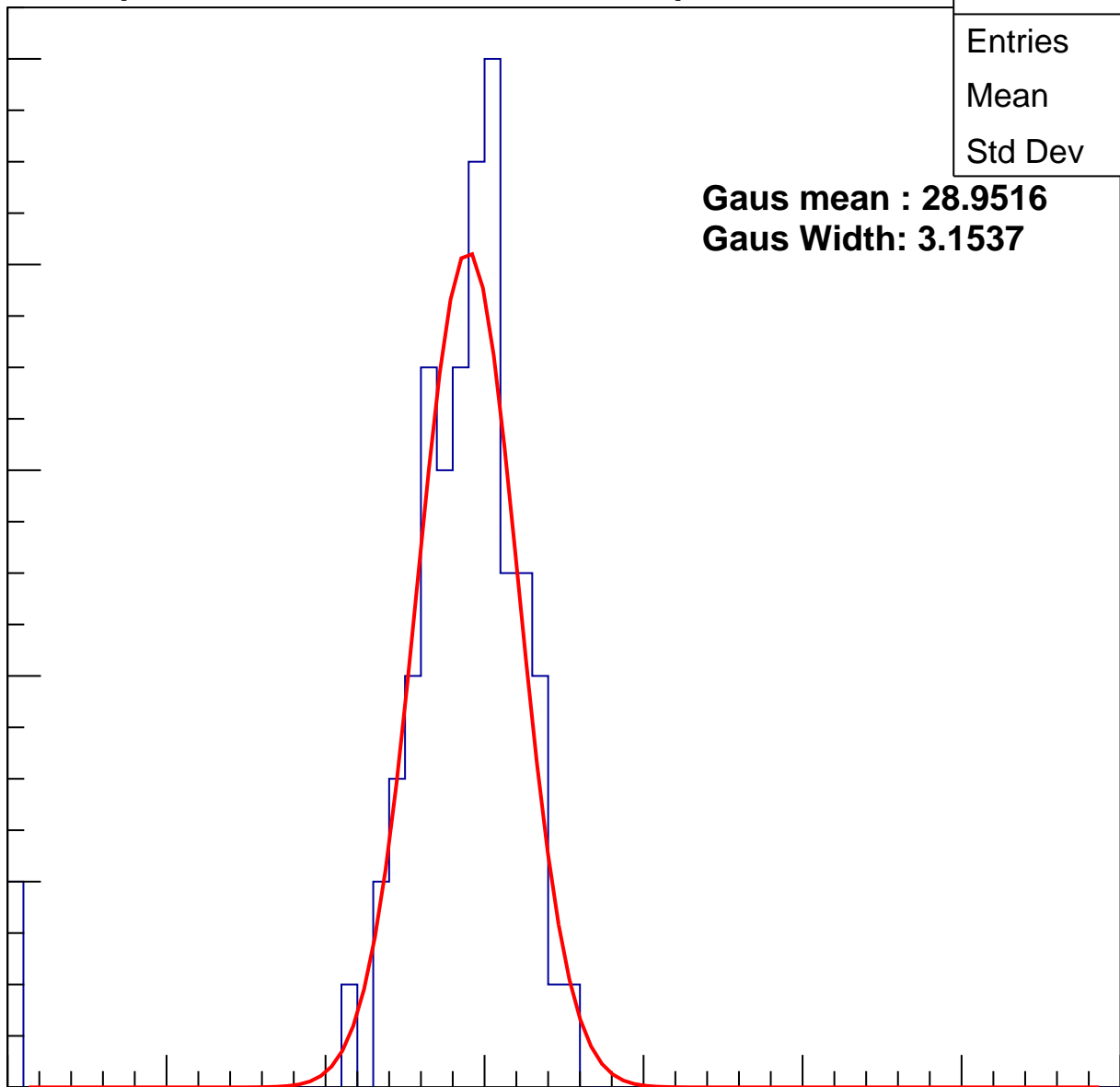
**Gaus Width: 3.1537**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch57, adc1

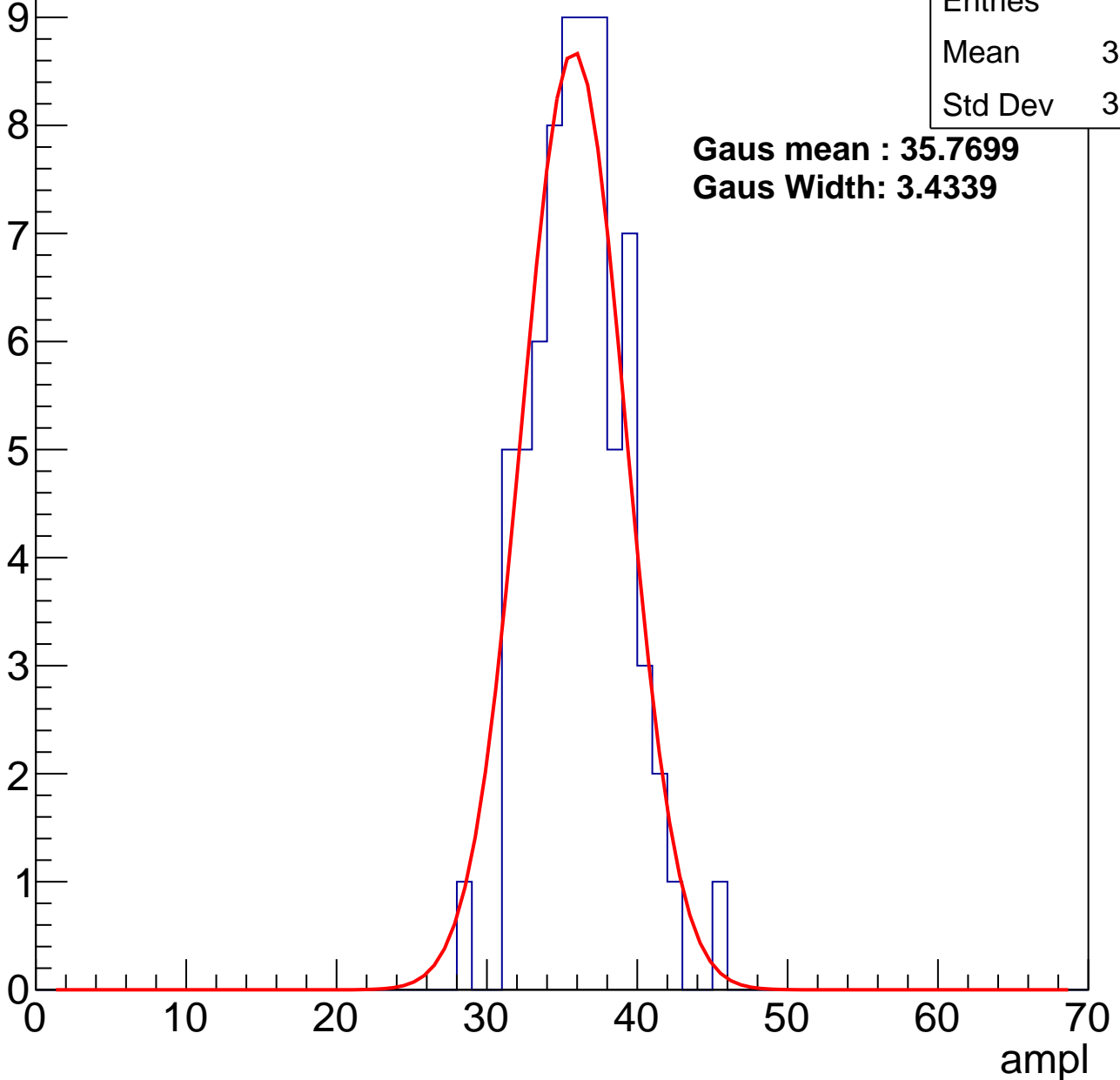
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.73
Std Dev	3.063

**Gaus mean : 35.7699**

**Gaus Width: 3.4339**



# B1L000S, U23-ch57, adc2

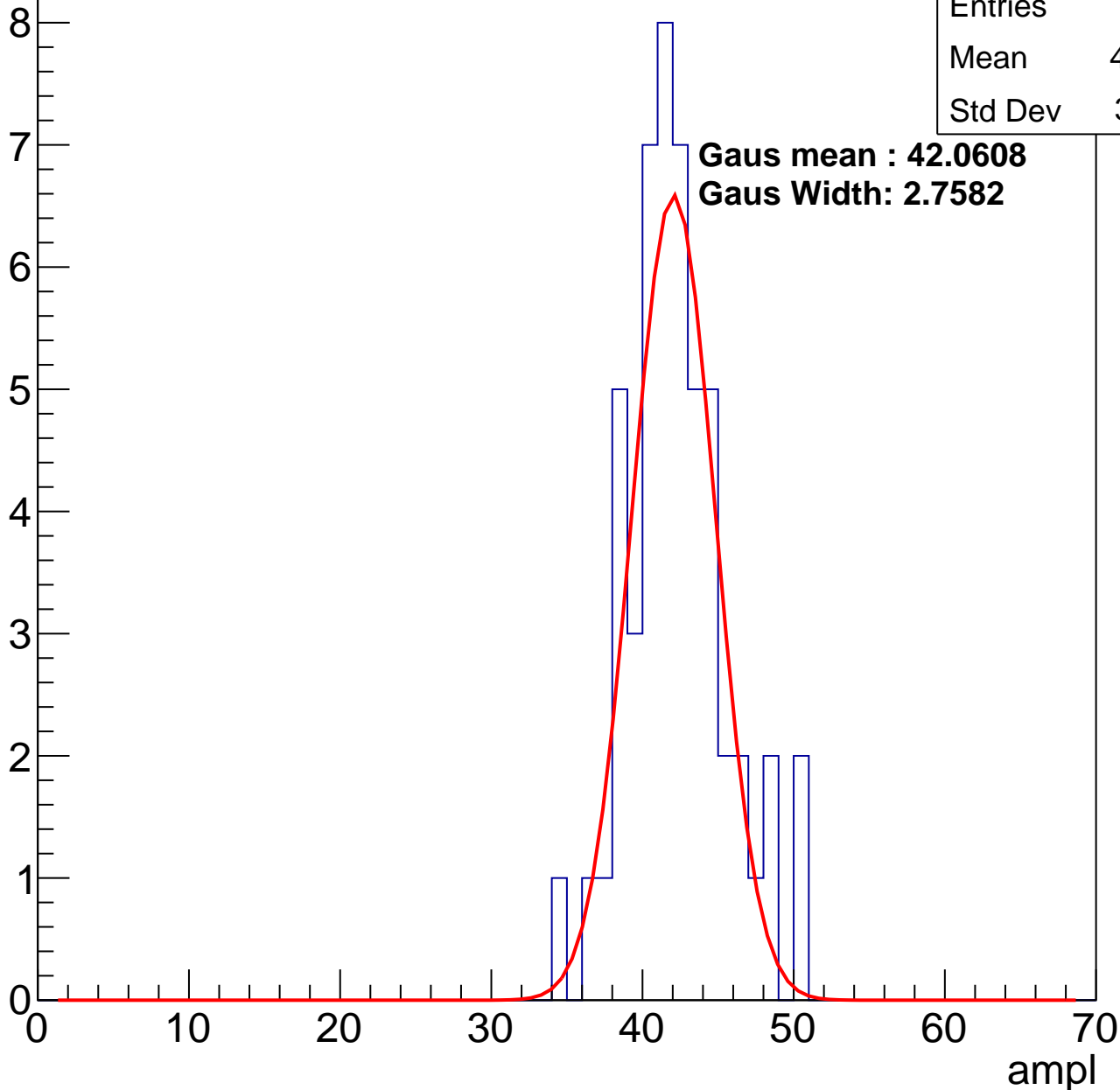
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.85
Std Dev	3.301

**Gaus mean : 42.0608**

**Gaus Width: 2.7582**

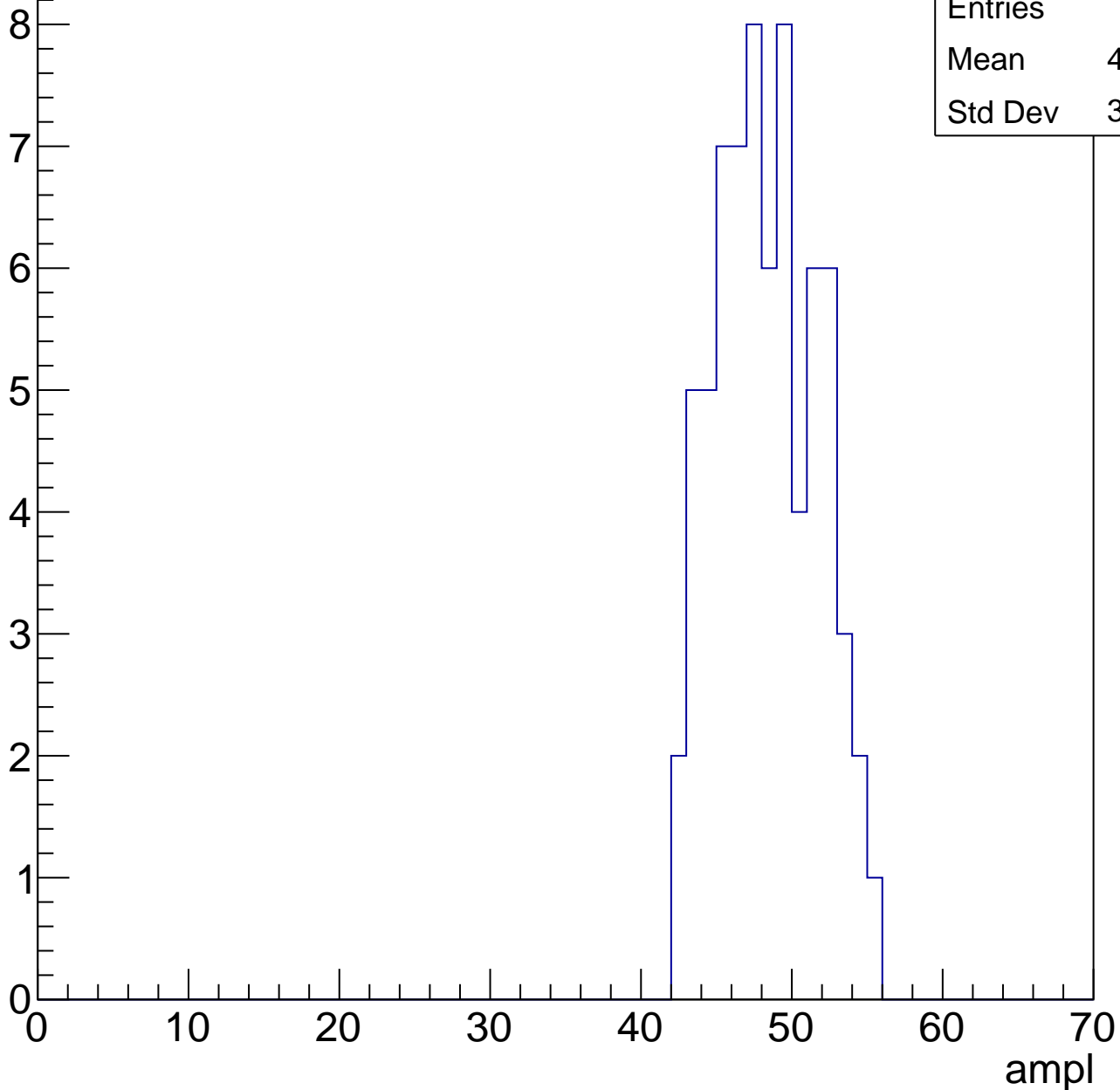


# B1L000S, U23-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

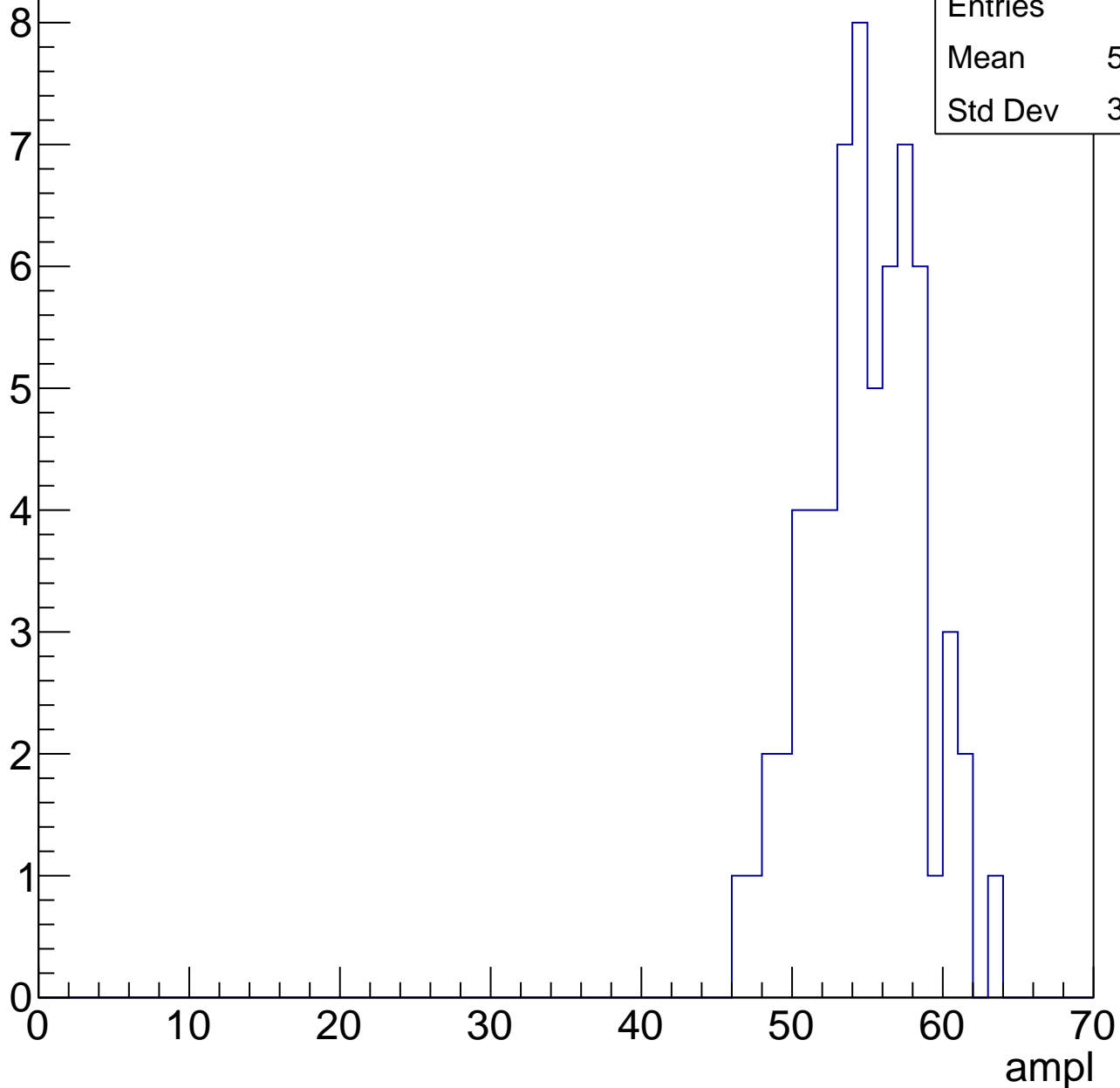
Entries	70
Mean	47.89
Std Dev	3.263



# B1L000S, U23-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

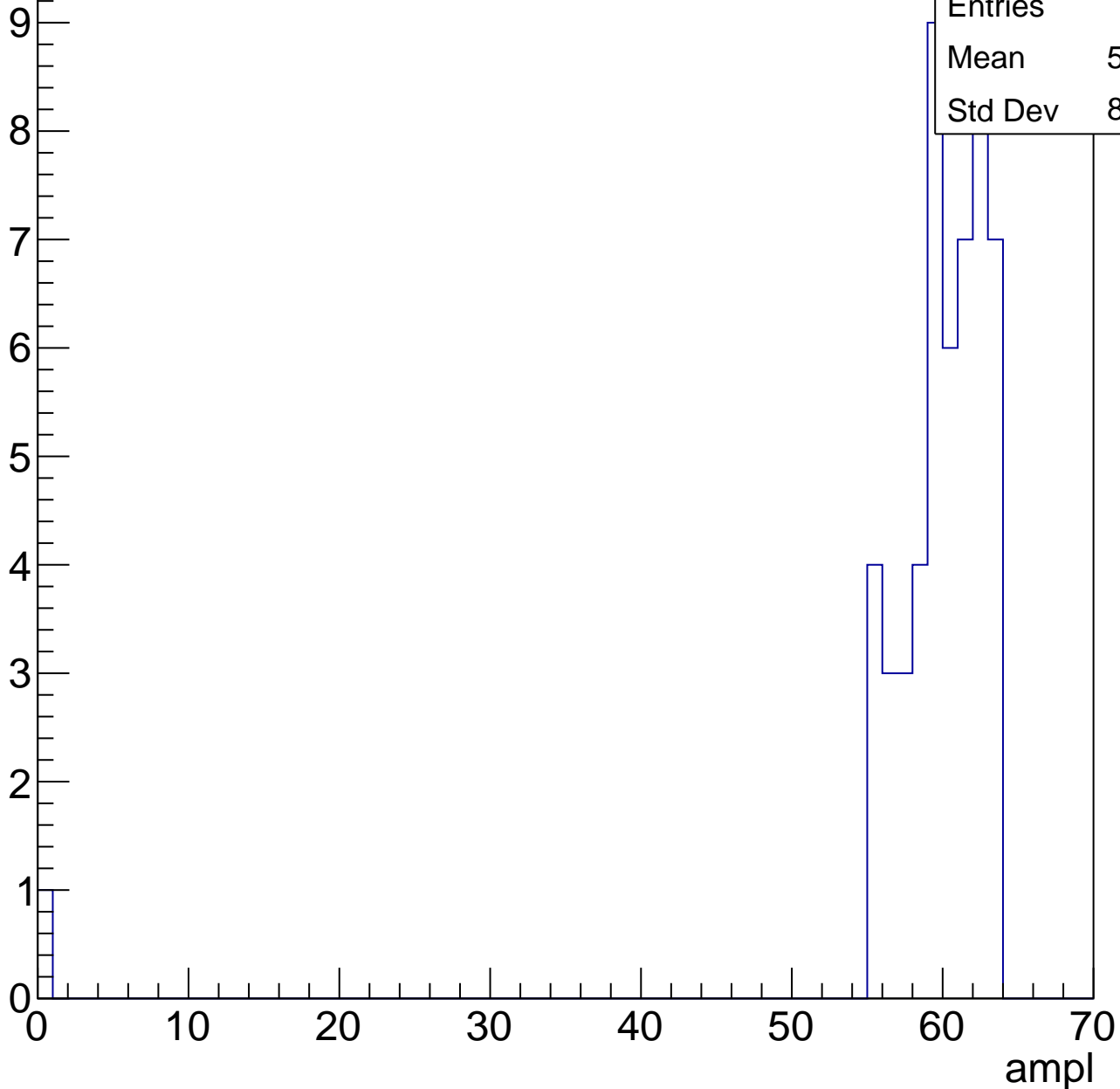


Entries	64
Mean	54.44
Std Dev	3.635

# B1L000S, U23-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

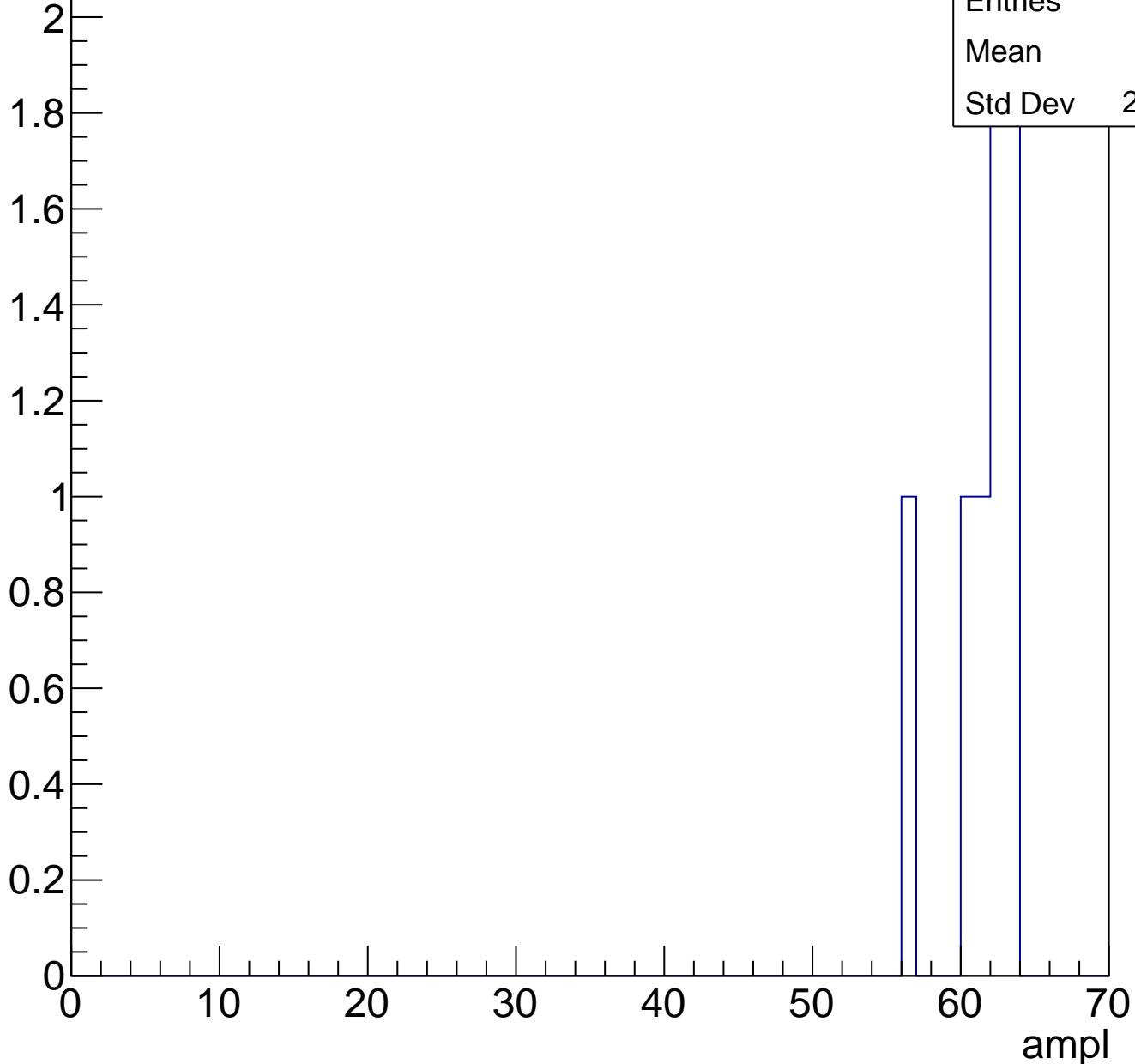
Entry



# B1L000S, U23-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L000S, U23-ch58, adc0

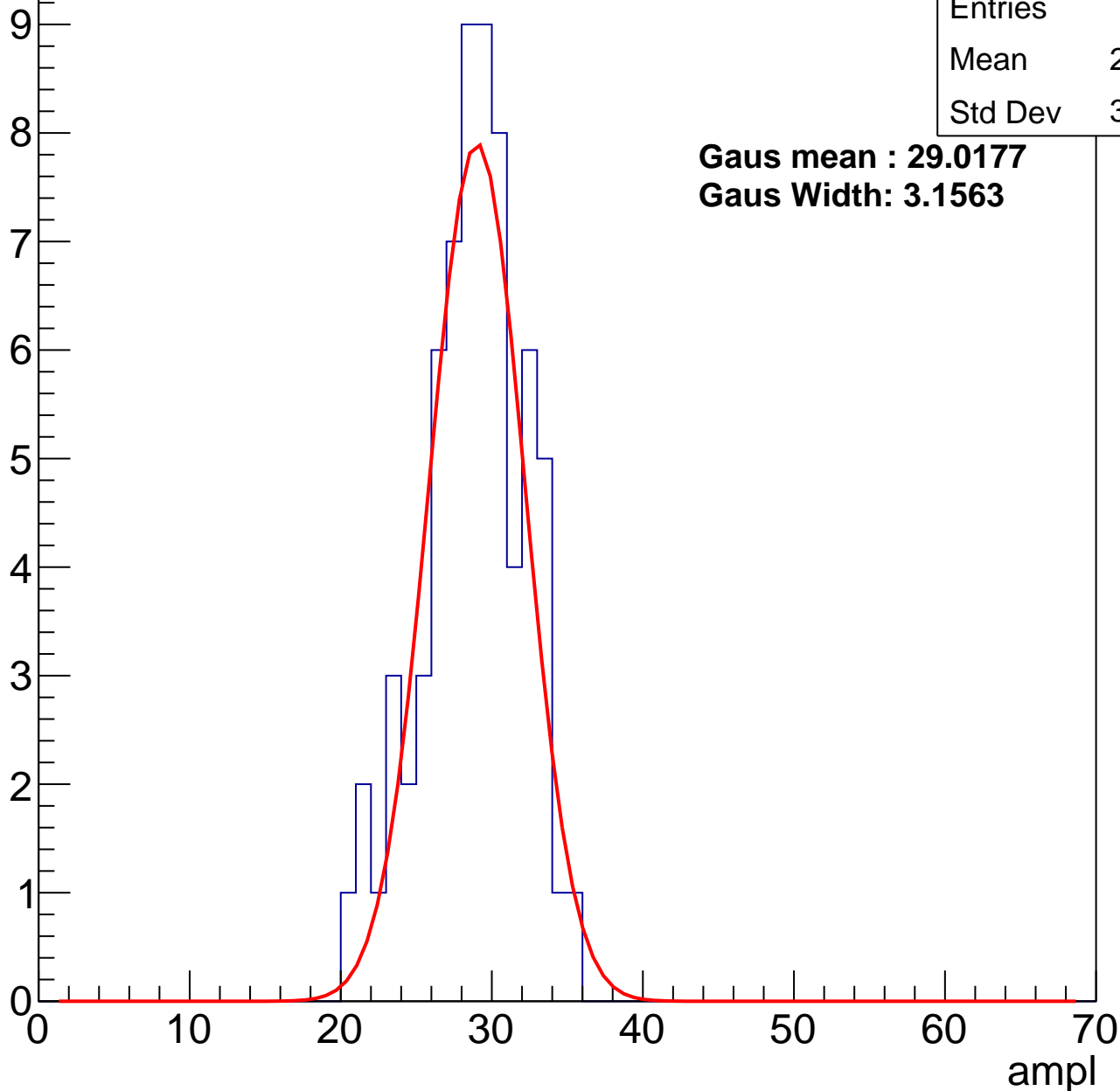
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.29
Std Dev	3.299

**Gaus mean : 29.0177**

**Gaus Width: 3.1563**

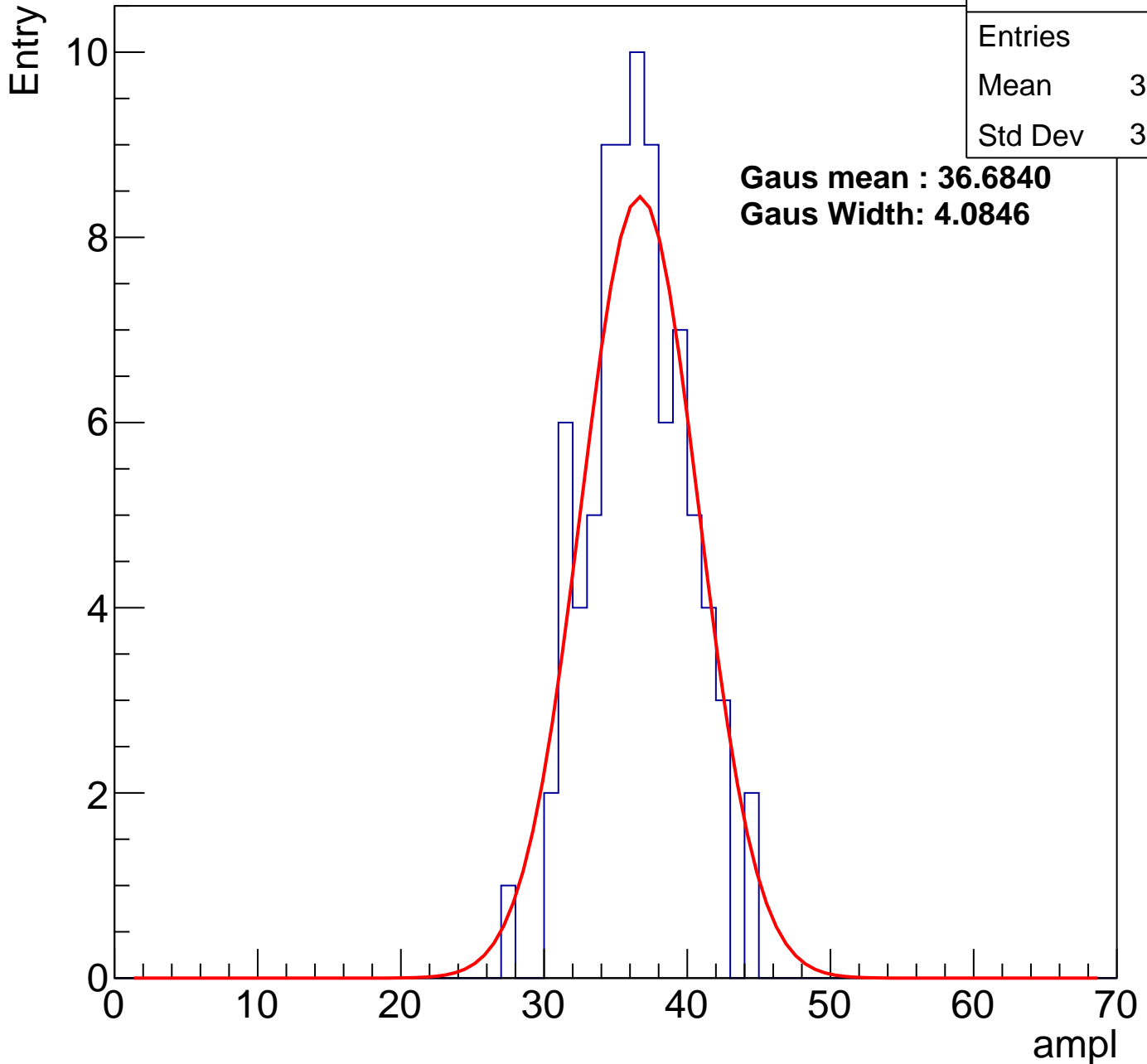


# B1L000S, U23-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	36.09
Std Dev	3.437

**Gaus mean : 36.6840**  
**Gaus Width: 4.0846**



# B1L000S, U23-ch58, adc2

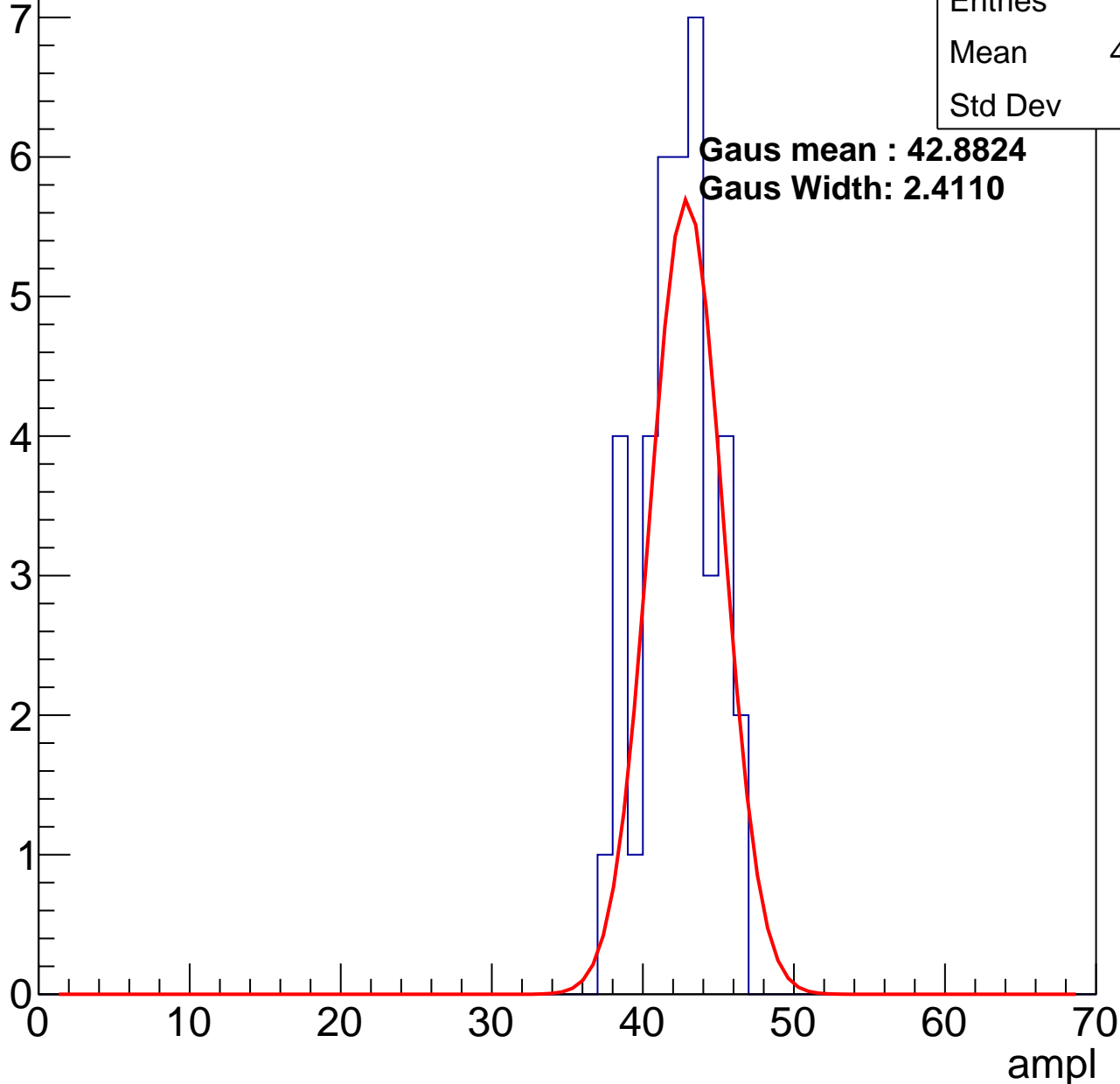
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	41.87
Std Dev	2.33

**Gaus mean : 42.8824**

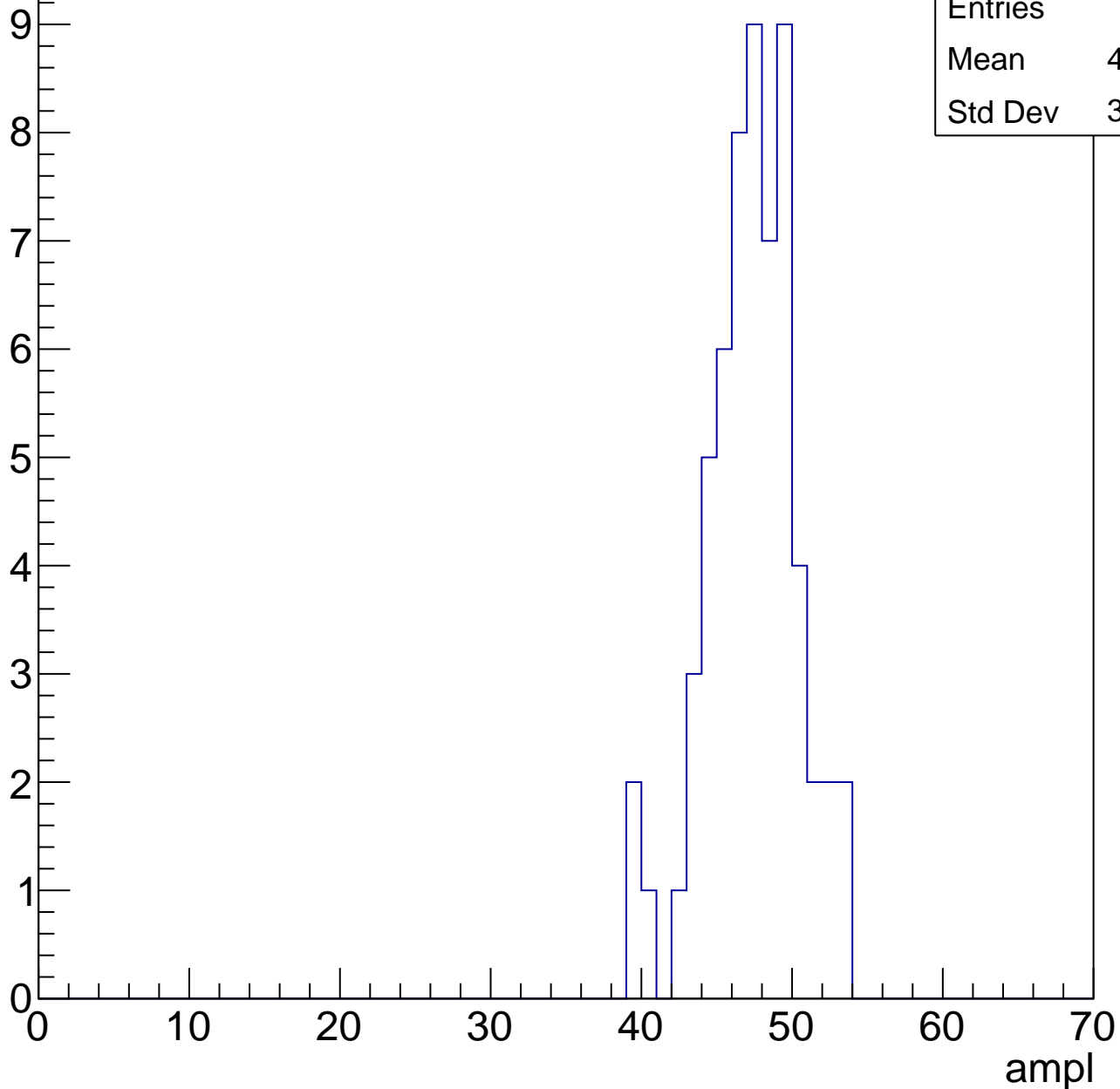
**Gaus Width: 2.4110**



# B1L000S, U23-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

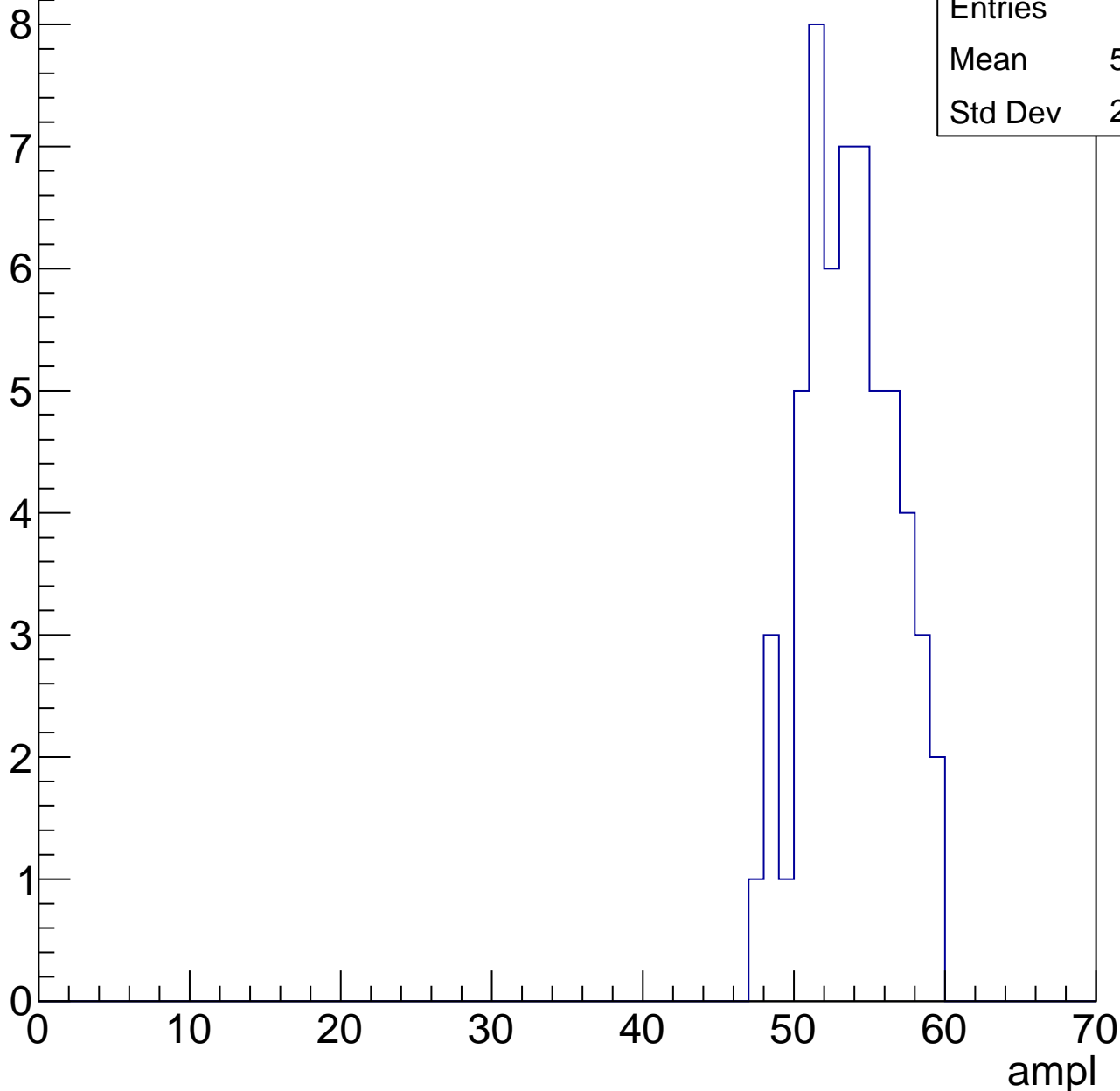


# B1L000S, U23-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

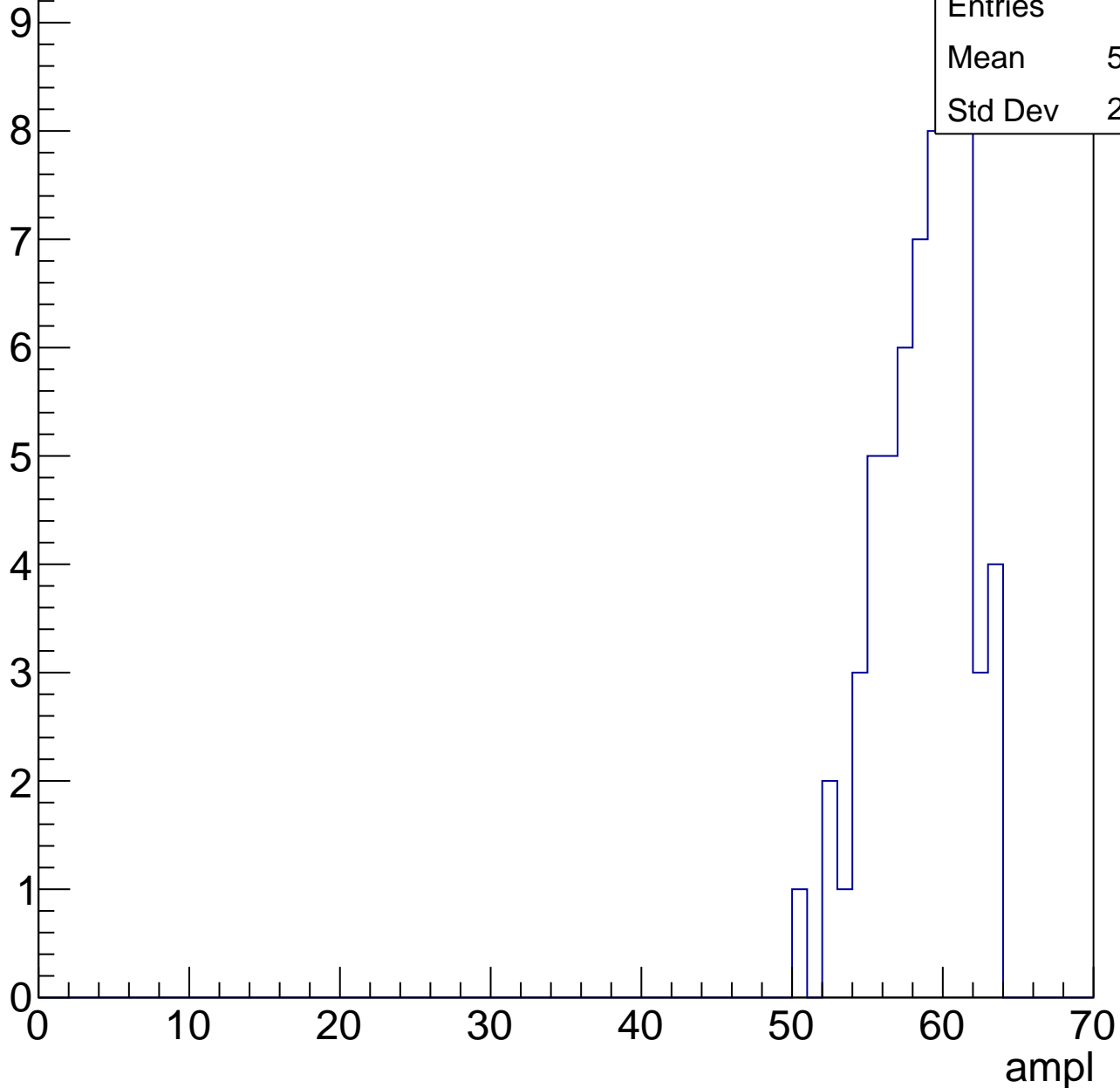
Entries	57
Mean	53.23
Std Dev	2.932



# B1L000S, U23-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

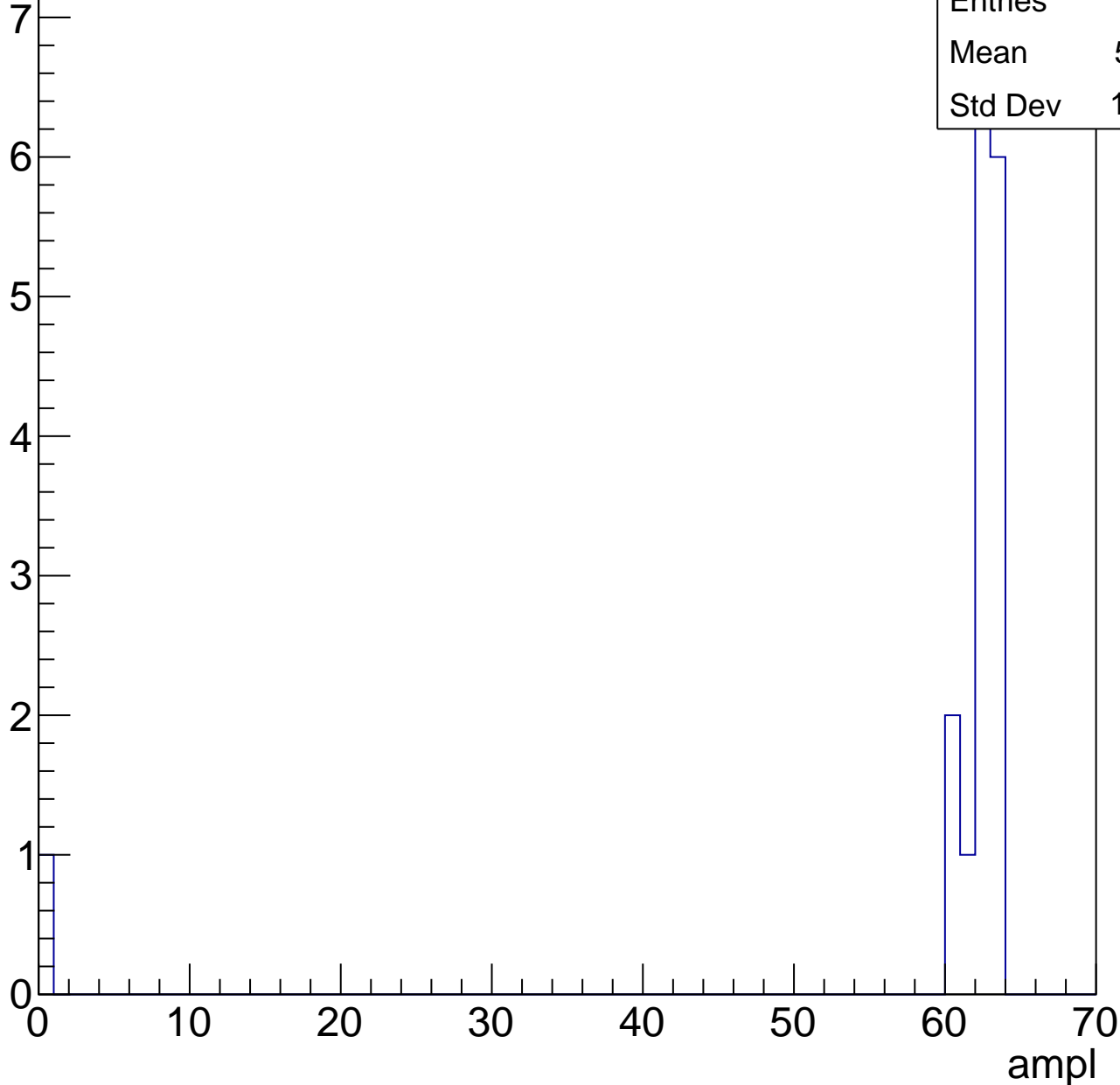


# B1L000S, U23-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.41
Std Dev	14.63





# B1L000S, U23-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	29.68
Std Dev	5.896

**Gaus mean : 30.9598**

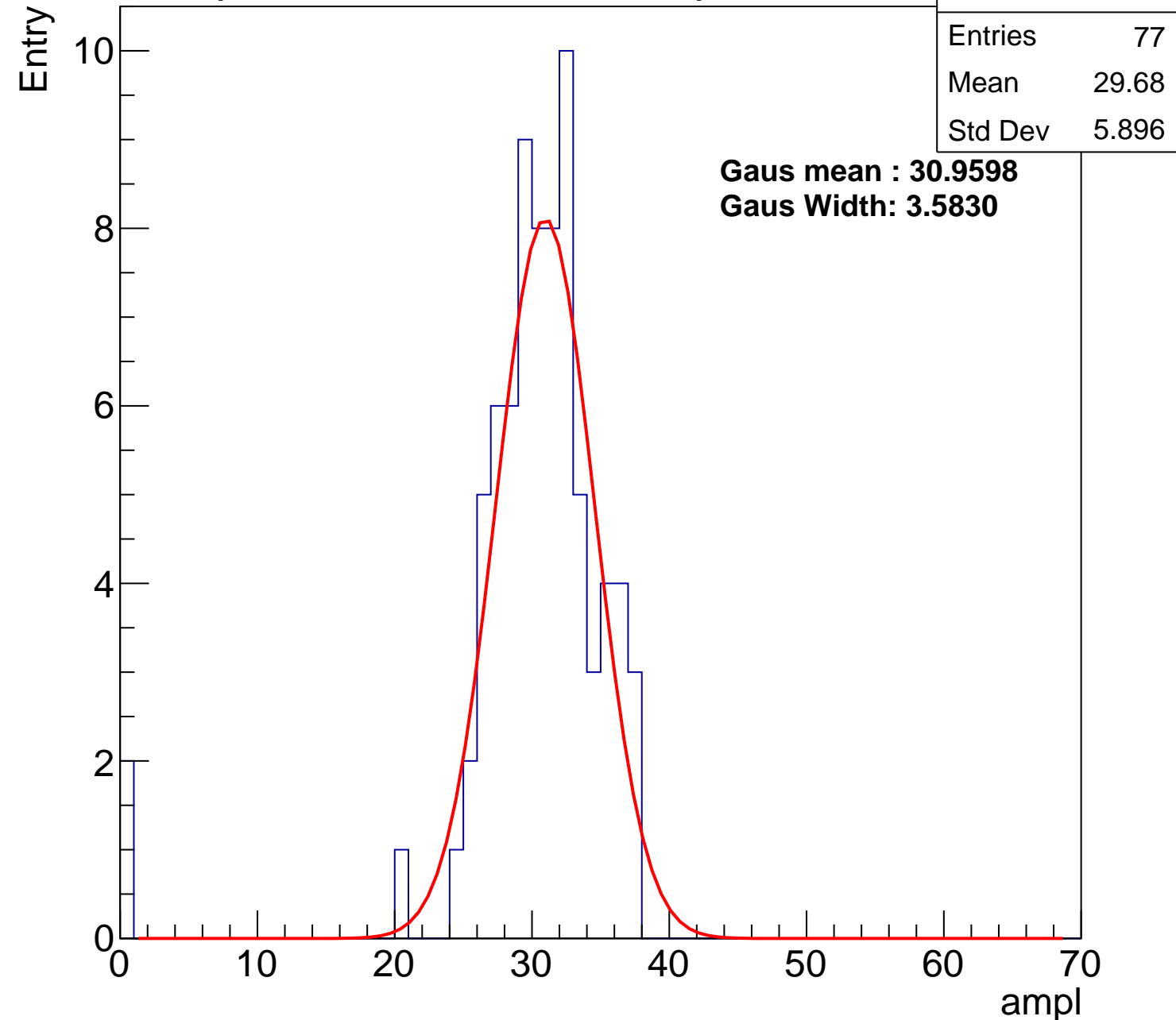
**Gaus Width: 3.5830**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch59, adc1

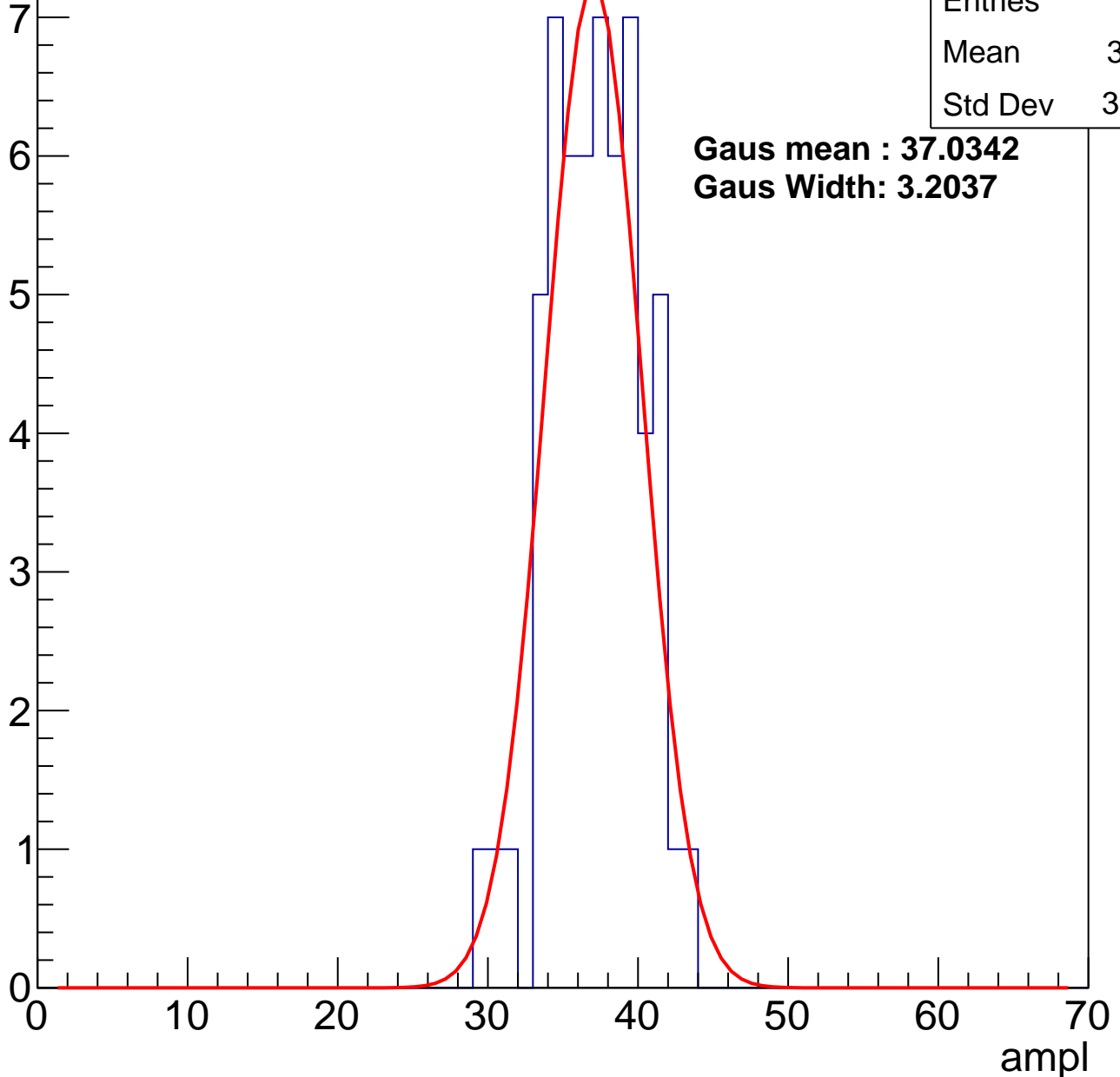
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	36.71
Std Dev	3.017

**Gaus mean : 37.0342**

**Gaus Width: 3.2037**



# B1L000S, U23-ch59, adc2

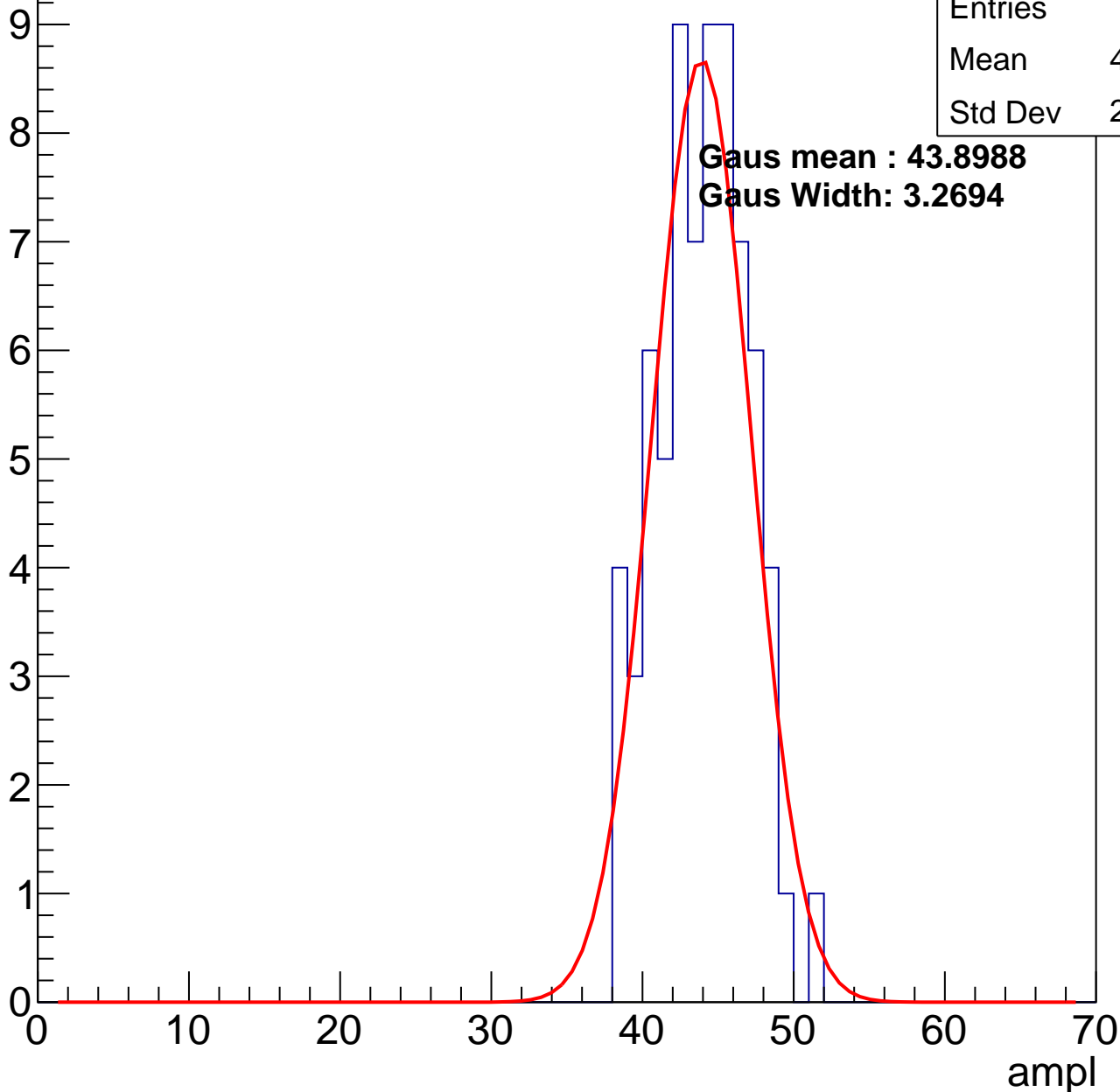
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	43.52
Std Dev	2.945

**Gaus mean : 43.8988**

**Gaus Width: 3.2694**

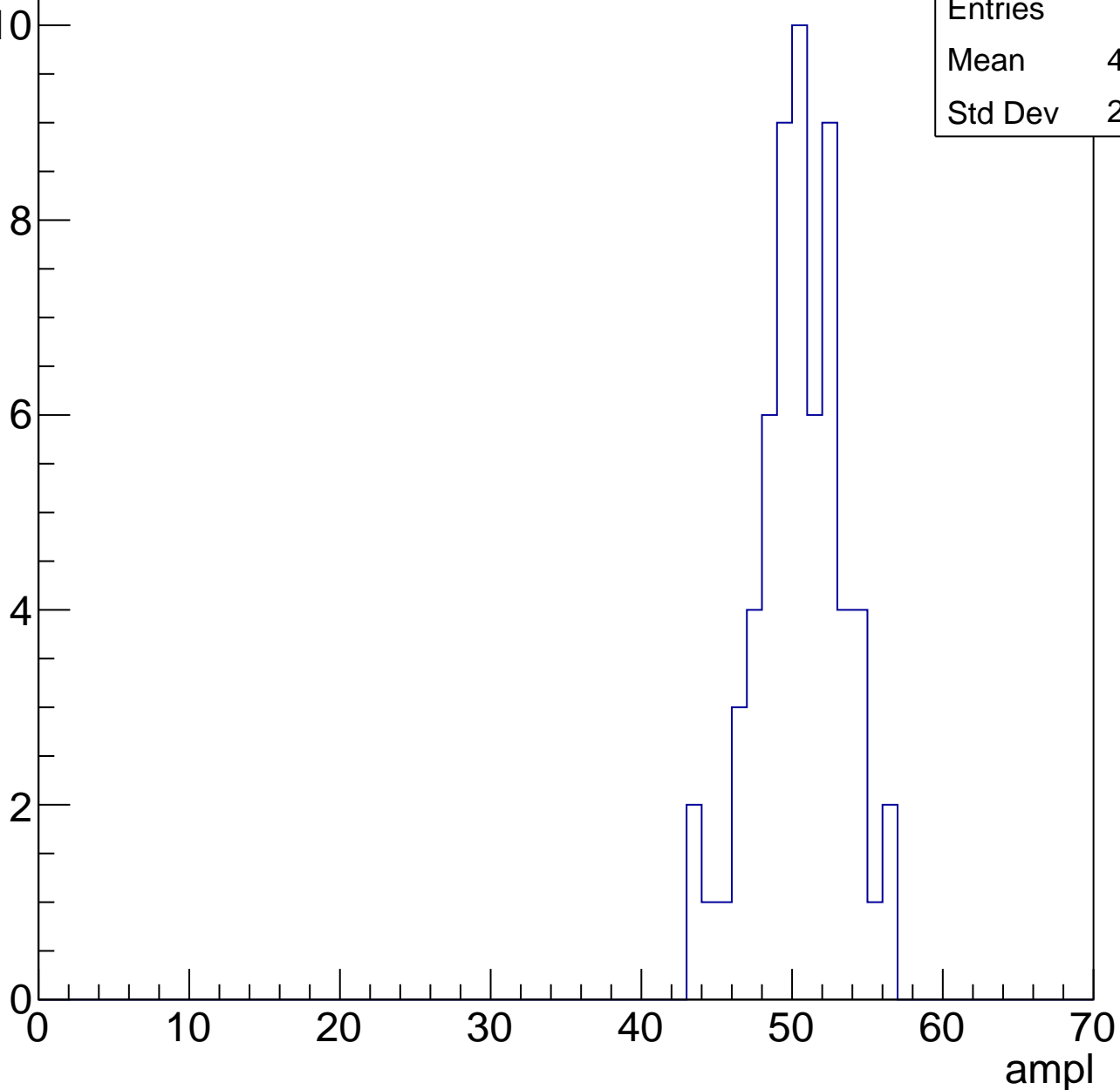


# B1L000S, U23-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	49.98
Std Dev	2.882

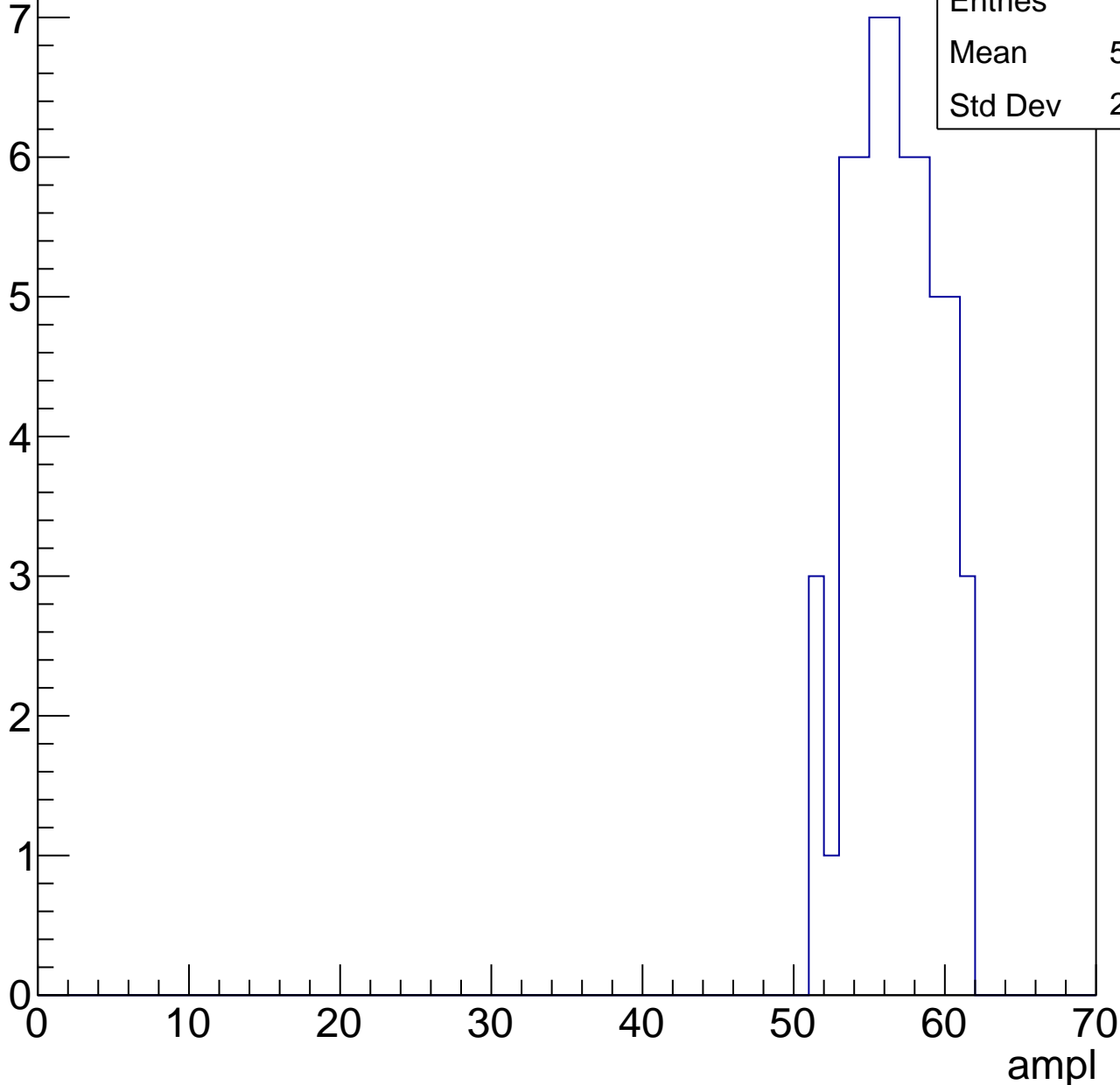


# B1L000S, U23-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	56.22
Std Dev	2.708

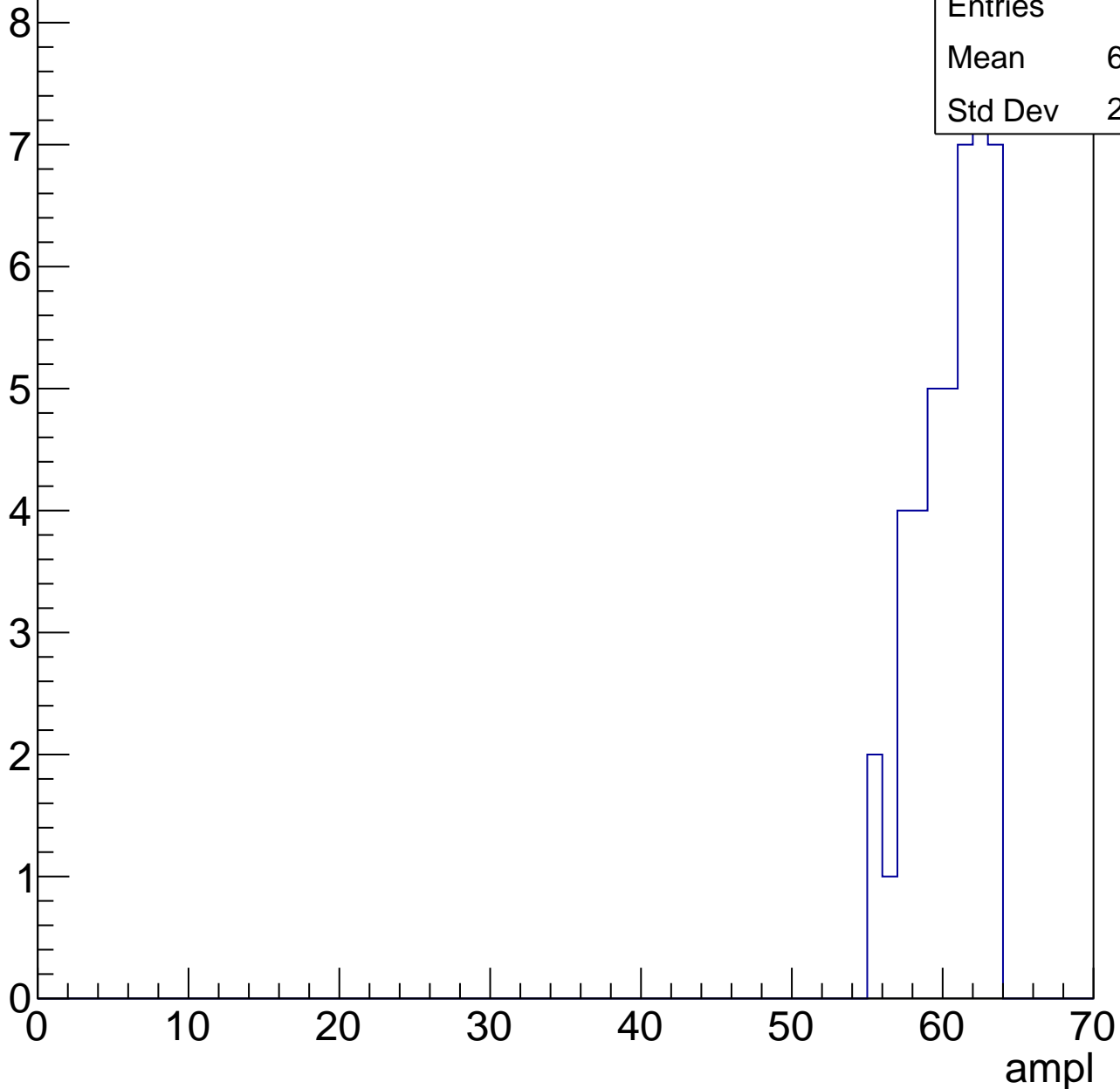


# B1L000S, U23-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	60.12
Std Dev	2.285



# B1L000S, U23-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch60, adc0

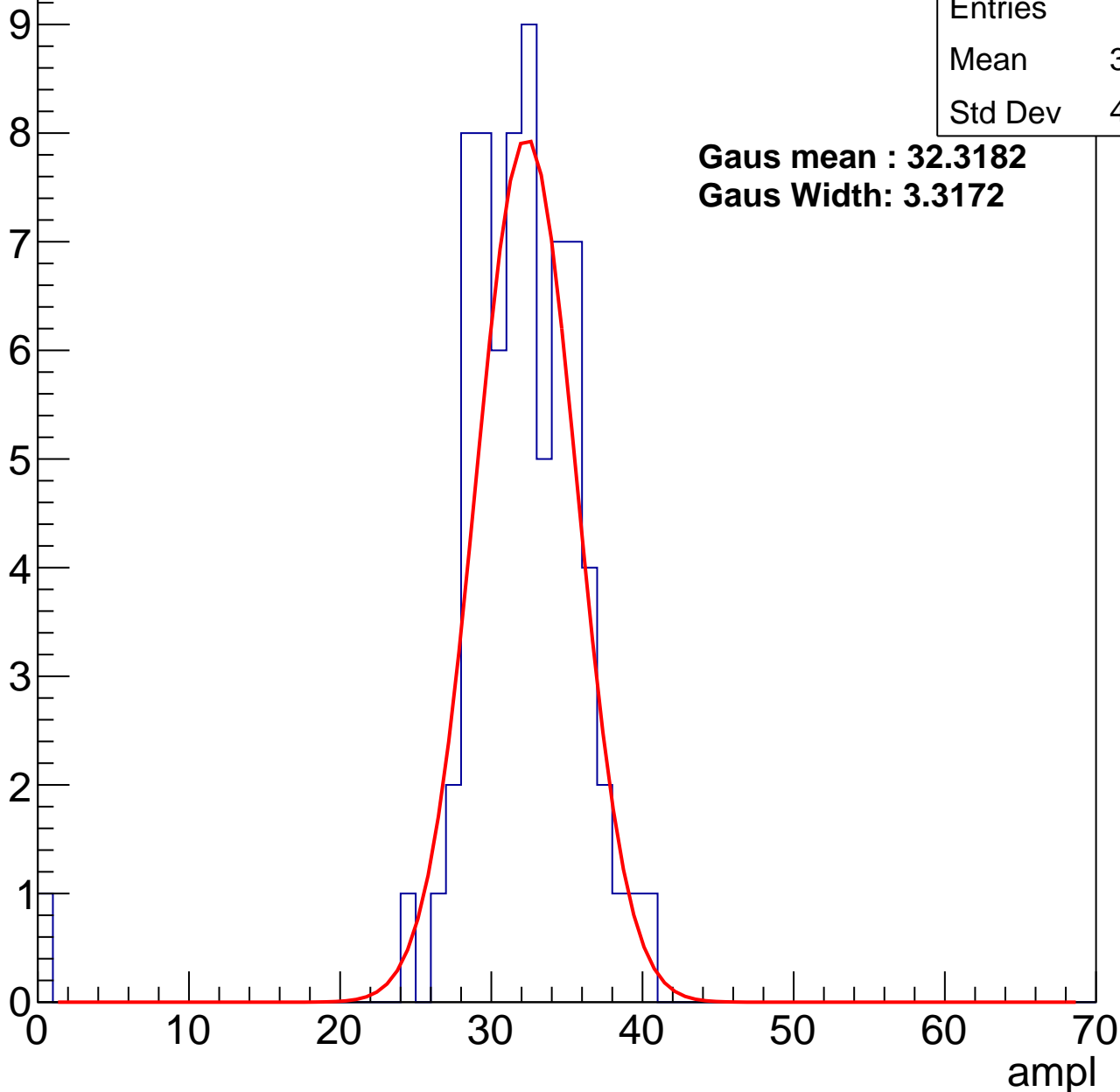
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	31.38
Std Dev	4.912

**Gaus mean : 32.3182**

**Gaus Width: 3.3172**



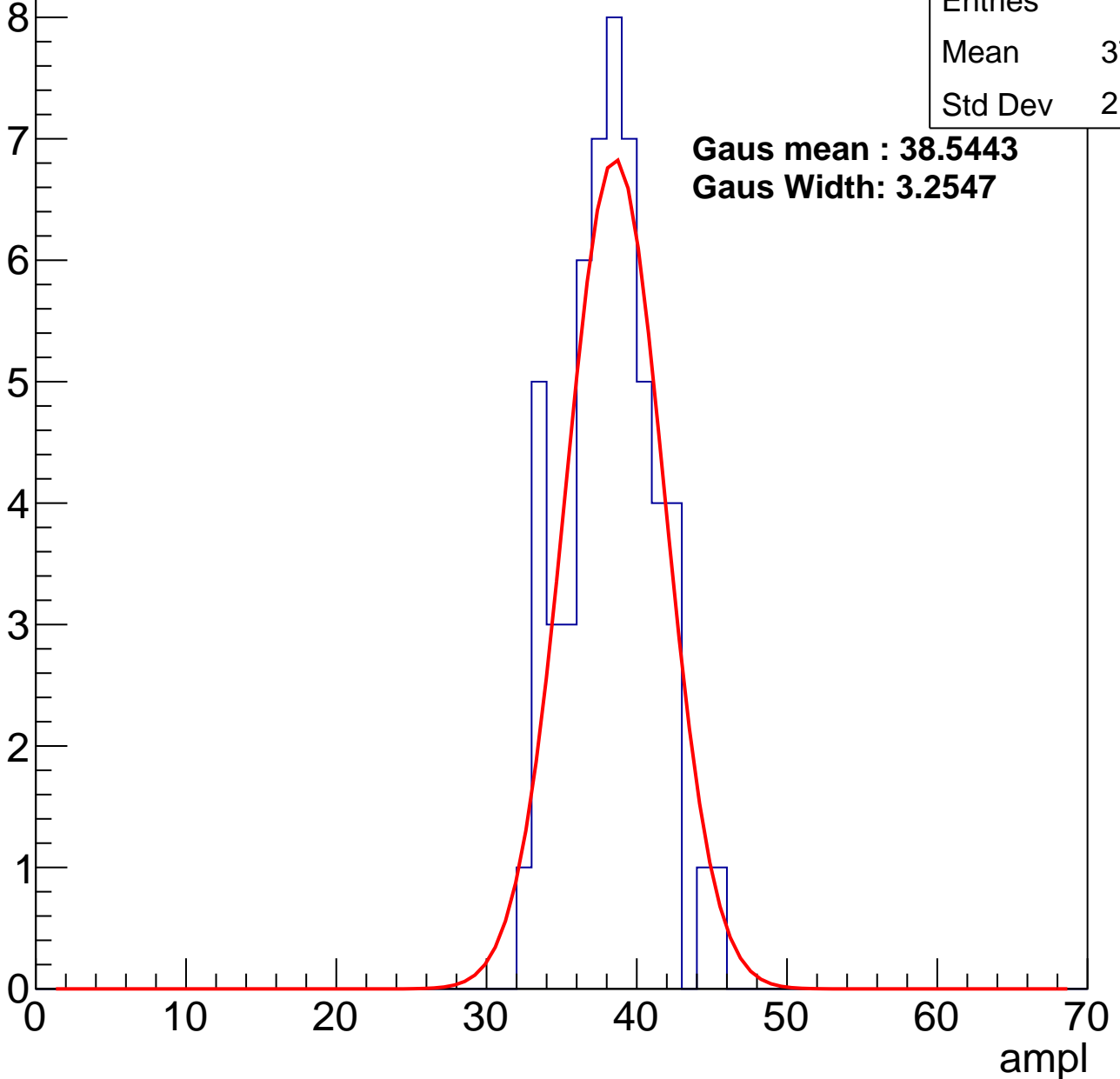
# B1L000S, U23-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	37.76
Std Dev	2.942

**Gaus mean : 38.5443**  
**Gaus Width: 3.2547**



# B1L000S, U23-ch60, adc2

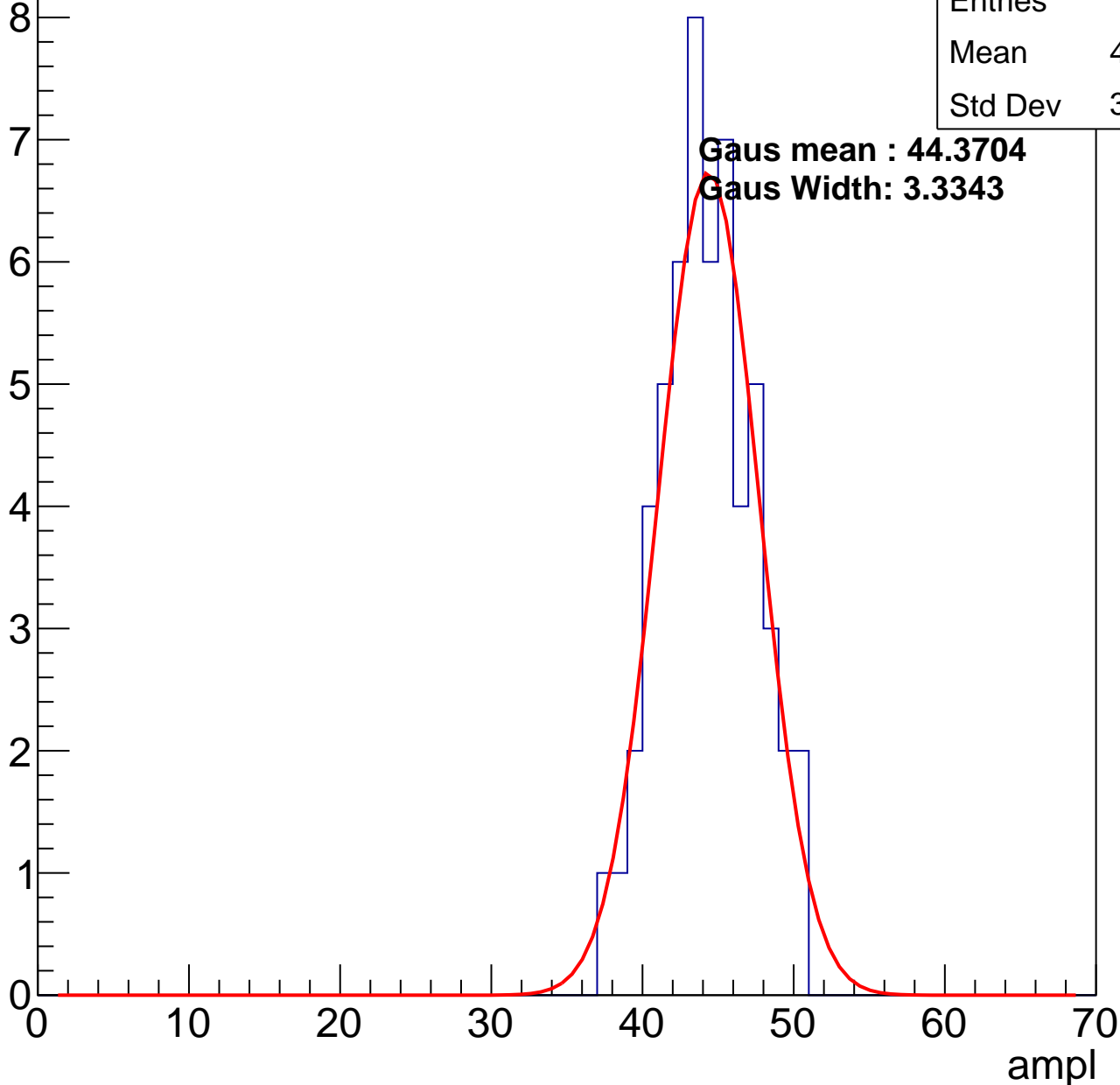
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	43.82
Std Dev	3.024

**Gaus mean : 44.3704**

**Gaus Width: 3.3343**

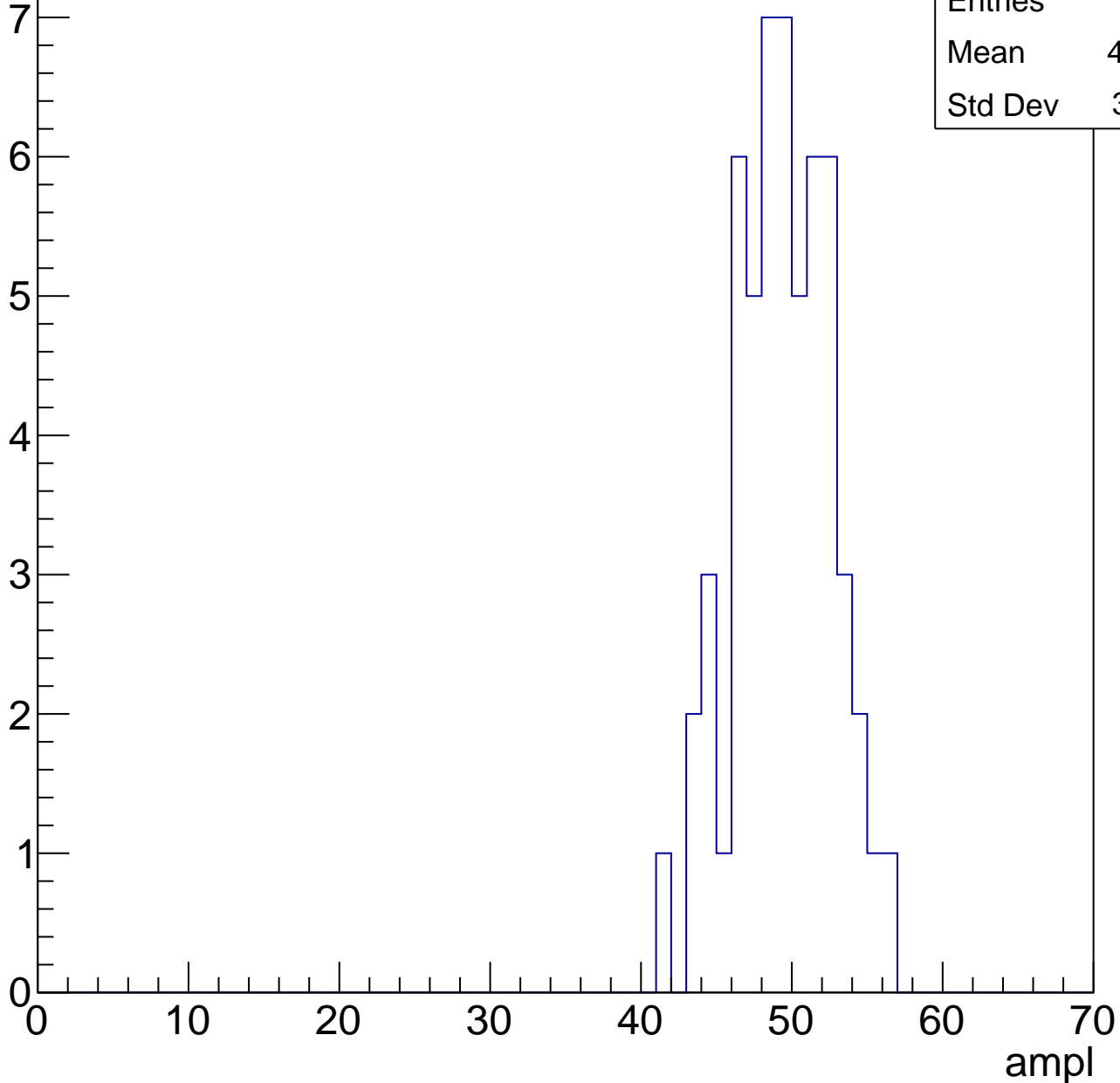


# B1L000S, U23-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

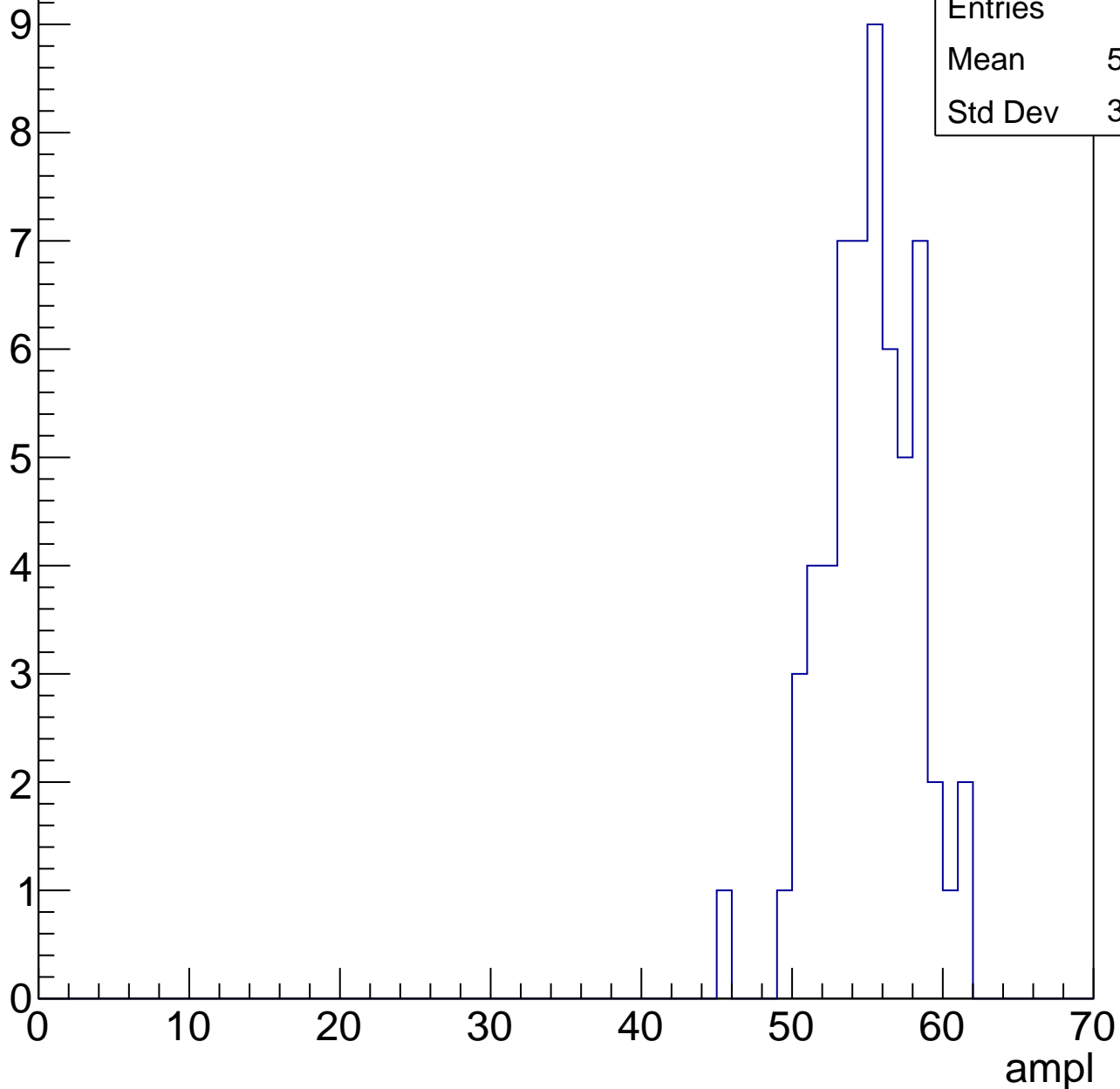
Entries	56
Mean	48.93
Std Dev	3.201



# B1L000S, U23-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

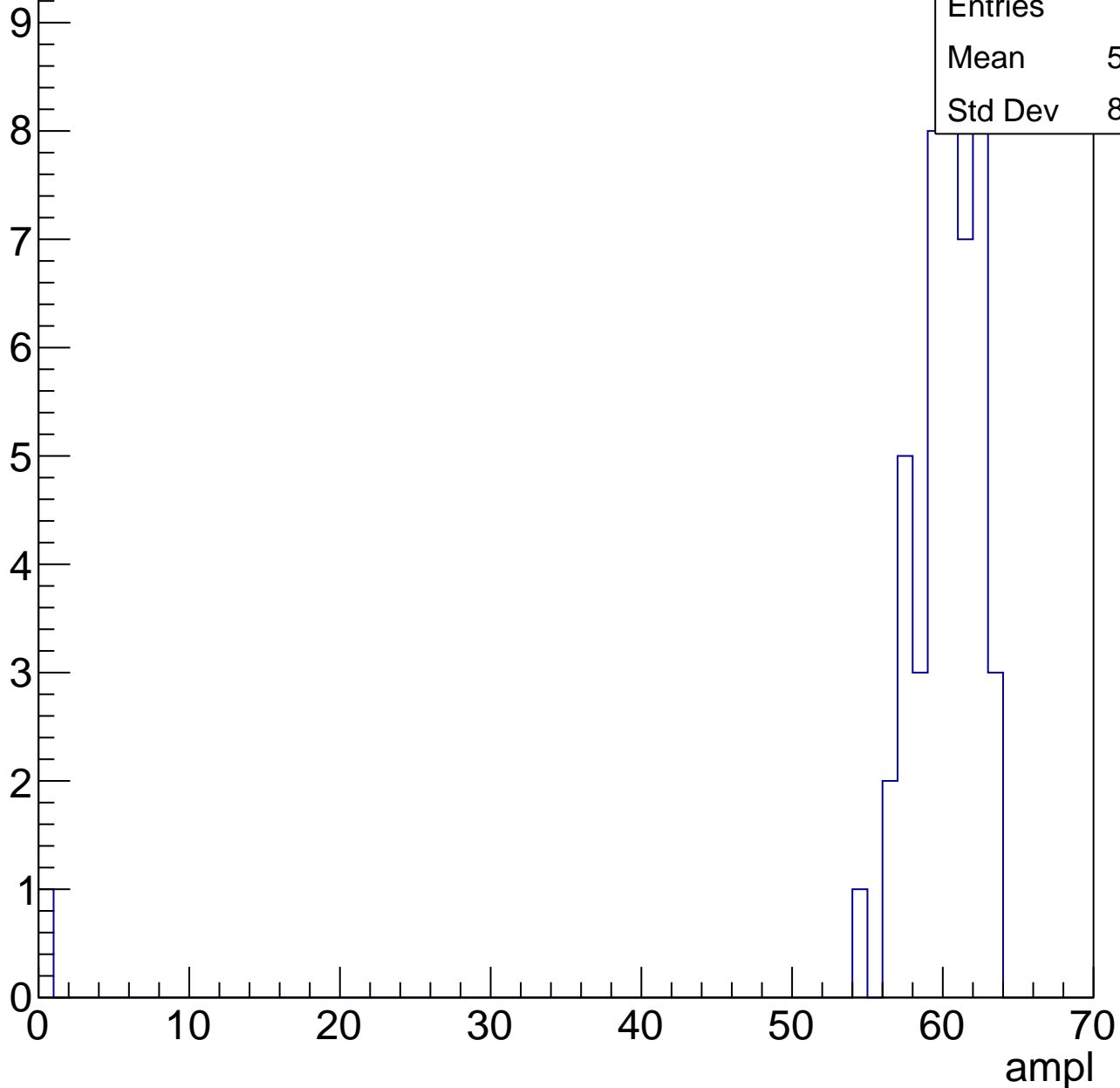
Entry



# B1L000S, U23-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

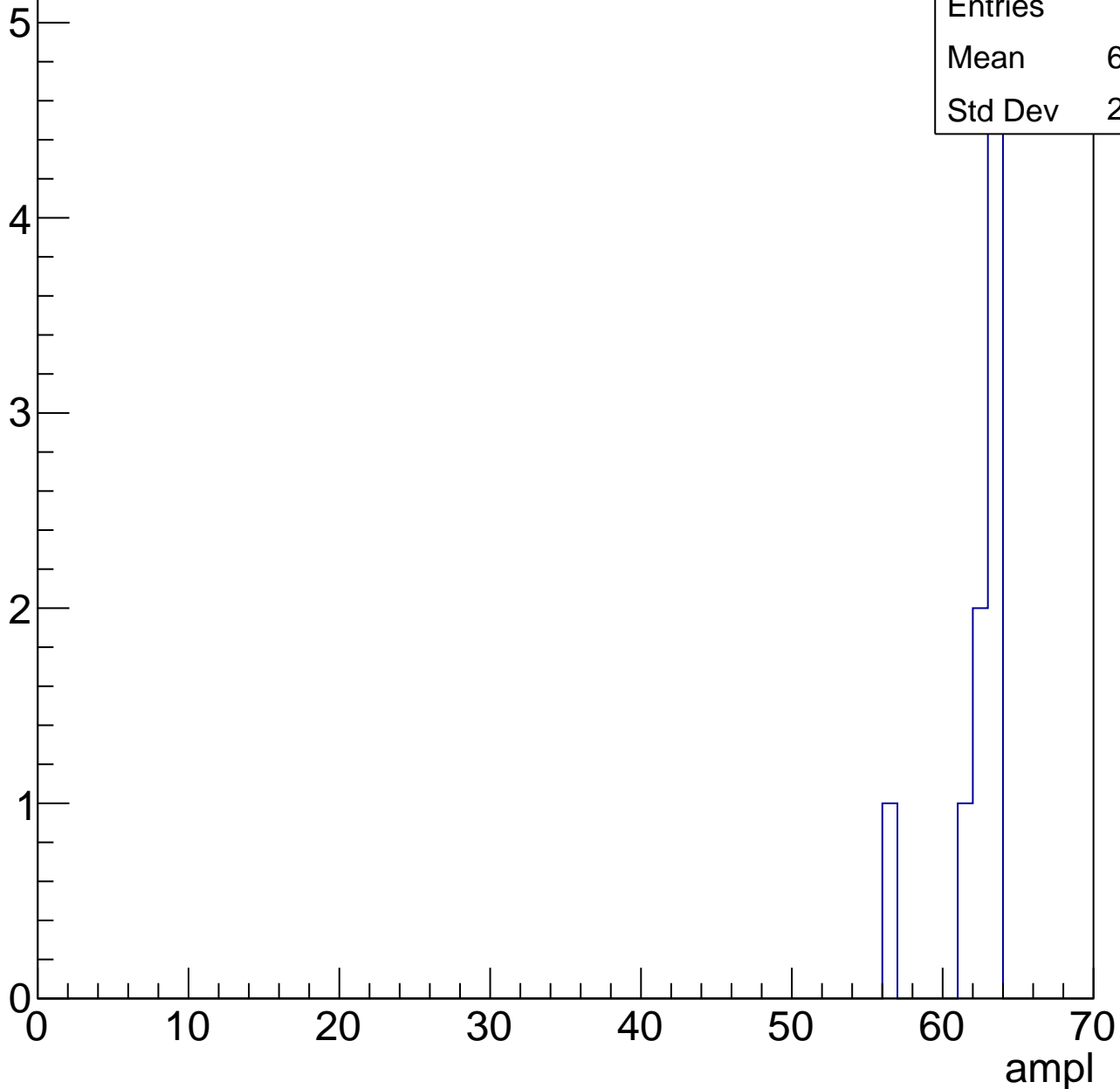


# B1L000S, U23-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	61.78
Std Dev	2.149

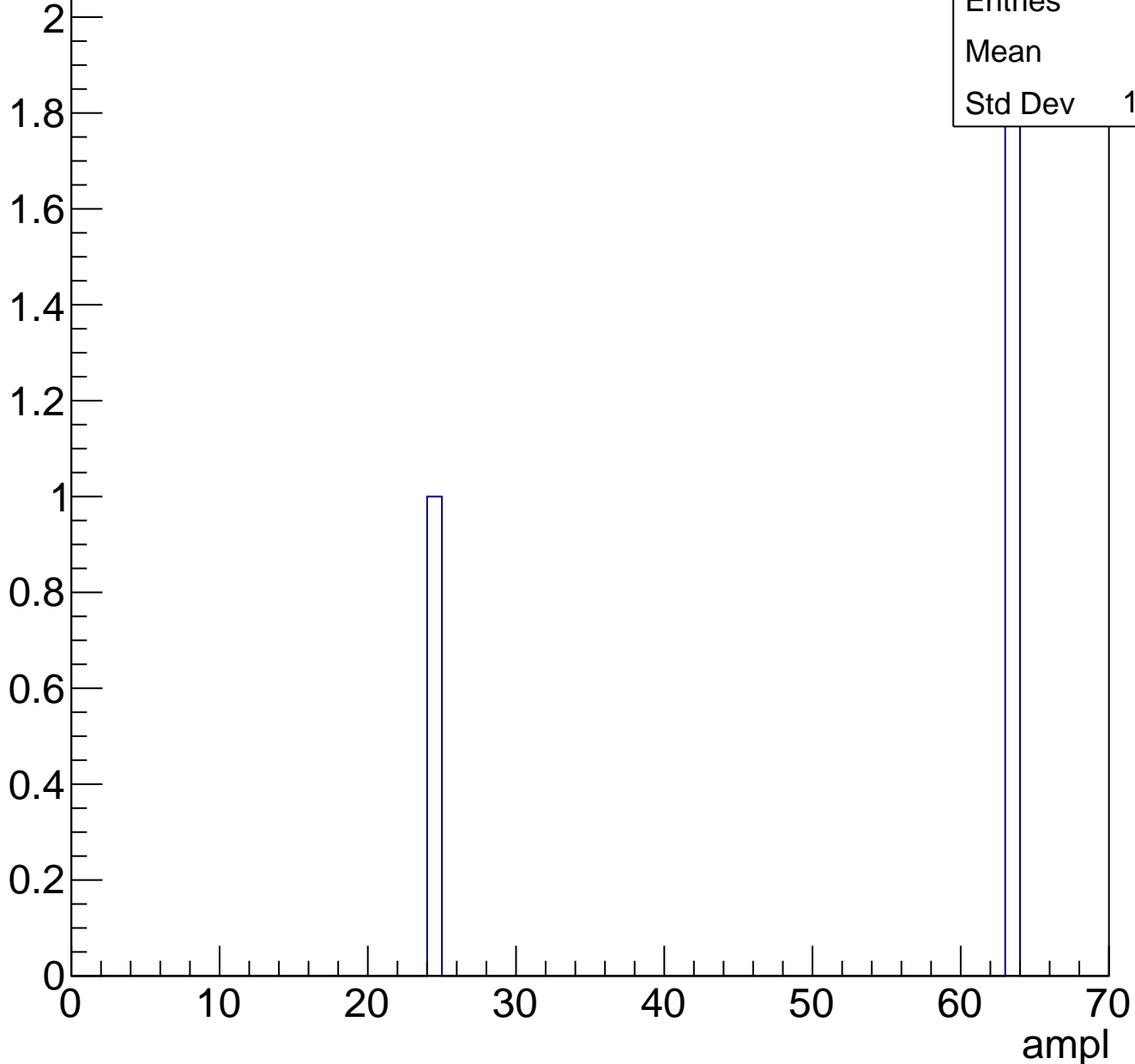




# B1L000S, U23-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch61, adc0

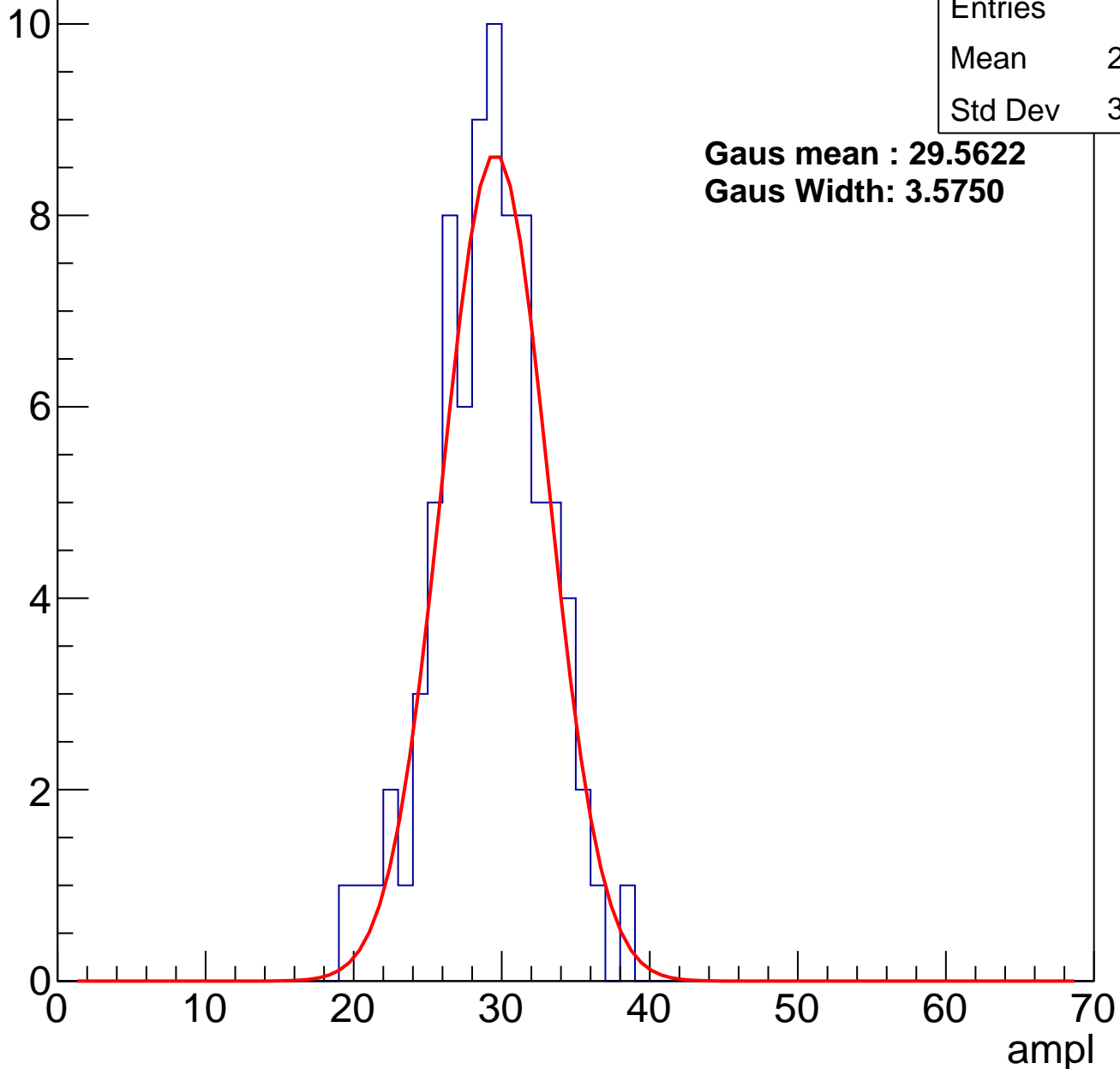
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	28.75
Std Dev	3.687

**Gaus mean : 29.5622**

**Gaus Width: 3.5750**

Entry



# B1L000S, U23-ch61, adc1

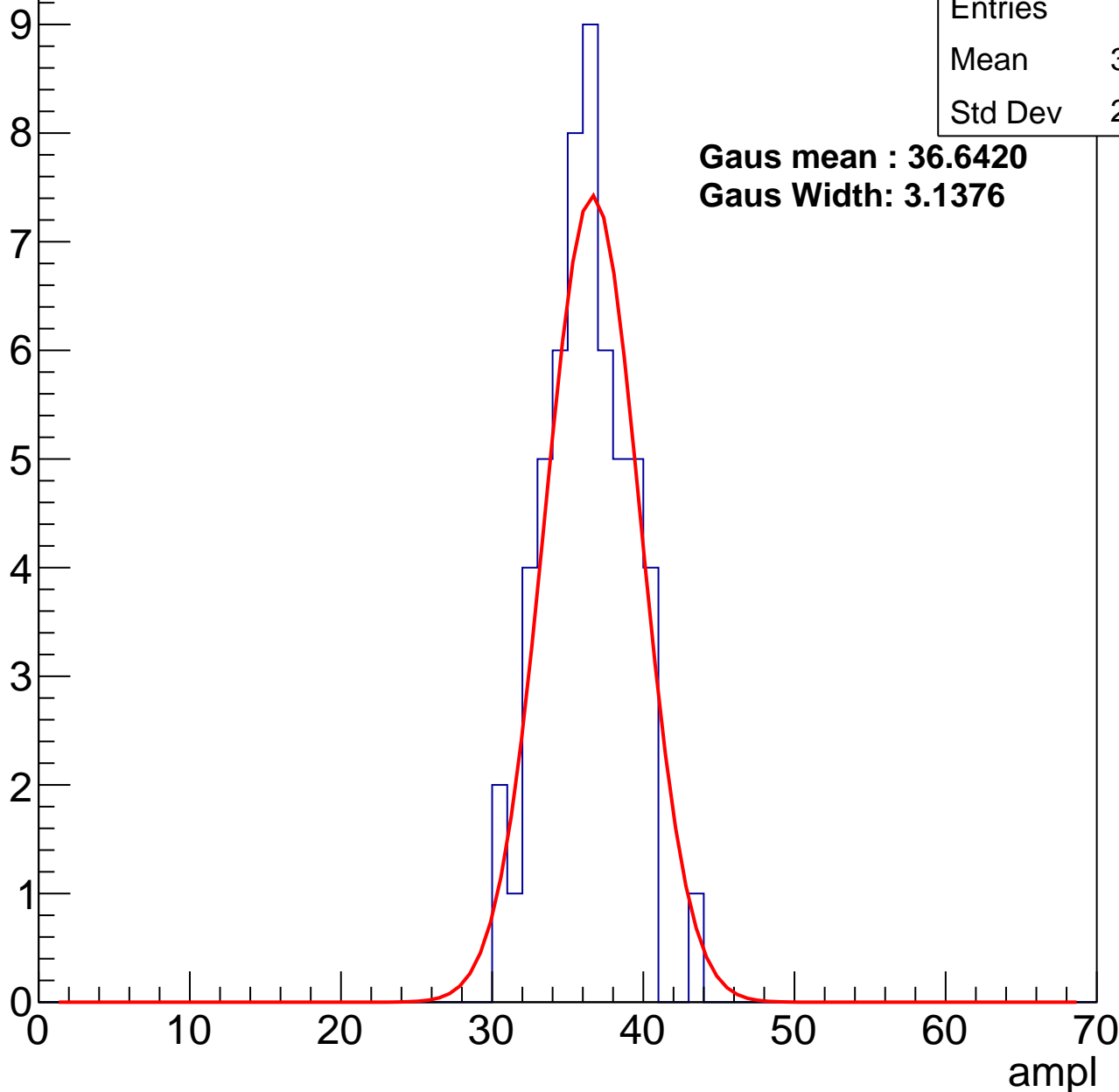
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.75
Std Dev	2.734

**Gaus mean : 36.6420**

**Gaus Width: 3.1376**



# B1L000S, U23-ch61, adc2

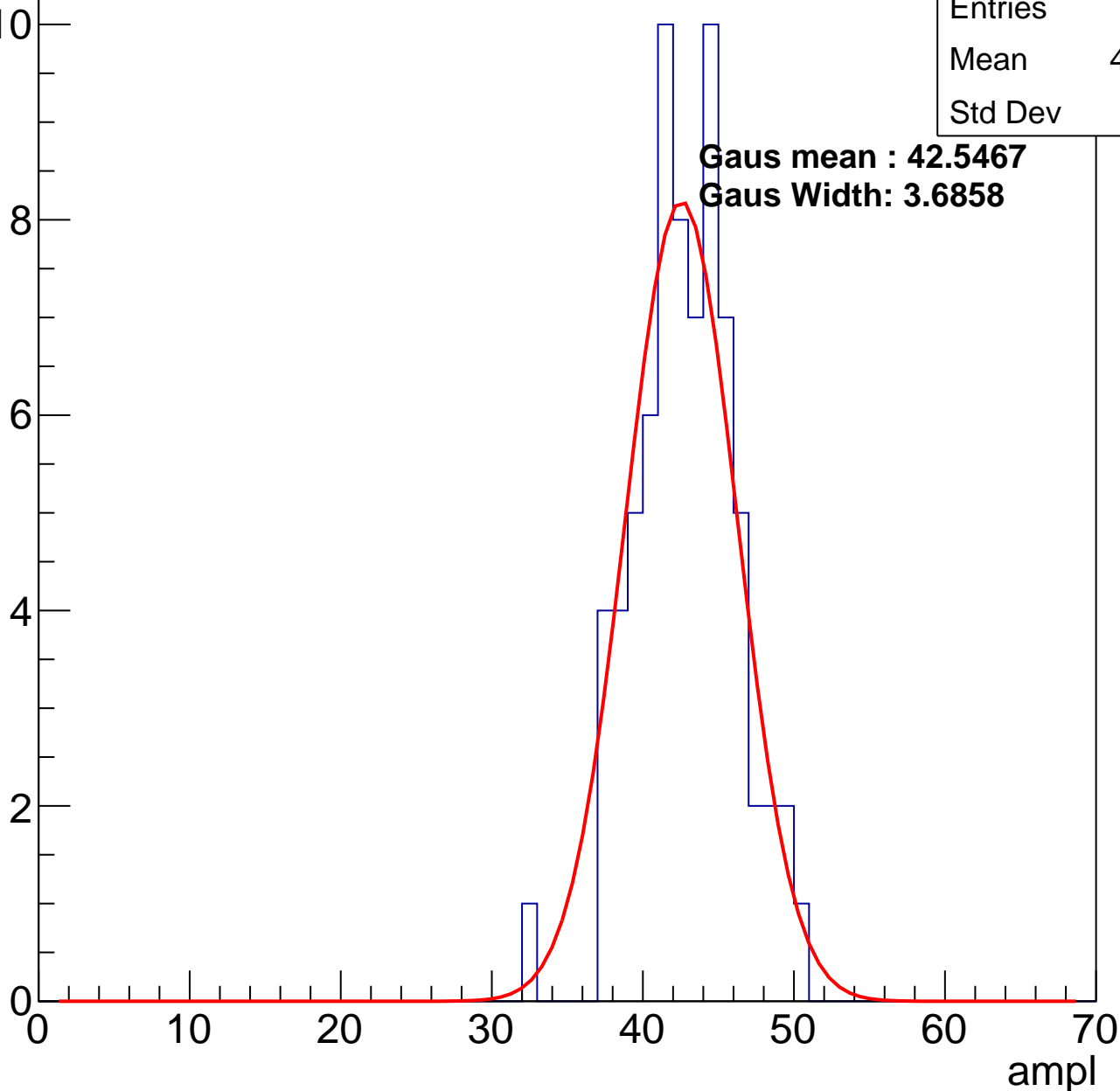
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	42.39
Std Dev	3.3

**Gaus mean : 42.5467**

**Gaus Width: 3.6858**

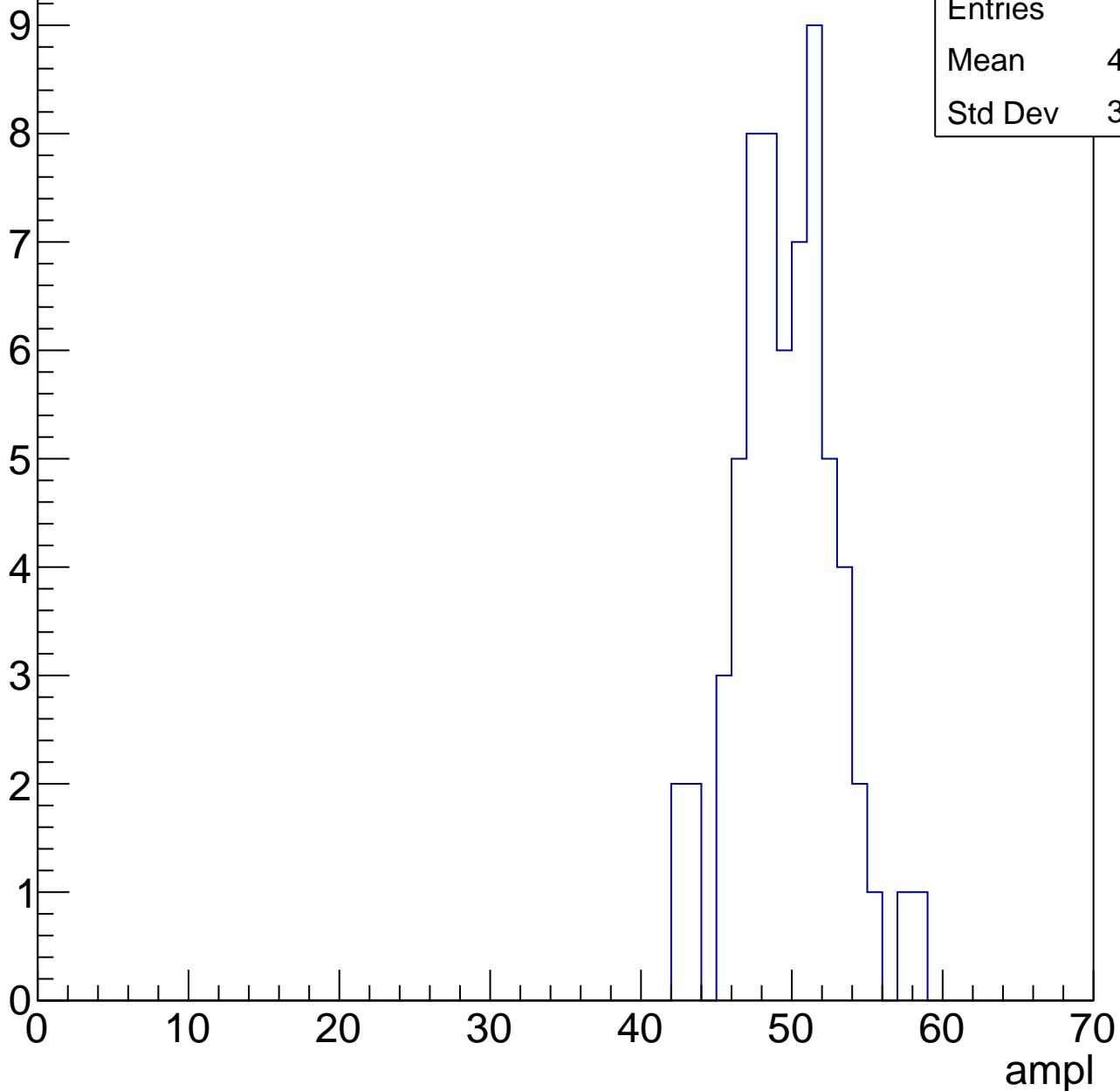


# B1L000S, U23-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

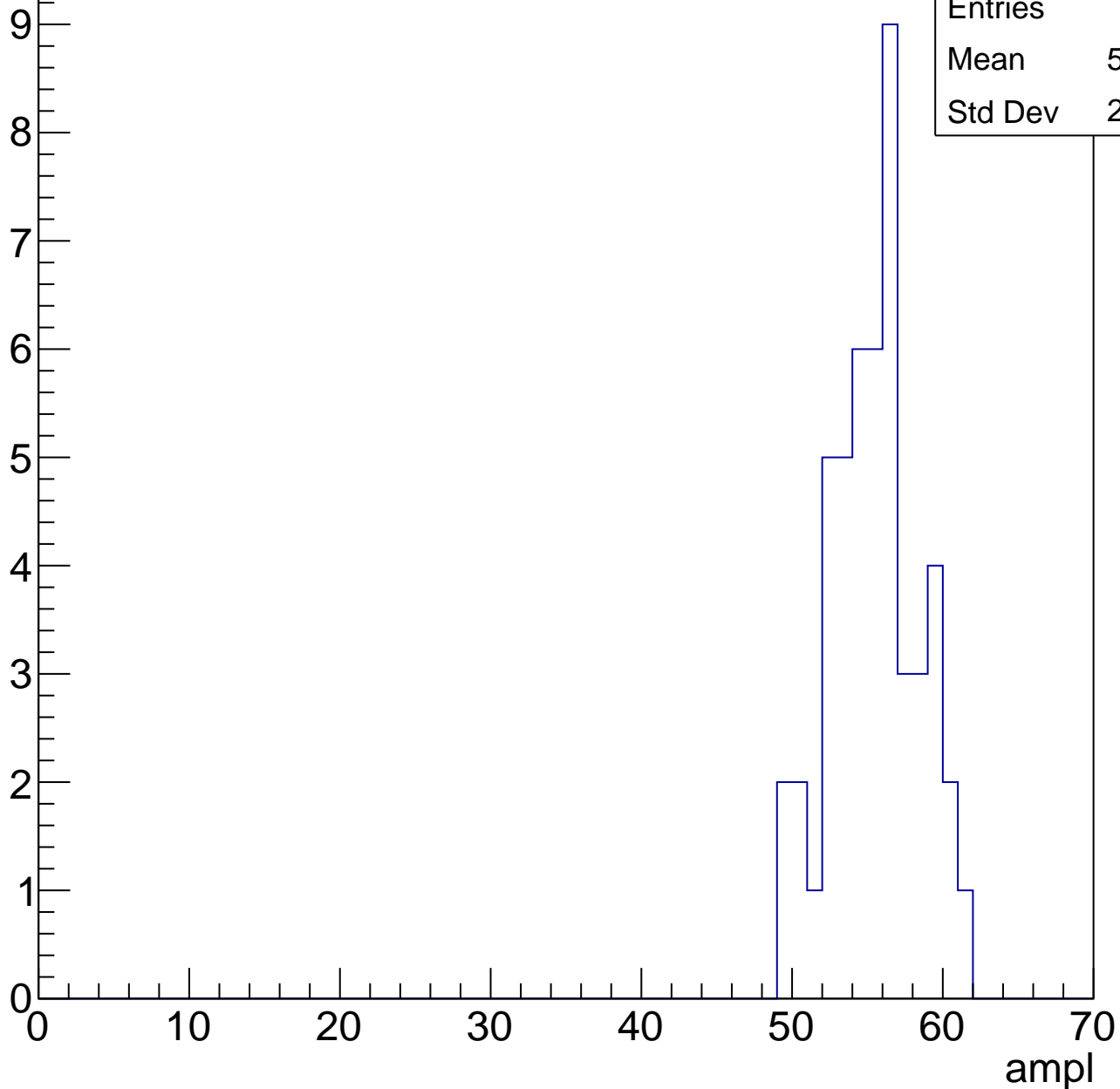
Entries	64
Mean	49.19
Std Dev	3.269



# B1L000S, U23-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

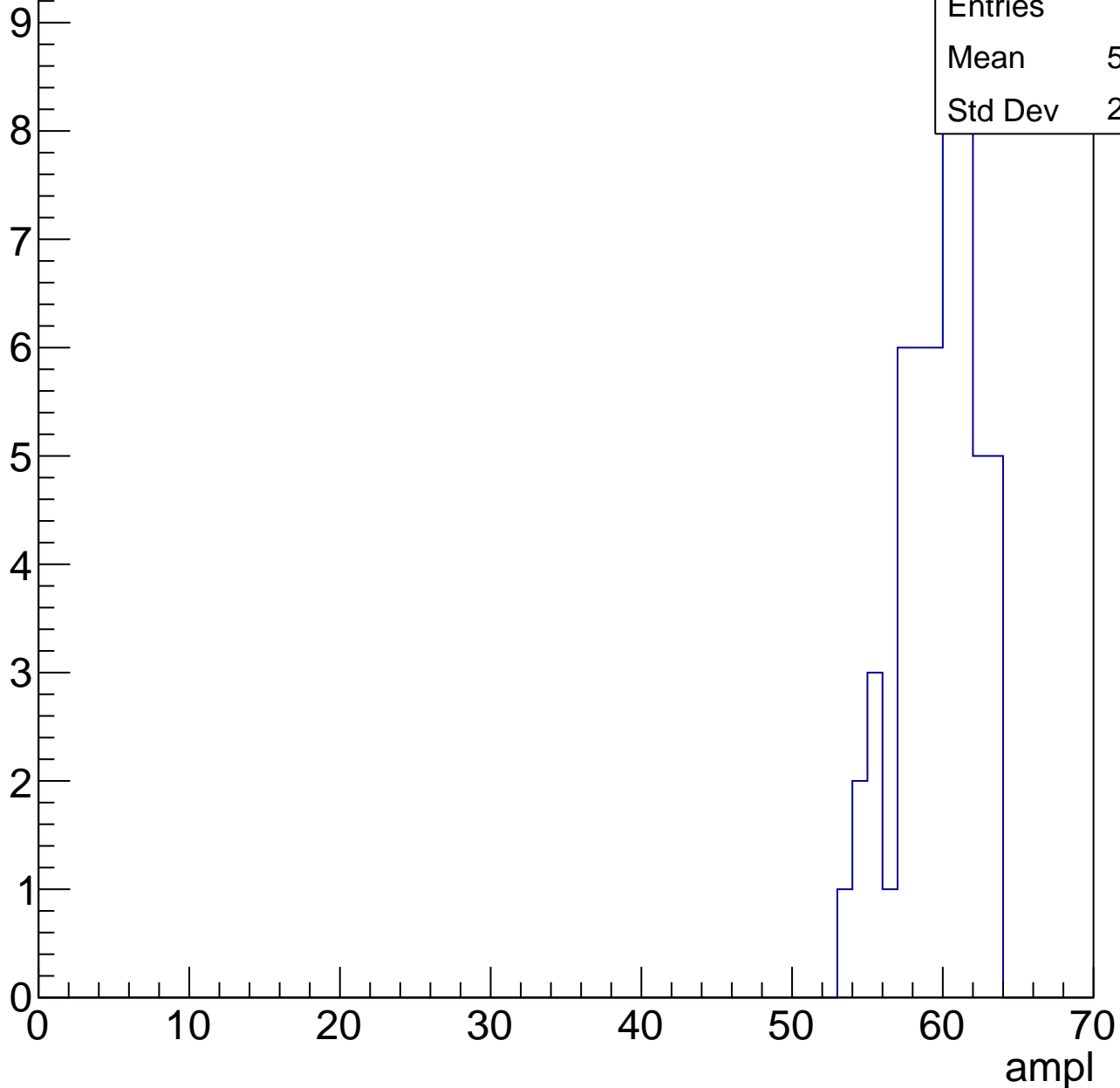
Entry



# B1L000S, U23-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

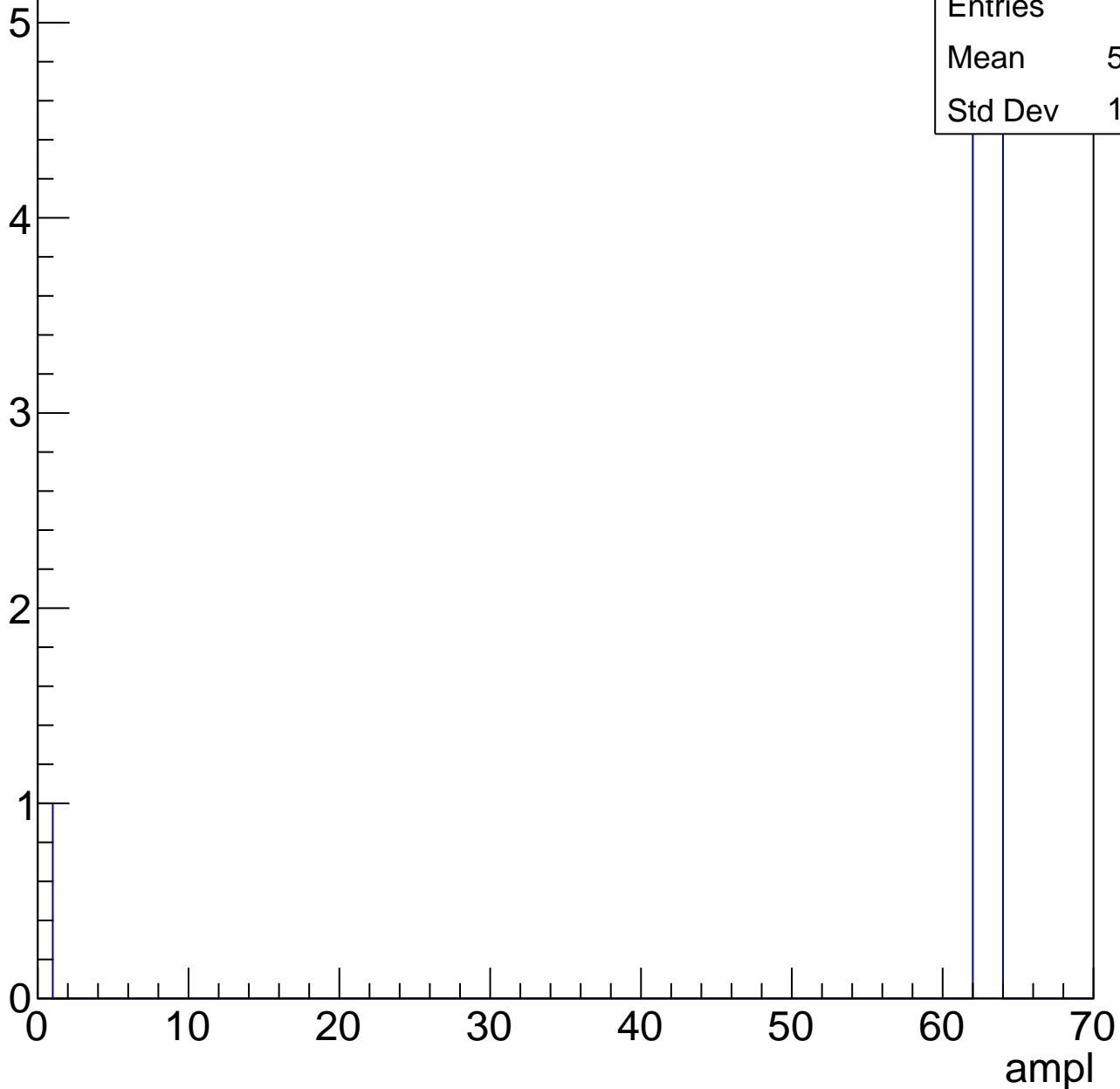


# B1L000S, U23-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	56.82
Std Dev	17.97





# B1L000S, U23-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch62, adc0

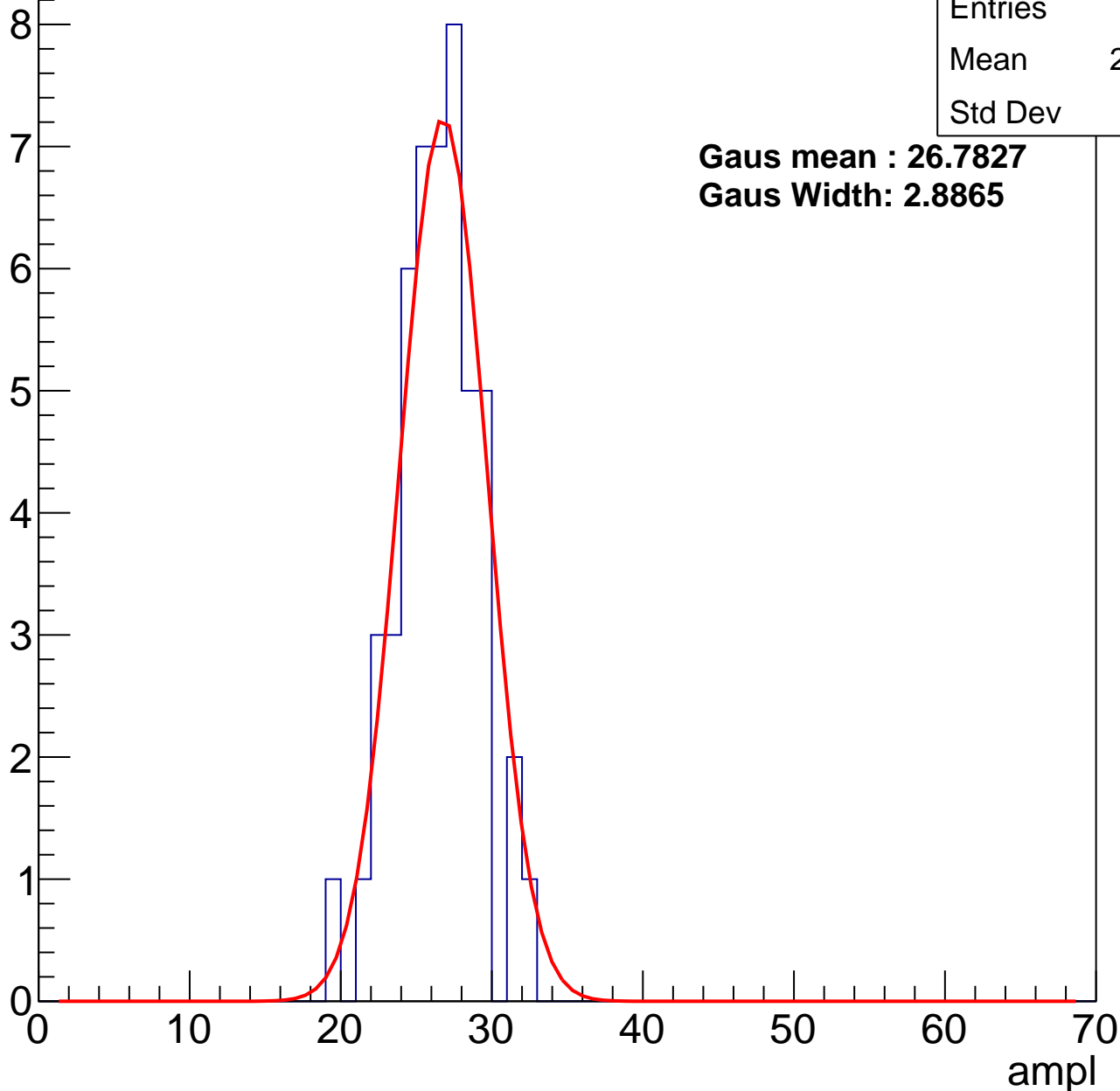
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	25.94
Std Dev	2.63

**Gaus mean : 26.7827**

**Gaus Width: 2.8865**



# B1L000S, U23-ch62, adc1

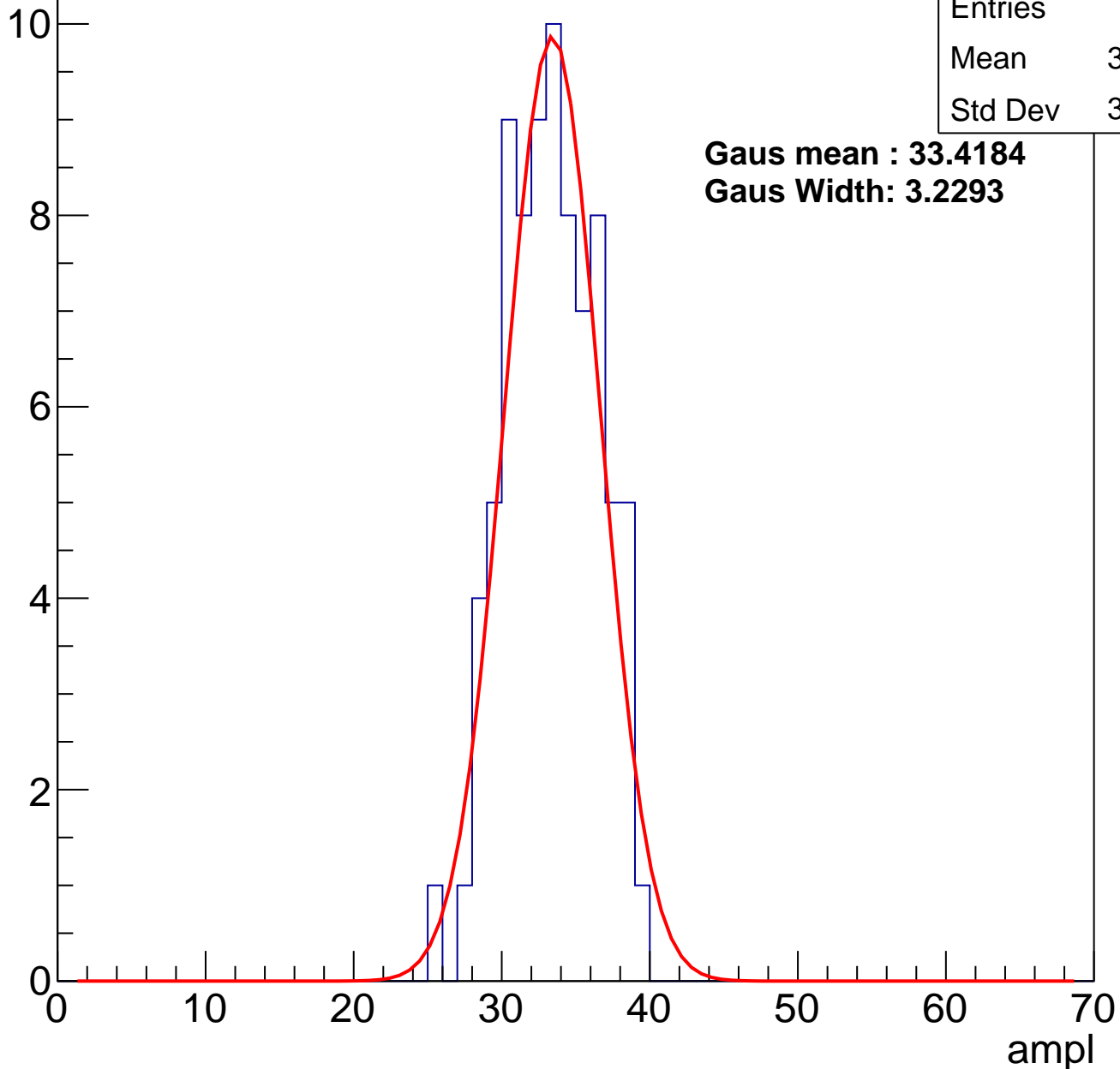
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	32.89
Std Dev	3.043

**Gaus mean : 33.4184**

**Gaus Width: 3.2293**

Entry



# B1L000S, U23-ch62, adc2

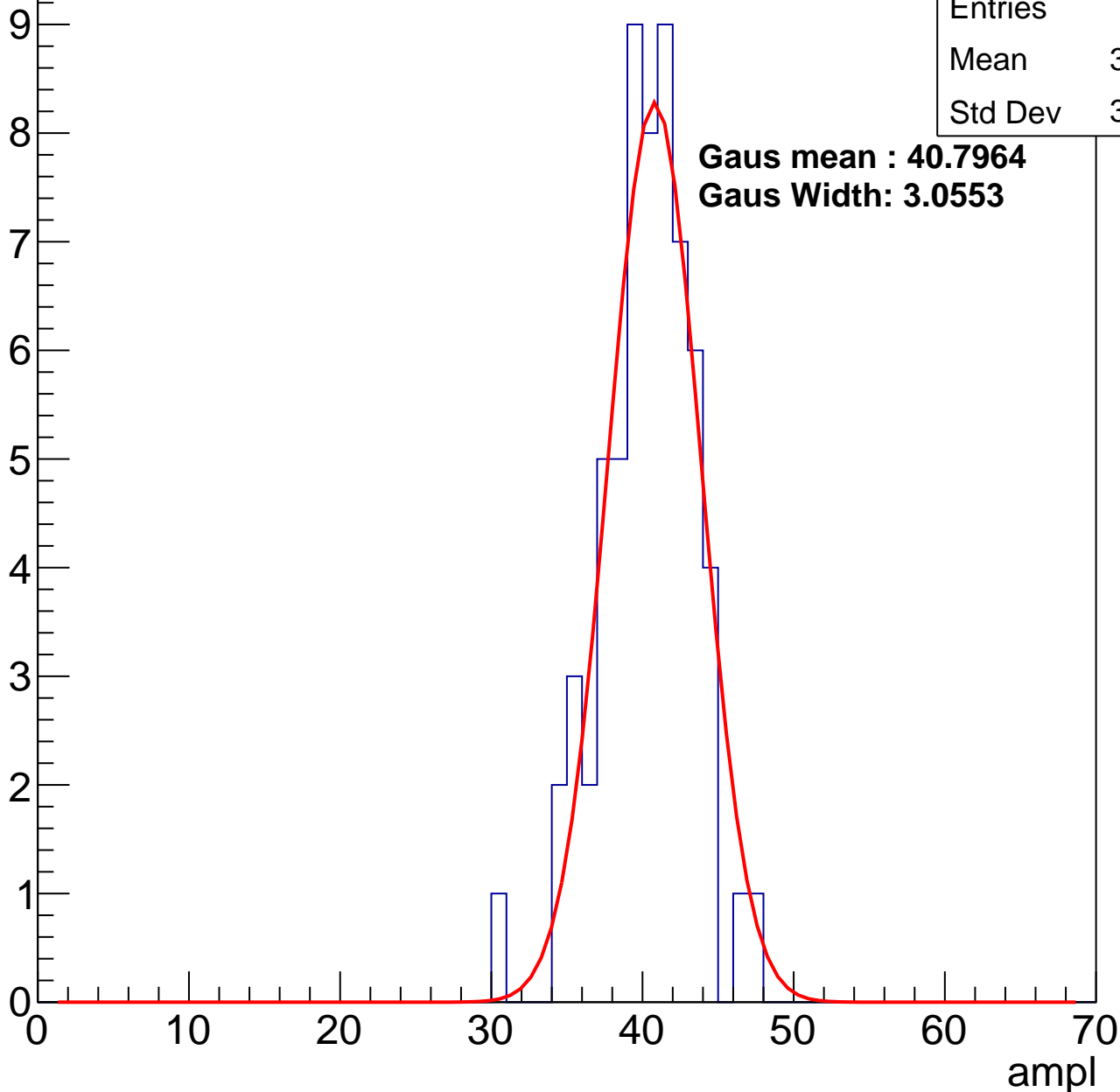
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	39.86
Std Dev	3.065

**Gaus mean : 40.7964**

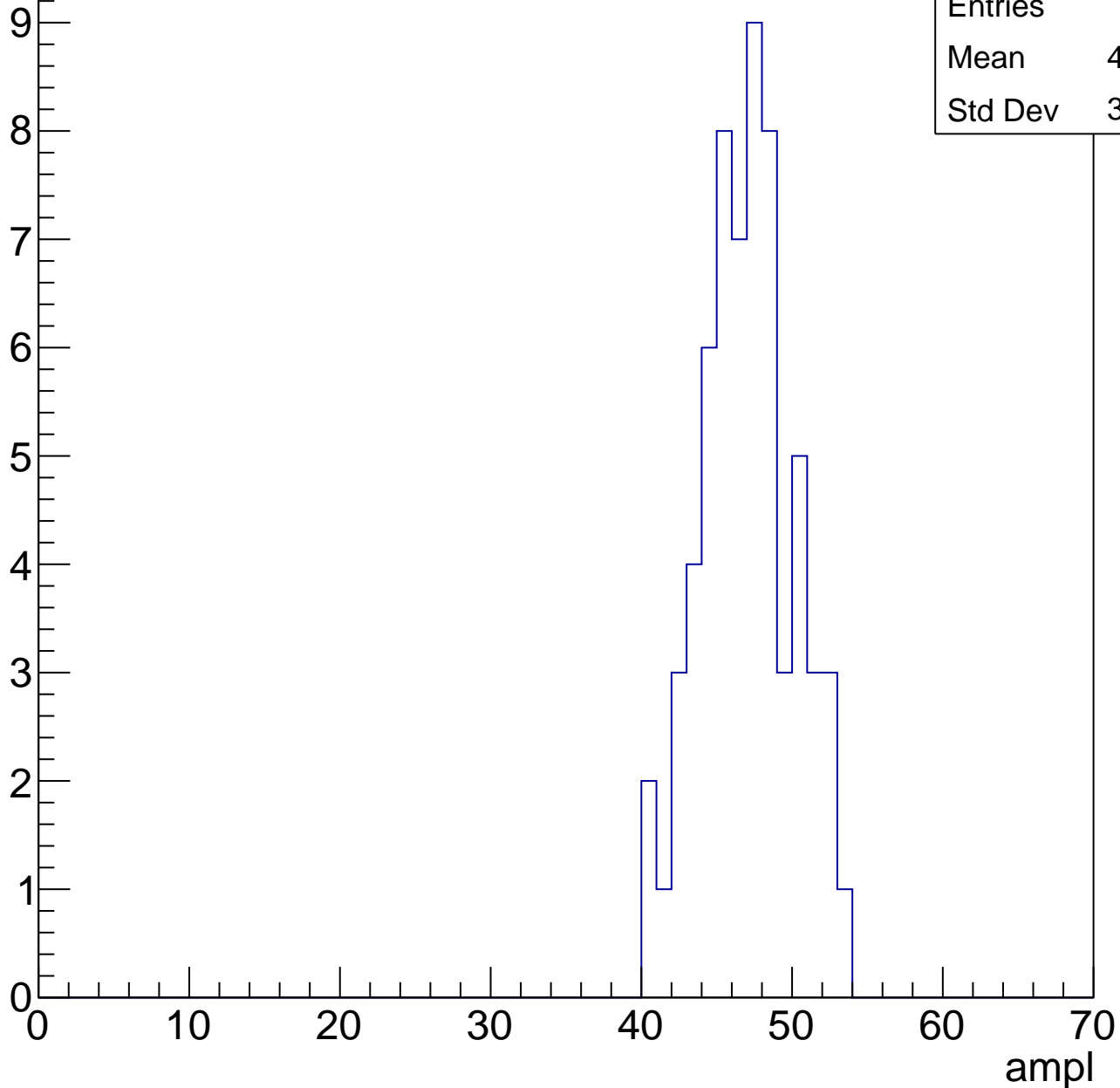
**Gaus Width: 3.0553**



# B1L000S, U23-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



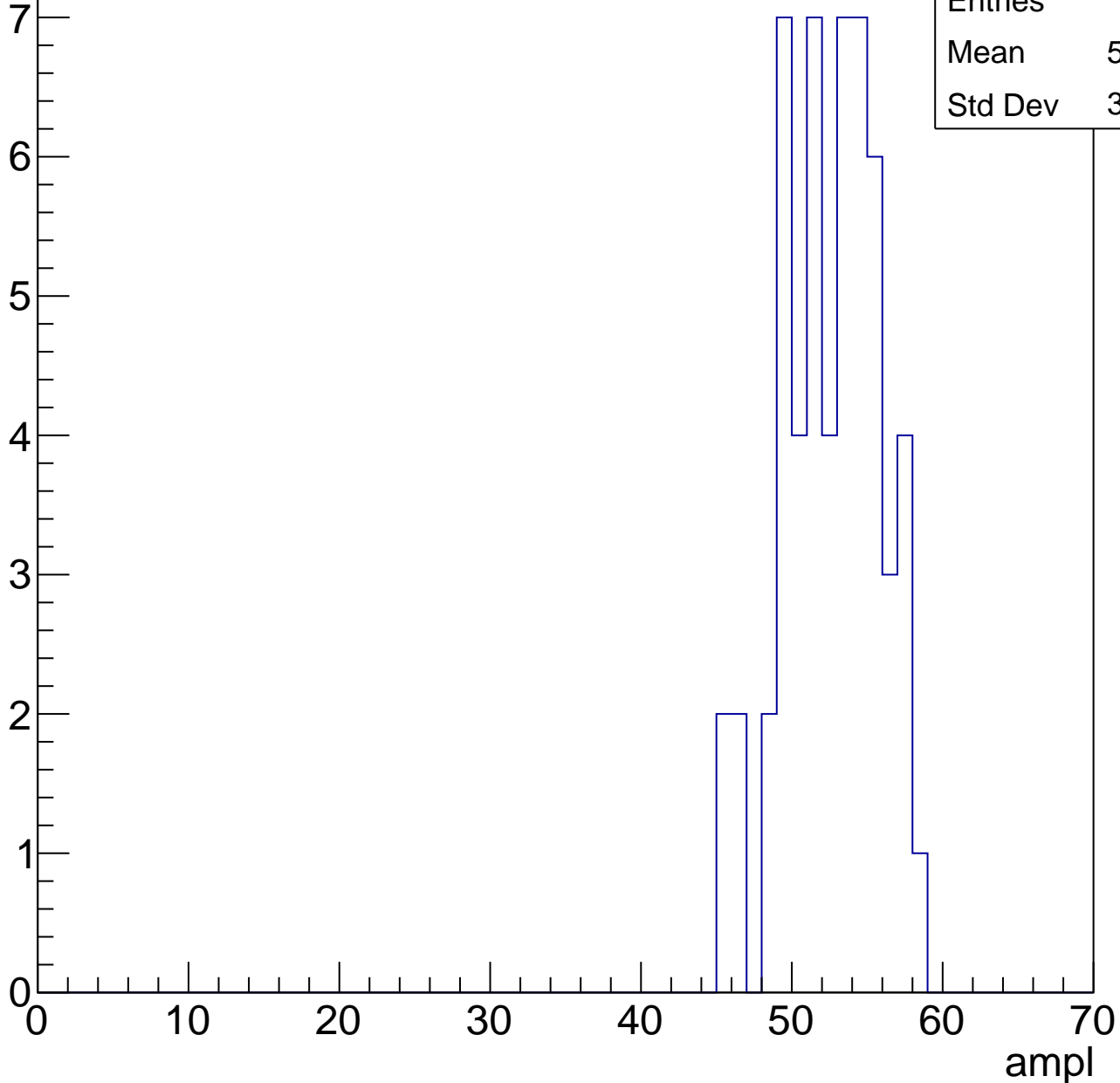
Entries	63
Mean	46.52
Std Dev	3.023

# B1L000S, U23-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	52.12
Std Dev	3.163

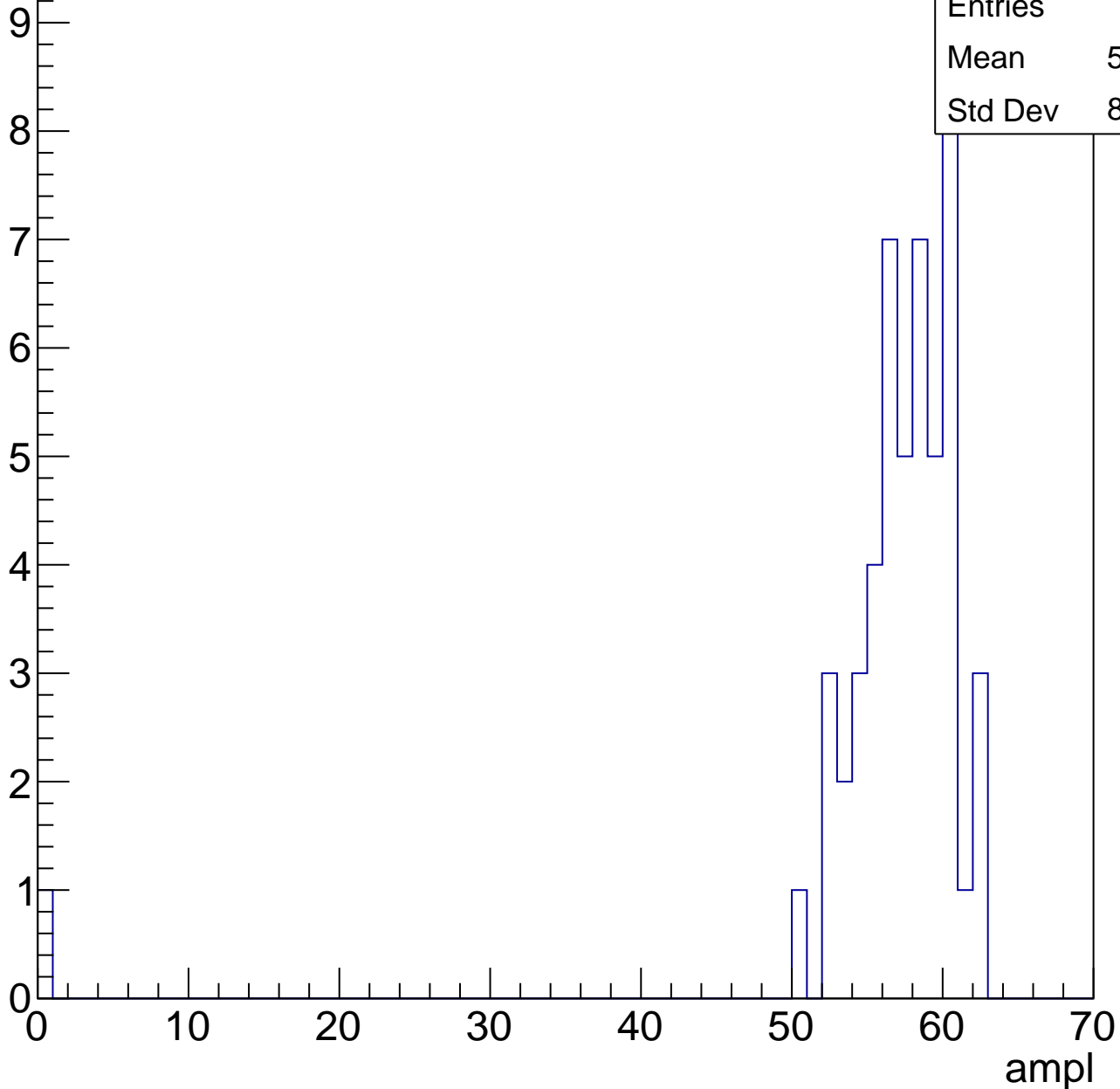


# B1L000S, U23-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	56.06
Std Dev	8.412

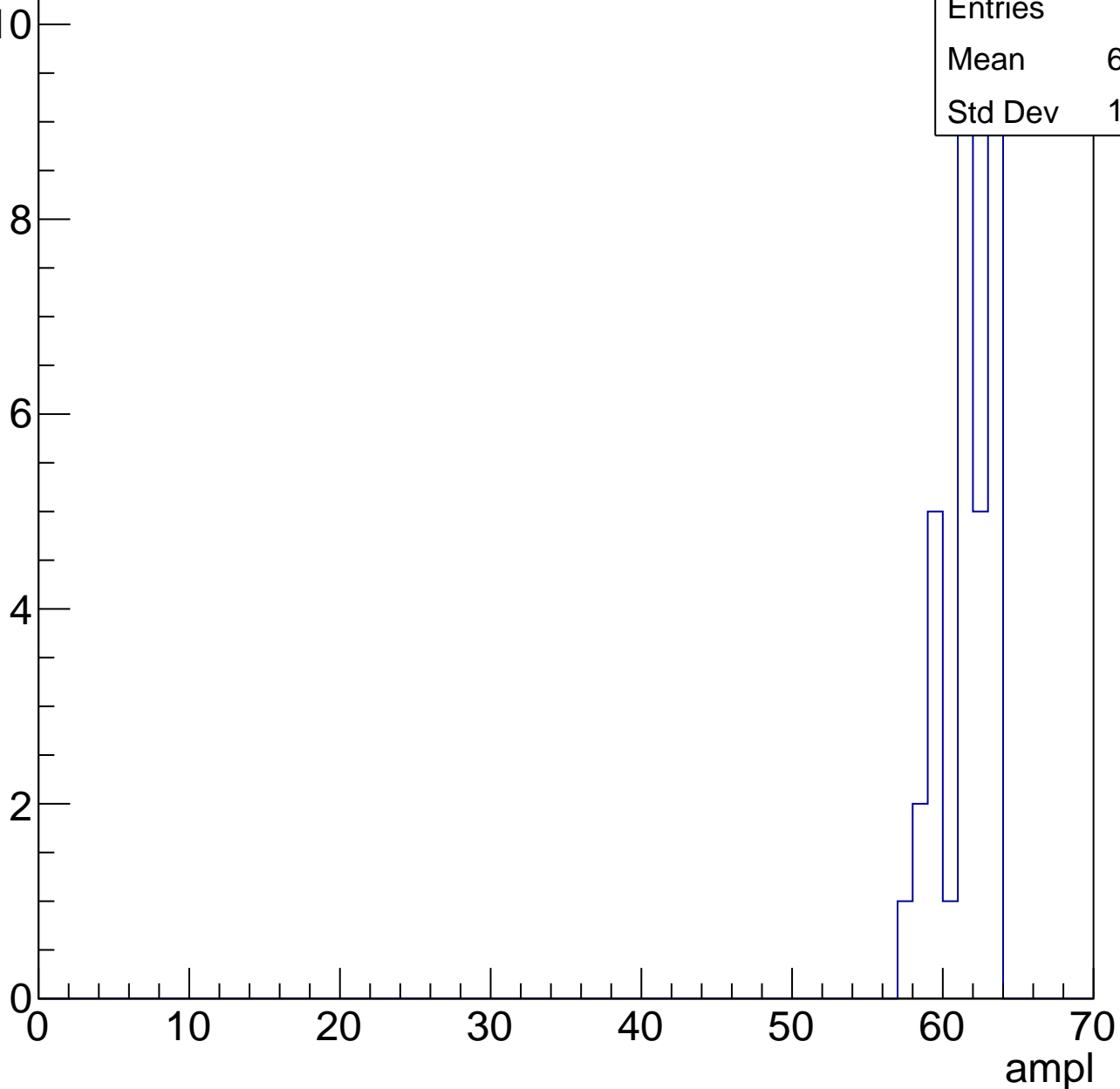


# B1L000S, U23-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	61.12
Std Dev	1.737

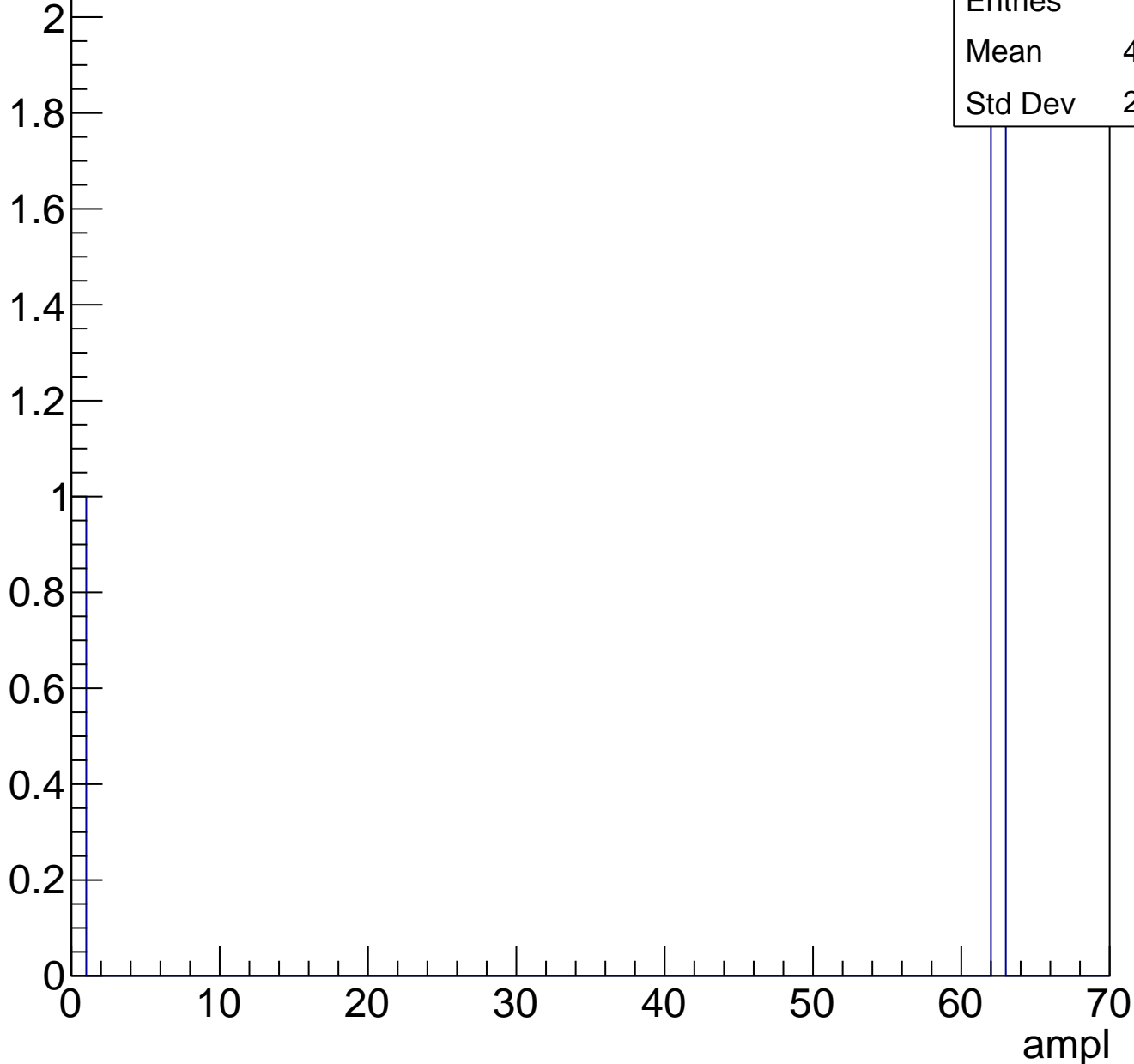




# B1L000S, U23-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch63, adc0

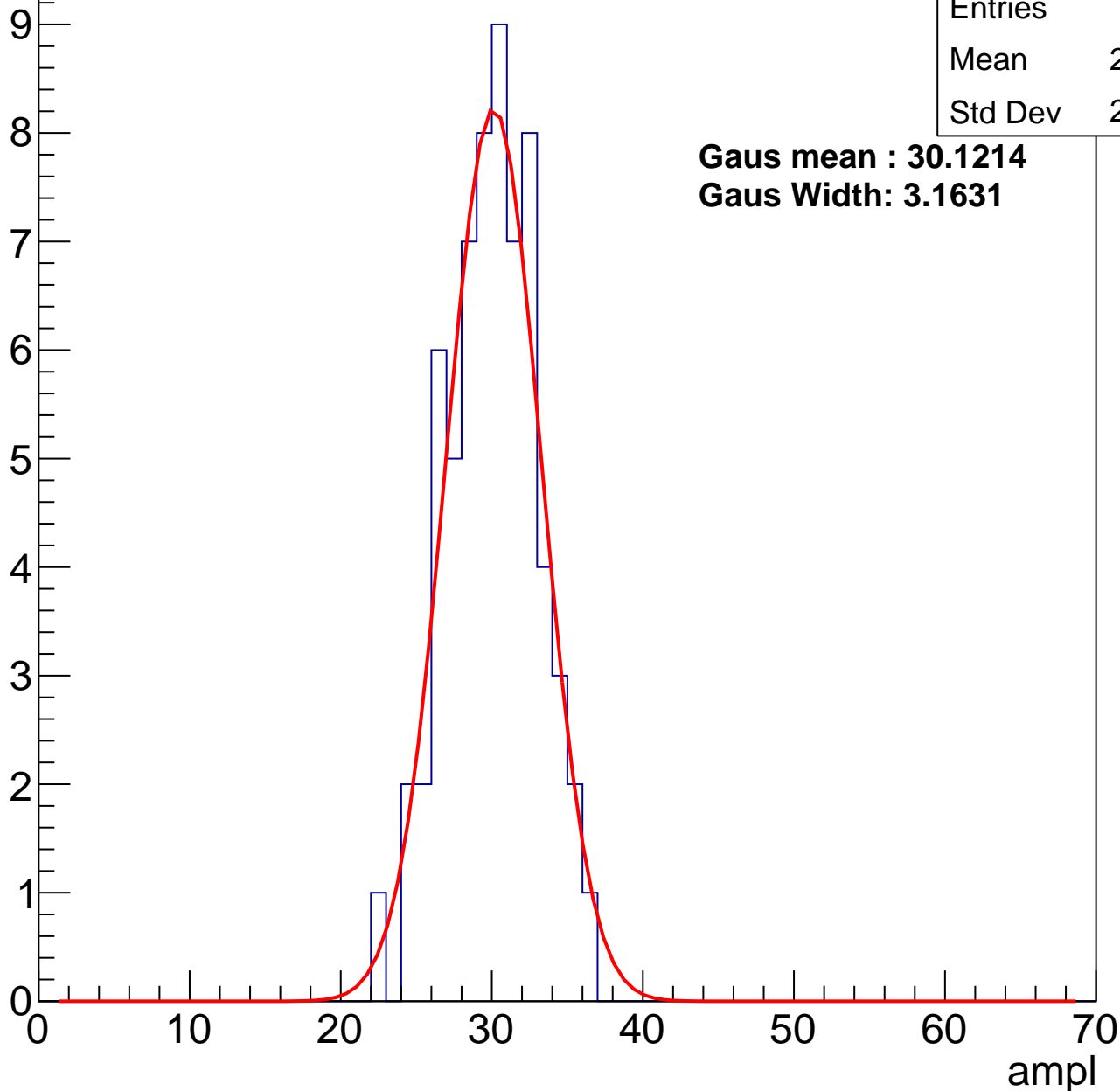
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.57
Std Dev	2.935

**Gaus mean : 30.1214**

**Gaus Width: 3.1631**



# B1L000S, U23-ch63, adc1

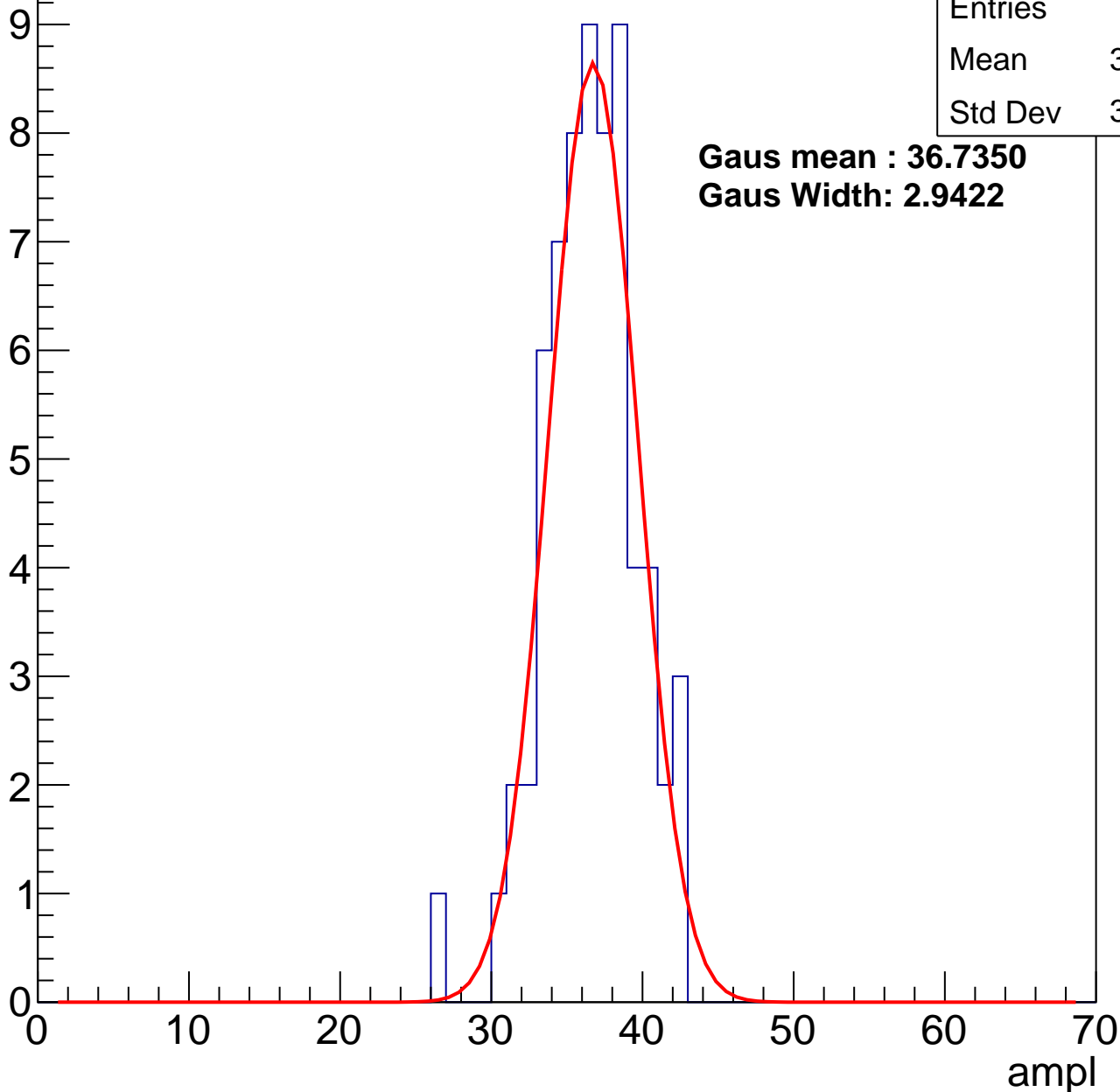
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	36.12
Std Dev	3.038

**Gaus mean : 36.7350**

**Gaus Width: 2.9422**



# B1L000S, U23-ch63, adc2

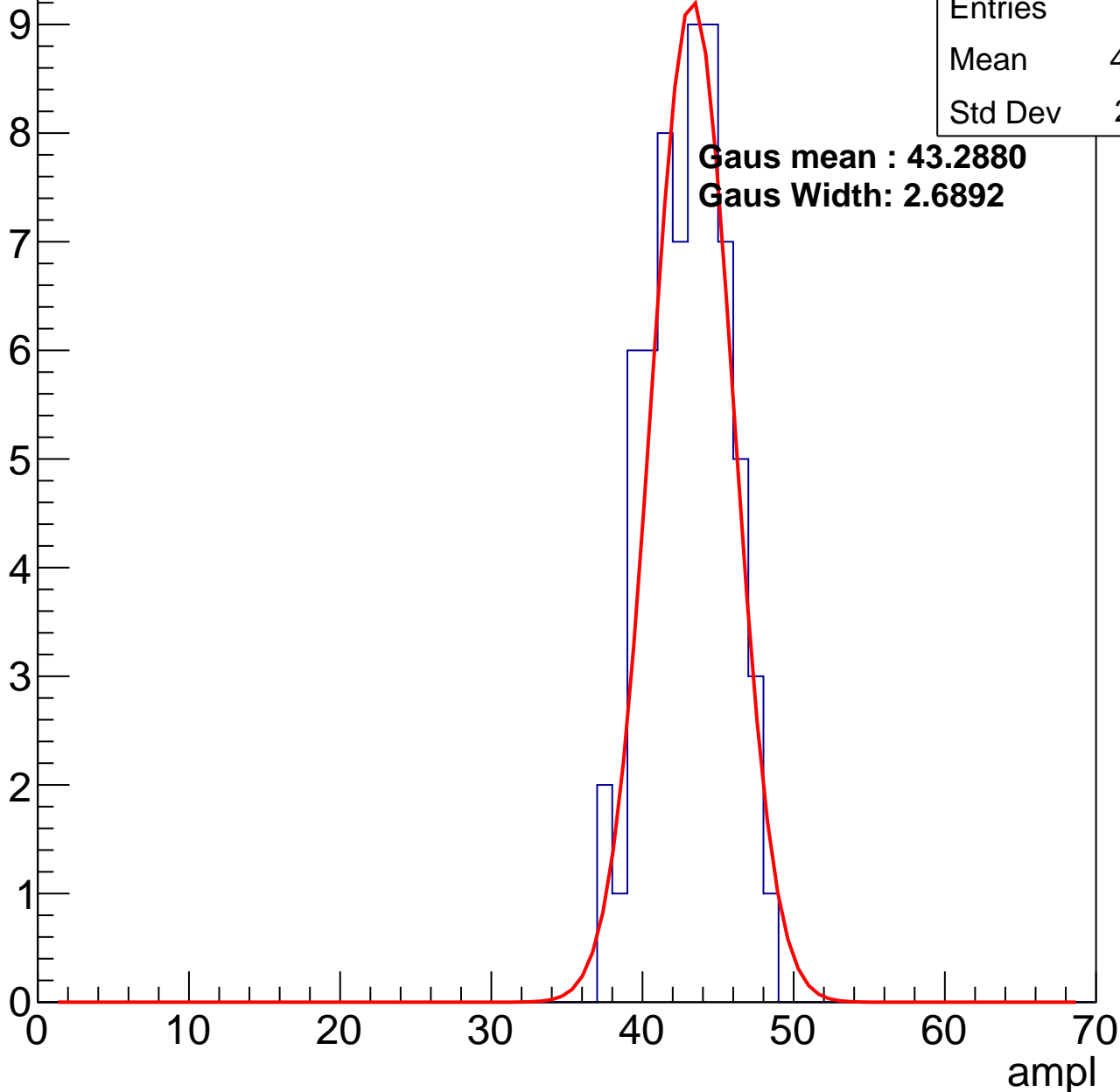
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	42.58
Std Dev	2.591

**Gaus mean : 43.2880**

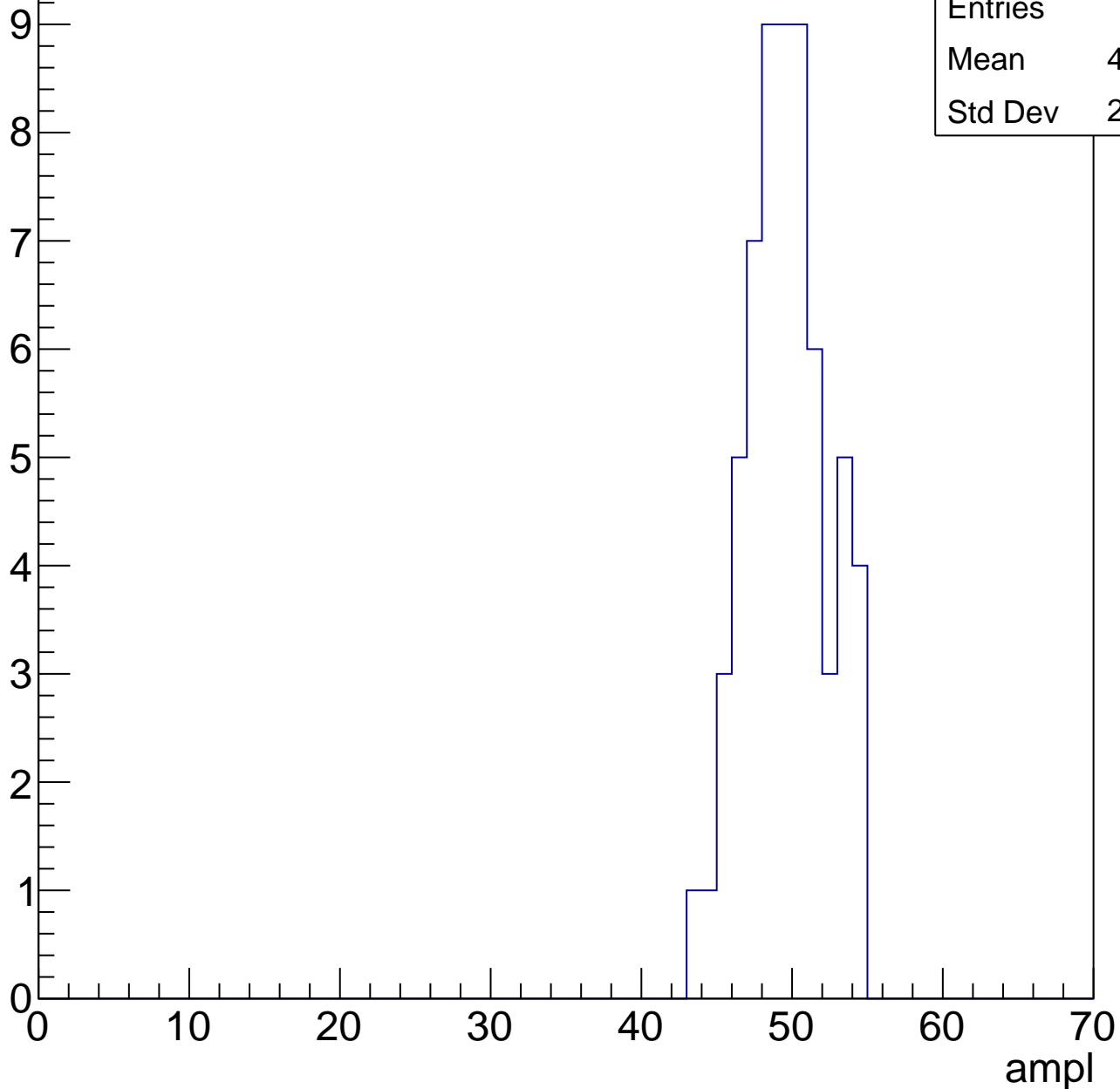
**Gaus Width: 2.6892**



# B1L000S, U23-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

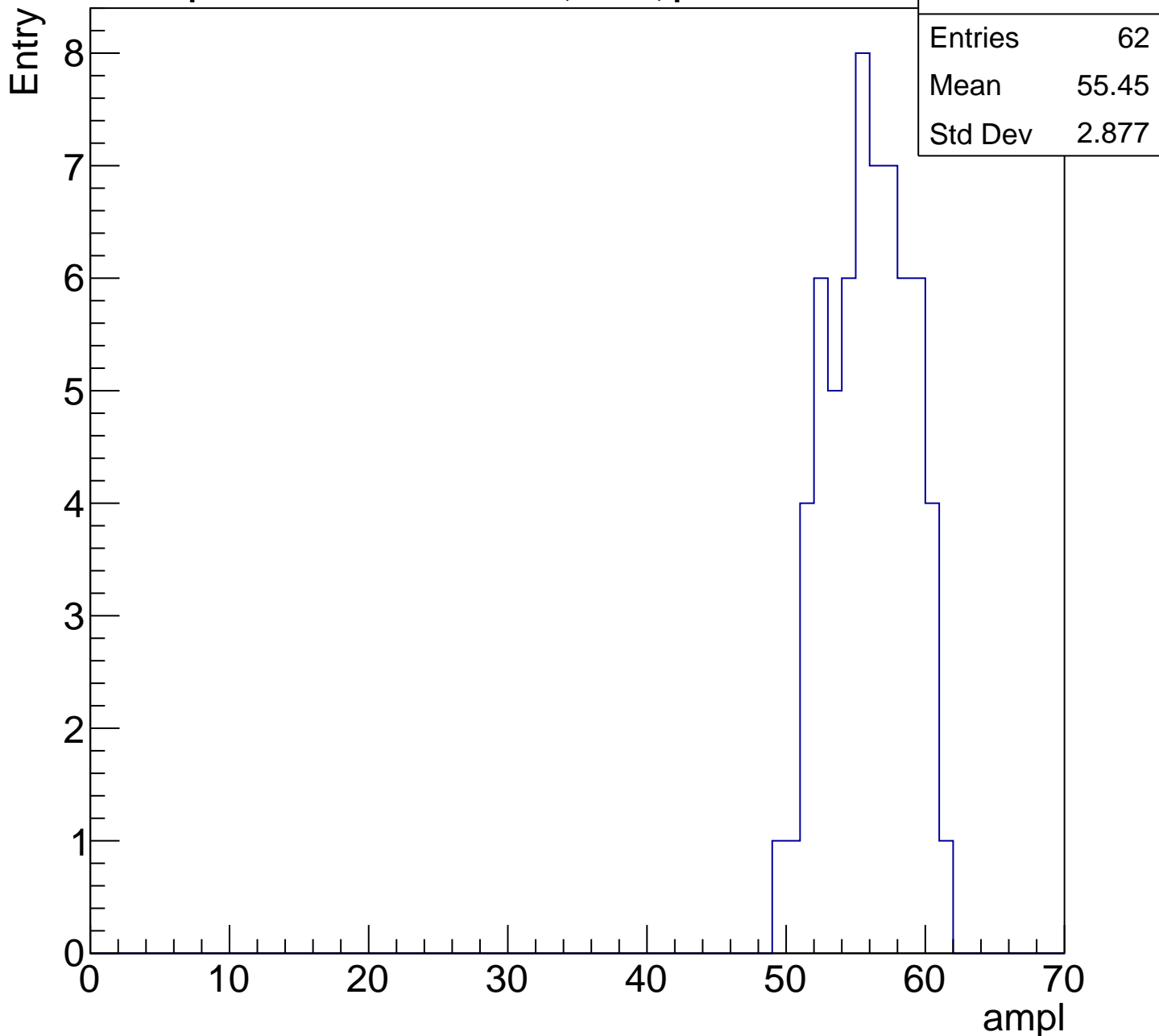
Entry



Entries	62
Mean	49.15
Std Dev	2.633

# B1L000S, U23-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

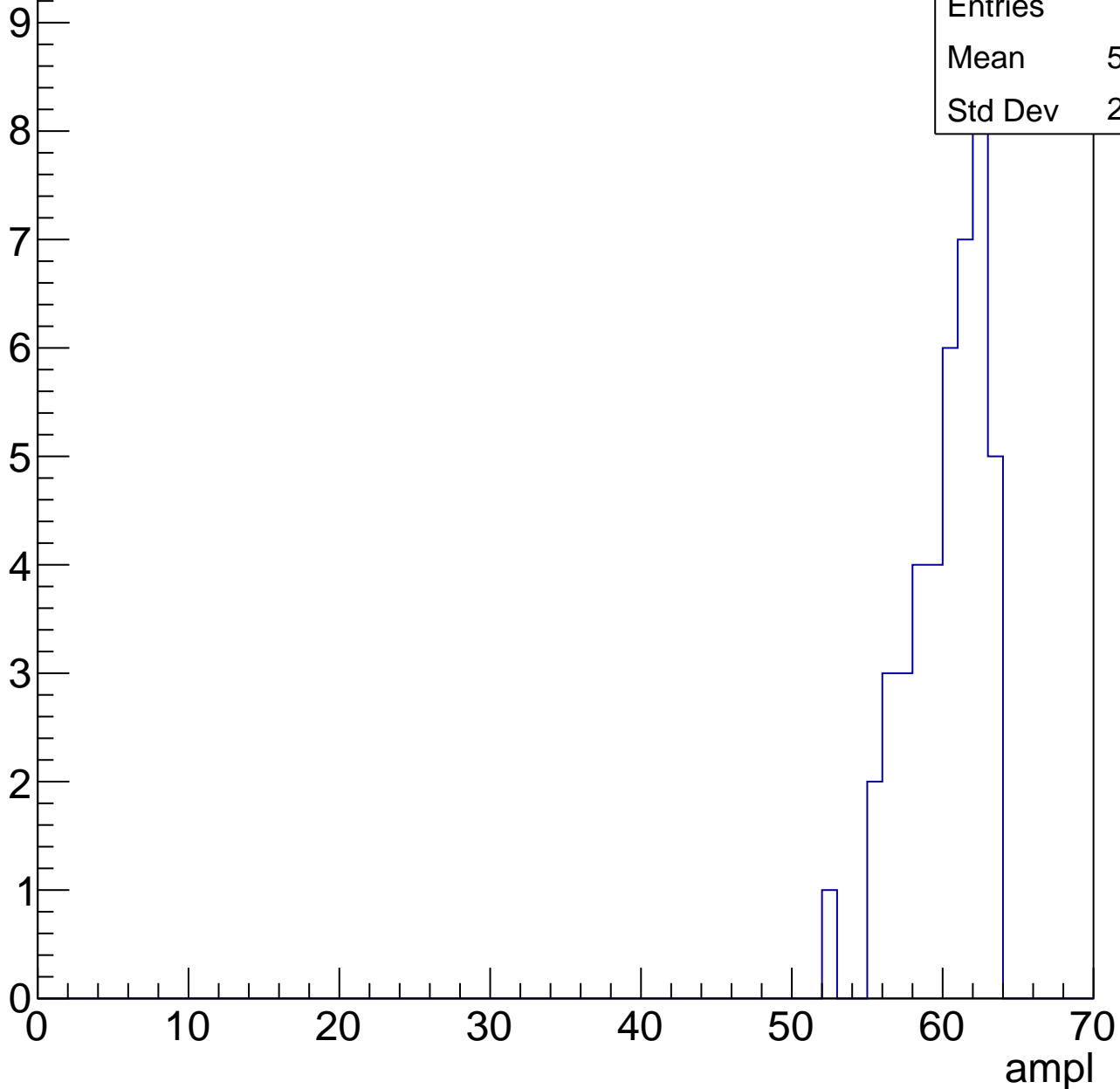


# B1L000S, U23-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	59.75
Std Dev	2.586

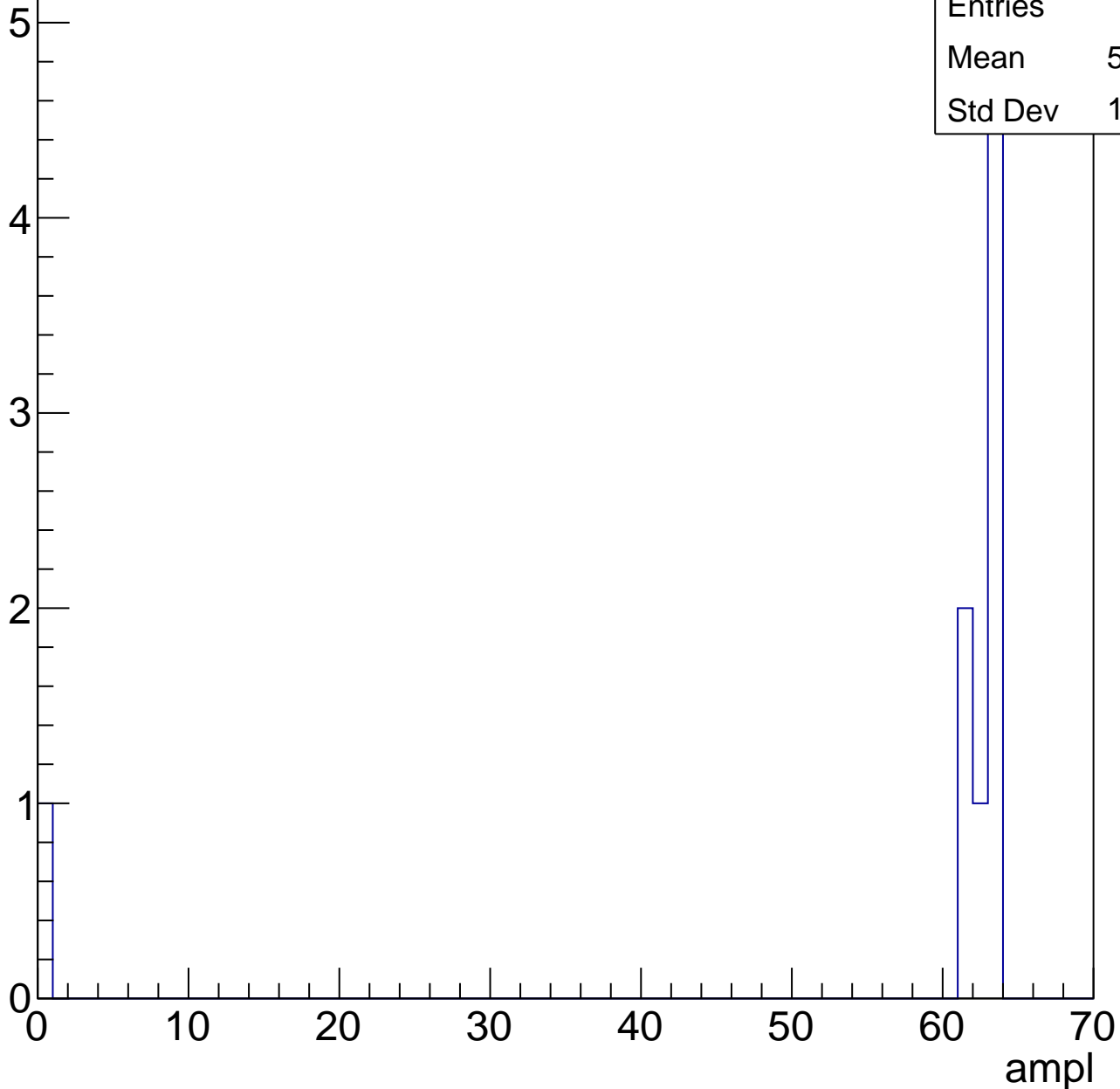


# B1L000S, U23-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	55.44
Std Dev	19.62





# B1L000S, U23-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch64, adc0

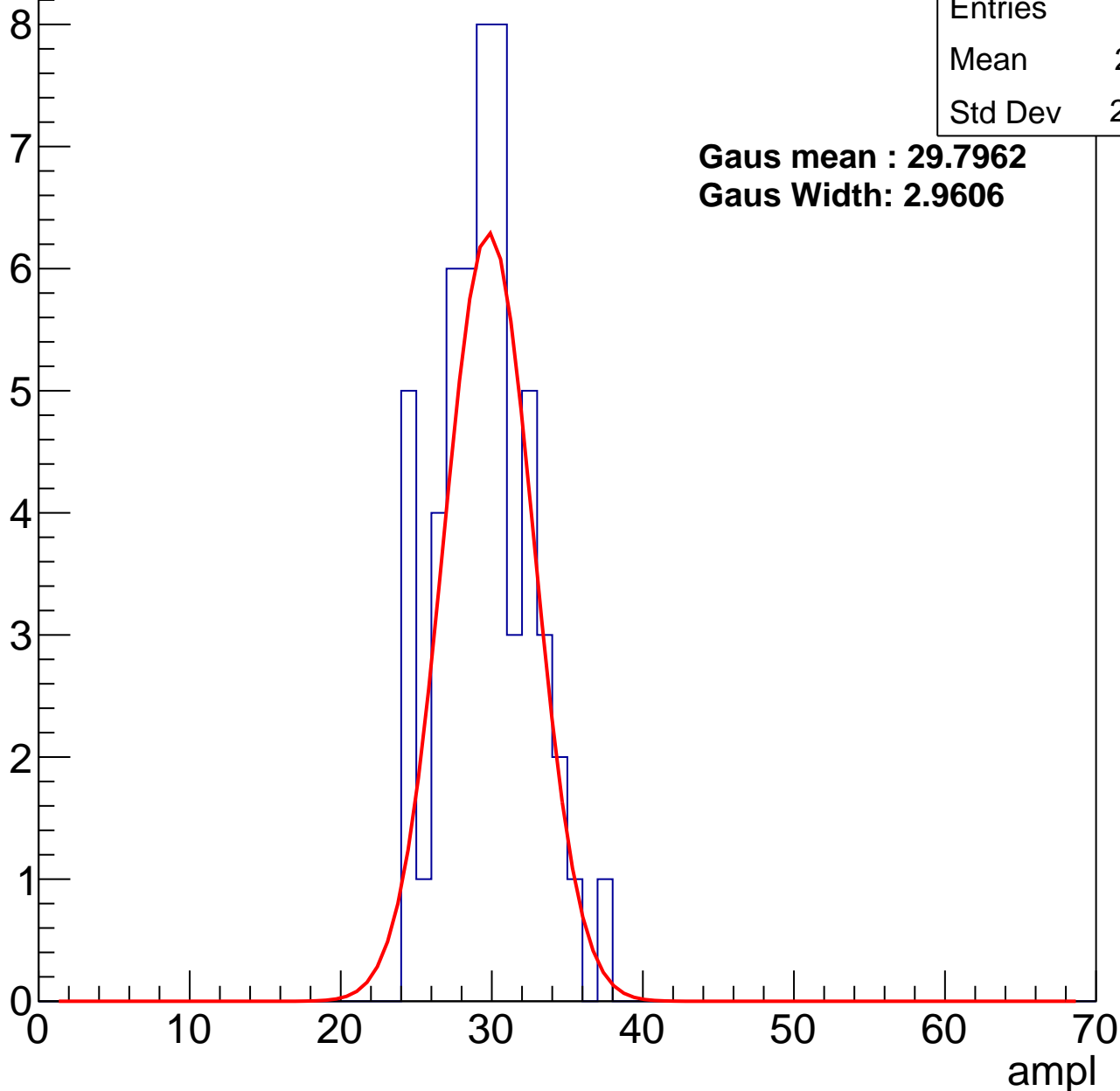
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	29.11
Std Dev	2.976

**Gaus mean : 29.7962**

**Gaus Width: 2.9606**



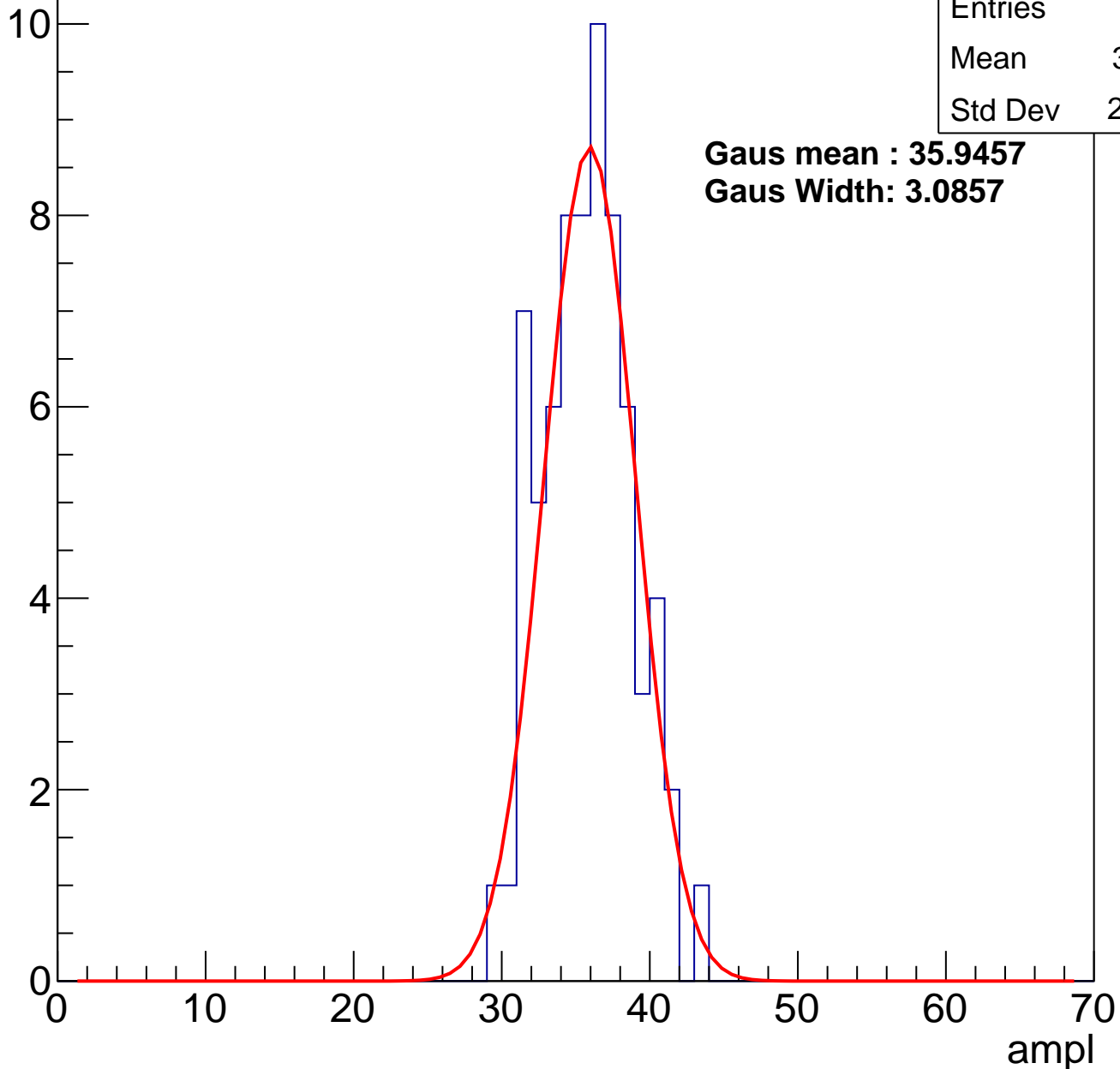
# B1L000S, U23-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	35.31
Std Dev	2.983

**Gaus mean : 35.9457**  
**Gaus Width: 3.0857**

Entry



# B1L000S, U23-ch64, adc2

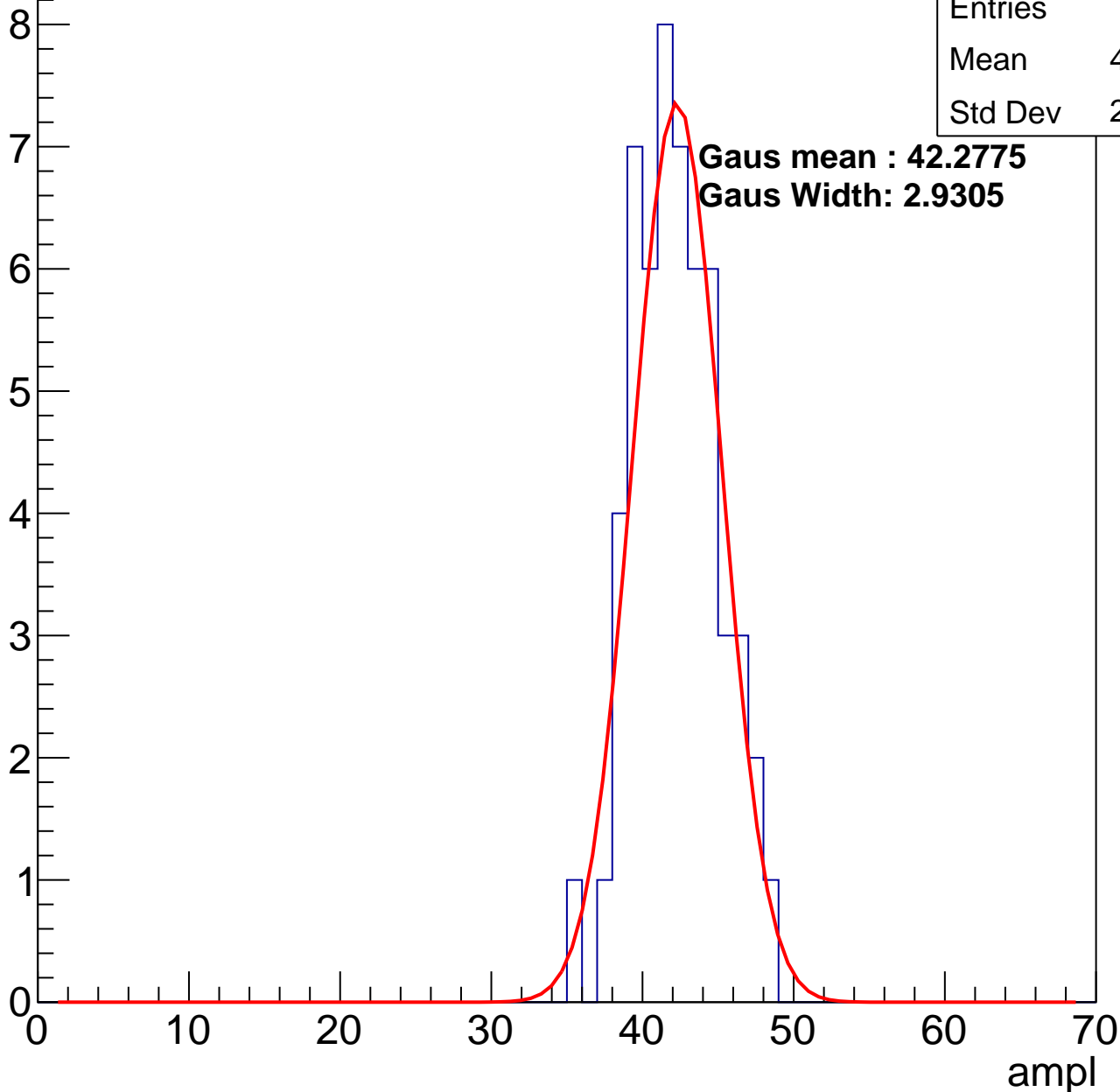
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.75
Std Dev	2.765

**Gaus mean : 42.2775**

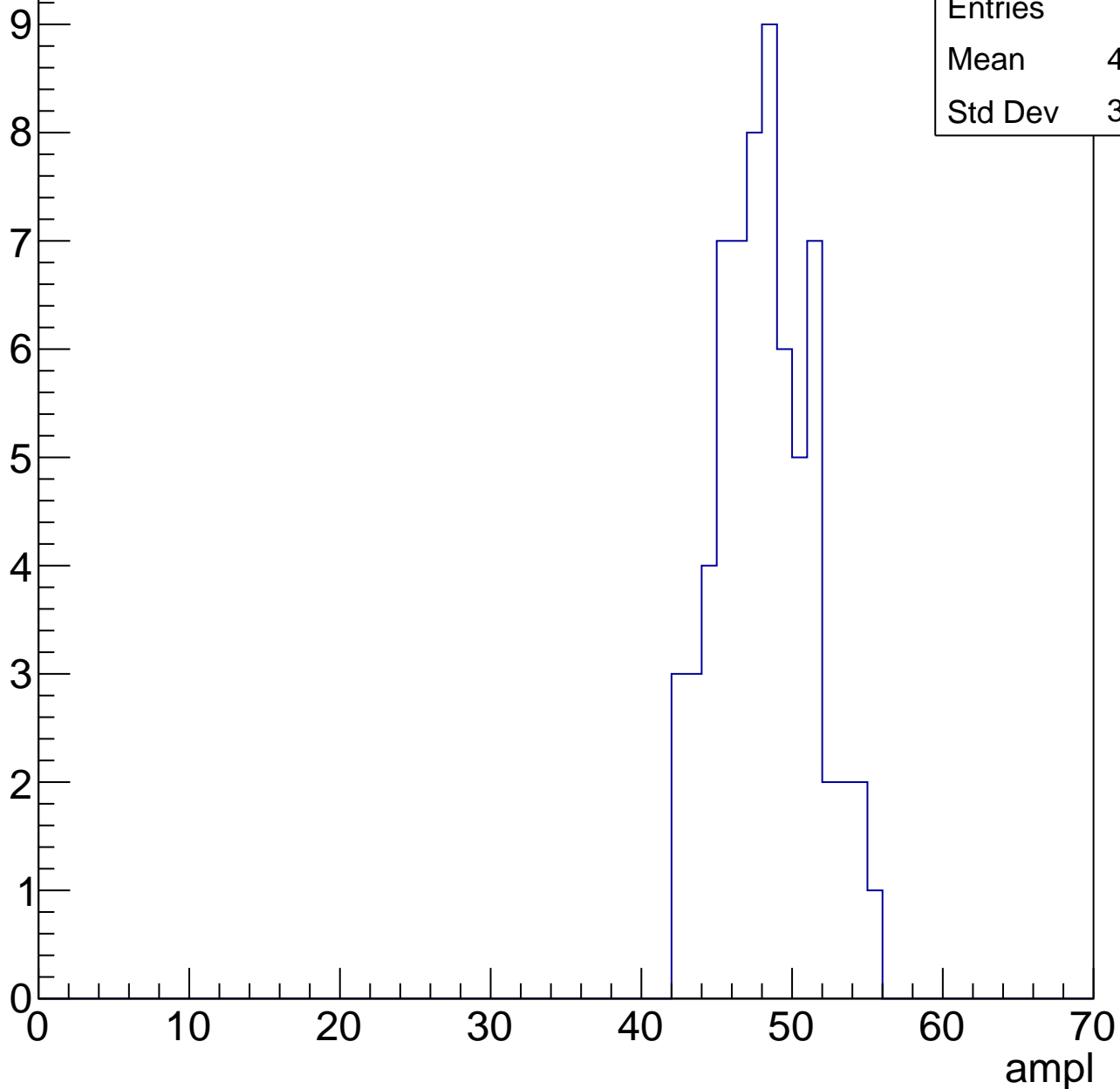
**Gaus Width: 2.9305**



# B1L000S, U23-ch64, adc3

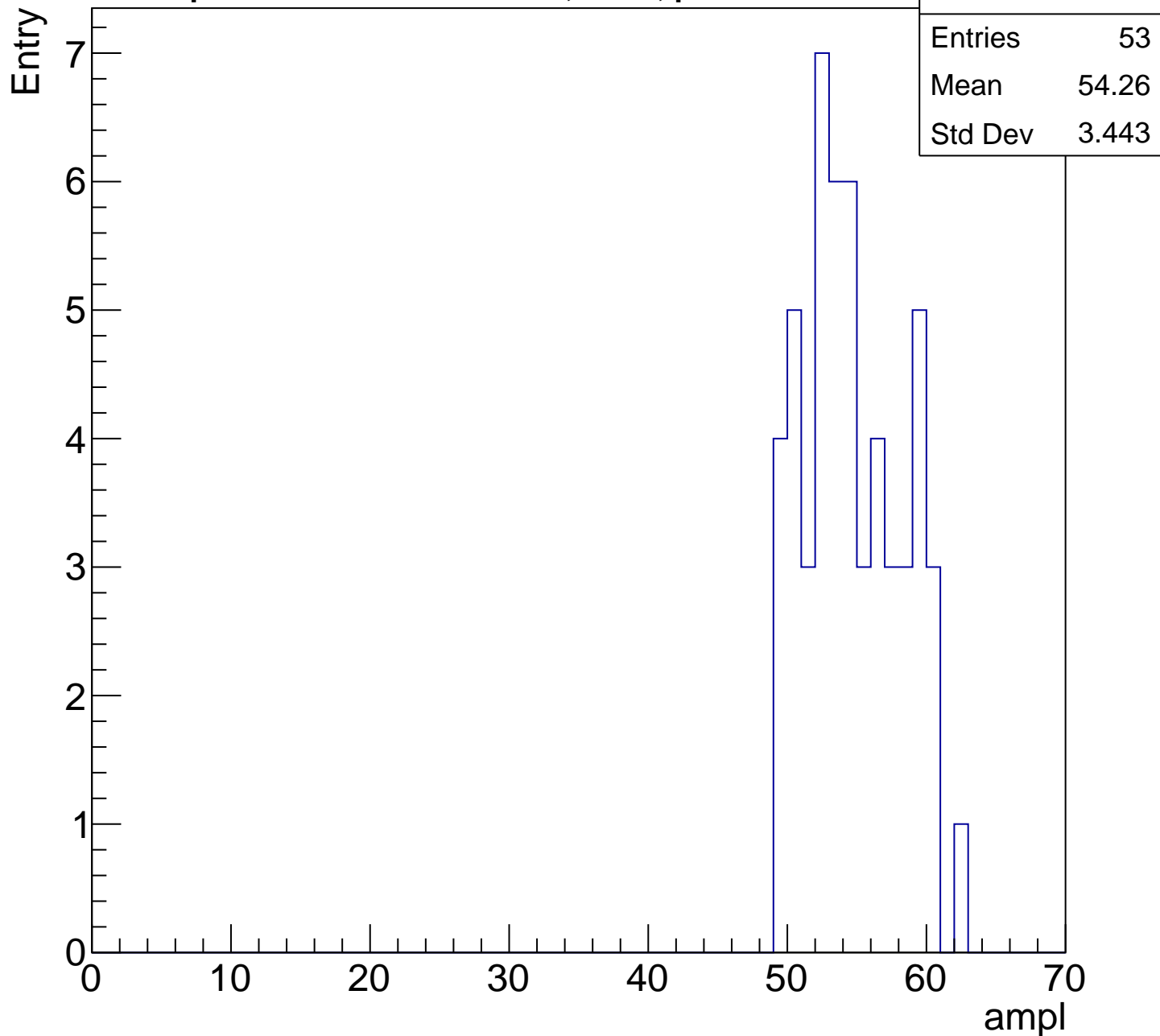
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

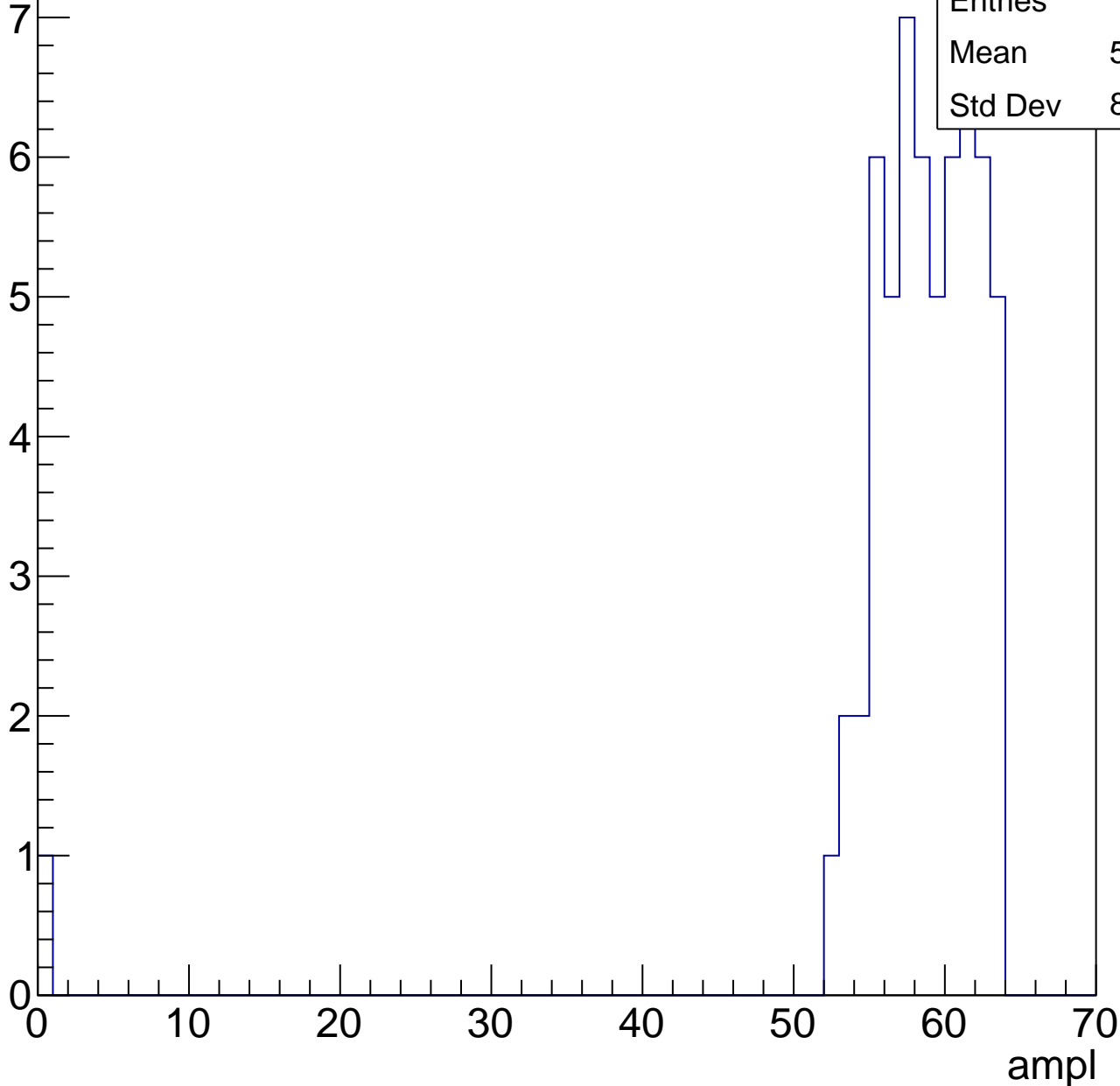


# B1L000S, U23-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

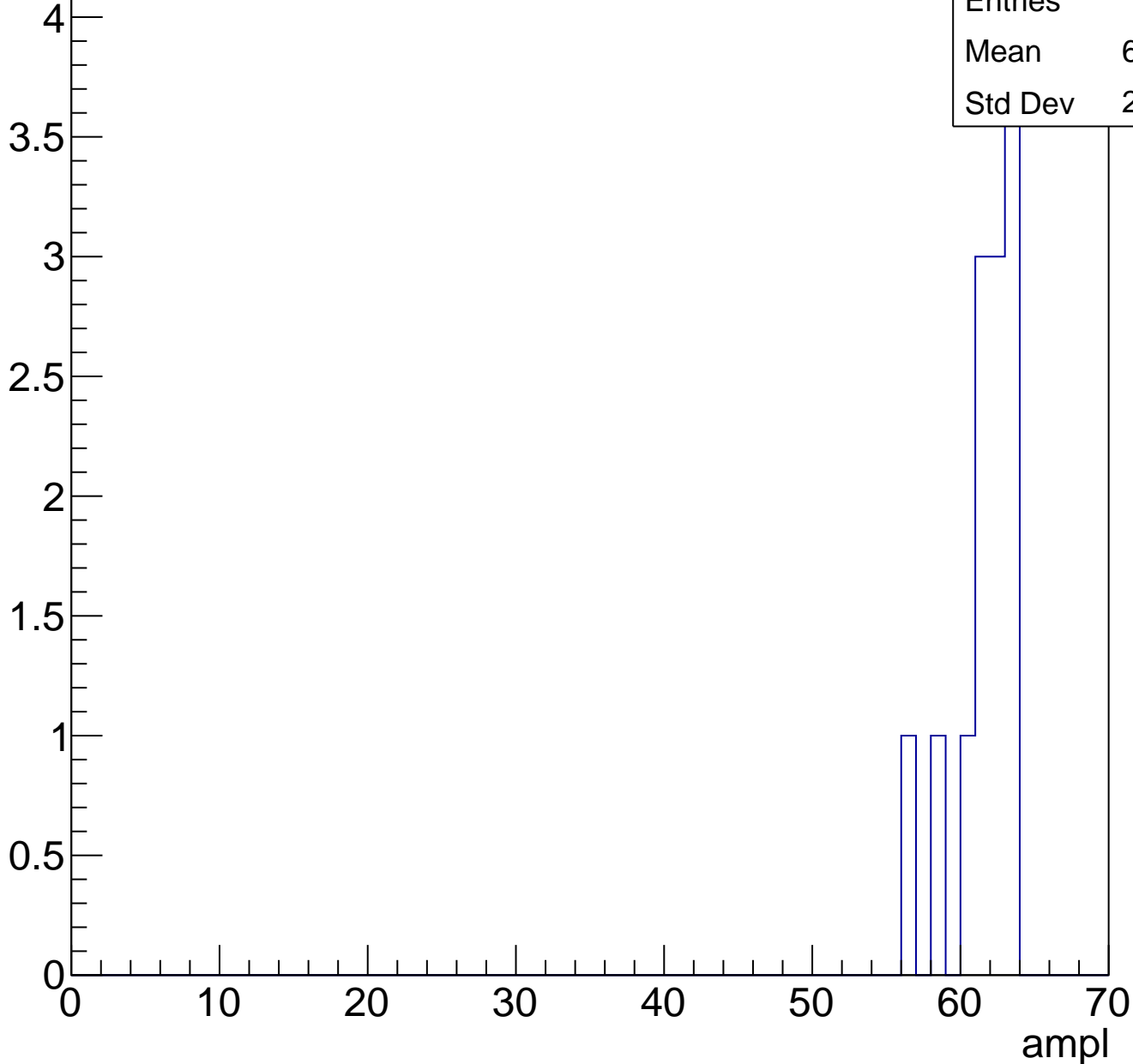
Entries	59
Mean	57.49
Std Dev	8.089



# B1L000S, U23-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

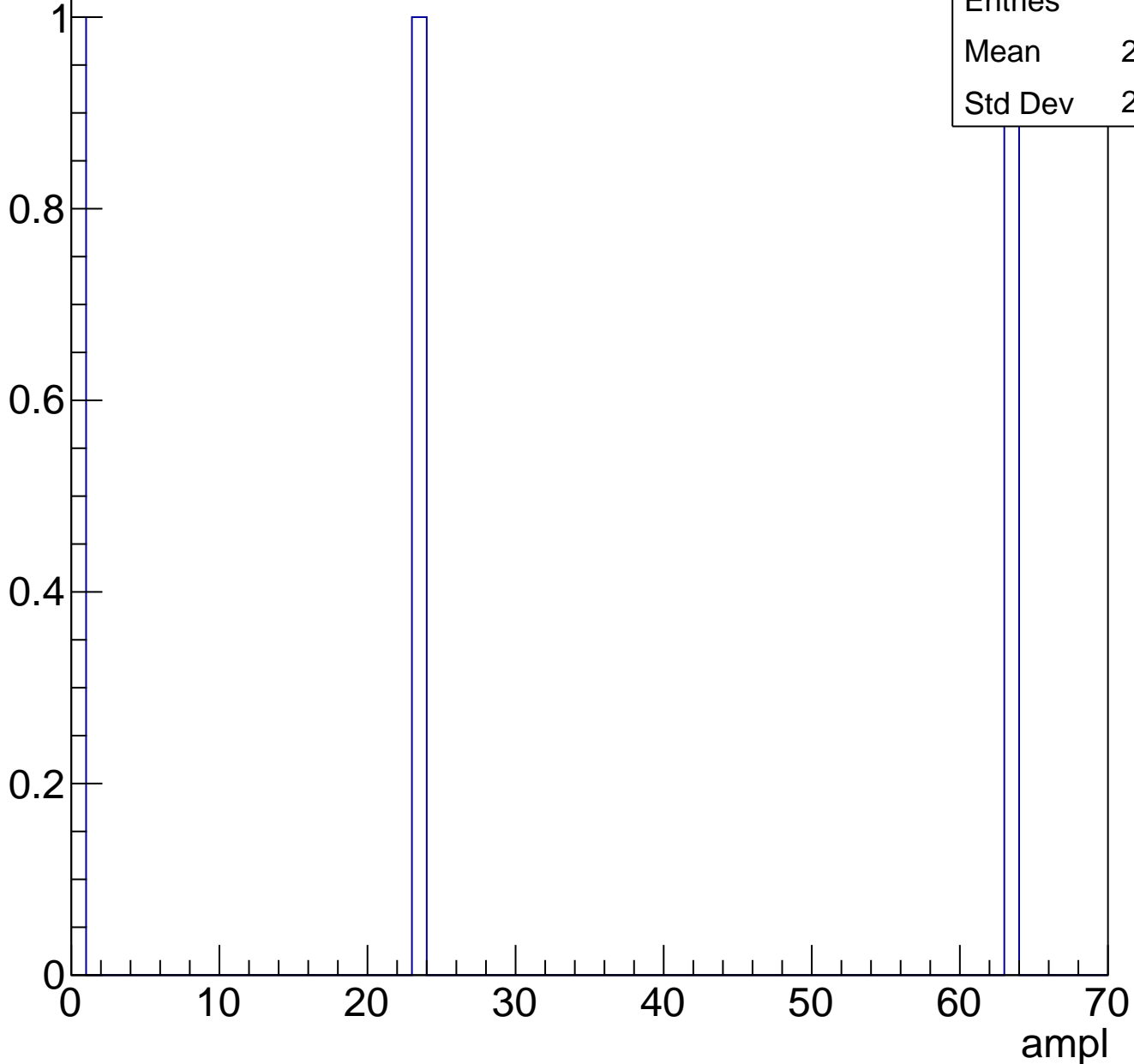




# B1L000S, U23-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B1L000S, U23-ch65, adc0

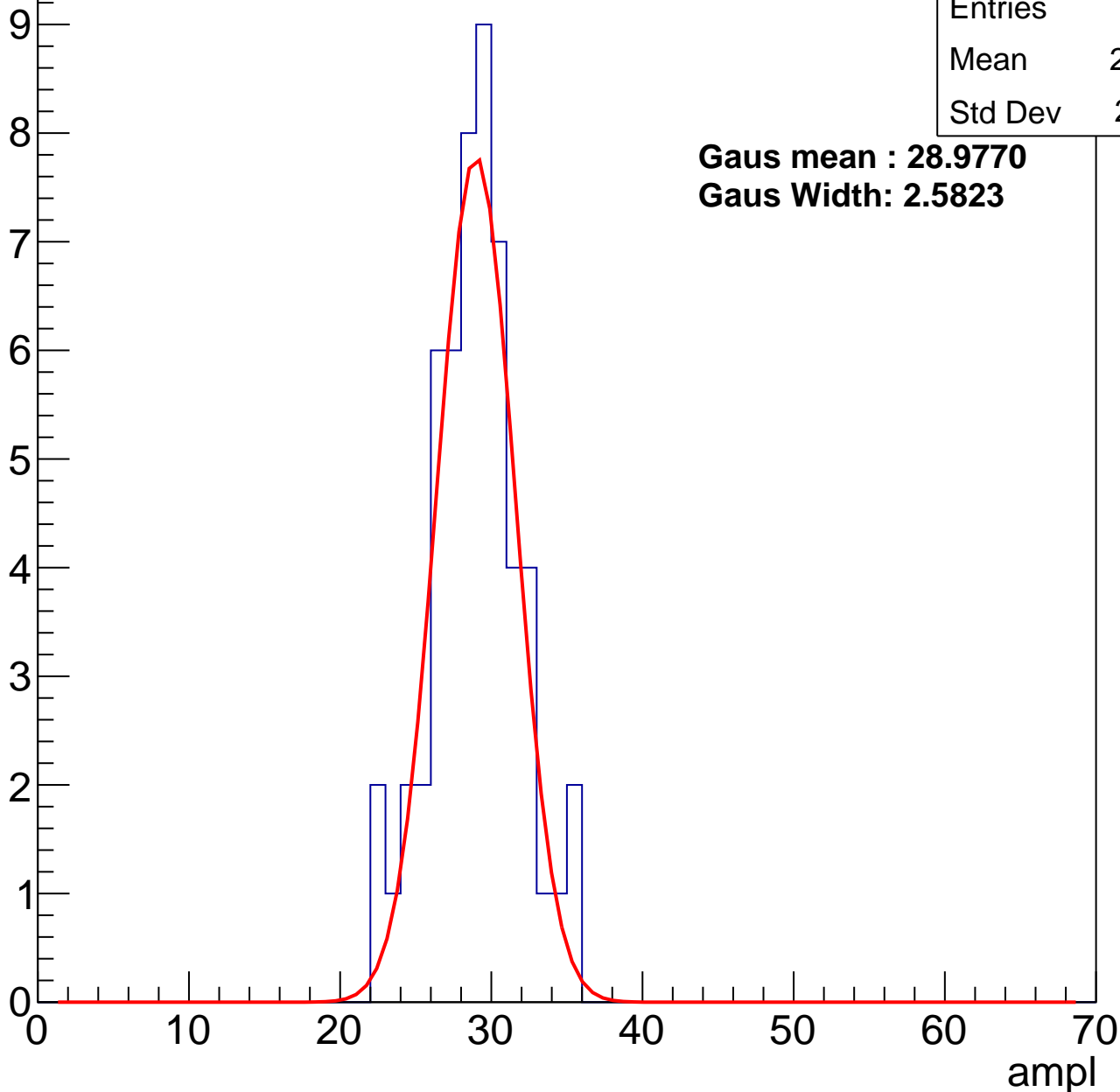
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.49
Std Dev	2.891

**Gaus mean : 28.9770**

**Gaus Width: 2.5823**



# B1L000S, U23-ch65, adc1

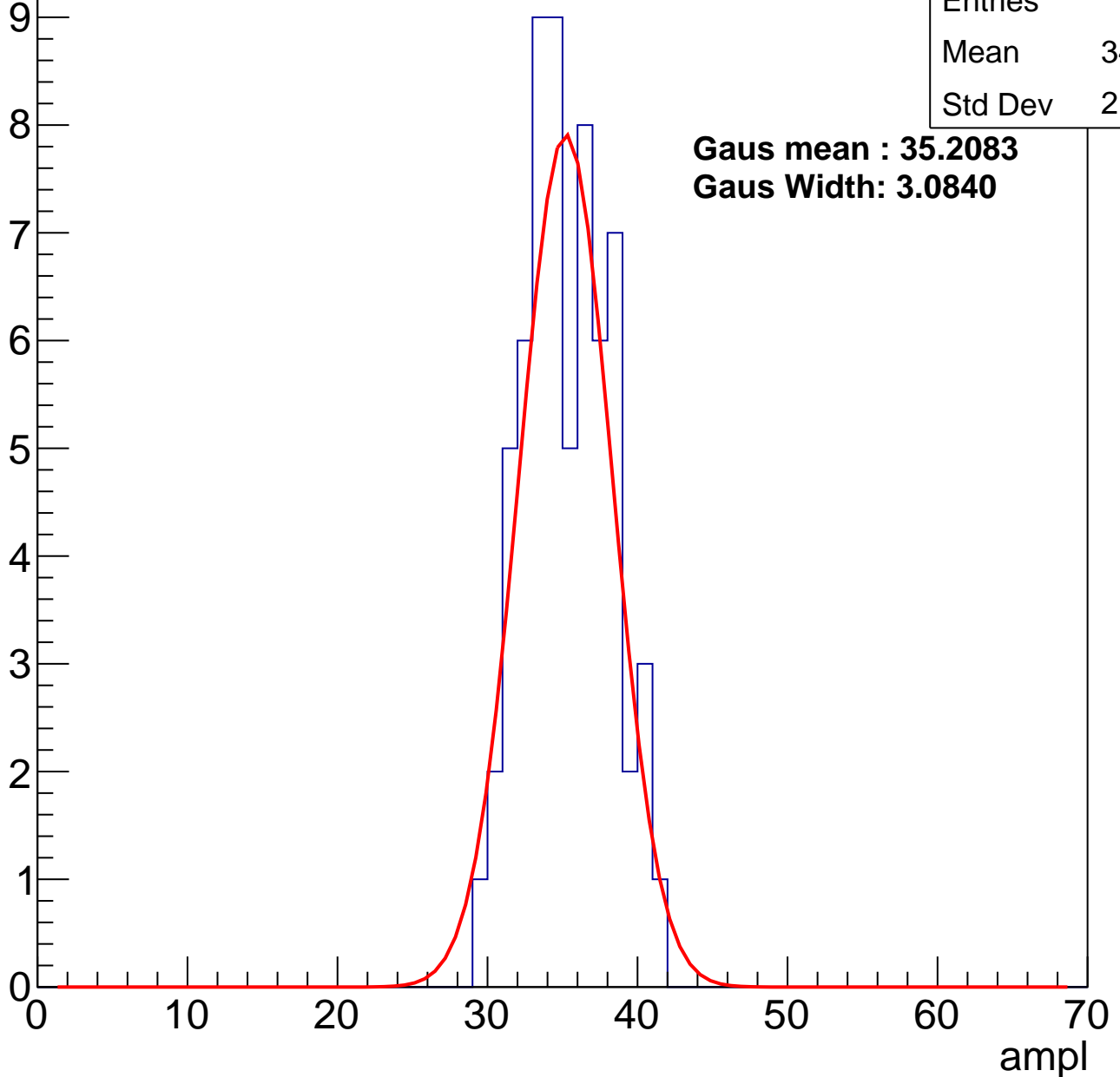
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.83
Std Dev	2.798

**Gaus mean : 35.2083**

**Gaus Width: 3.0840**



# B1L000S, U23-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	41.3
Std Dev	2.947

**Gaus mean : 41.9476**

**Gaus Width: 3.0992**

10

8

6

4

2

0

0

10

20

30

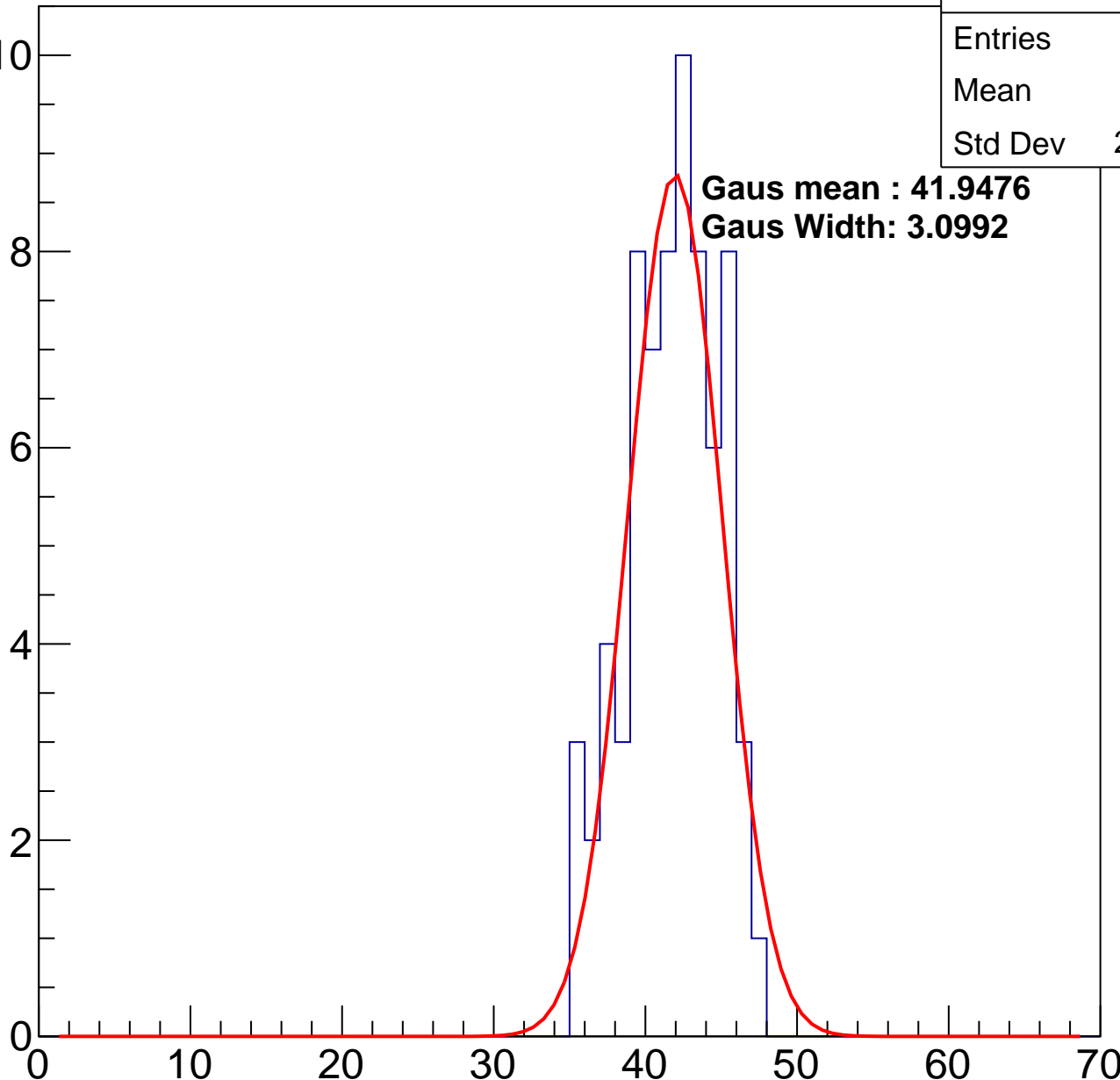
40

50

60

70

ampl

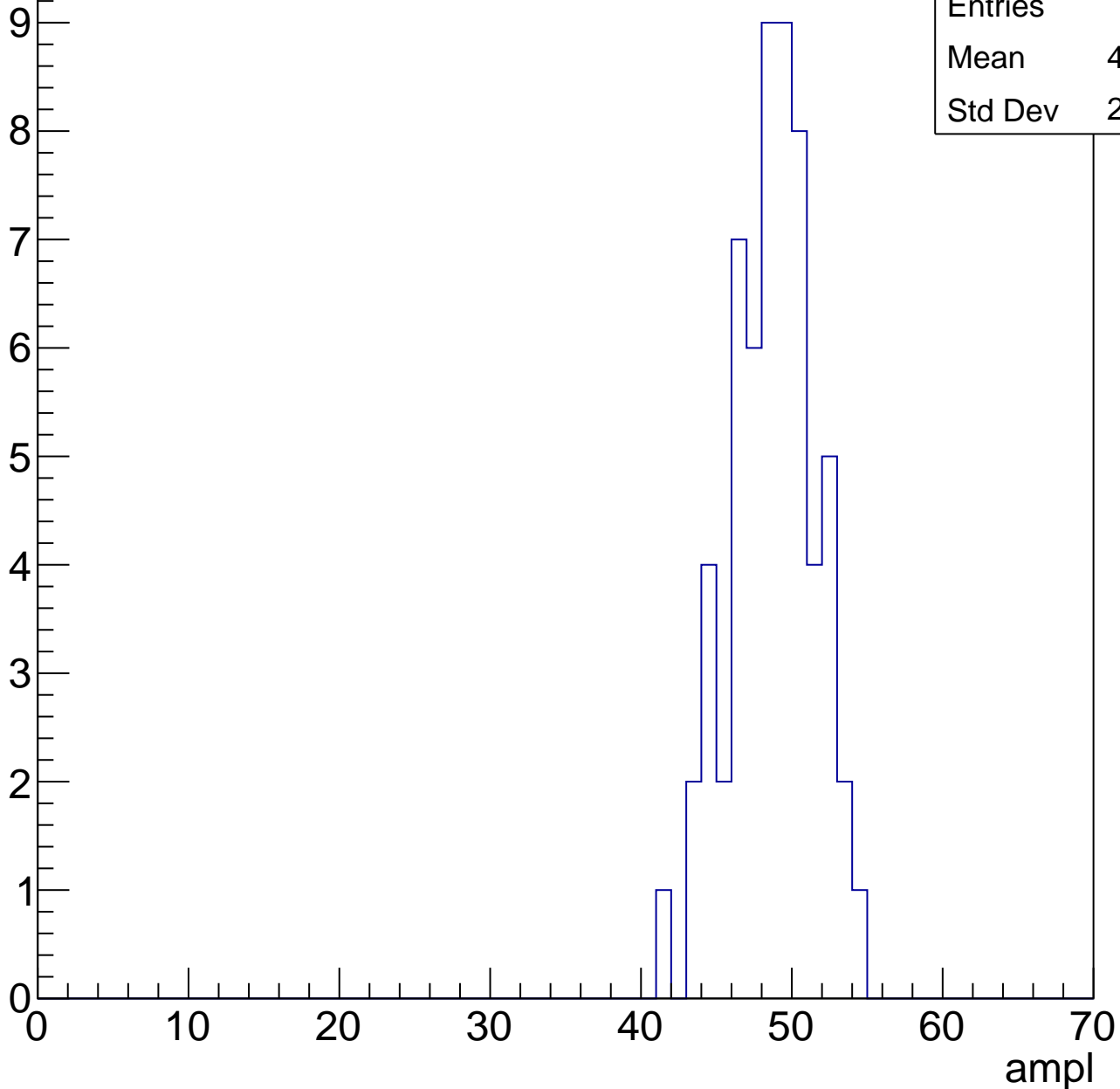


# B1L000S, U23-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

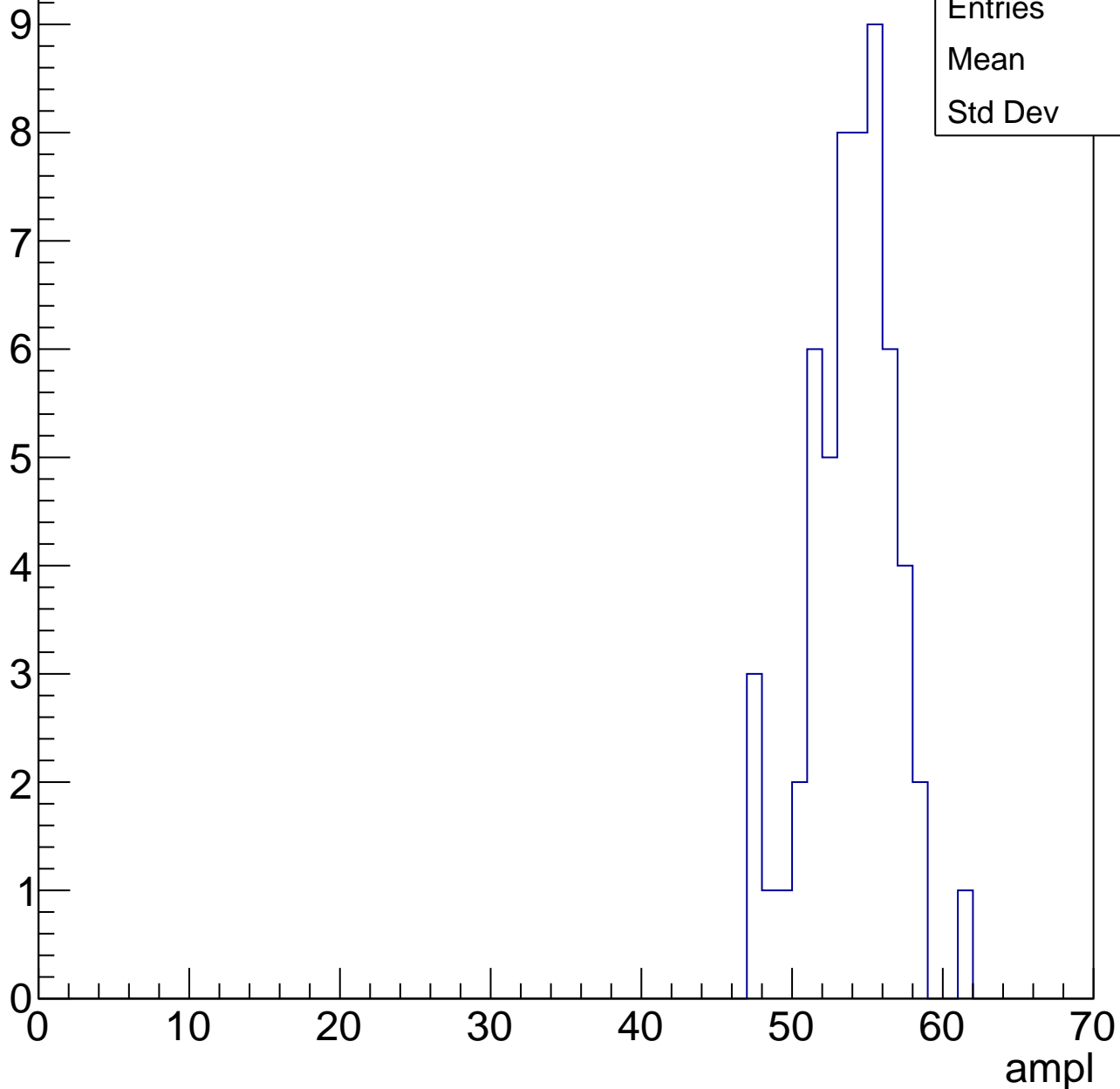
Entries	60
Mean	48.23
Std Dev	2.753



# B1L000S, U23-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



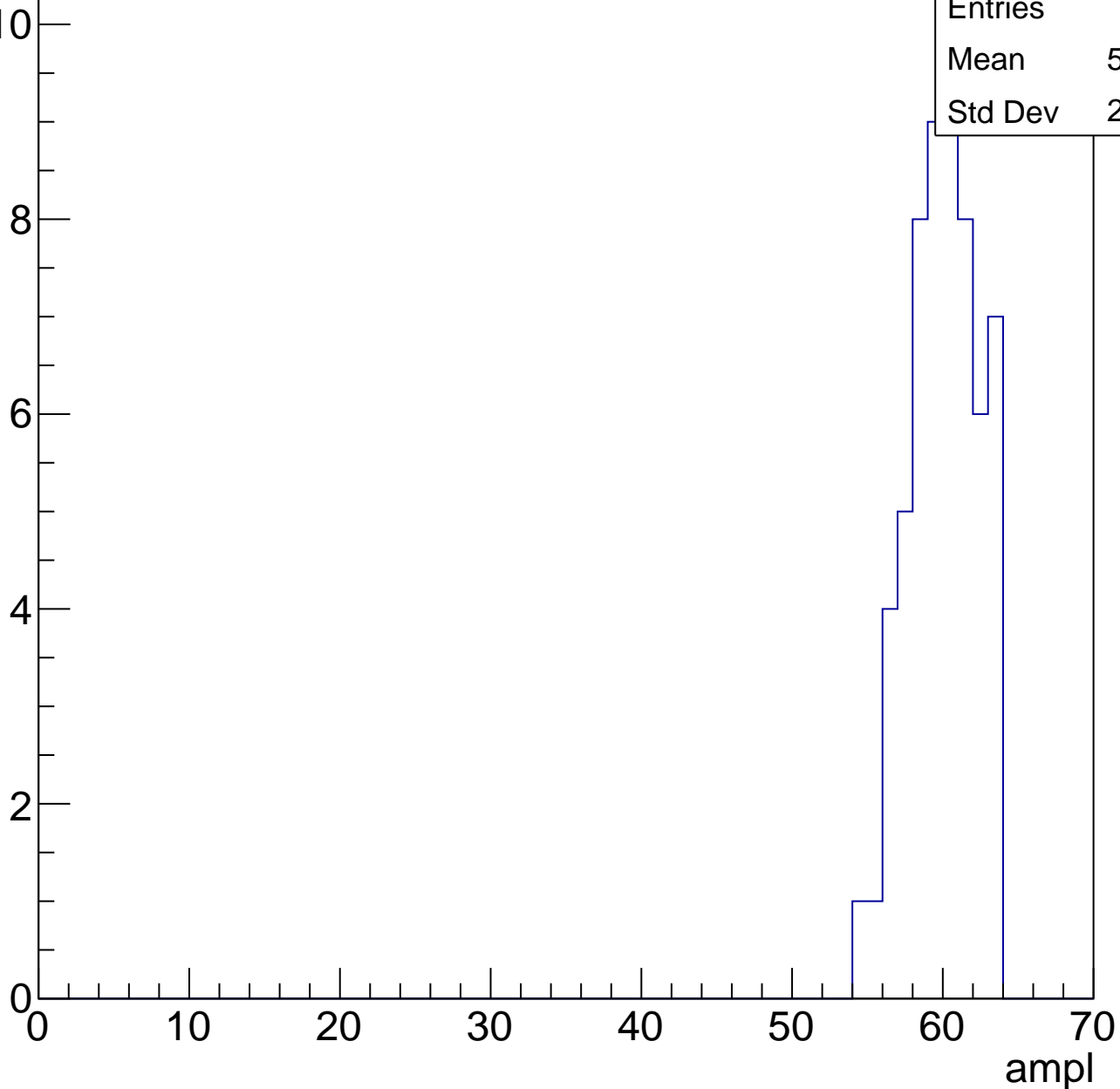
Entries	56
Mean	53.5
Std Dev	2.86

# B1L000S, U23-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

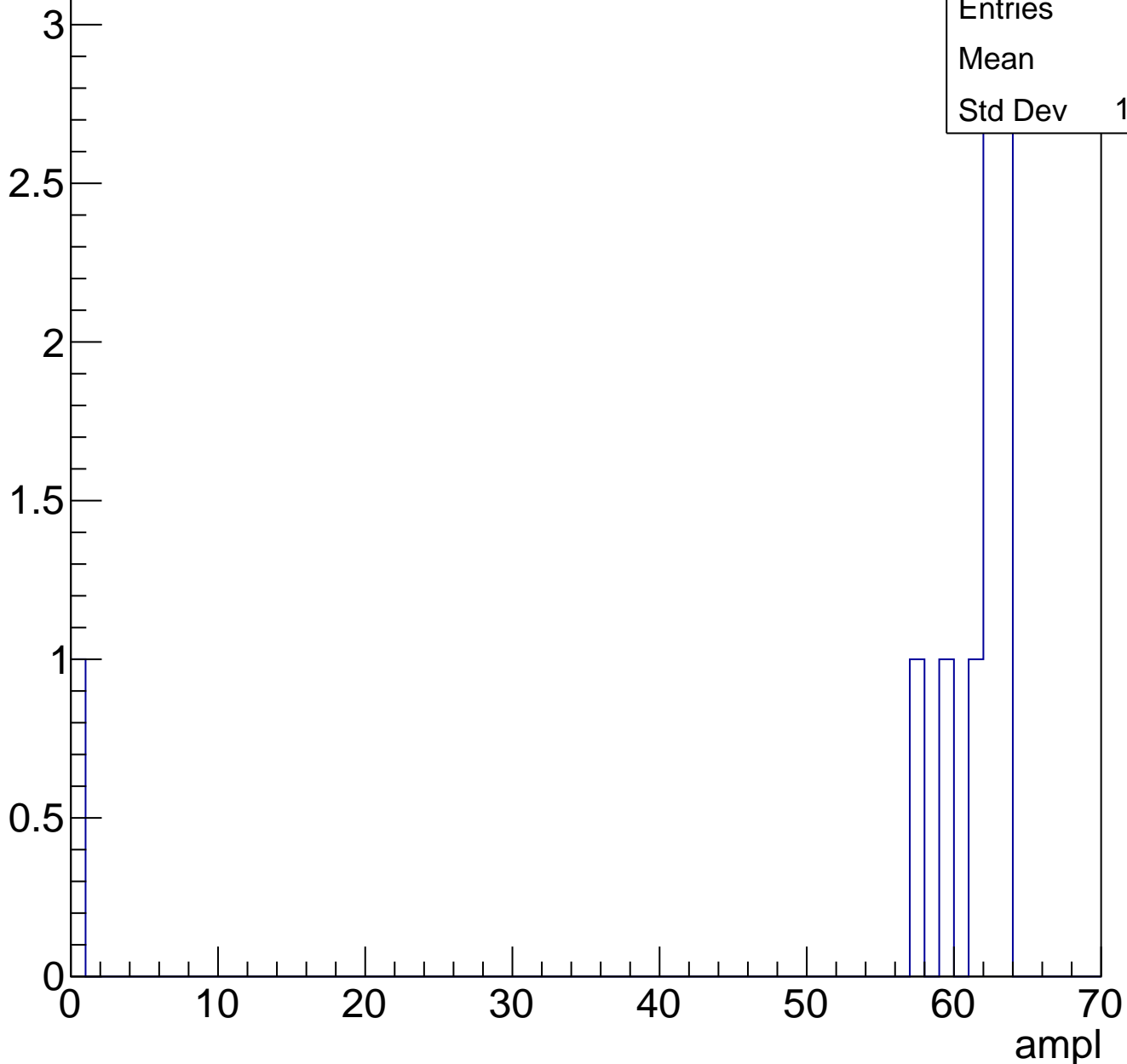
Entries	59
Mean	59.56
Std Dev	2.234



# B1L000S, U23-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch66, adc0

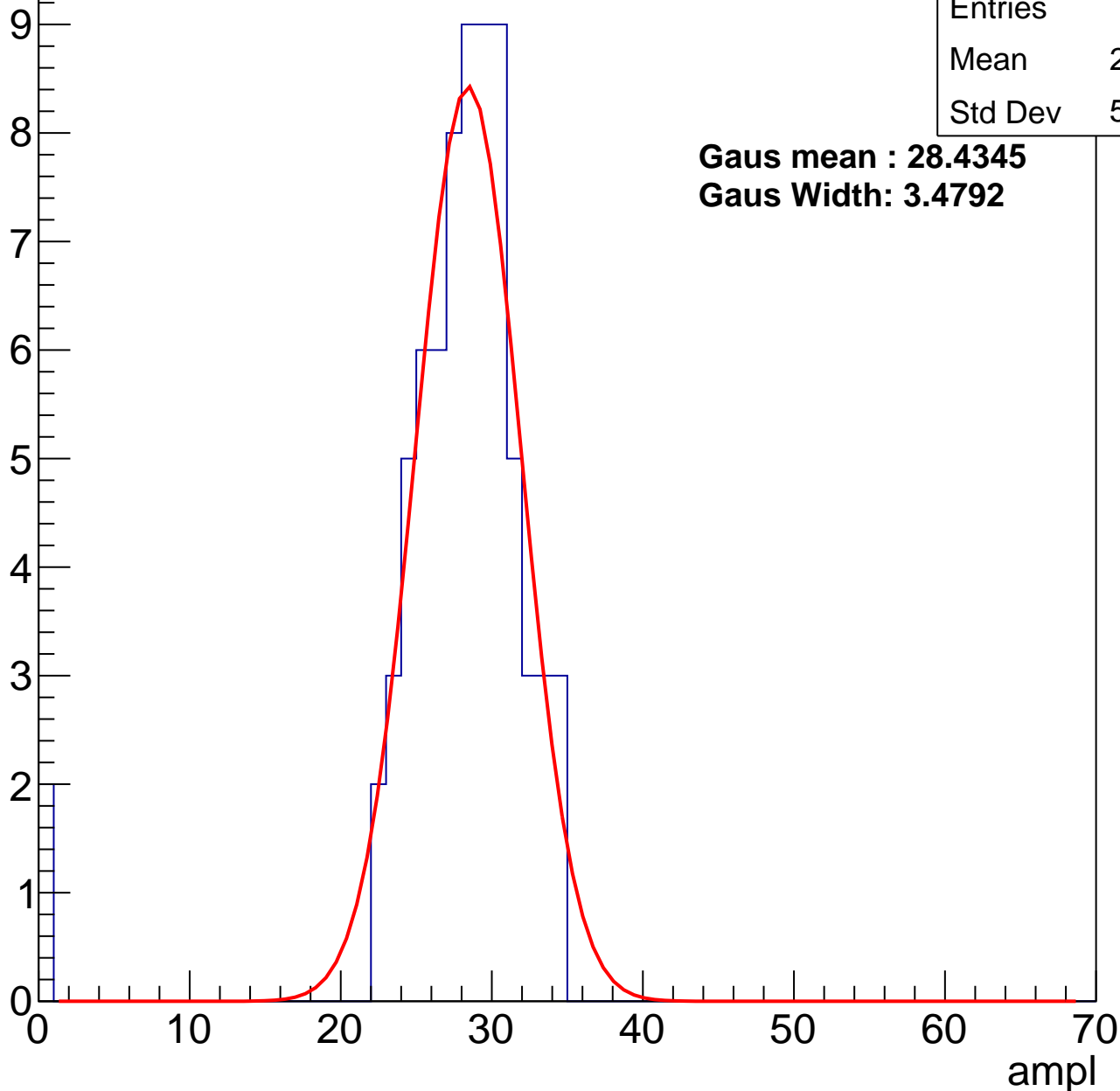
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	27.26
Std Dev	5.442

**Gaus mean : 28.4345**

**Gaus Width: 3.4792**



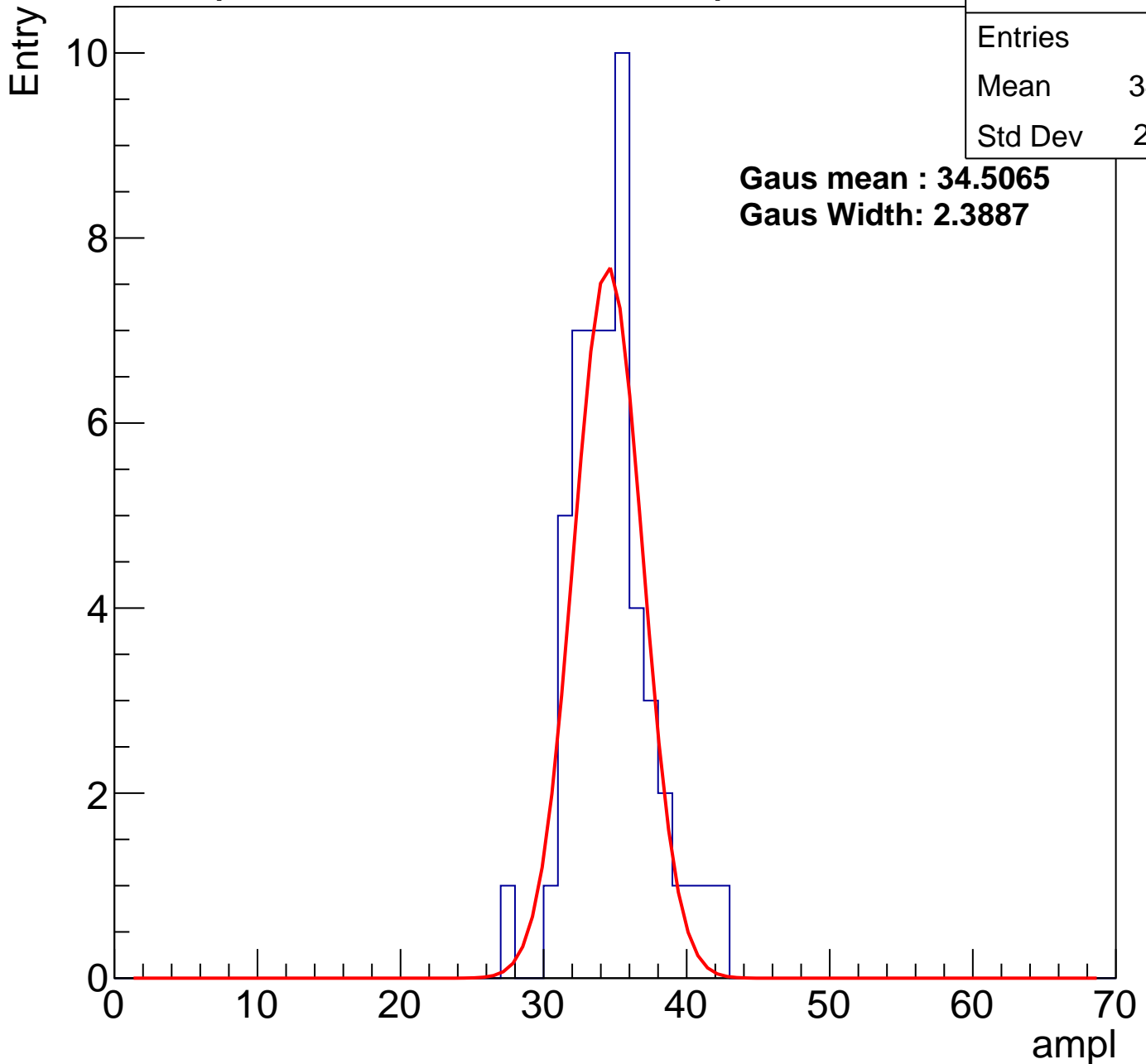
# B1L000S, U23-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	51
Mean	34.27
Std Dev	2.801

**Gaus mean : 34.5065**

**Gaus Width: 2.3887**



# B1L000S, U23-ch66, adc2

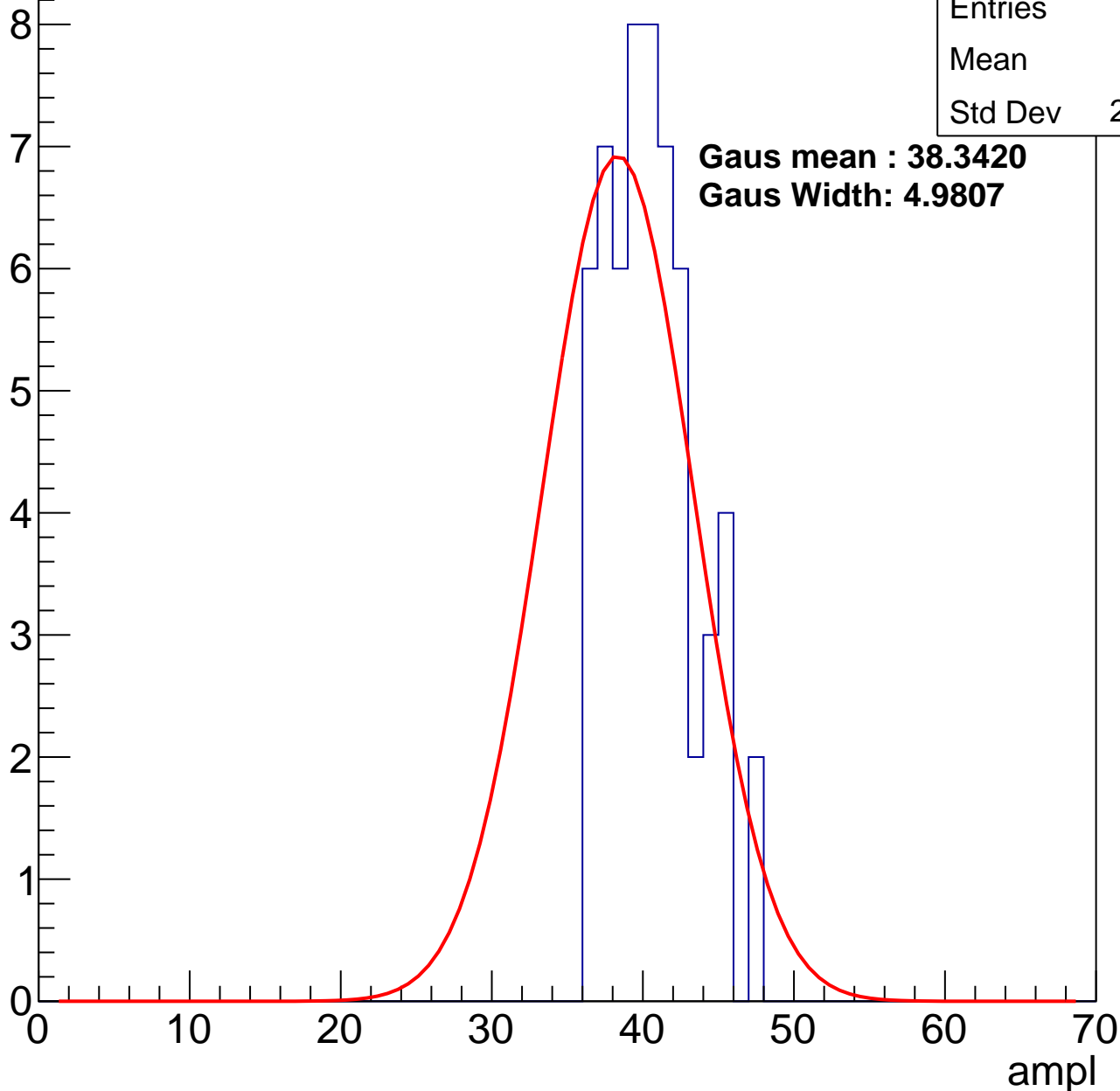
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	40.1
Std Dev	2.868

**Gaus mean : 38.3420**

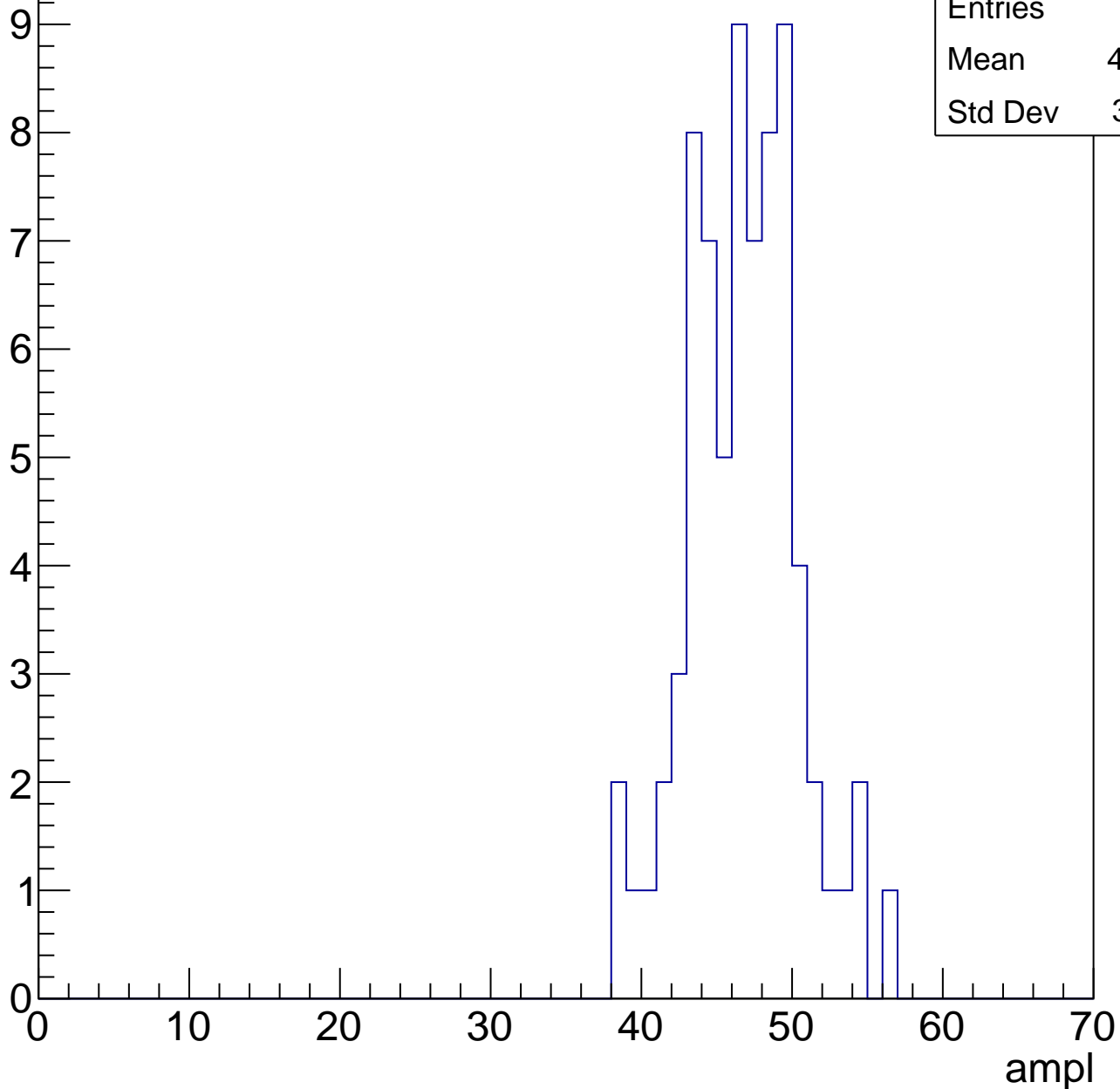
**Gaus Width: 4.9807**



# B1L000S, U23-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

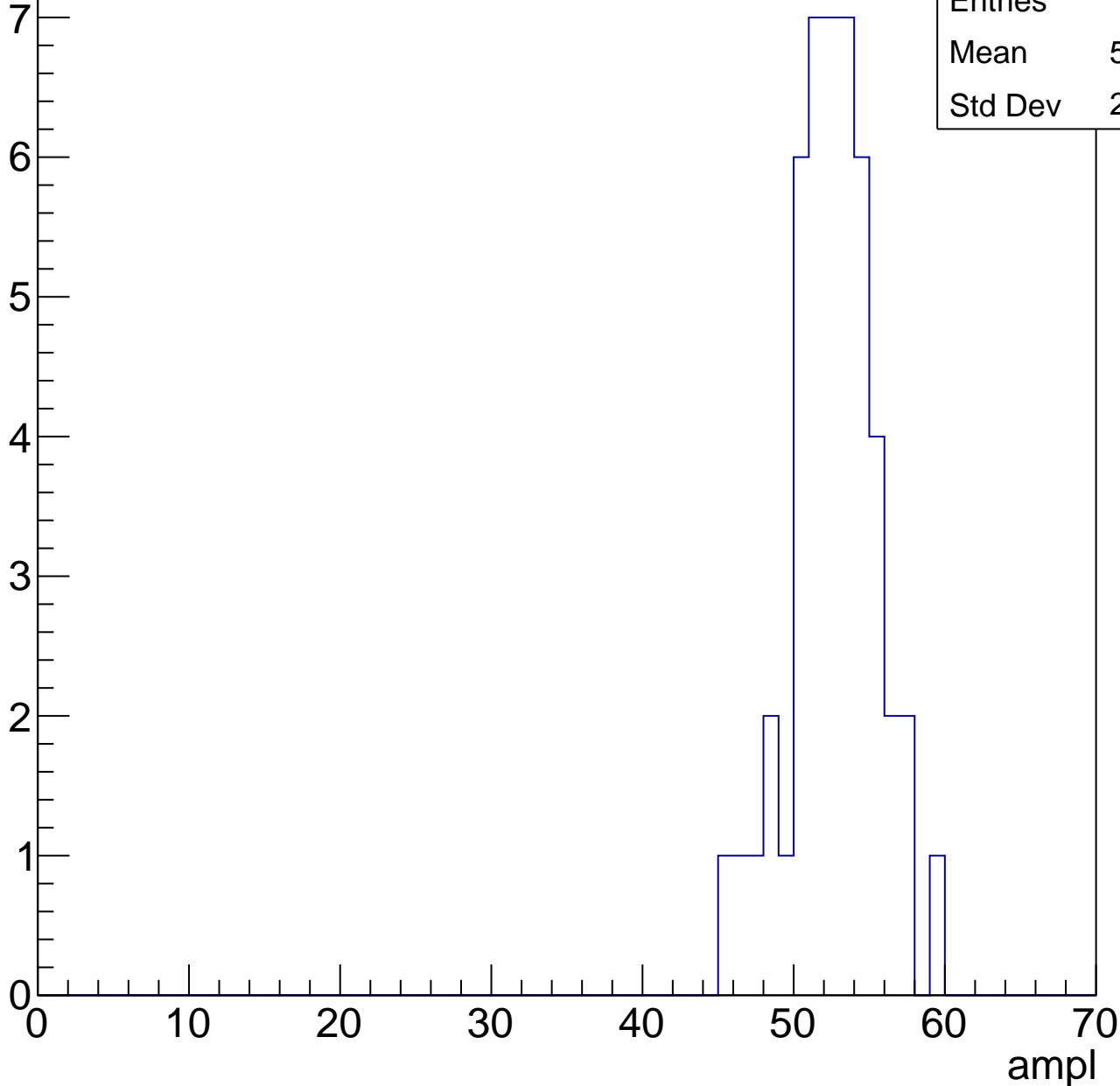


# B1L000S, U23-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	52.17
Std Dev	2.809

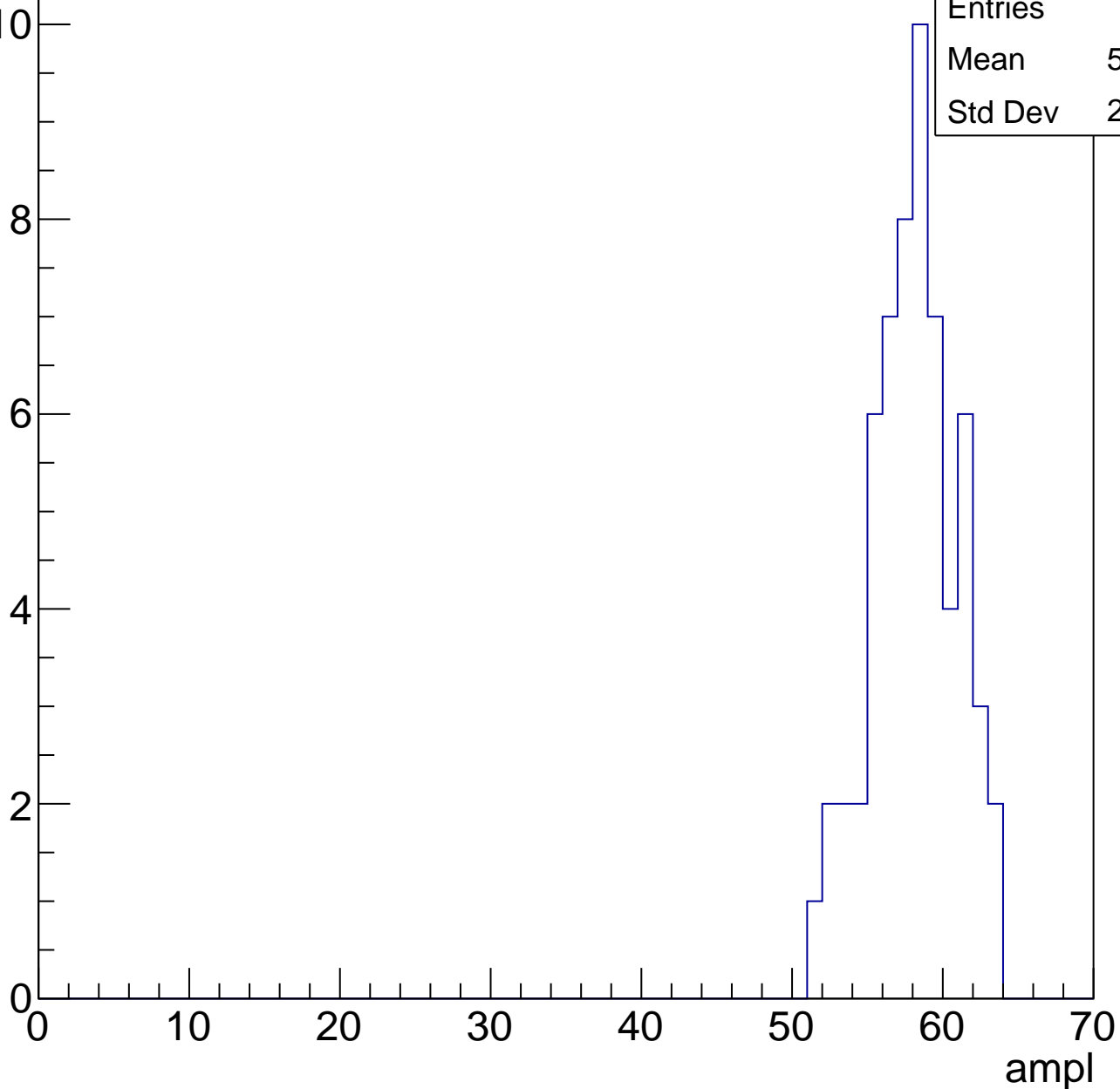


# B1L000S, U23-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

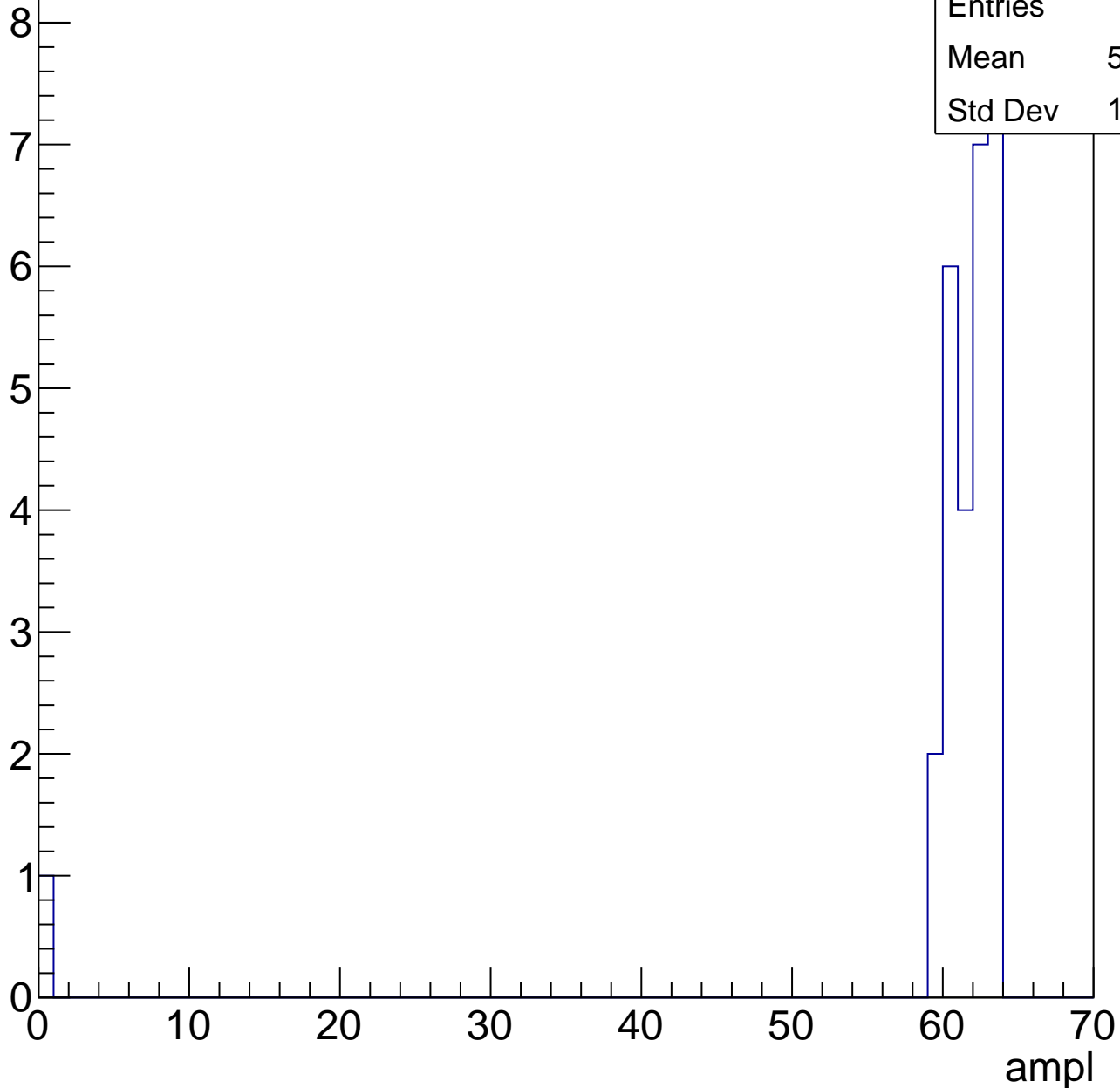
Entries	60
Mean	57.63
Std Dev	2.769



# B1L000S, U23-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch67, adc0

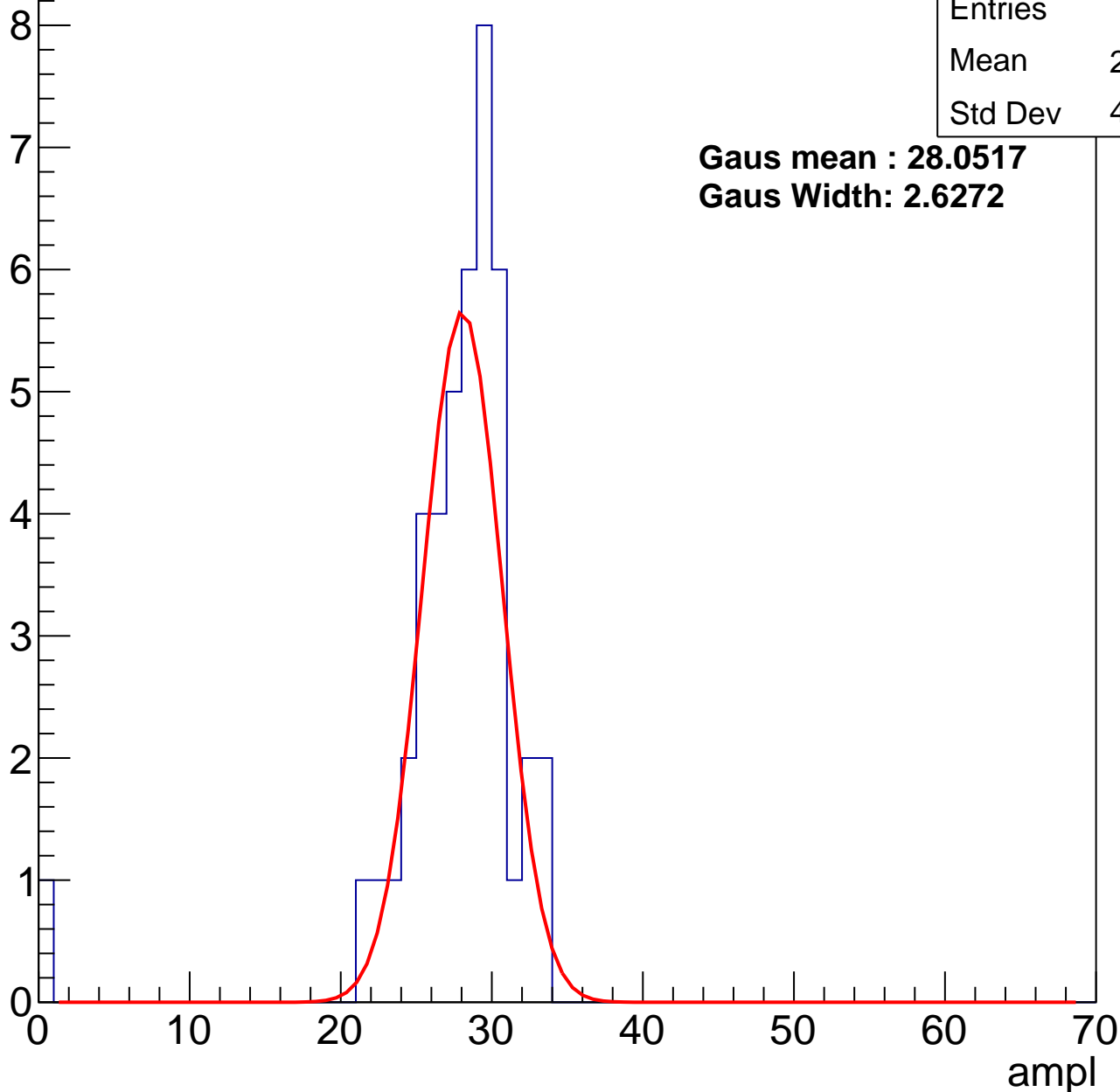
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	27.14
Std Dev	4.939

**Gaus mean : 28.0517**

**Gaus Width: 2.6272**

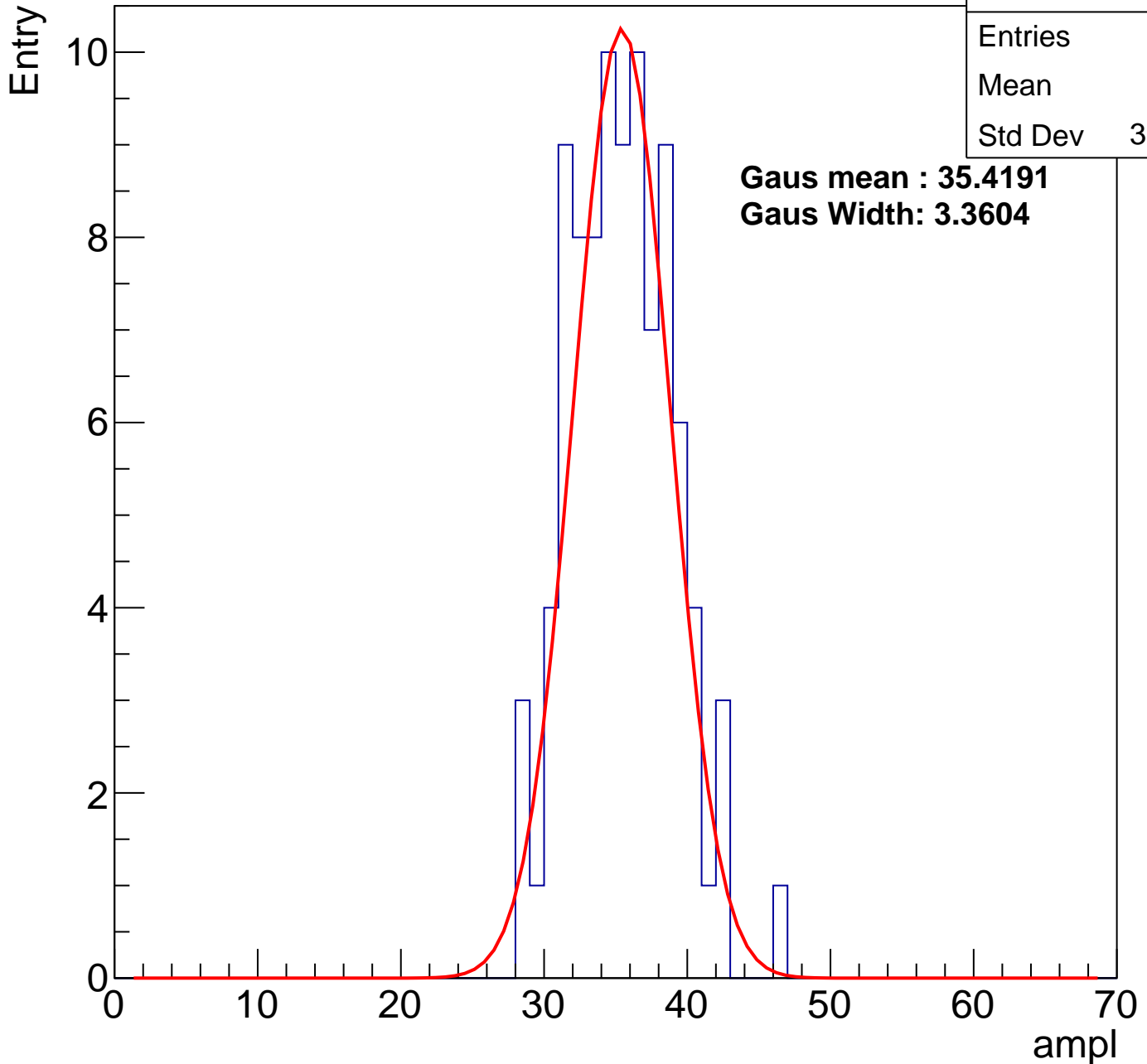


# B1L000S, U23-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	93
Mean	35
Std Dev	3.532

**Gaus mean : 35.4191**  
**Gaus Width: 3.3604**



# B1L000S, U23-ch67, adc2

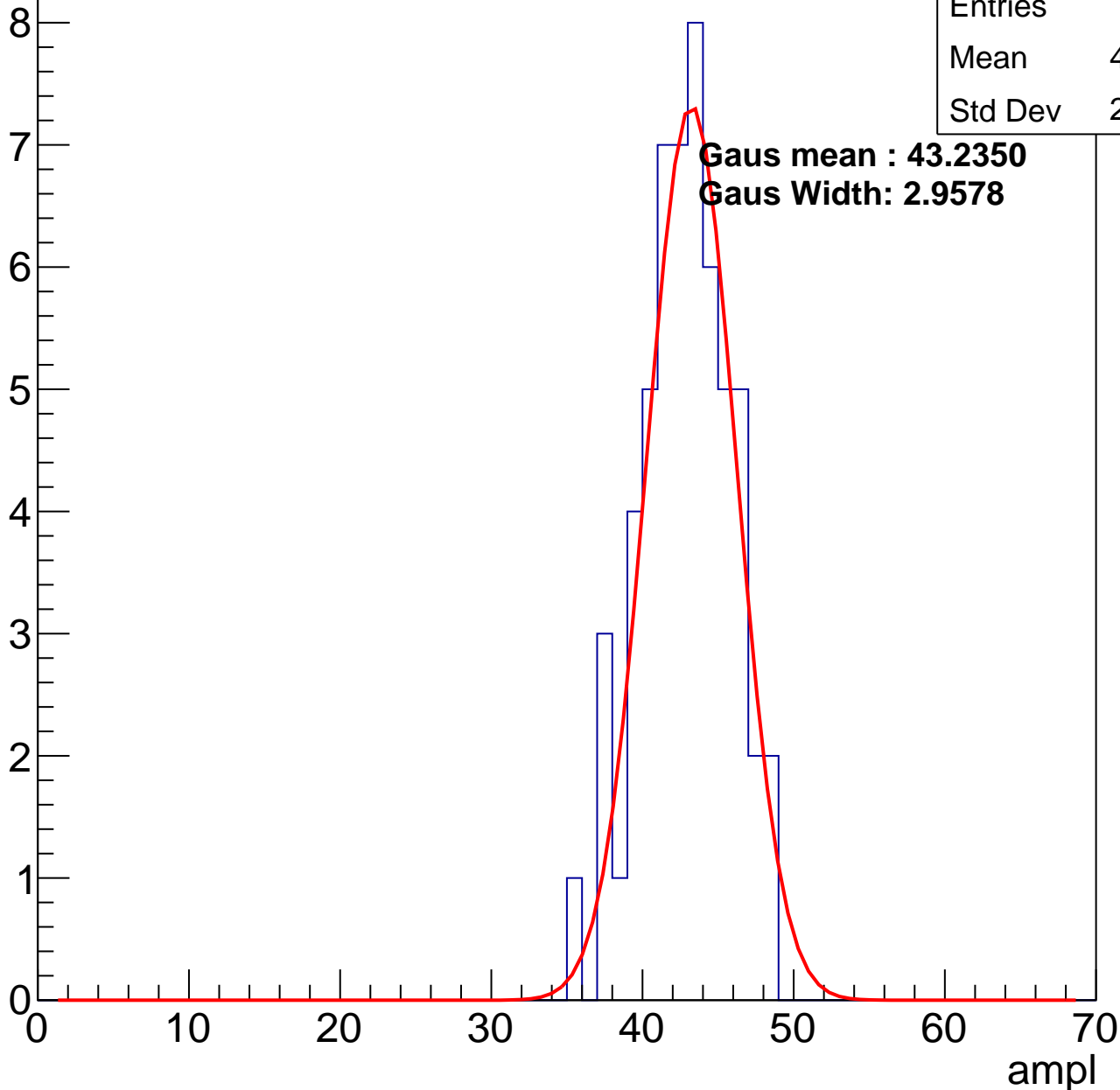
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.39
Std Dev	2.907

**Gaus mean : 43.2350**

**Gaus Width: 2.9578**

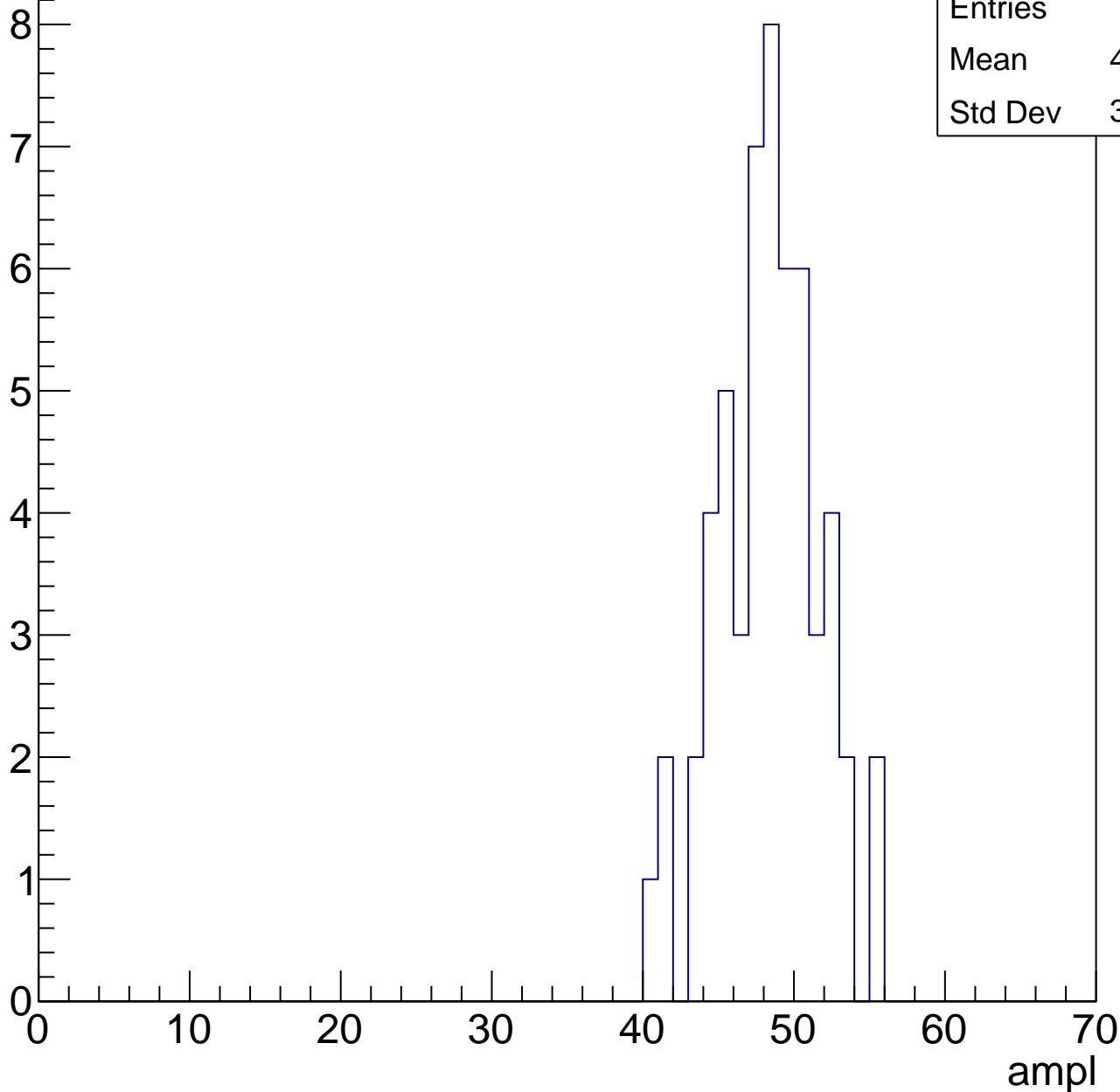


# B1L000S, U23-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

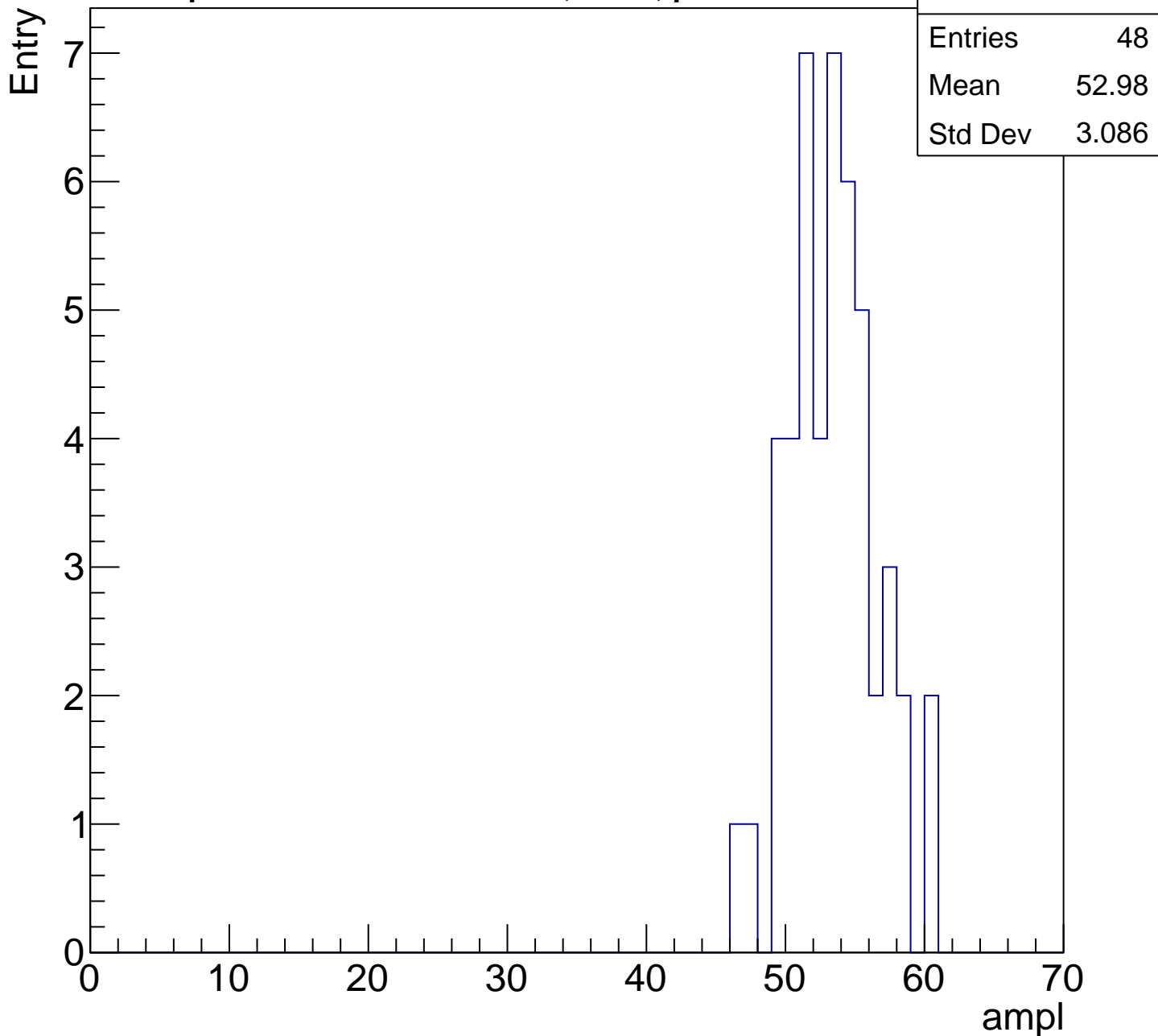
Entry

Entries	55
Mean	47.84
Std Dev	3.324



# B1L000S, U23-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

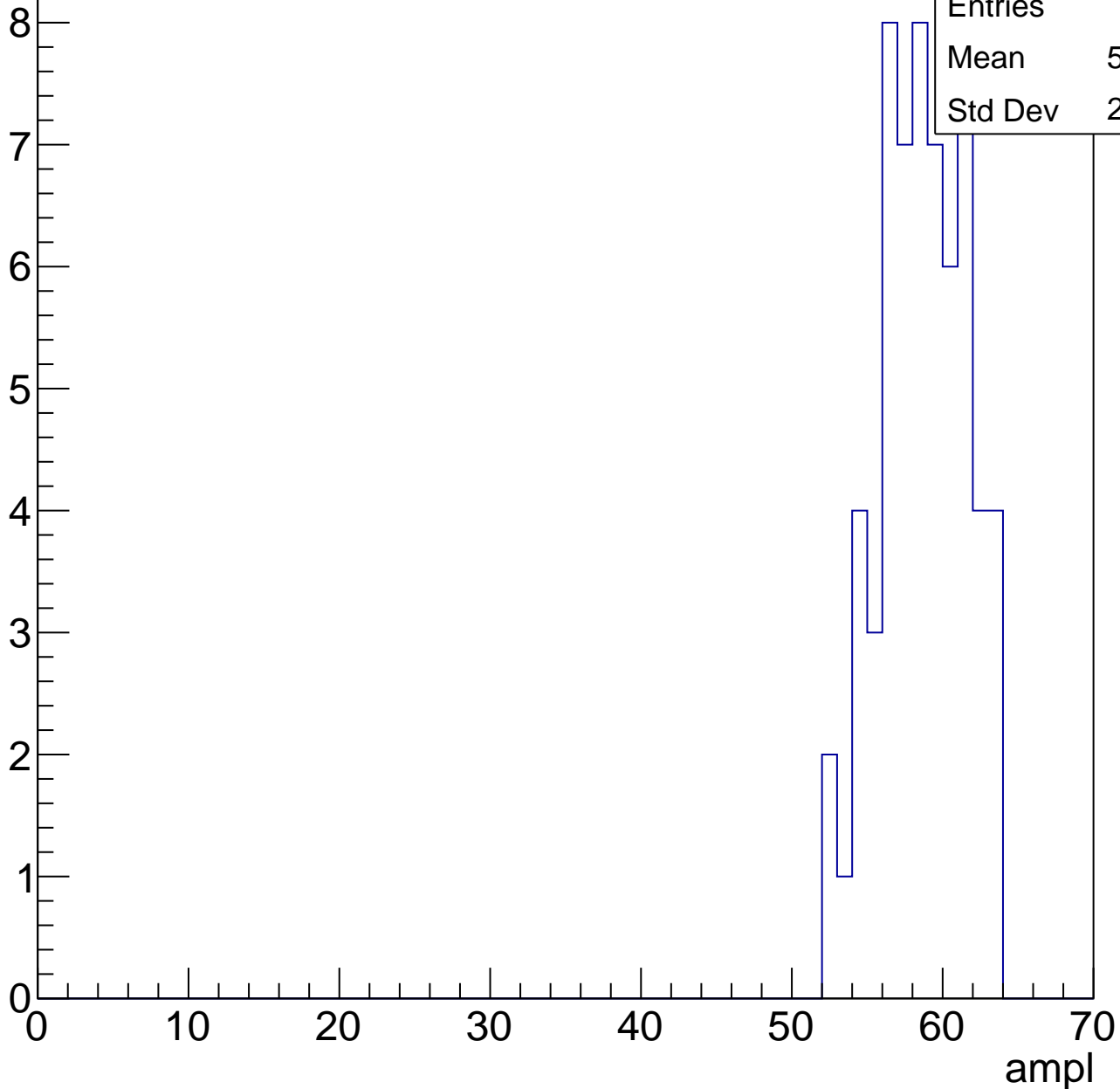


# B1L000S, U23-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	58.23
Std Dev	2.814

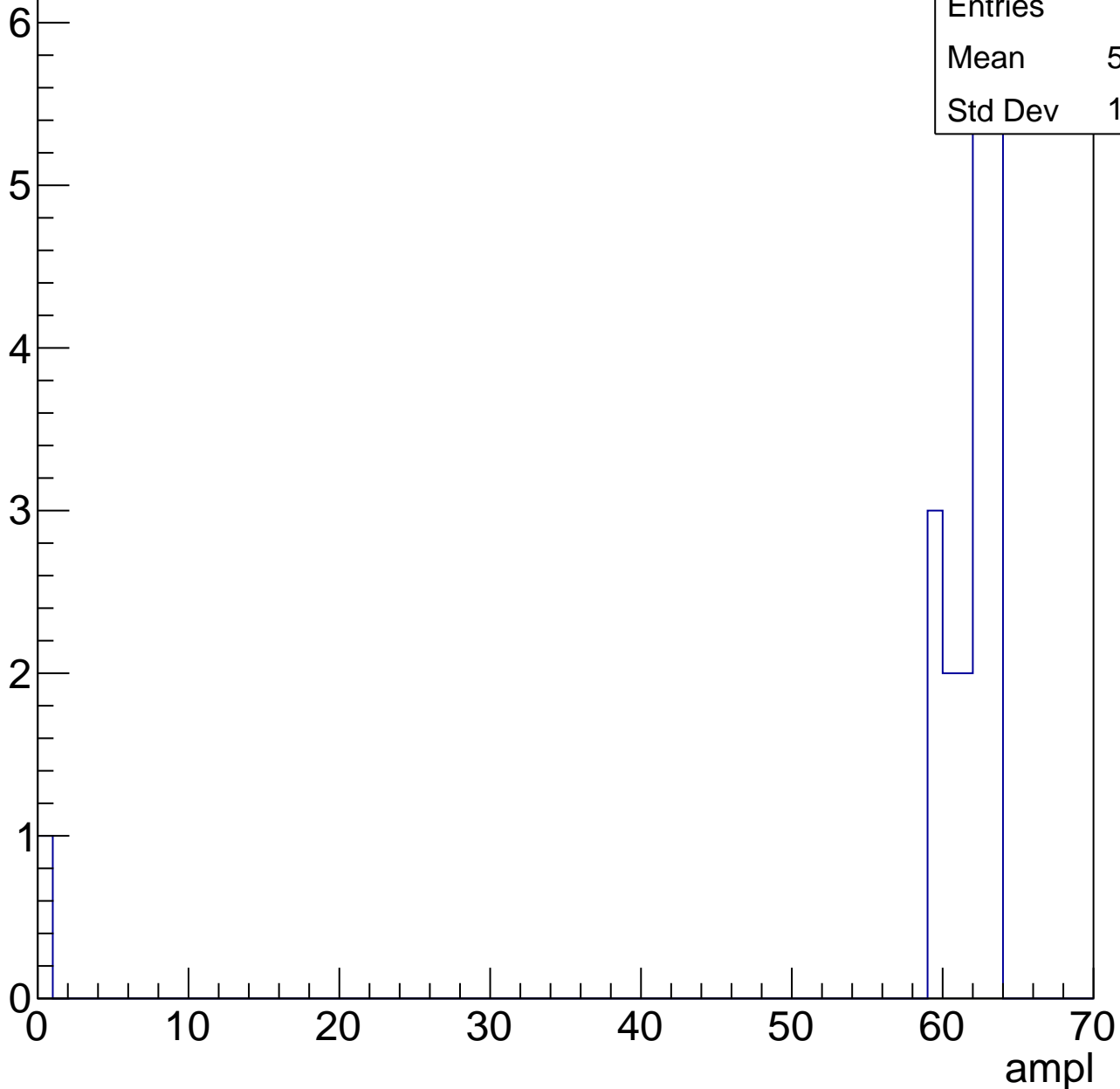


# B1L000S, U23-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.48





# B1L000S, U23-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U23-ch68, adc0

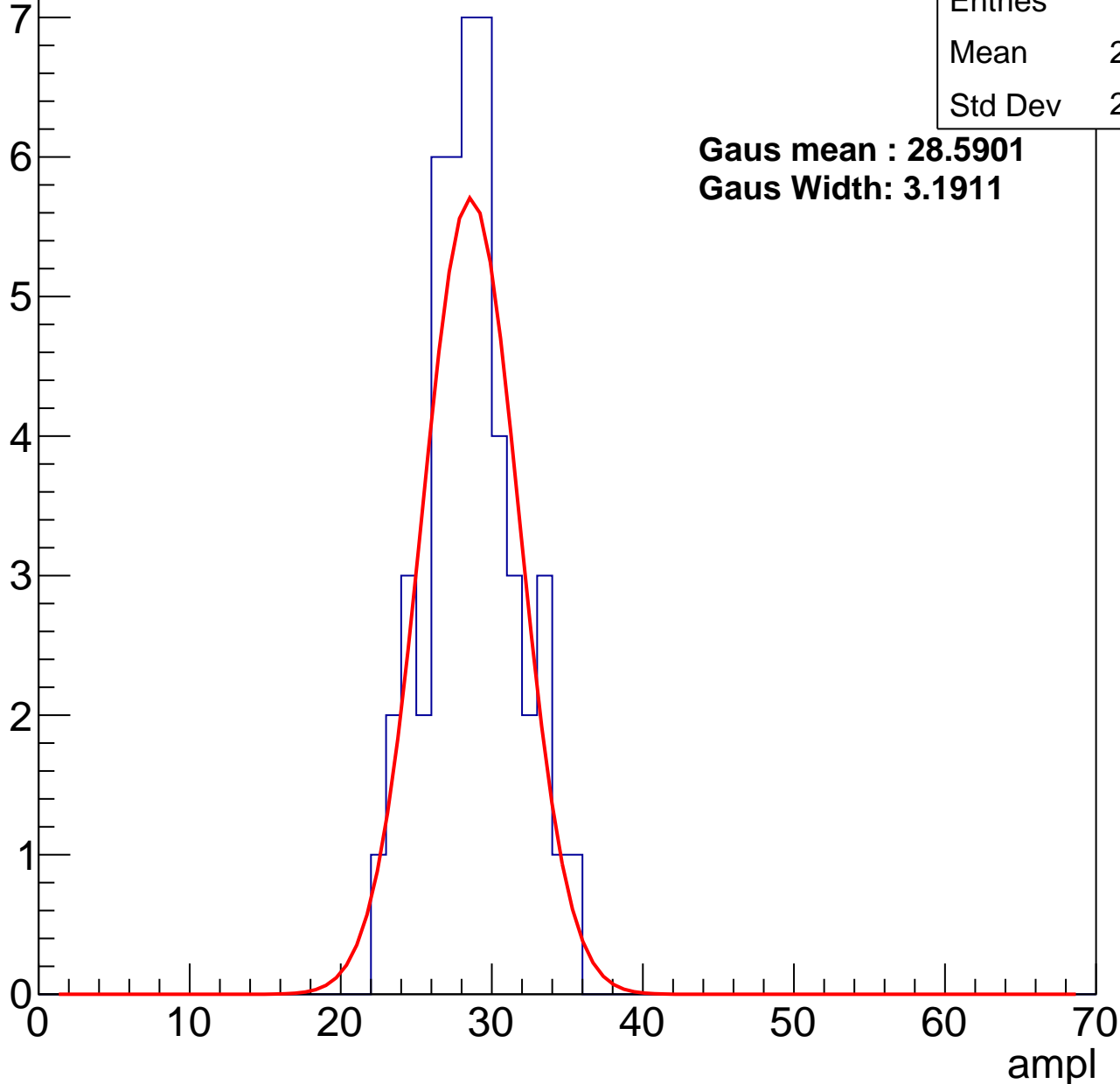
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.17
Std Dev	2.967

**Gaus mean : 28.5901**

**Gaus Width: 3.1911**

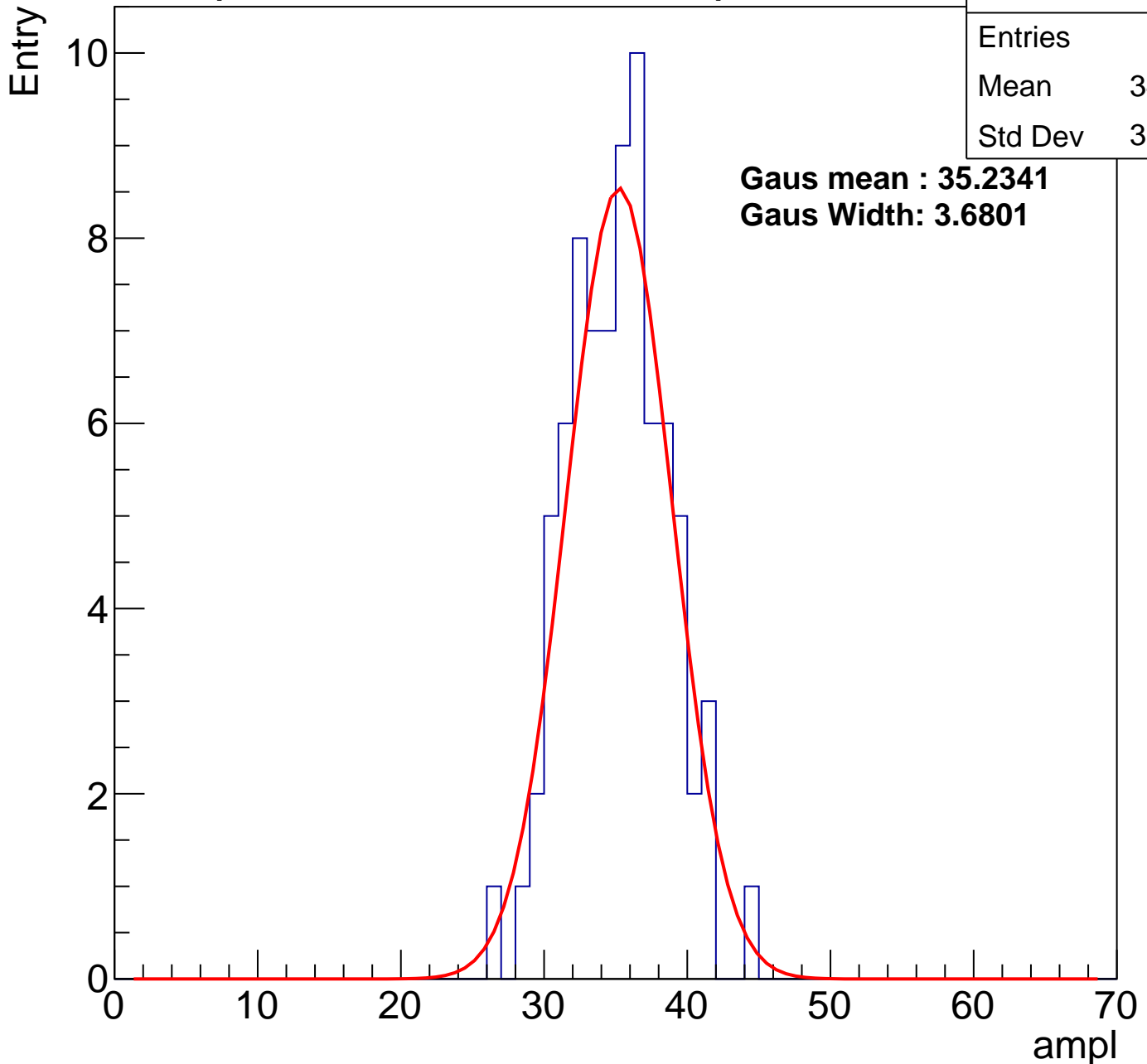


# B1L000S, U23-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	34.68
Std Dev	3.429

**Gaus mean : 35.2341**  
**Gaus Width: 3.6801**

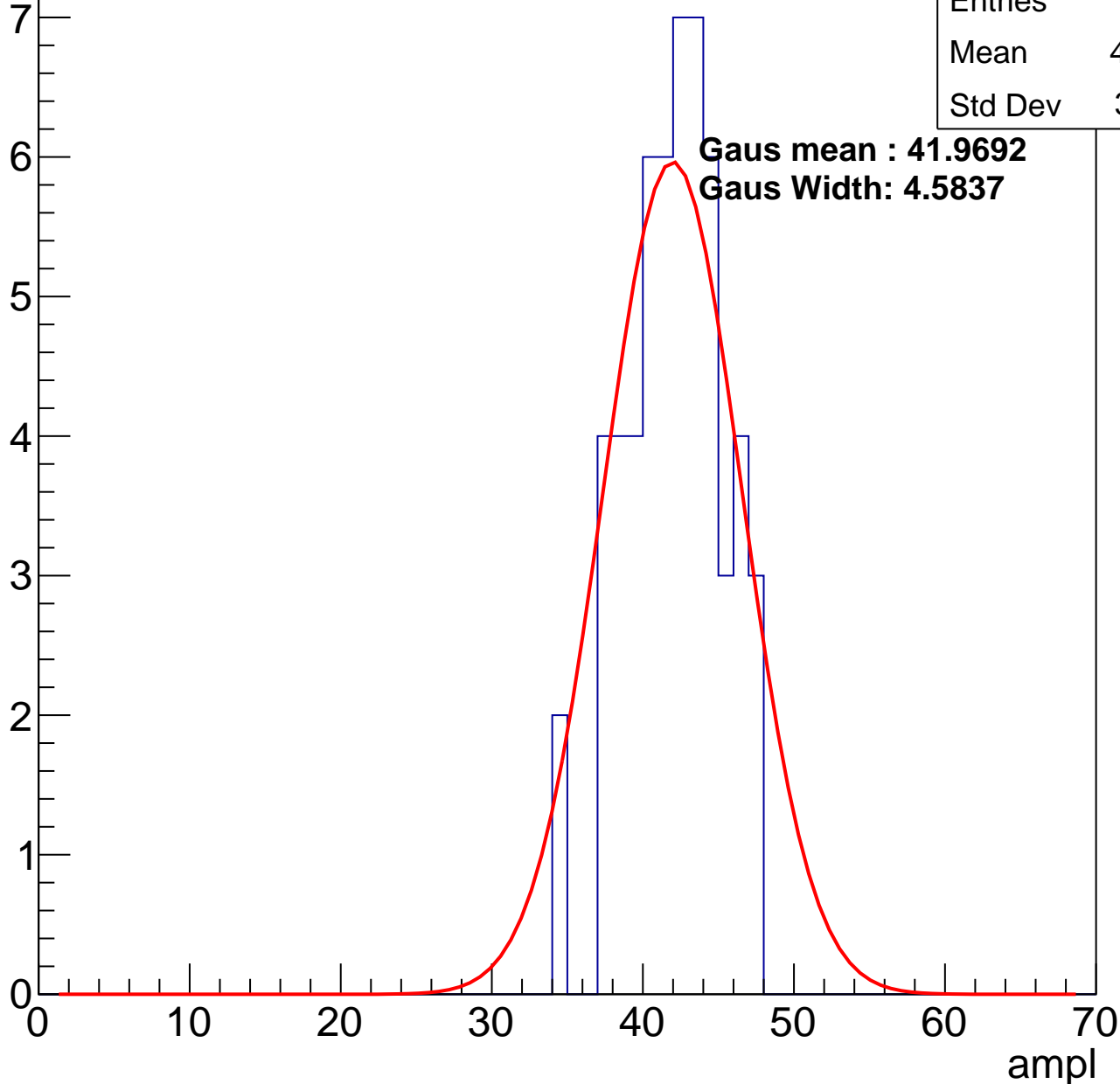


# B1L000S, U23-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.59
Std Dev	3.121



# B1L000S, U23-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	47.6
Std Dev	3.491

Entry

10

8

6

4

2

0

0

10

20

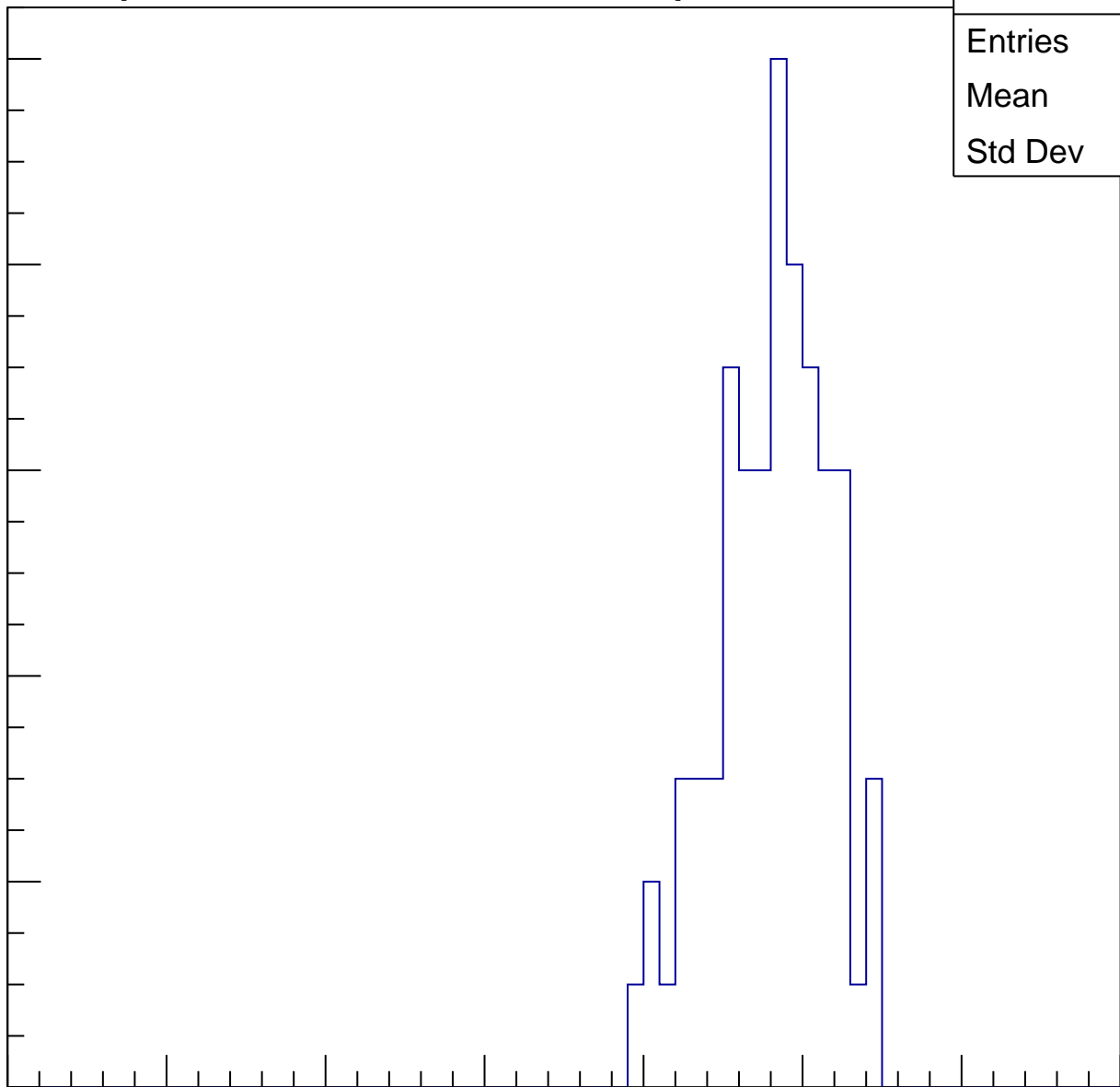
30

40

50

60

ampl

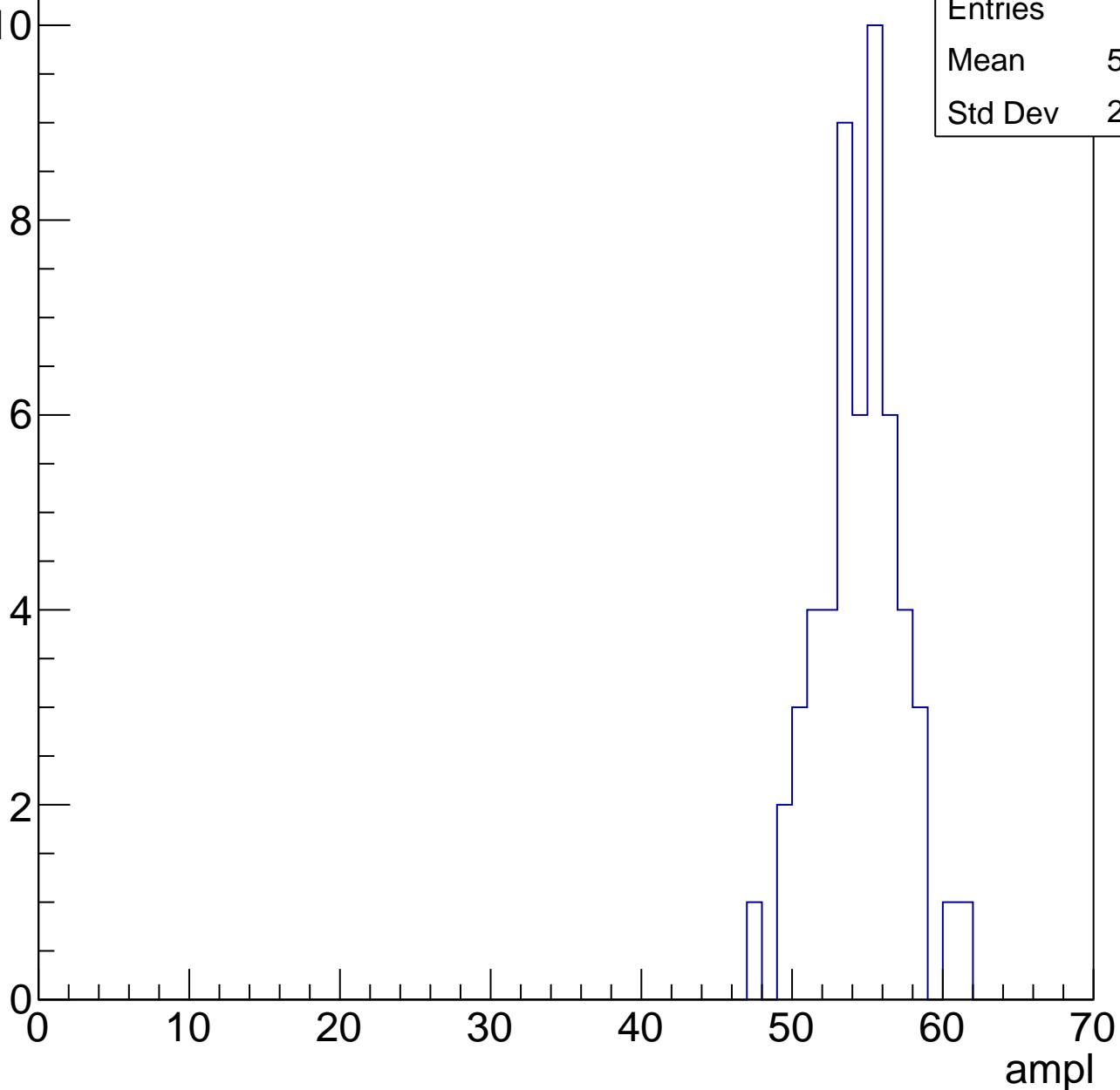


# B1L000S, U23-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.02
Std Dev	2.759



# B1L000S, U23-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

Entries	49
Mean	57.92
Std Dev	8.604

ampl

0

10

20

30

40

50

60

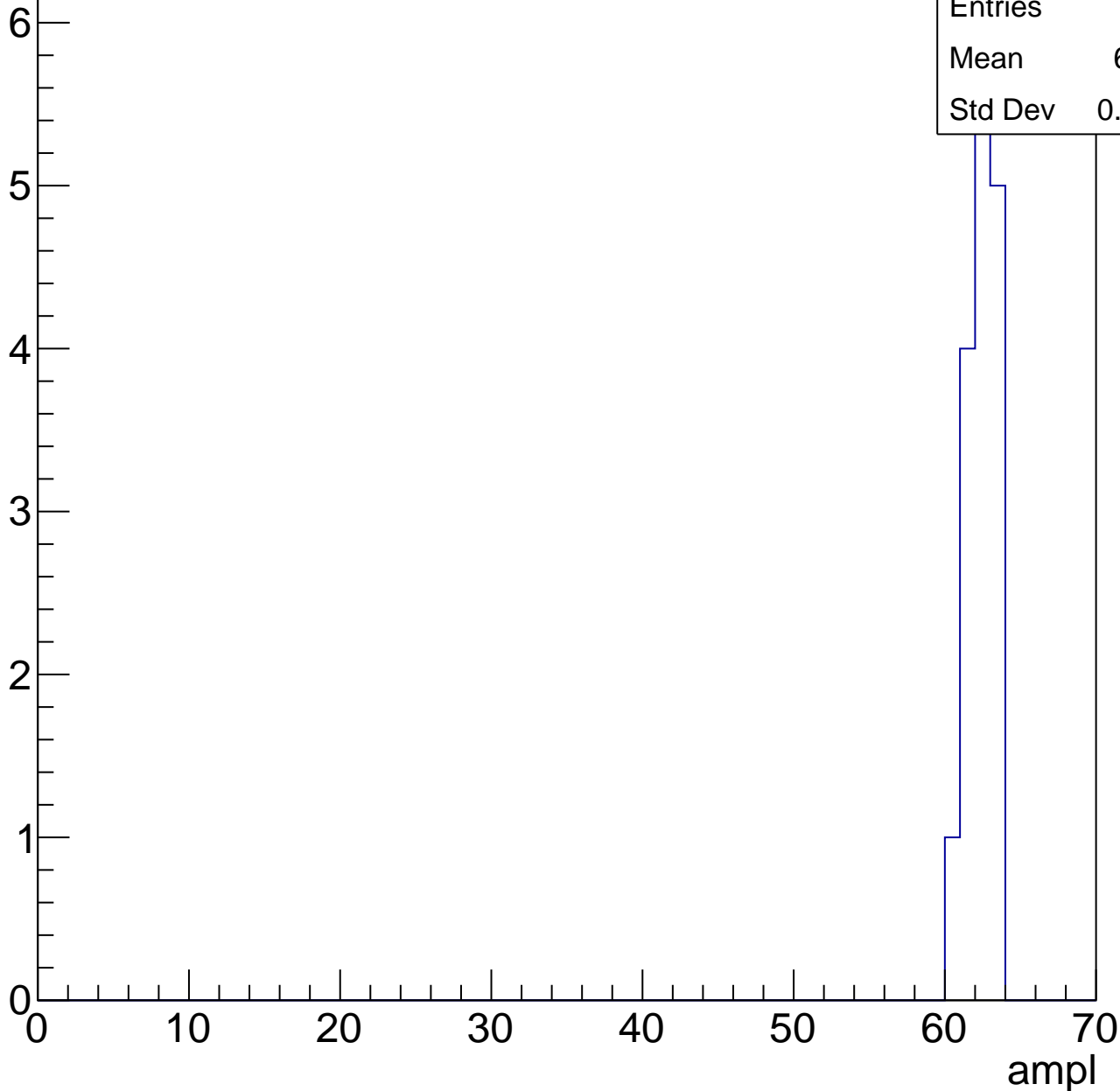
70

# B1L000S, U23-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.94
Std Dev	0.8992

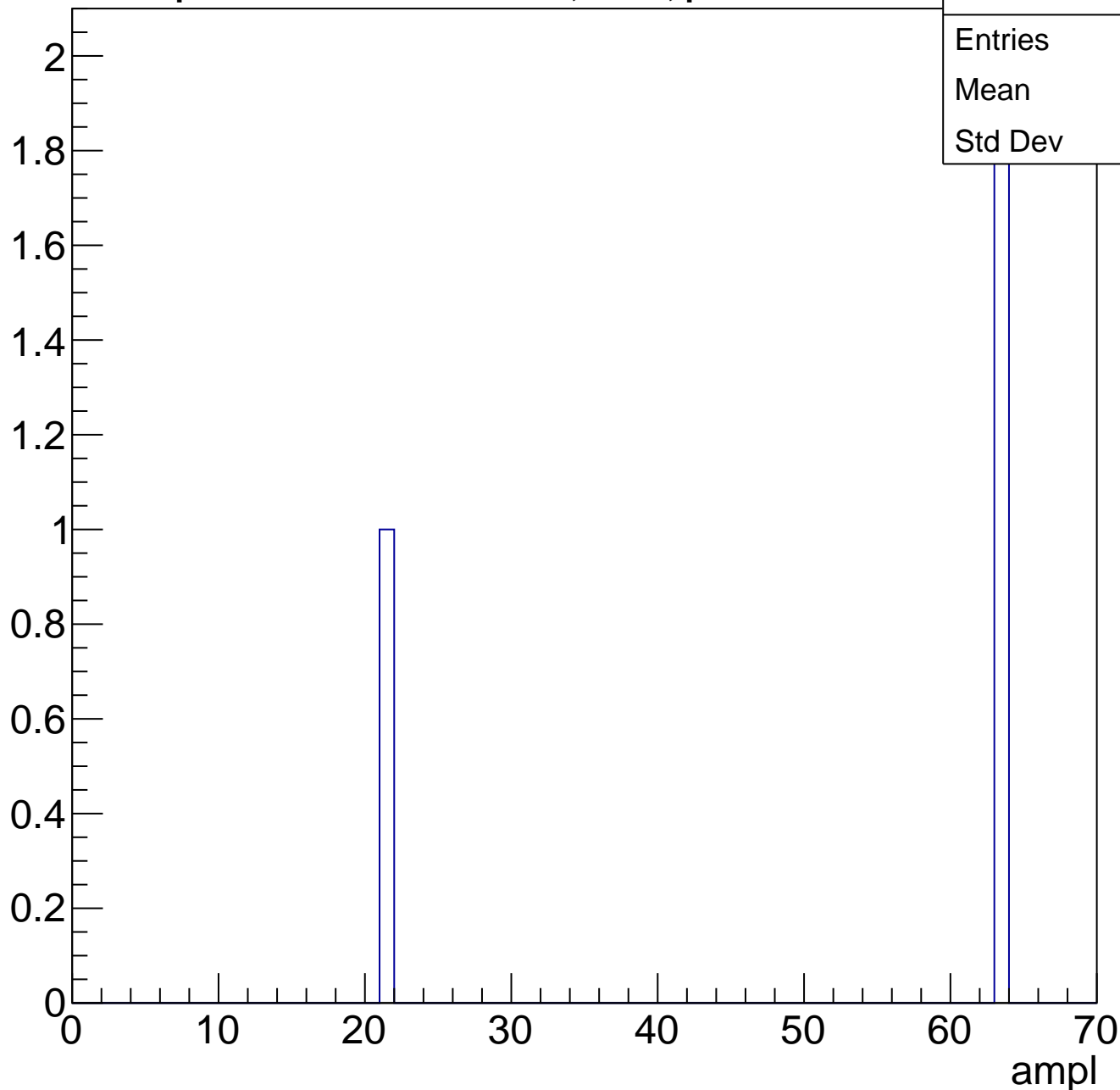




# B1L000S, U23-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch69, adc0

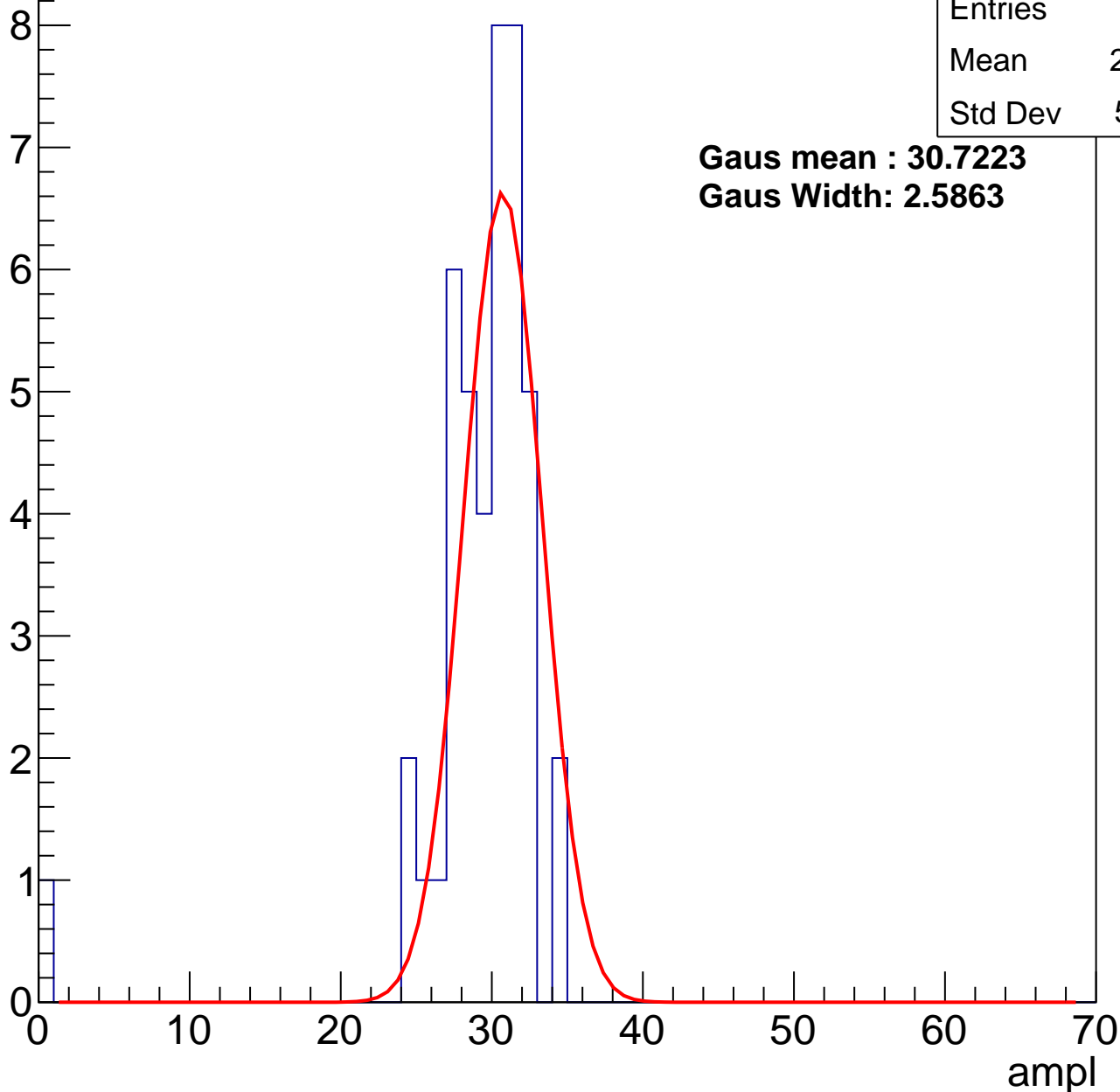
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	28.67
Std Dev	5.001

**Gaus mean : 30.7223**

**Gaus Width: 2.5863**

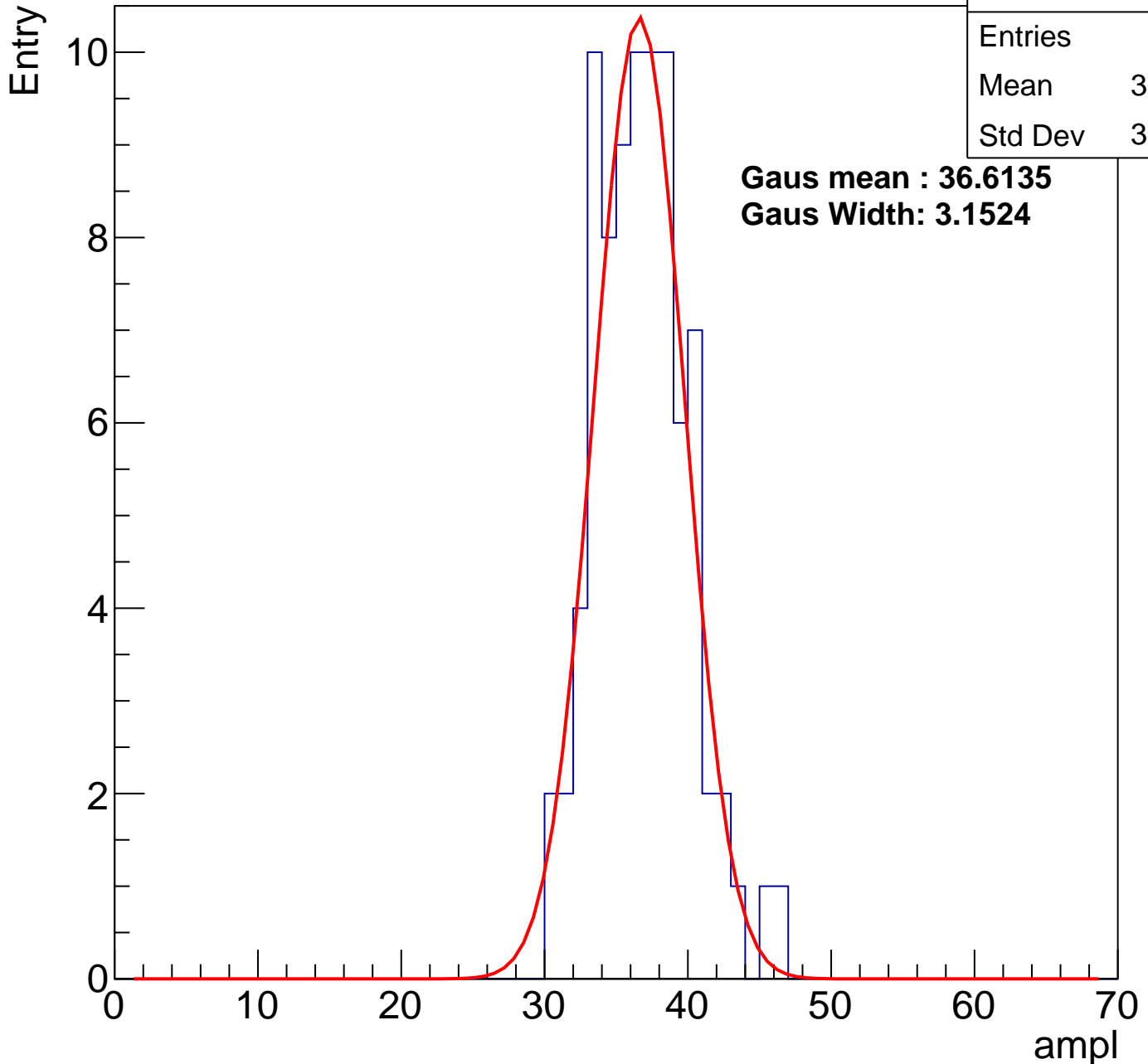


# B1L000S, U23-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	36.36
Std Dev	3.206

**Gaus mean : 36.6135**  
**Gaus Width: 3.1524**



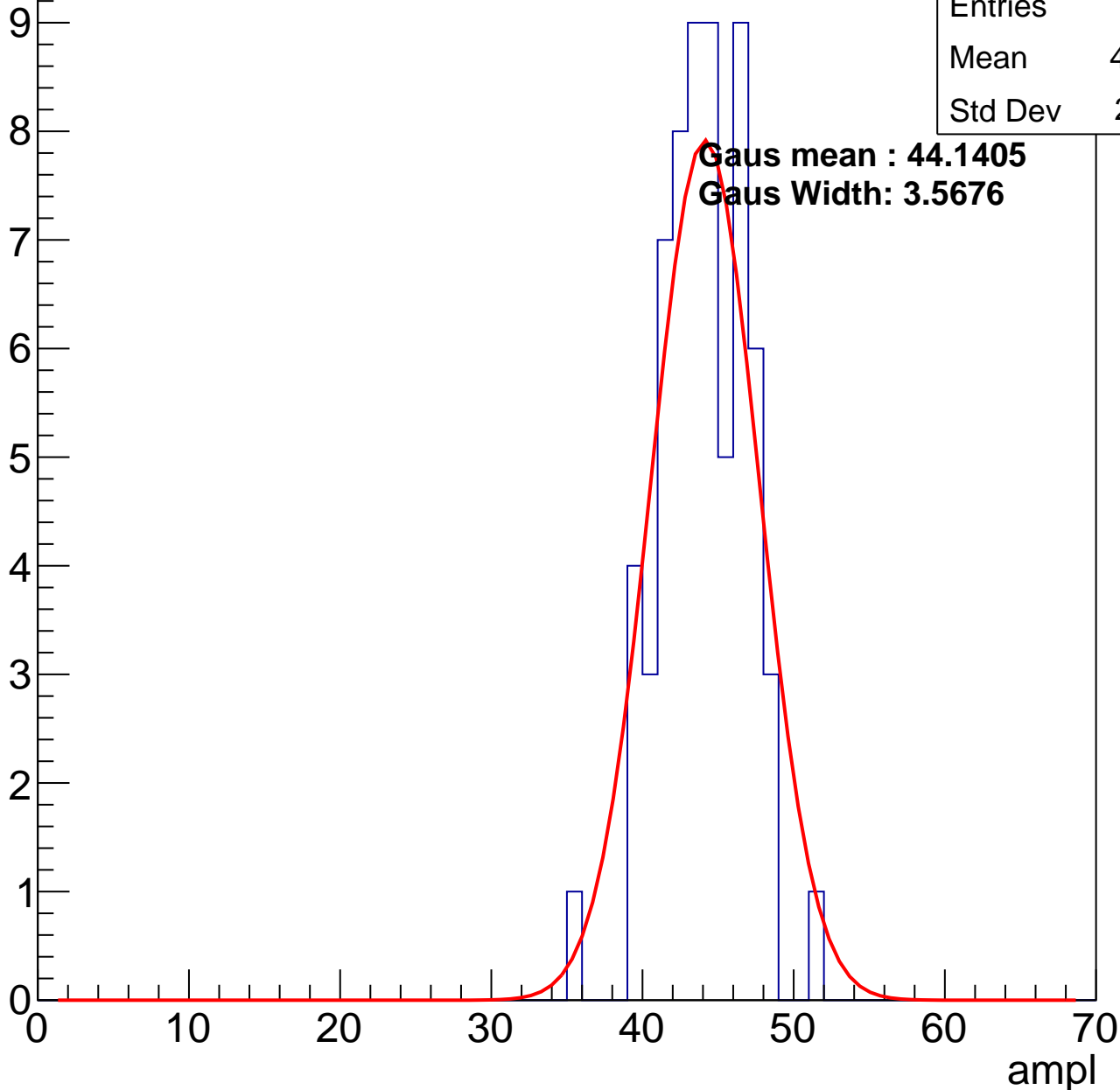
# B1L000S, U23-ch69, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.58
Std Dev	2.811

**Gaus mean : 44.1405**  
**Gaus Width: 3.5676**

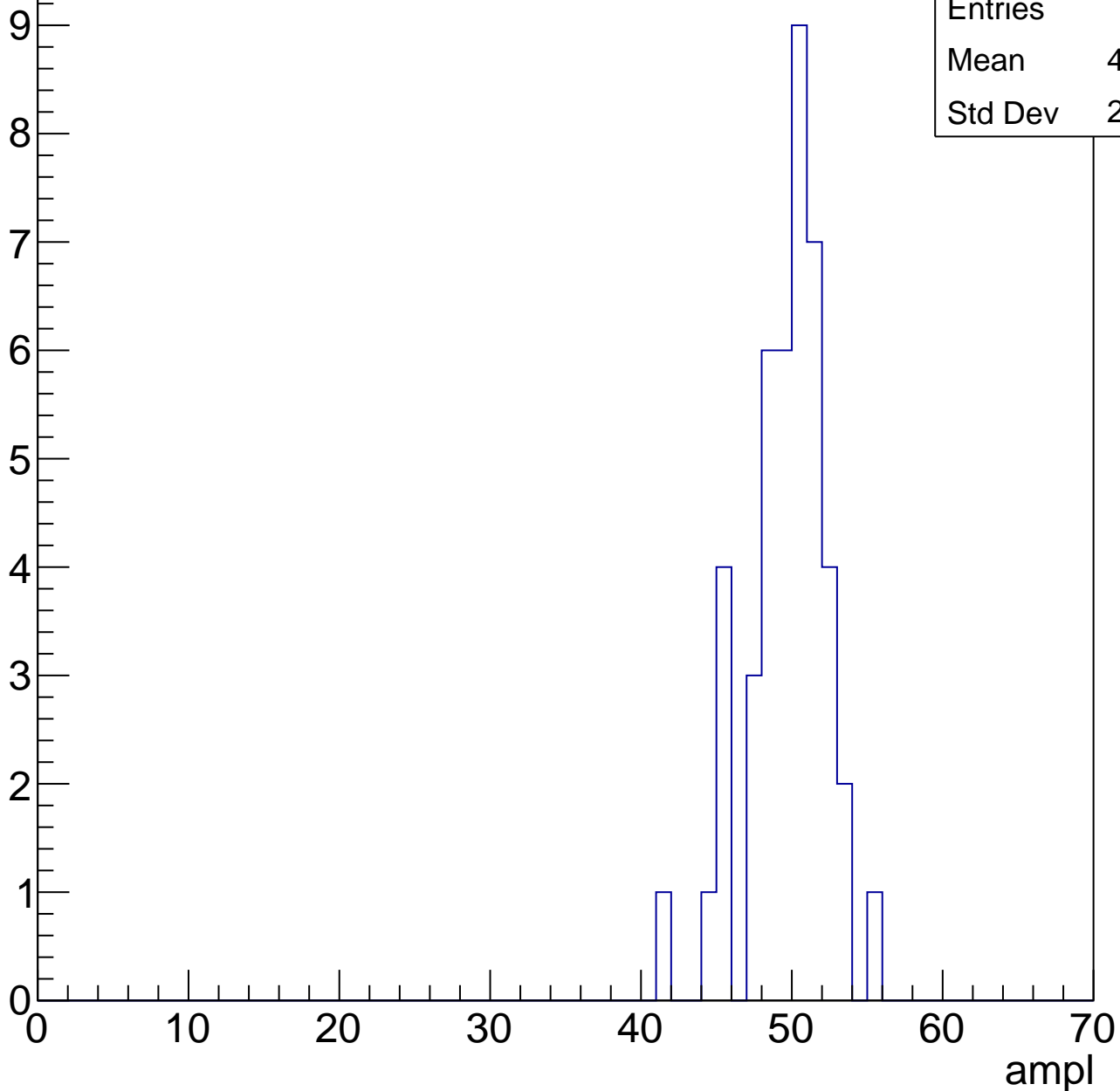


# B1L000S, U23-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	49.18
Std Dev	2.657

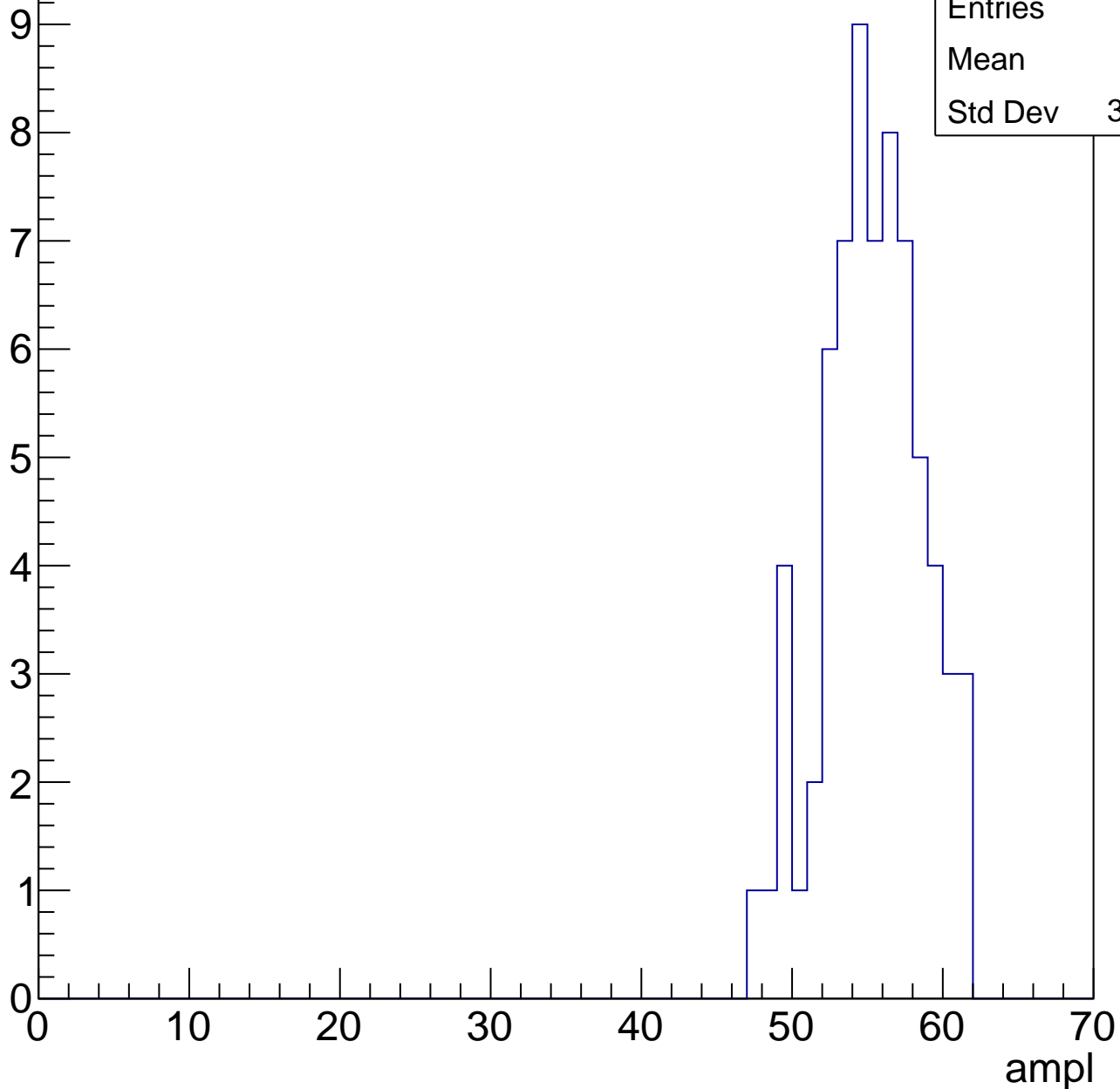


# B1L000S, U23-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

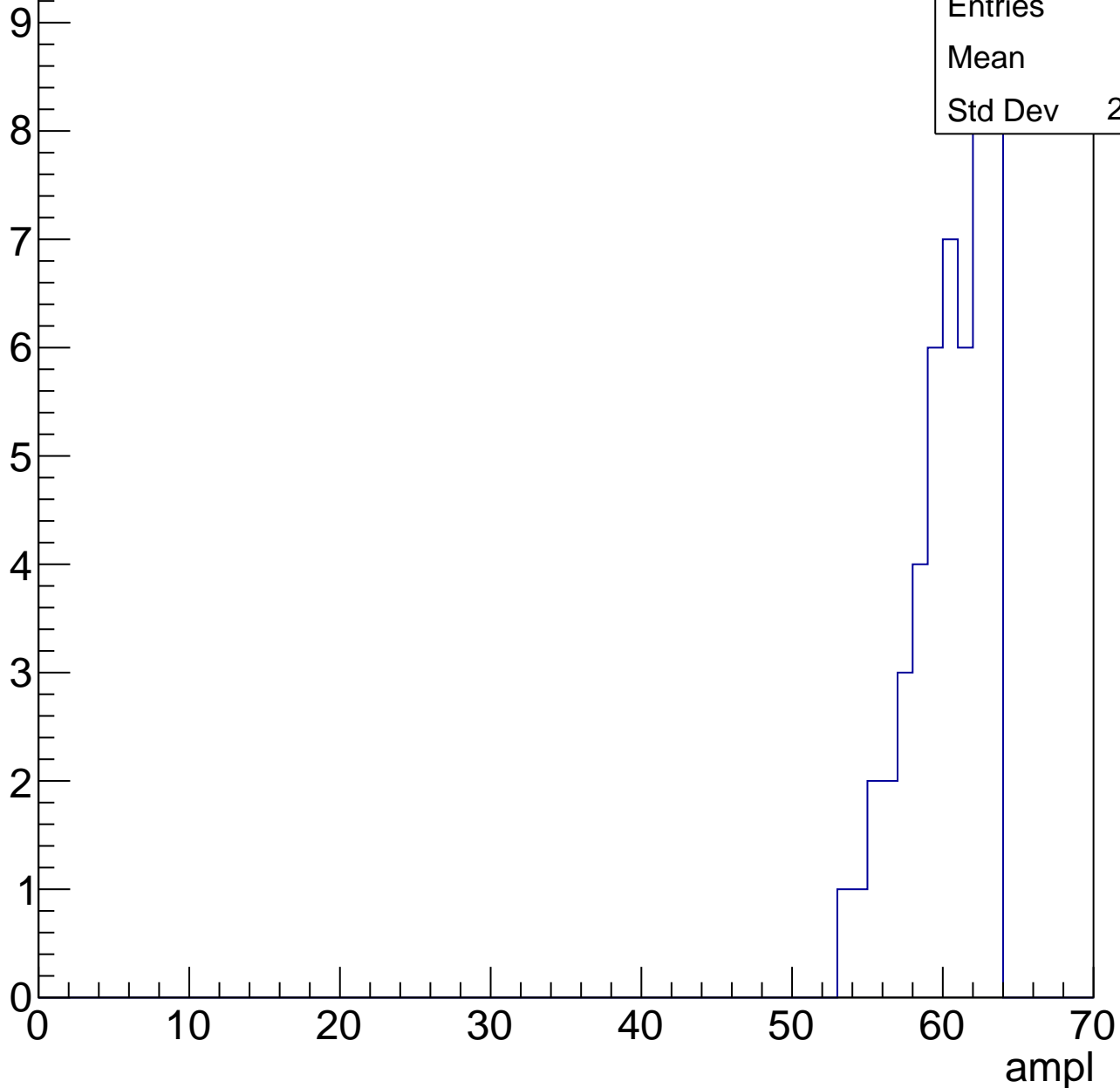
Entries	68
Mean	54.9
Std Dev	3.282



# B1L000S, U23-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

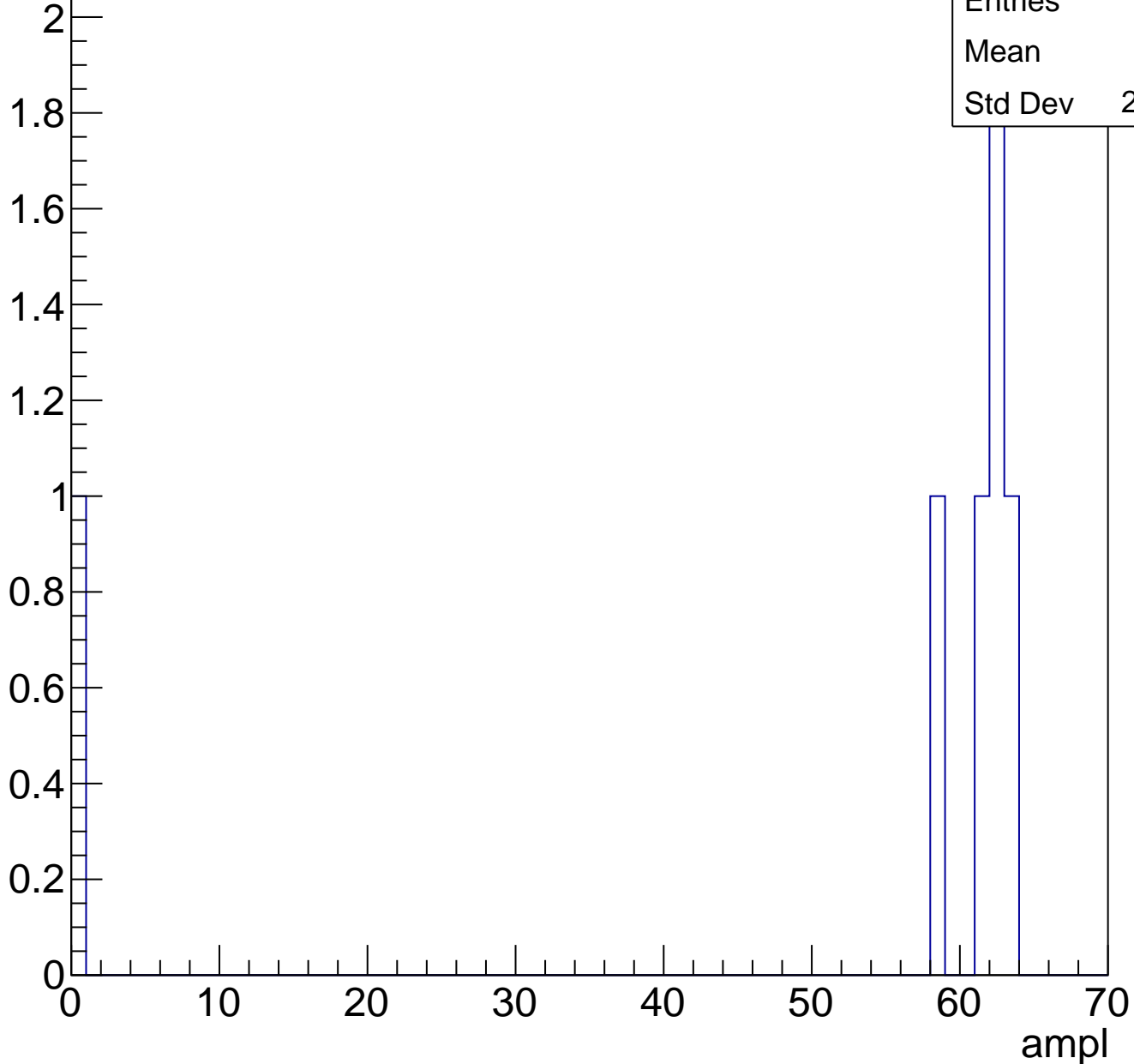
Entry



# B1L000S, U23-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch70, adc0

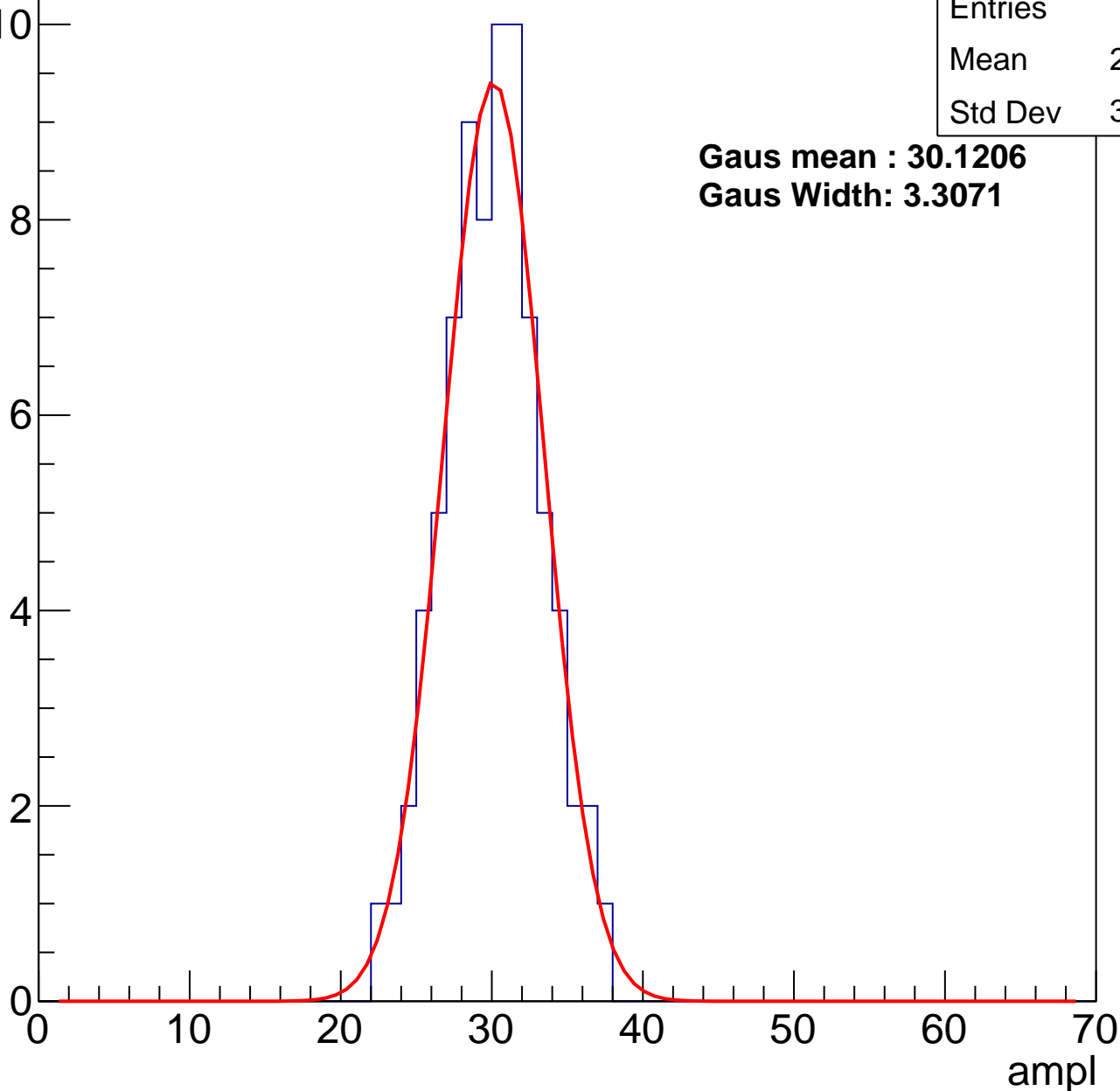
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	29.62
Std Dev	3.159

**Gaus mean : 30.1206**

**Gaus Width: 3.3071**



# B1L000S, U23-ch70, adc1

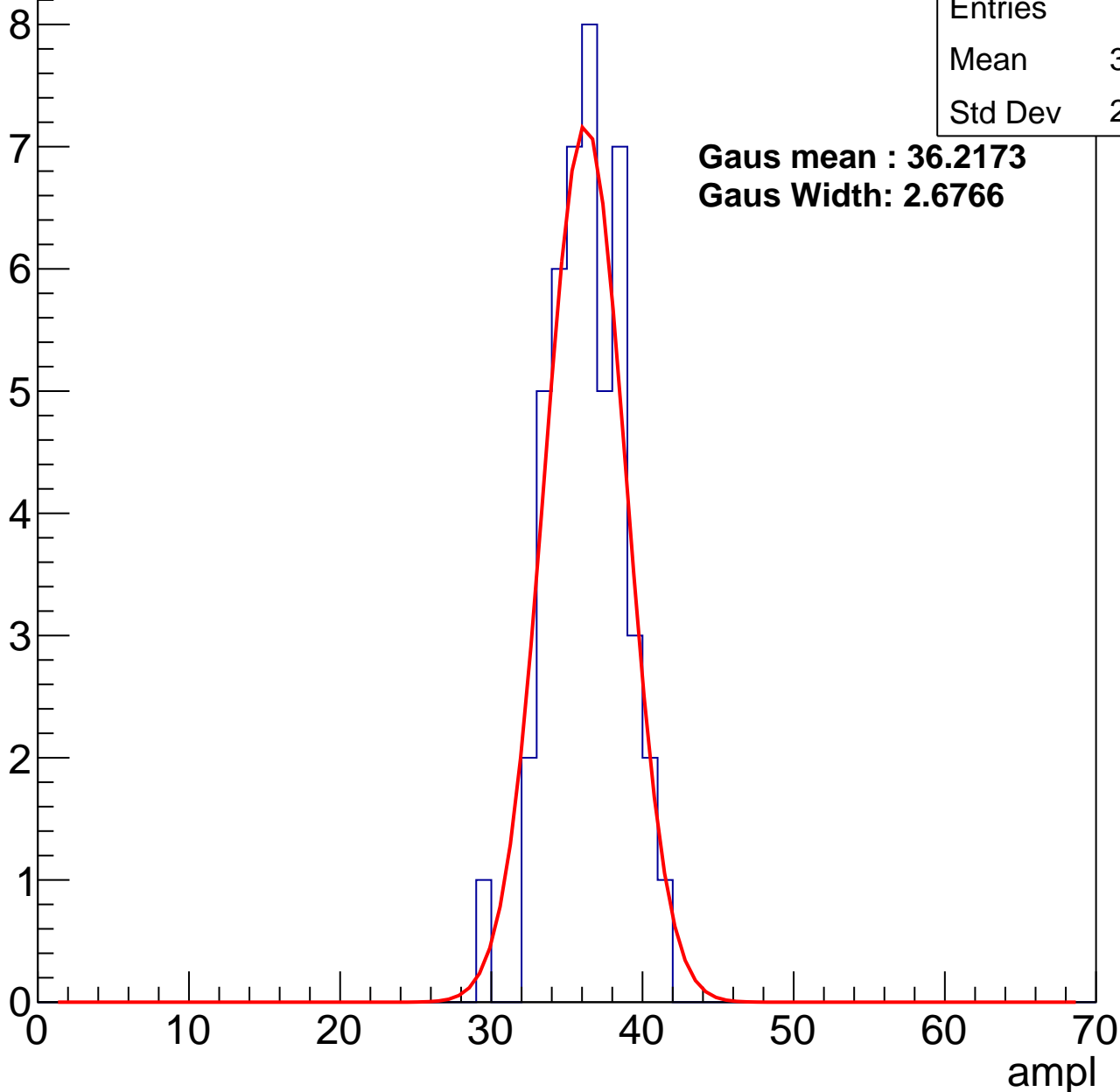
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	35.83
Std Dev	2.408

**Gaus mean : 36.2173**

**Gaus Width: 2.6766**



# B1L000S, U23-ch70, adc2

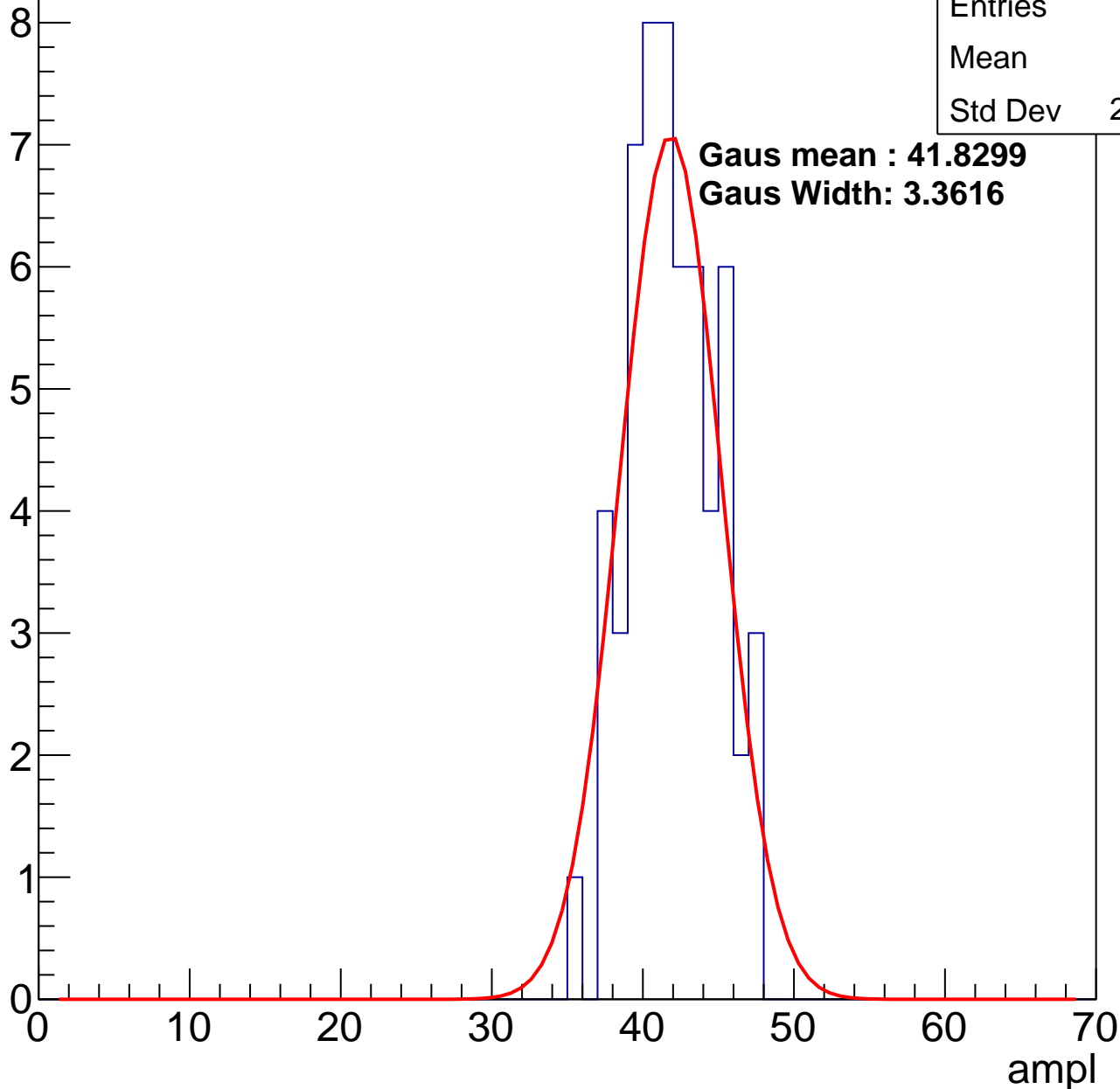
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.5
Std Dev	2.842

**Gaus mean : 41.8299**

**Gaus Width: 3.3616**

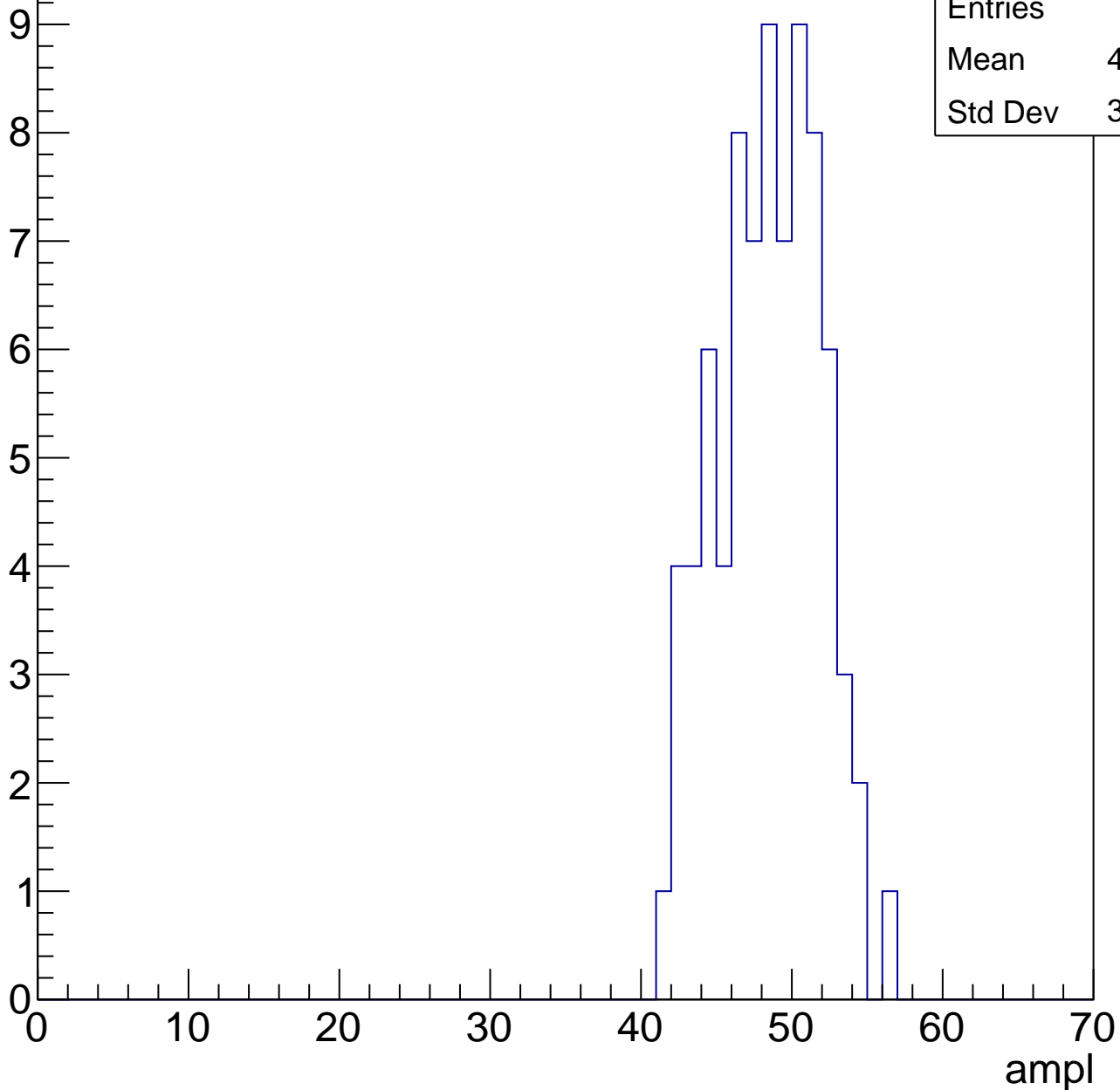


# B1L000S, U23-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

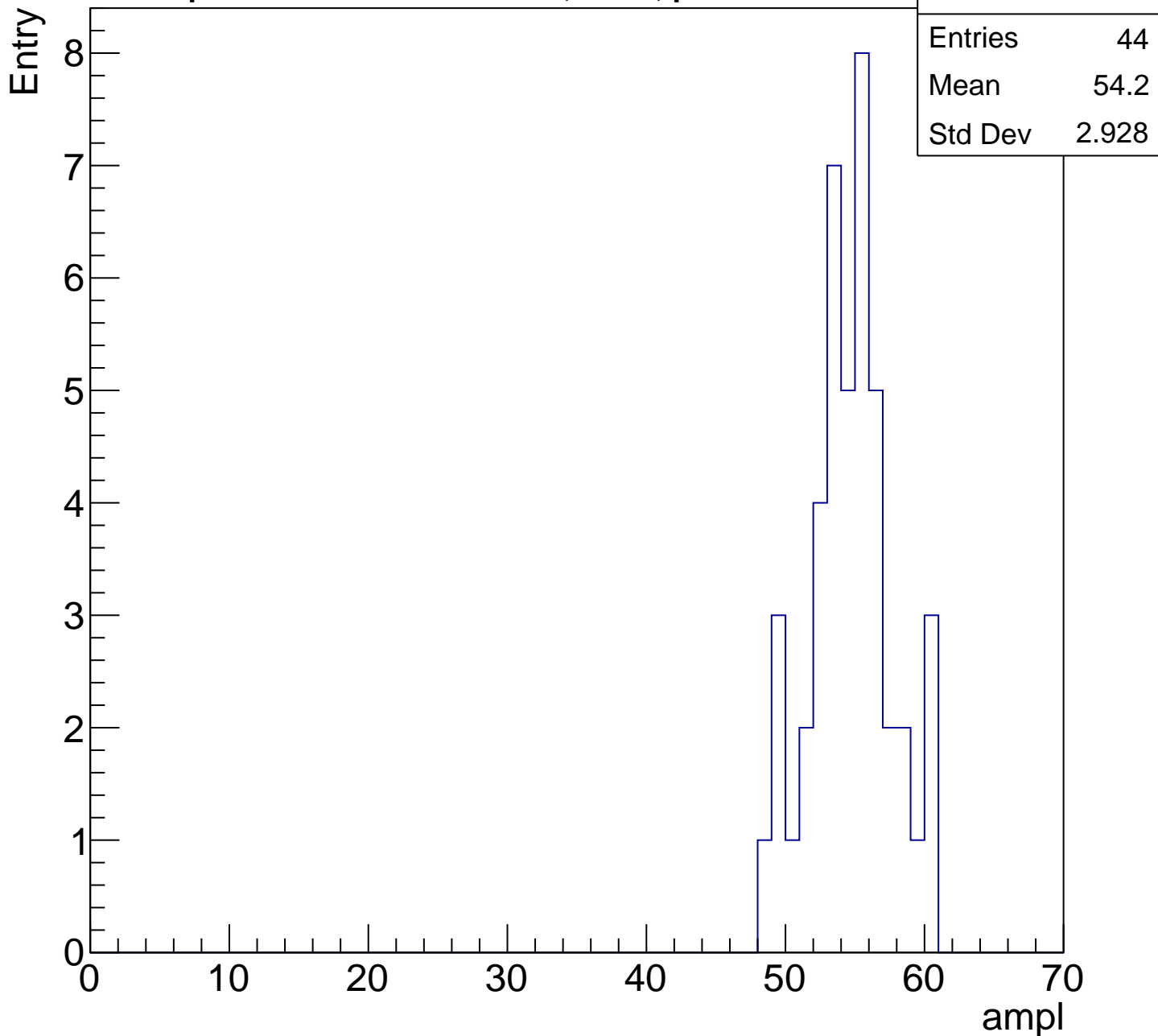
Entry

Entries	79
Mean	47.97
Std Dev	3.349



# B1L000S, U23-ch70, adc4

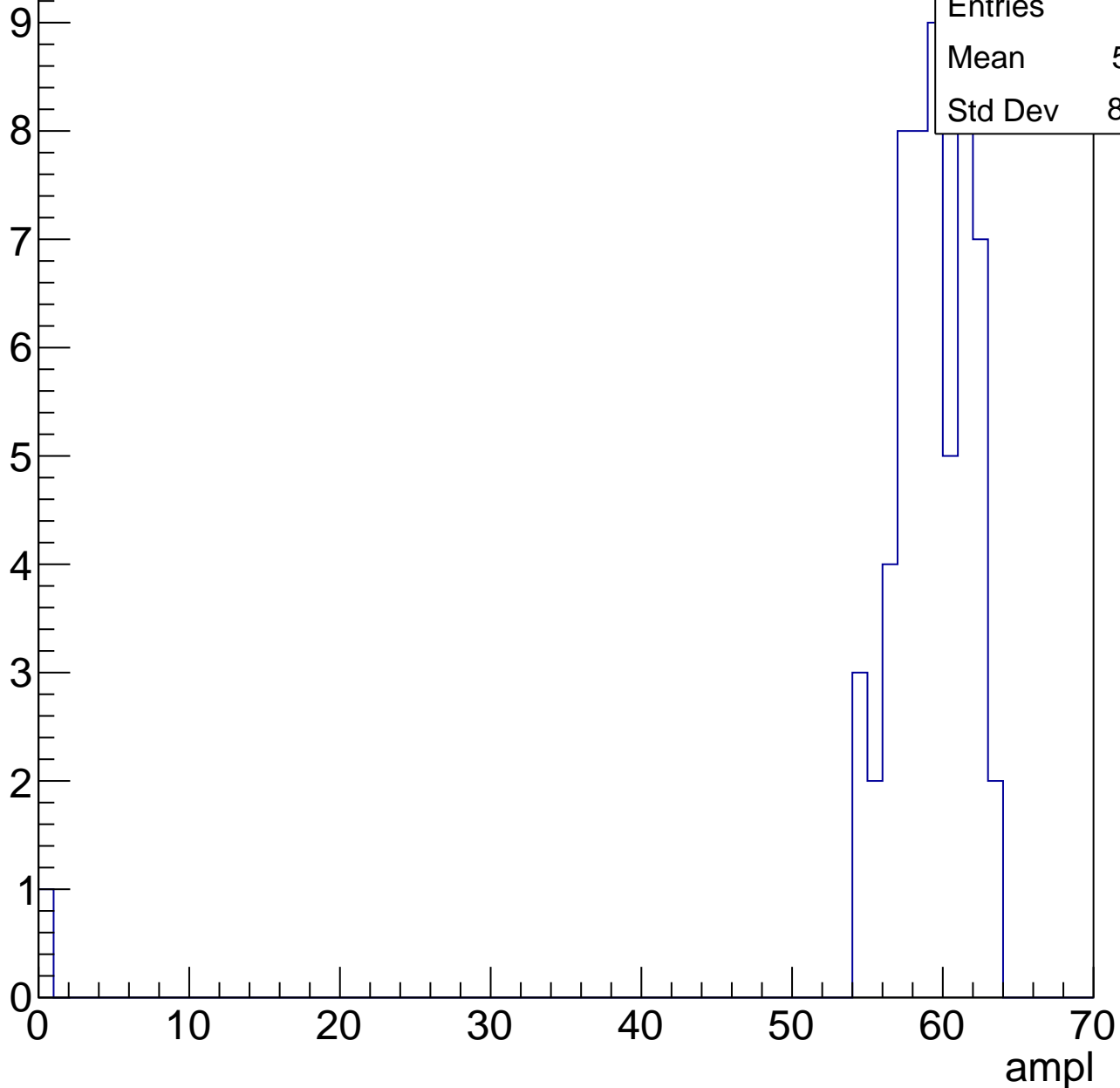
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

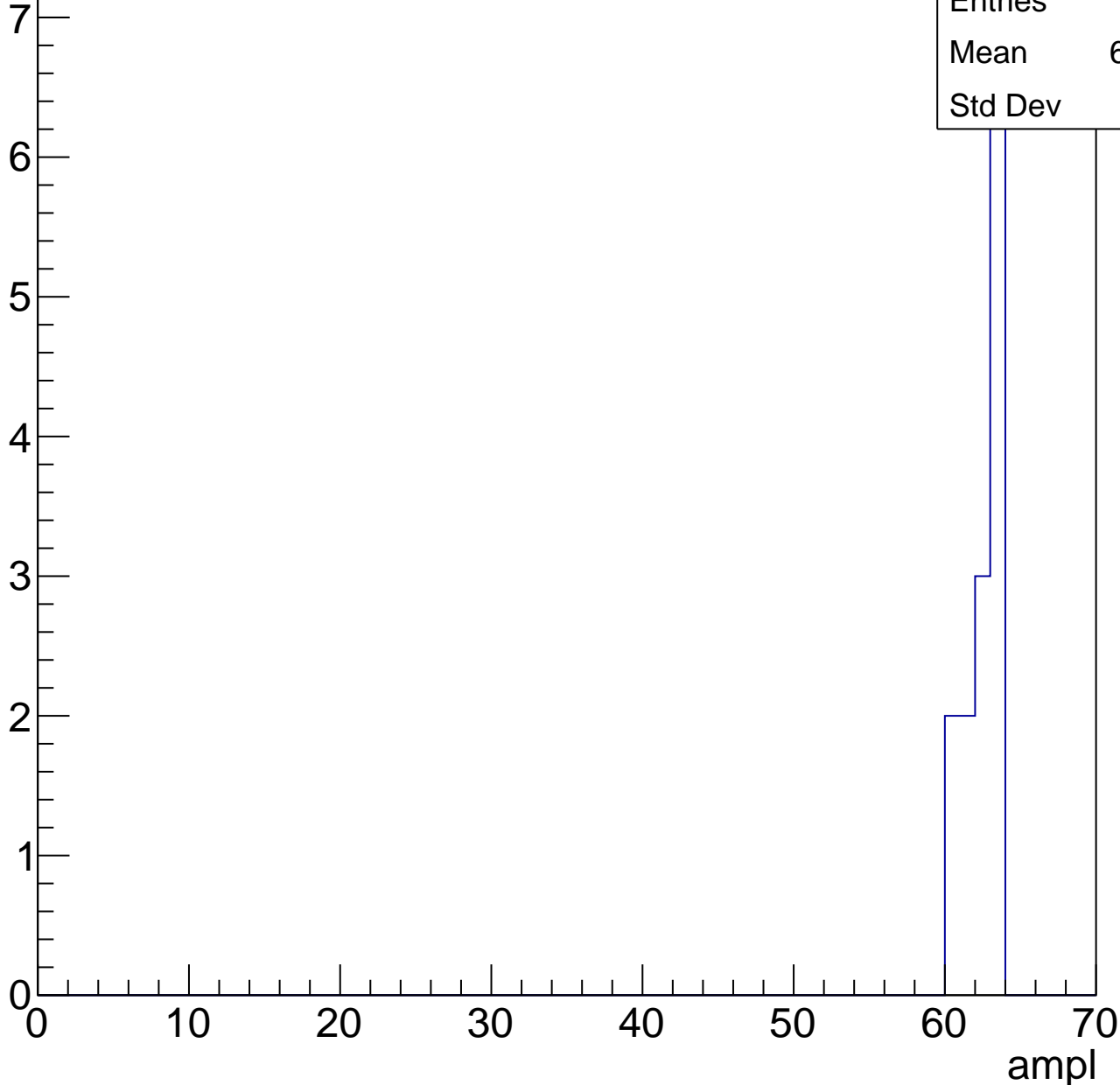


# B1L000S, U23-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	62.07
Std Dev	1.1





# B1L000S, U23-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U23-ch71, adc0

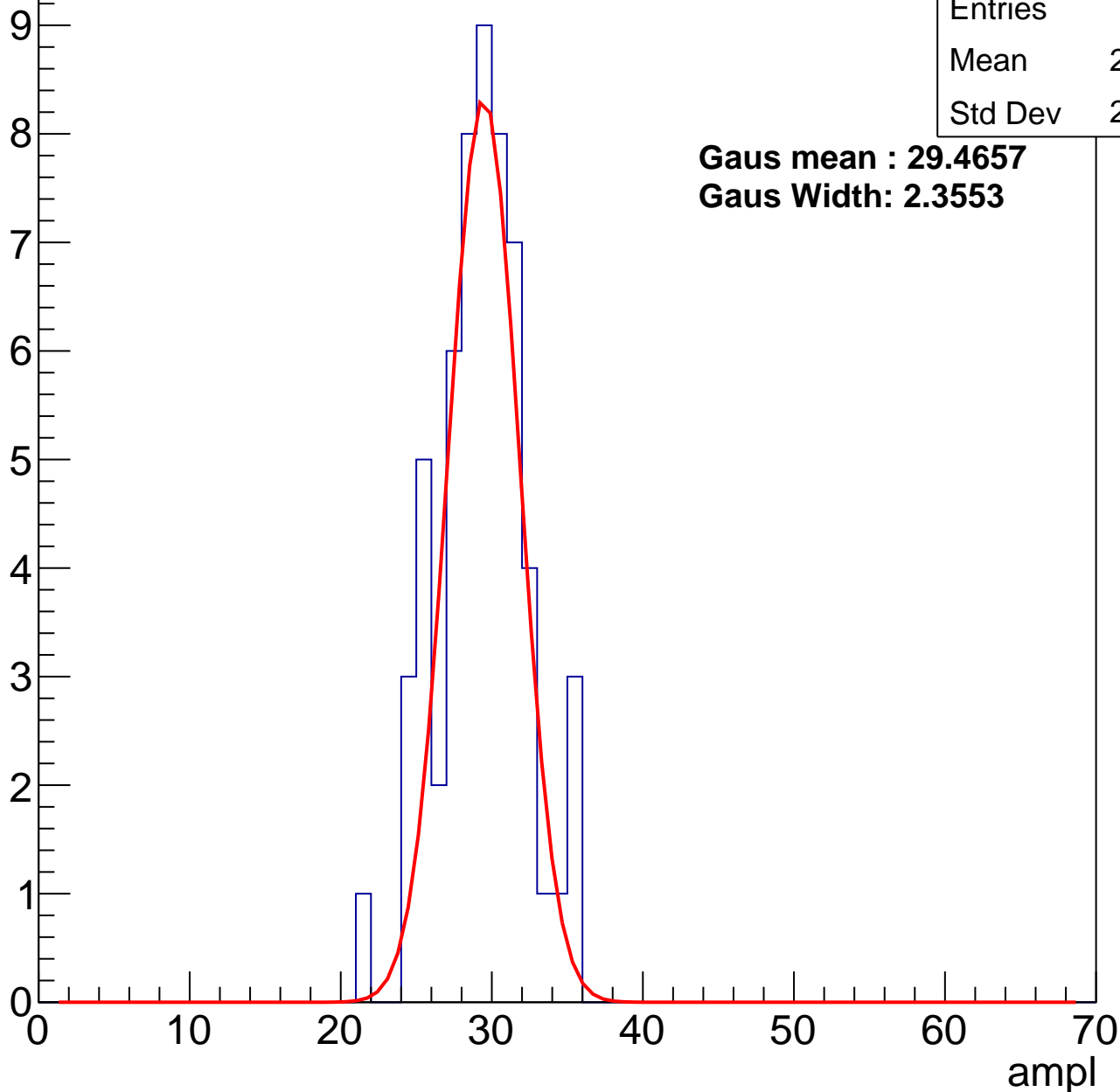
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	28.86
Std Dev	2.903

**Gaus mean : 29.4657**

**Gaus Width: 2.3553**



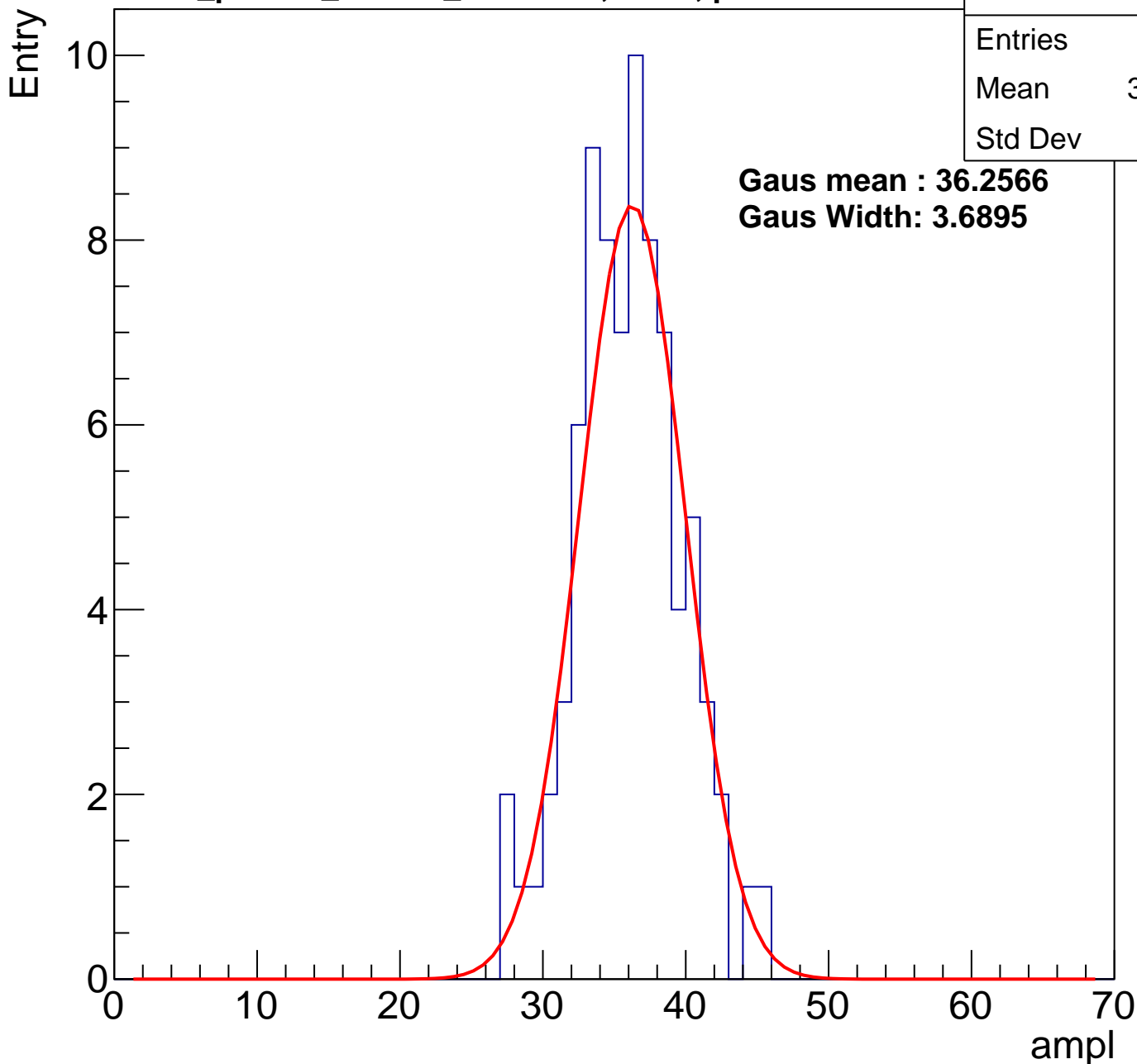
# B1L000S, U23-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	35.55
Std Dev	3.65

**Gaus mean : 36.2566**

**Gaus Width: 3.6895**



# B1L000S, U23-ch71, adc2

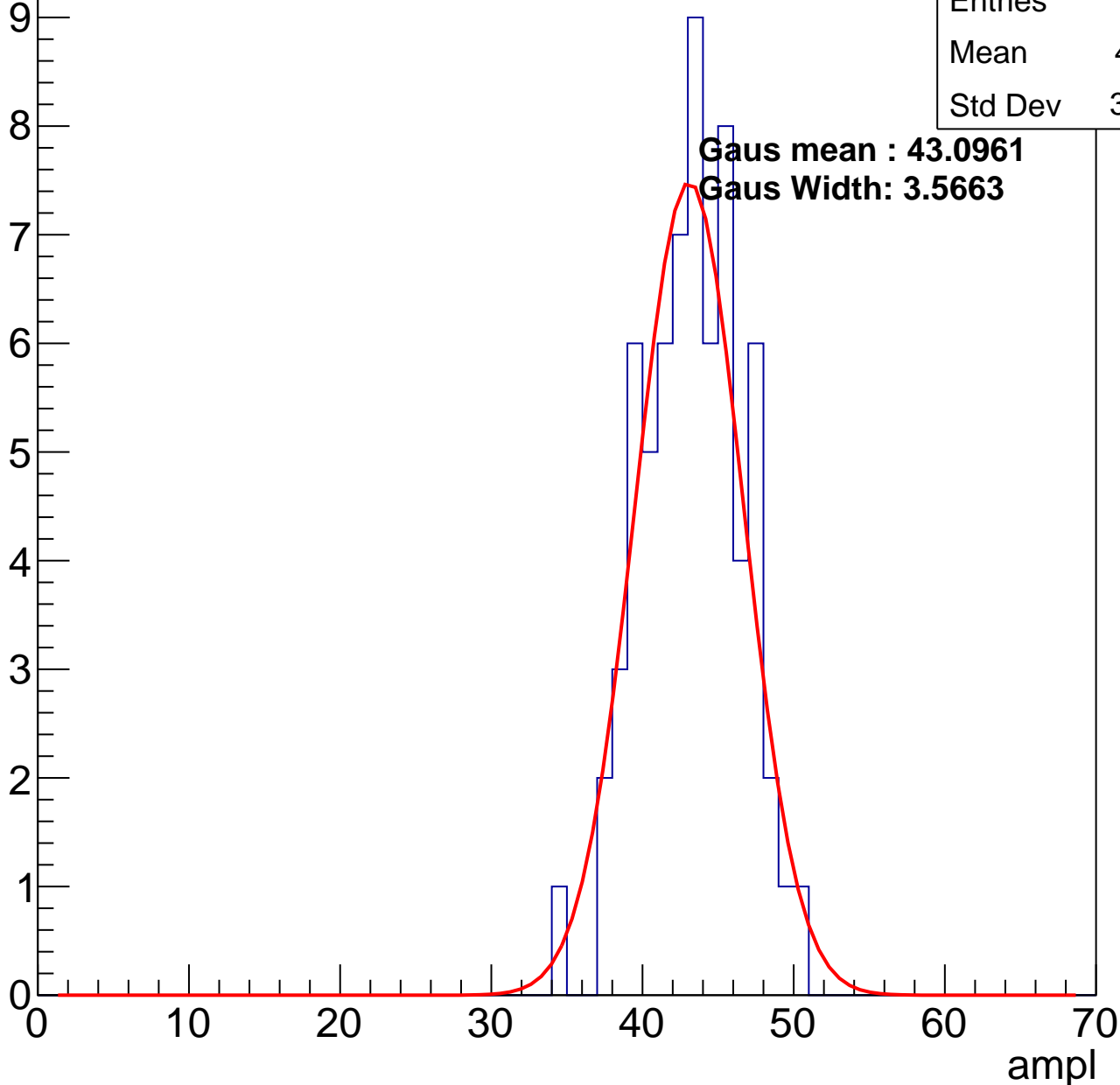
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.81
Std Dev	3.238

**Gaus mean : 43.0961**

**Gaus Width: 3.5663**

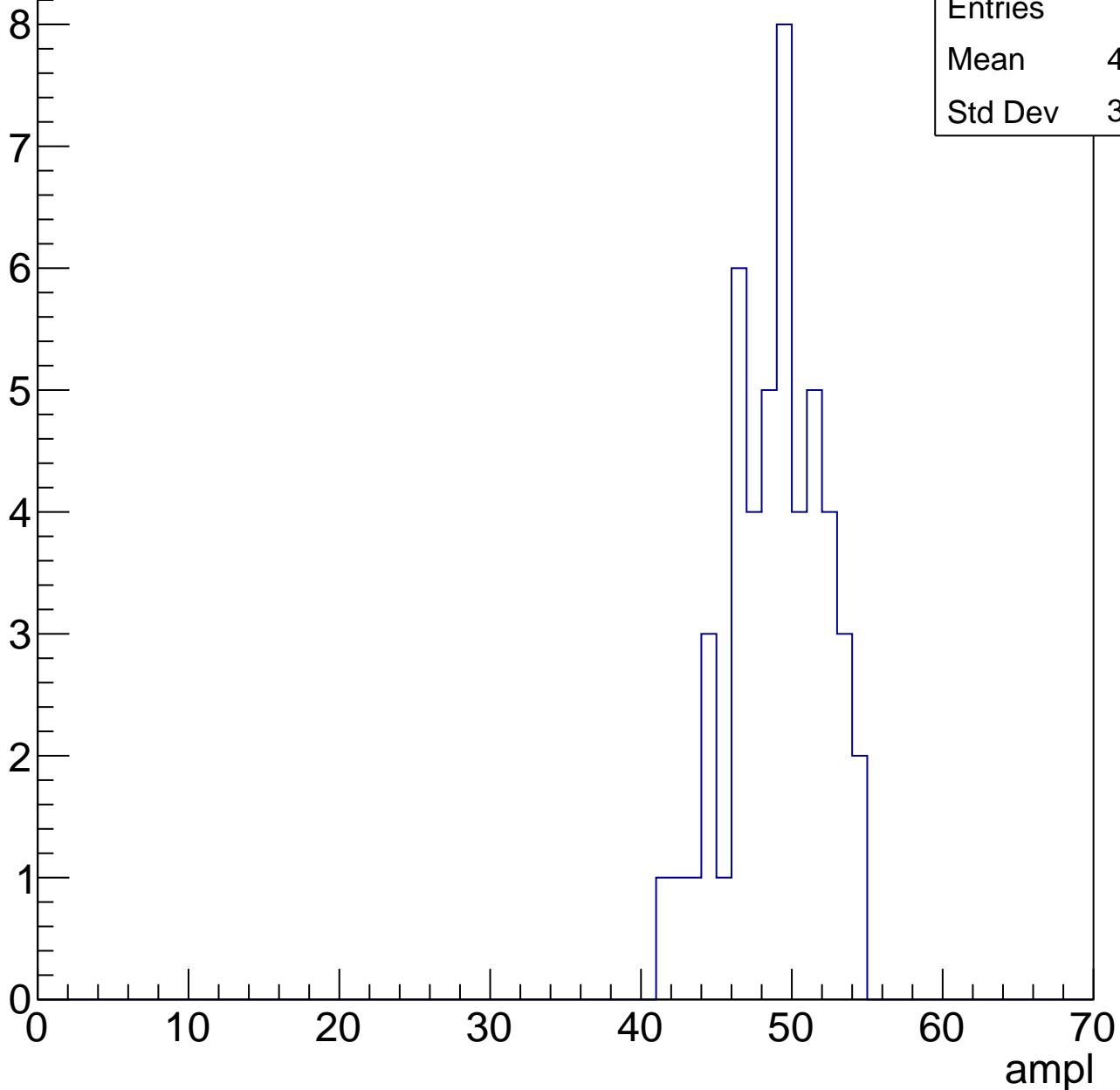


# B1L000S, U23-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

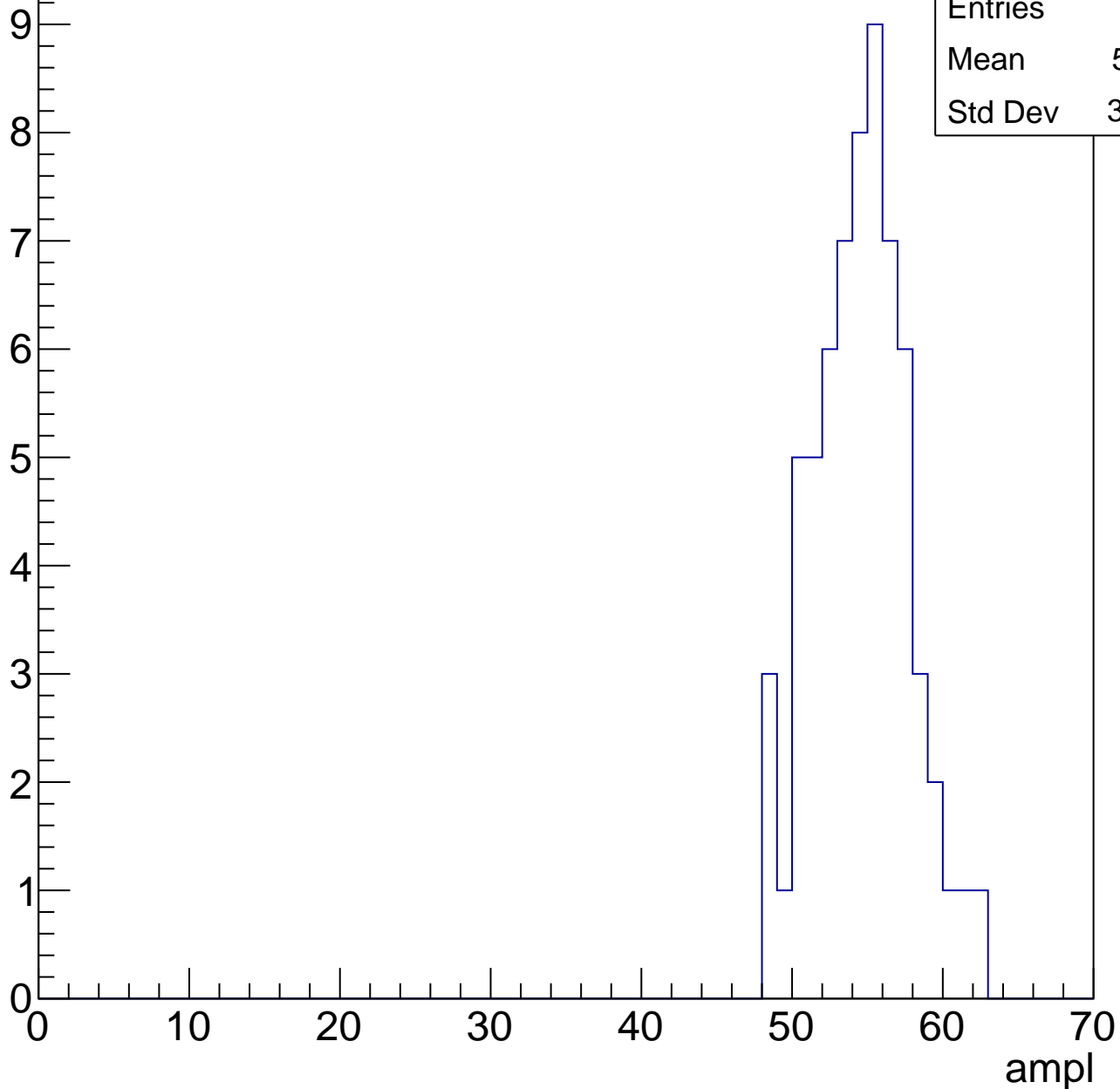
Entries	48
Mean	48.52
Std Dev	3.102



# B1L000S, U23-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

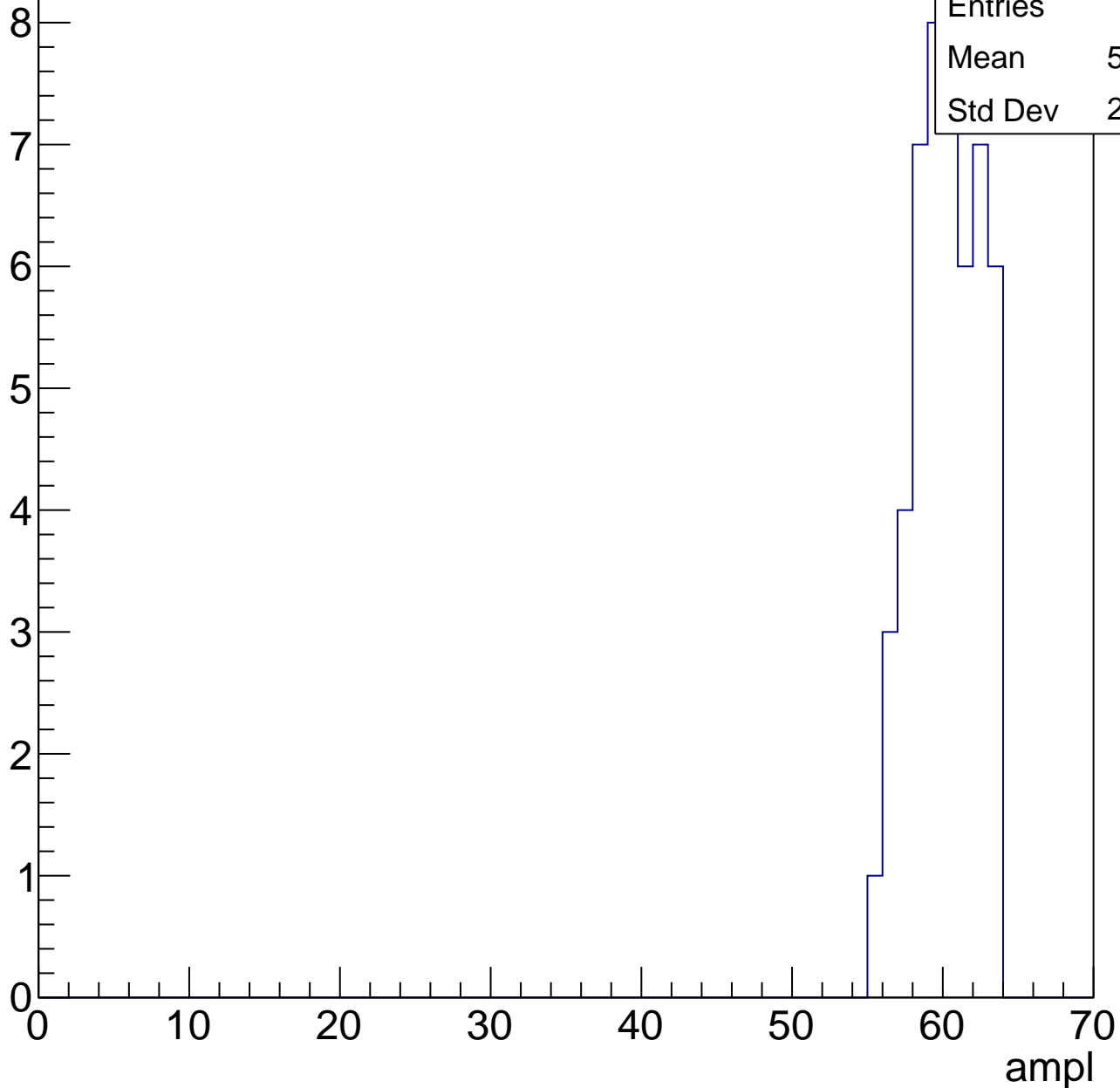
Entry



# B1L000S, U23-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

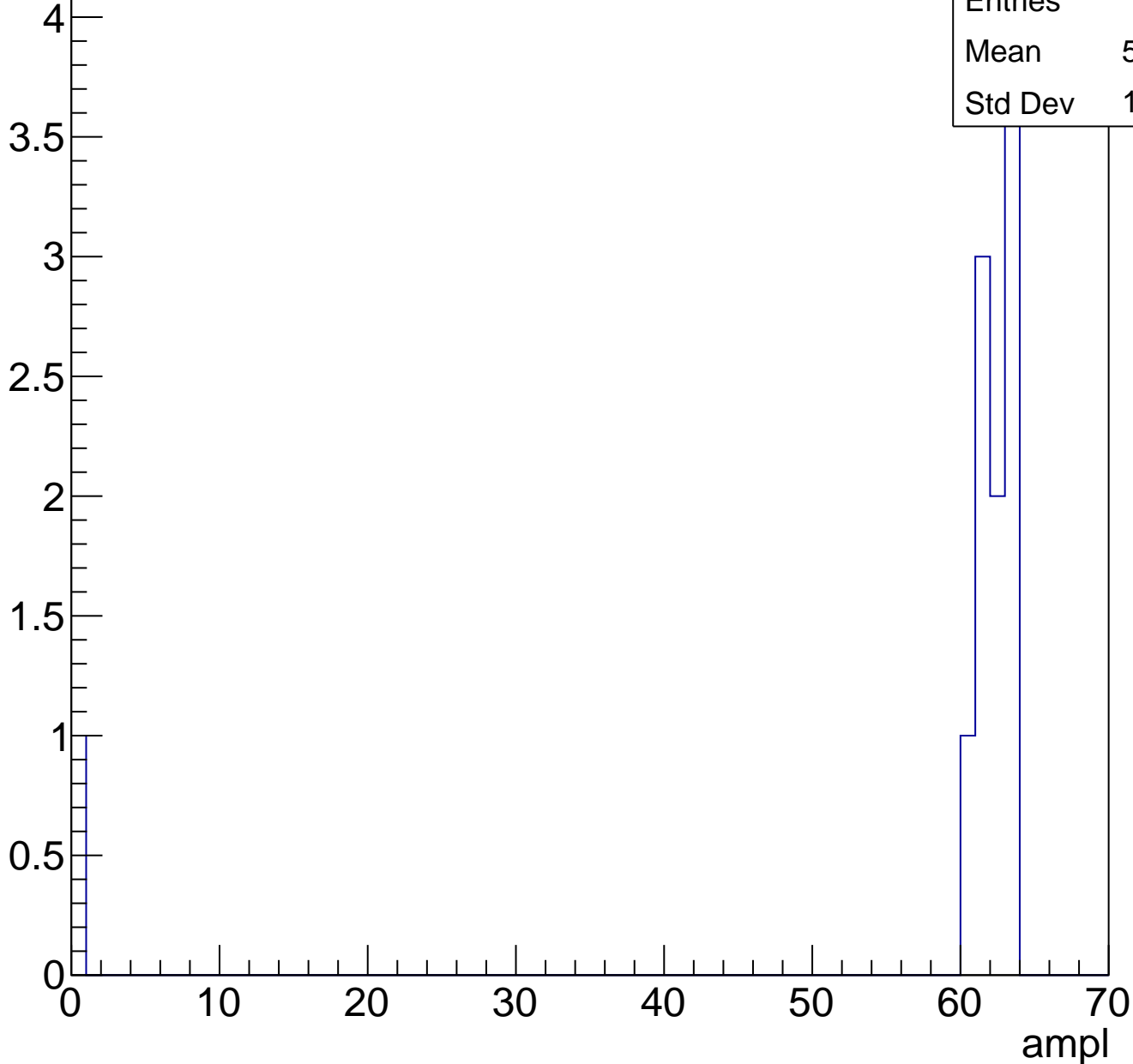


Entries	50
Mean	59.74
Std Dev	2.143

# B1L000S, U23-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch72, adc0

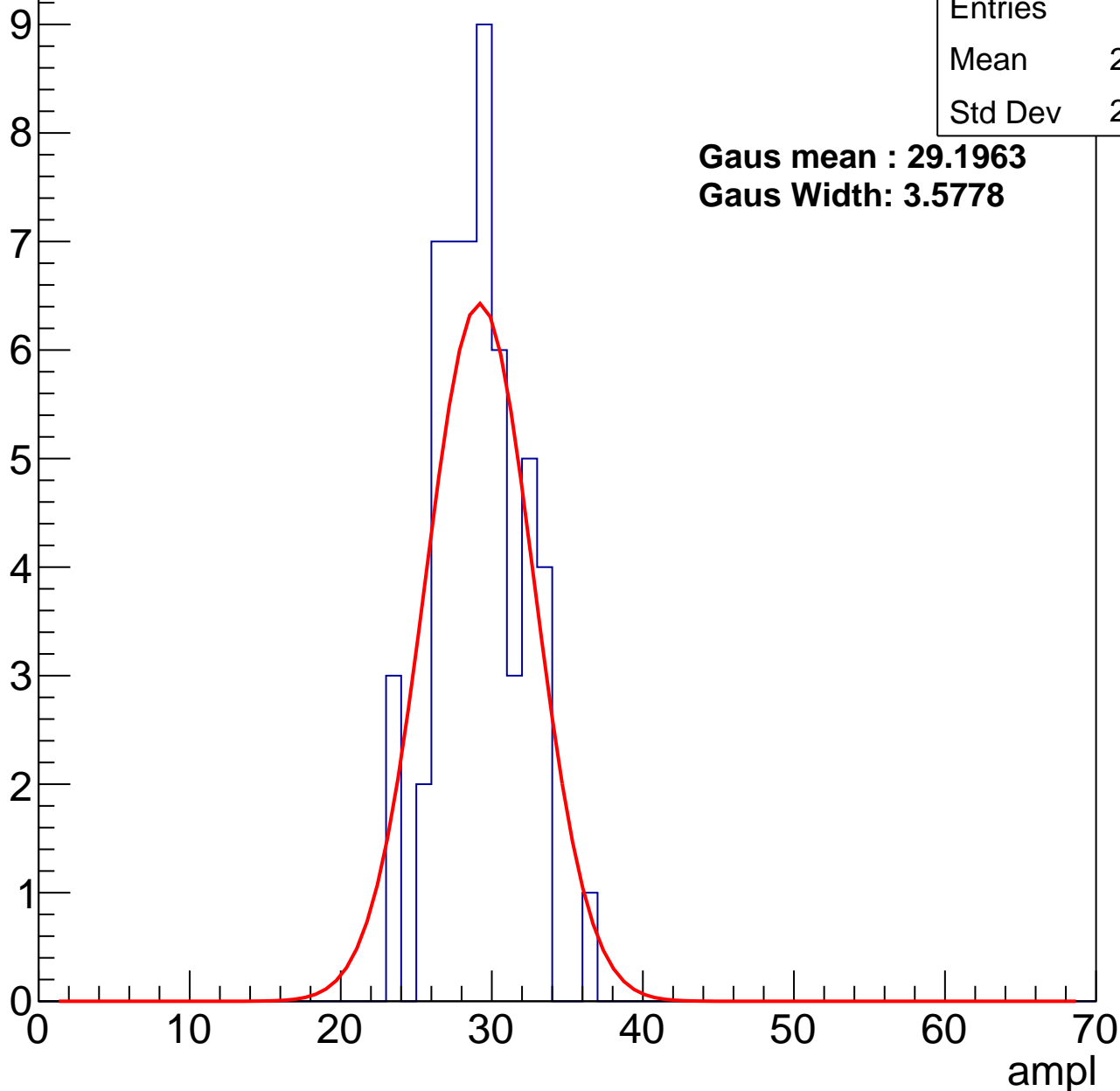
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	28.67
Std Dev	2.749

**Gaus mean : 29.1963**

**Gaus Width: 3.5778**



# B1L000S, U23-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	35
Std Dev	3.321

**Gaus mean : 35.5531**

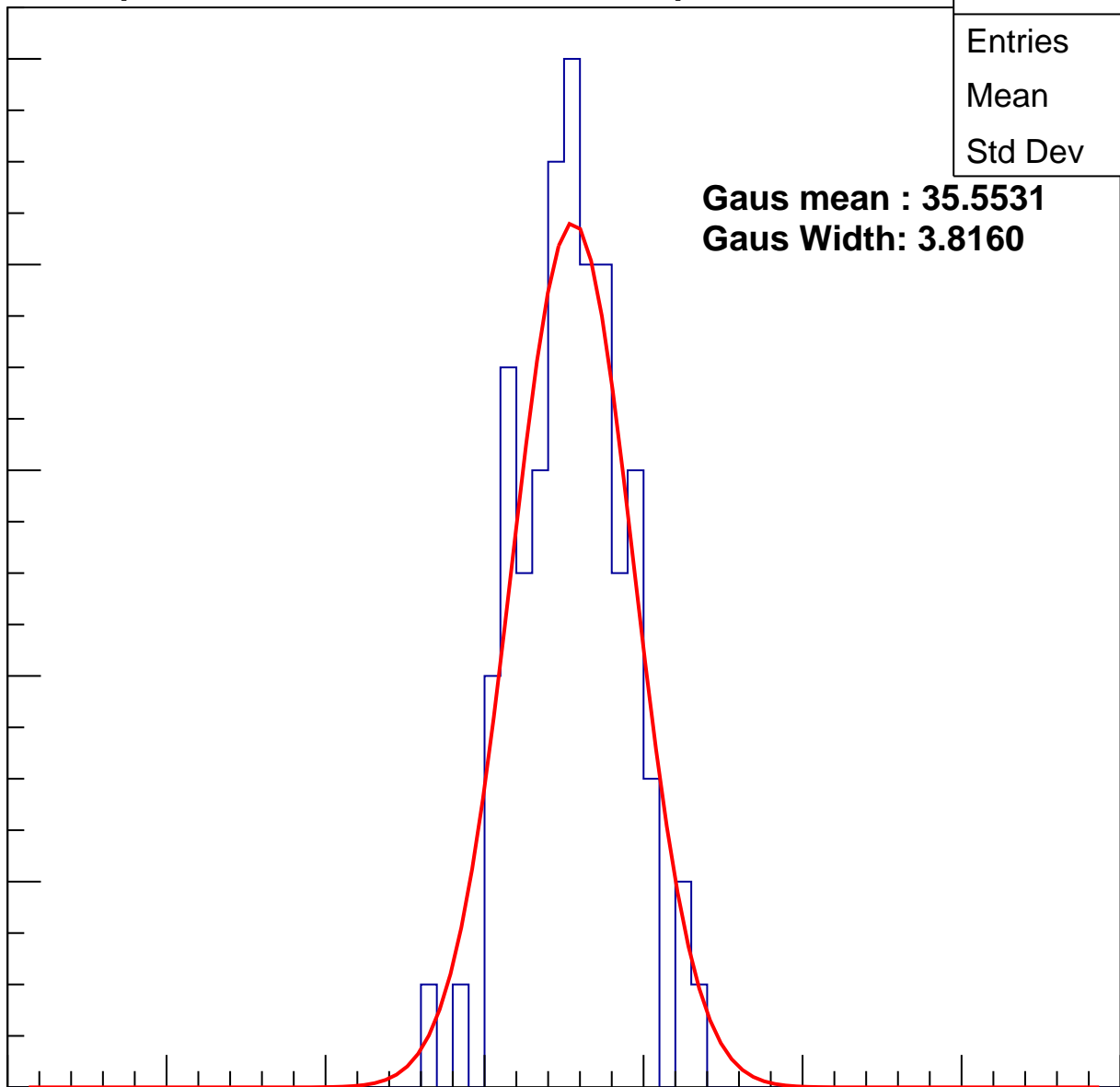
**Gaus Width: 3.8160**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch72, adc2

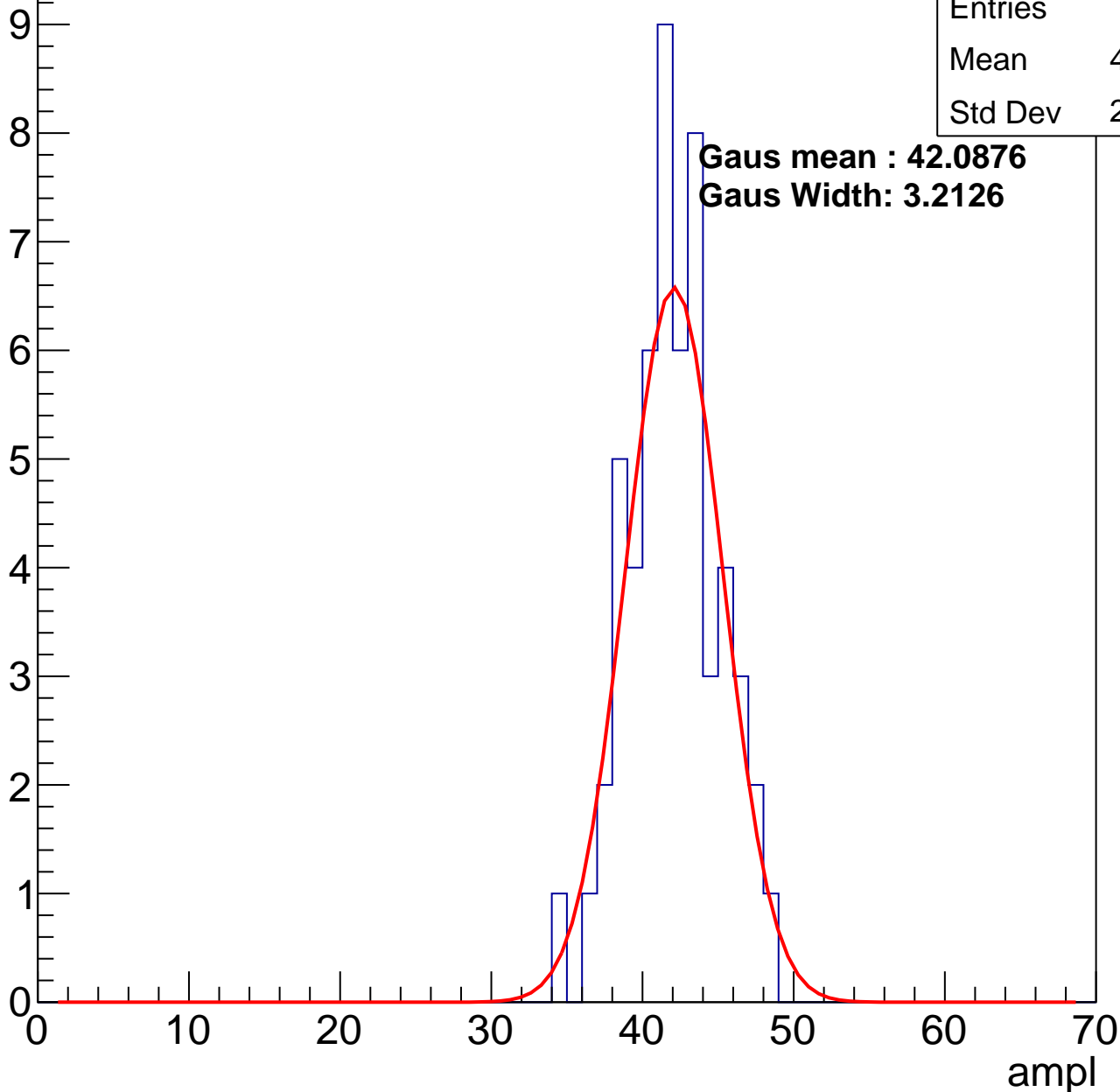
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.58
Std Dev	2.952

**Gaus mean : 42.0876**

**Gaus Width: 3.2126**

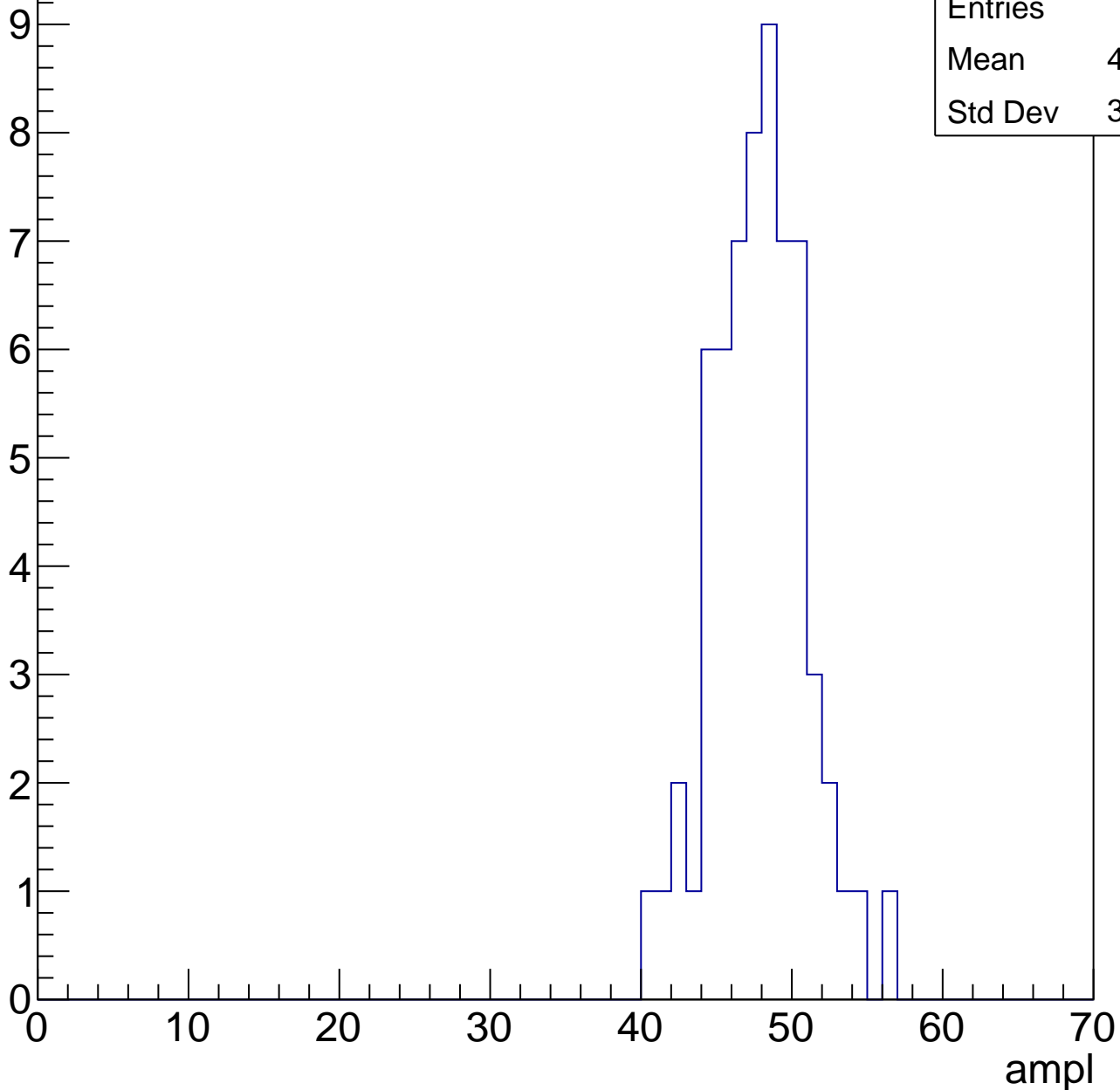


# B1L000S, U23-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

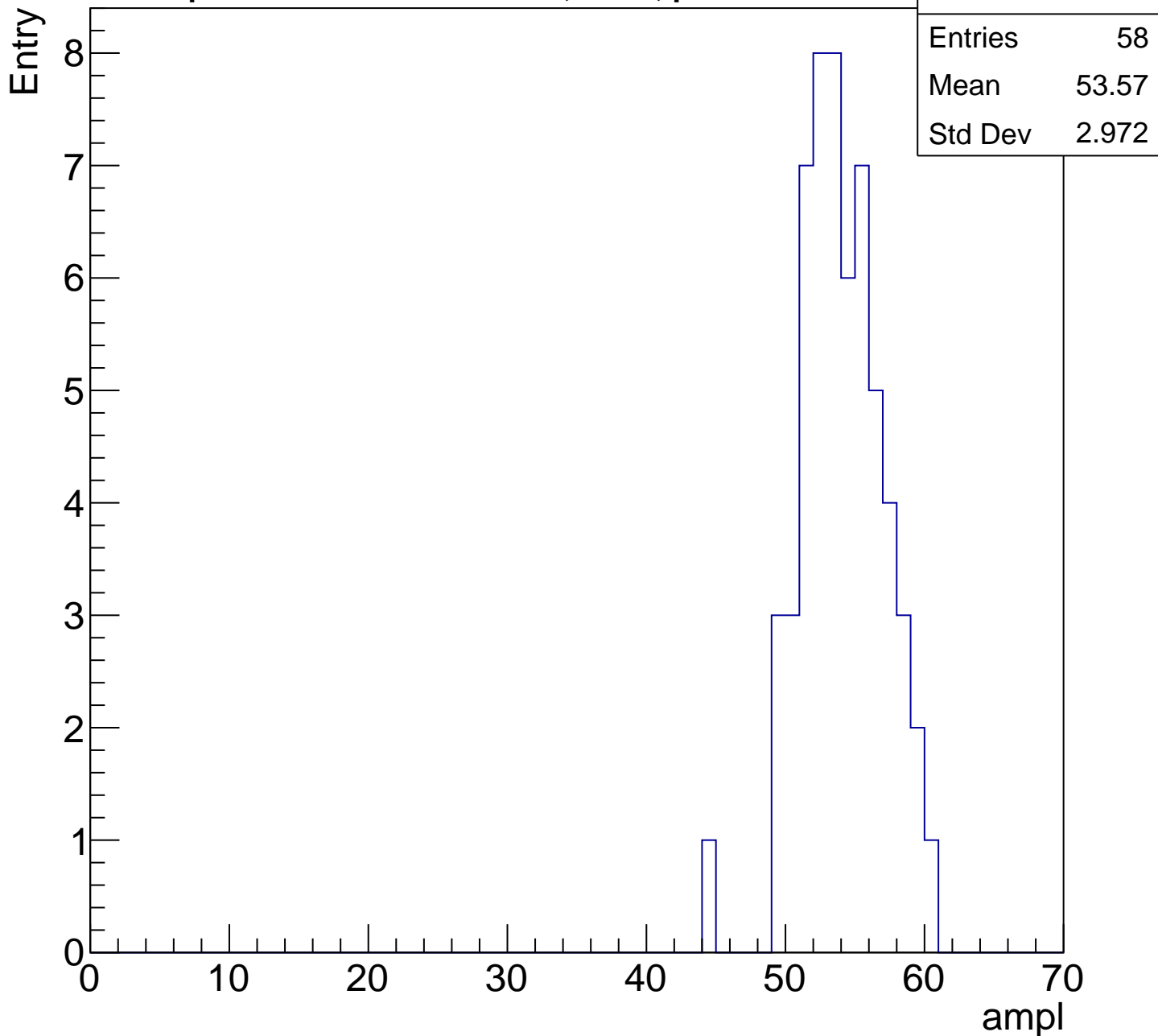
Entry

Entries	63
Mean	47.38
Std Dev	3.062



# B1L000S, U23-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch72, adc5

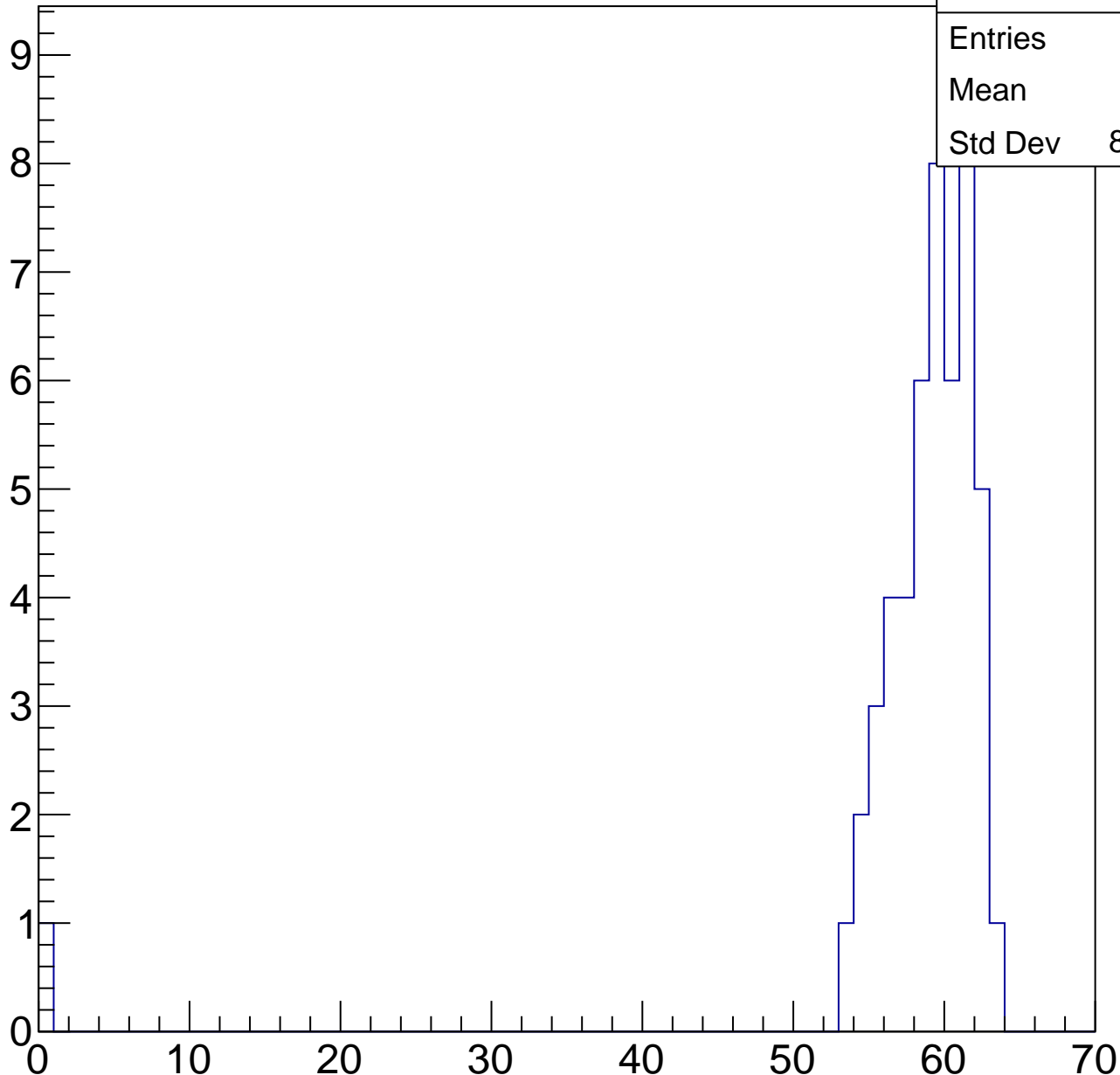
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	57.6
Std Dev	8.577

ampl

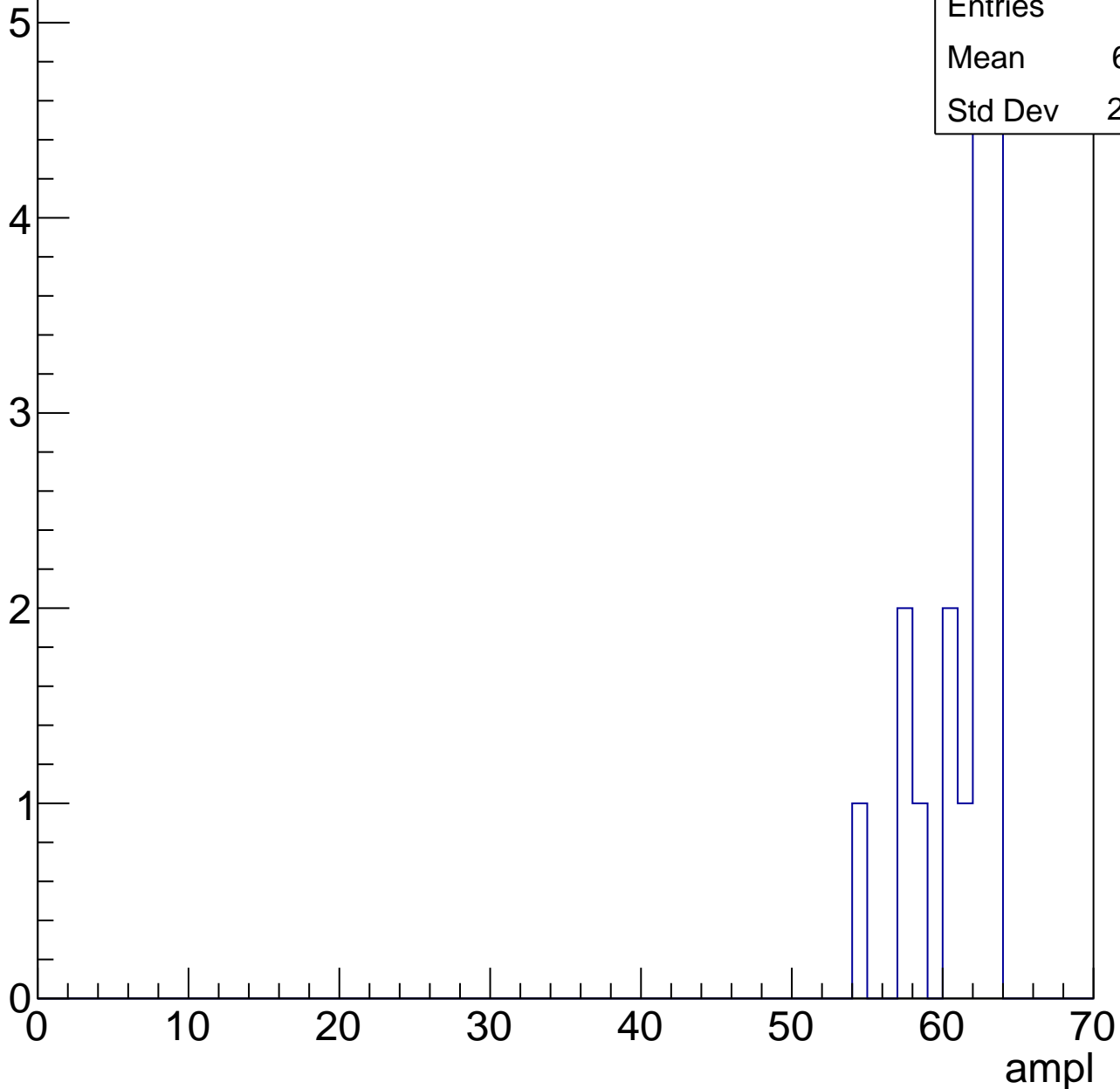


# B1L000S, U23-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	60.71
Std Dev	2.607

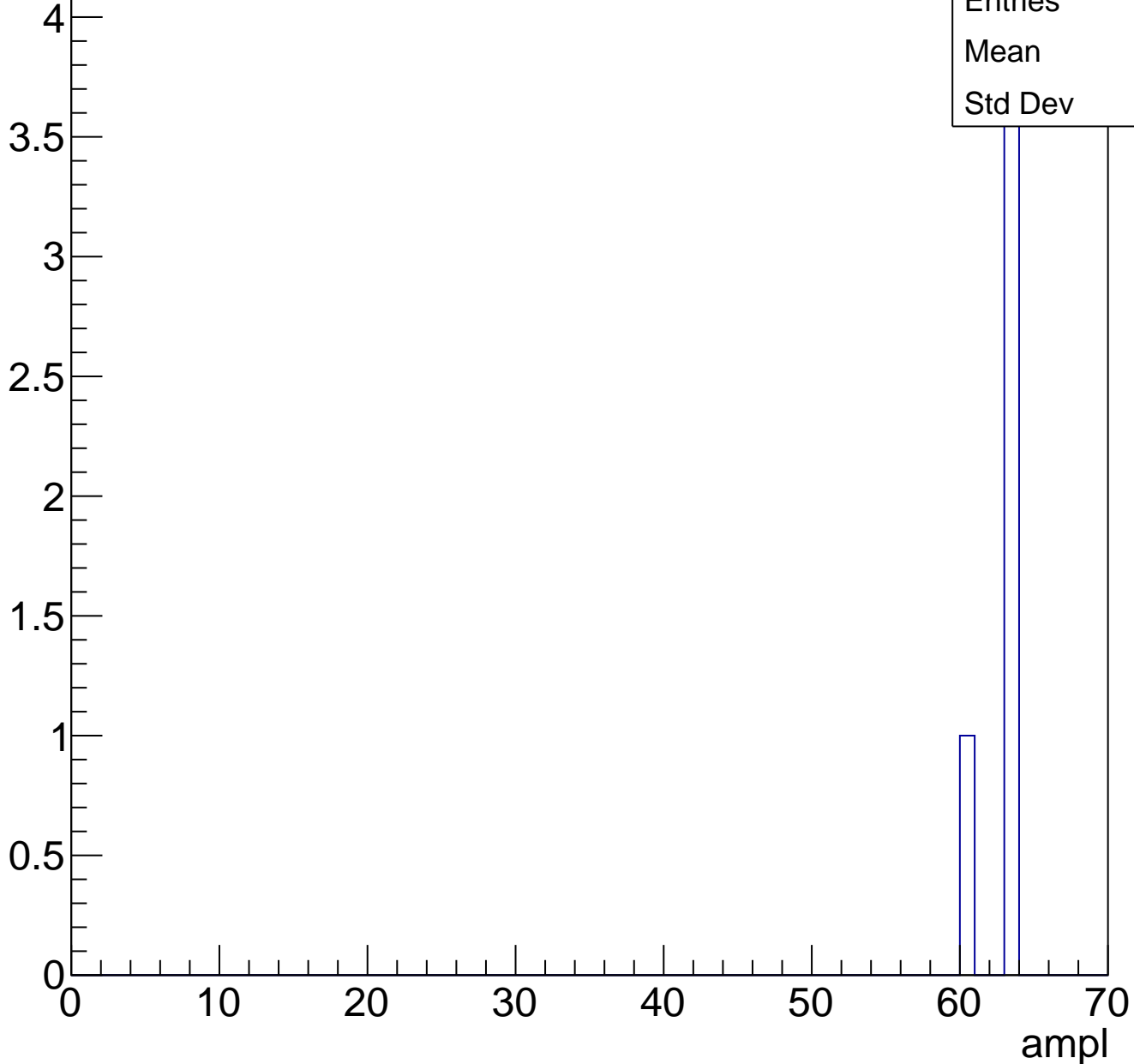




# B1L000S, U23-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch73, adc0

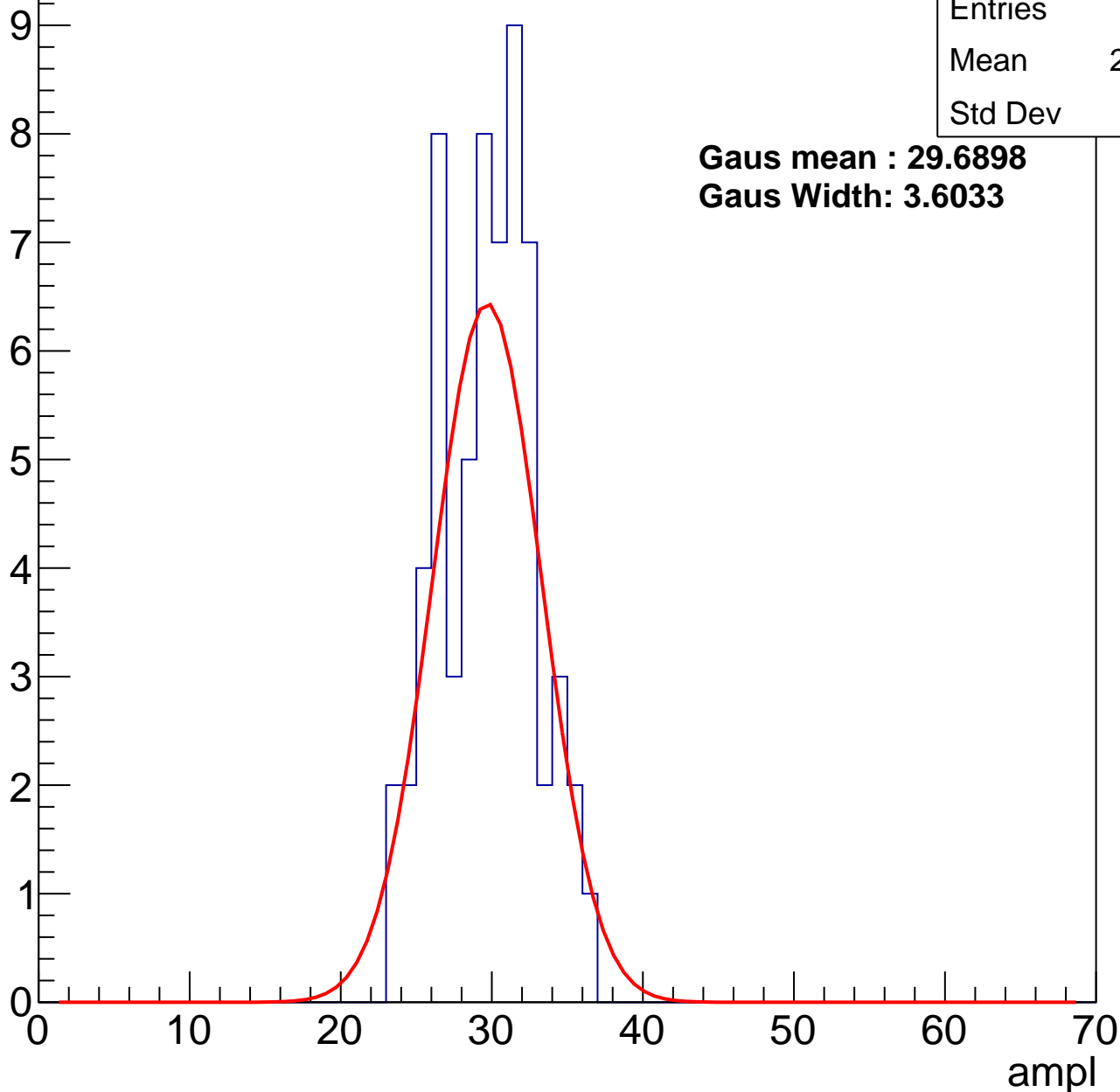
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.24
Std Dev	3.1

**Gaus mean : 29.6898**

**Gaus Width: 3.6033**



# B1L000S, U23-ch73, adc1

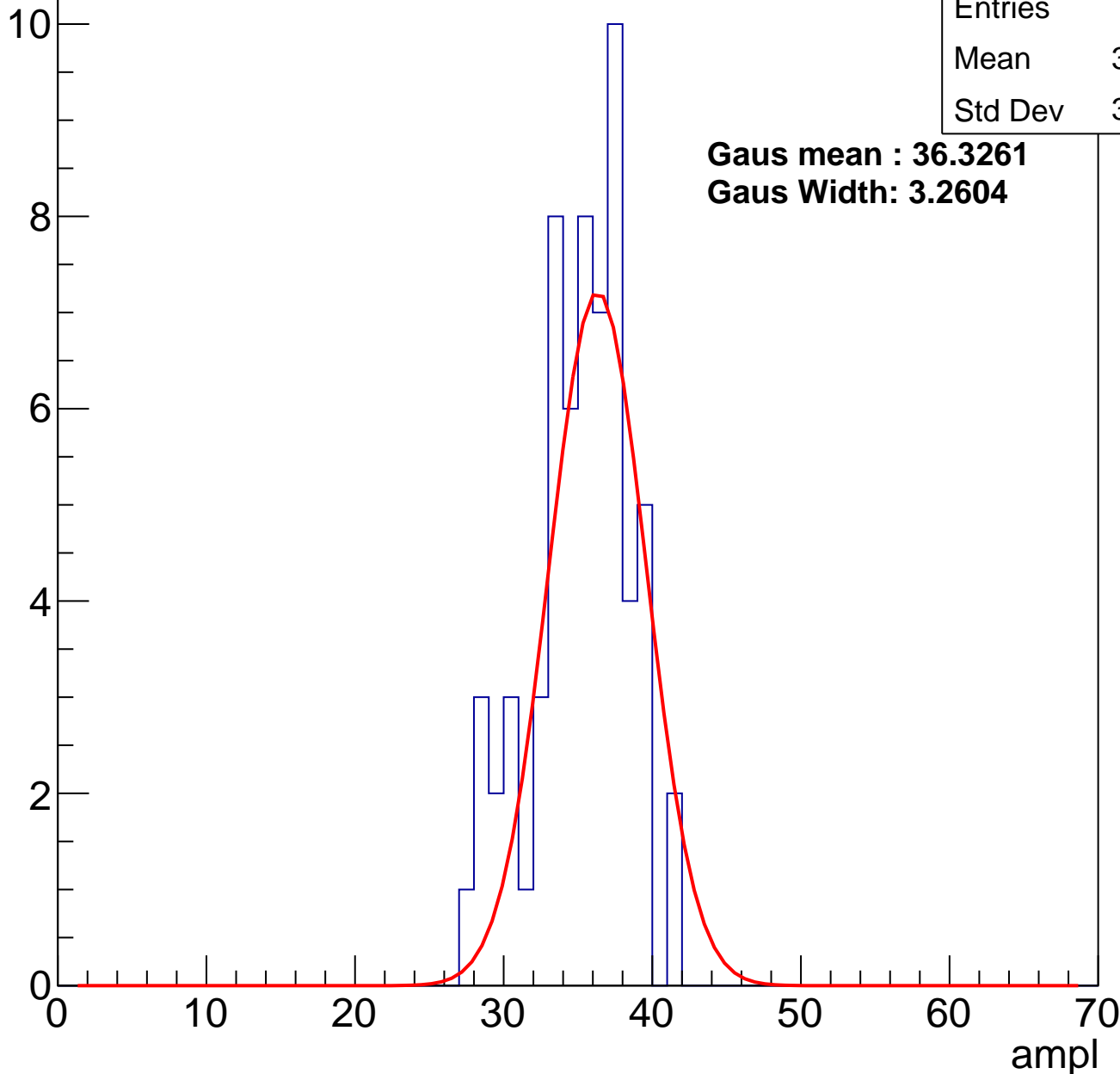
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	34.68
Std Dev	3.255

**Gaus mean : 36.3261**

**Gaus Width: 3.2604**

Entry



# B1L000S, U23-ch73, adc2

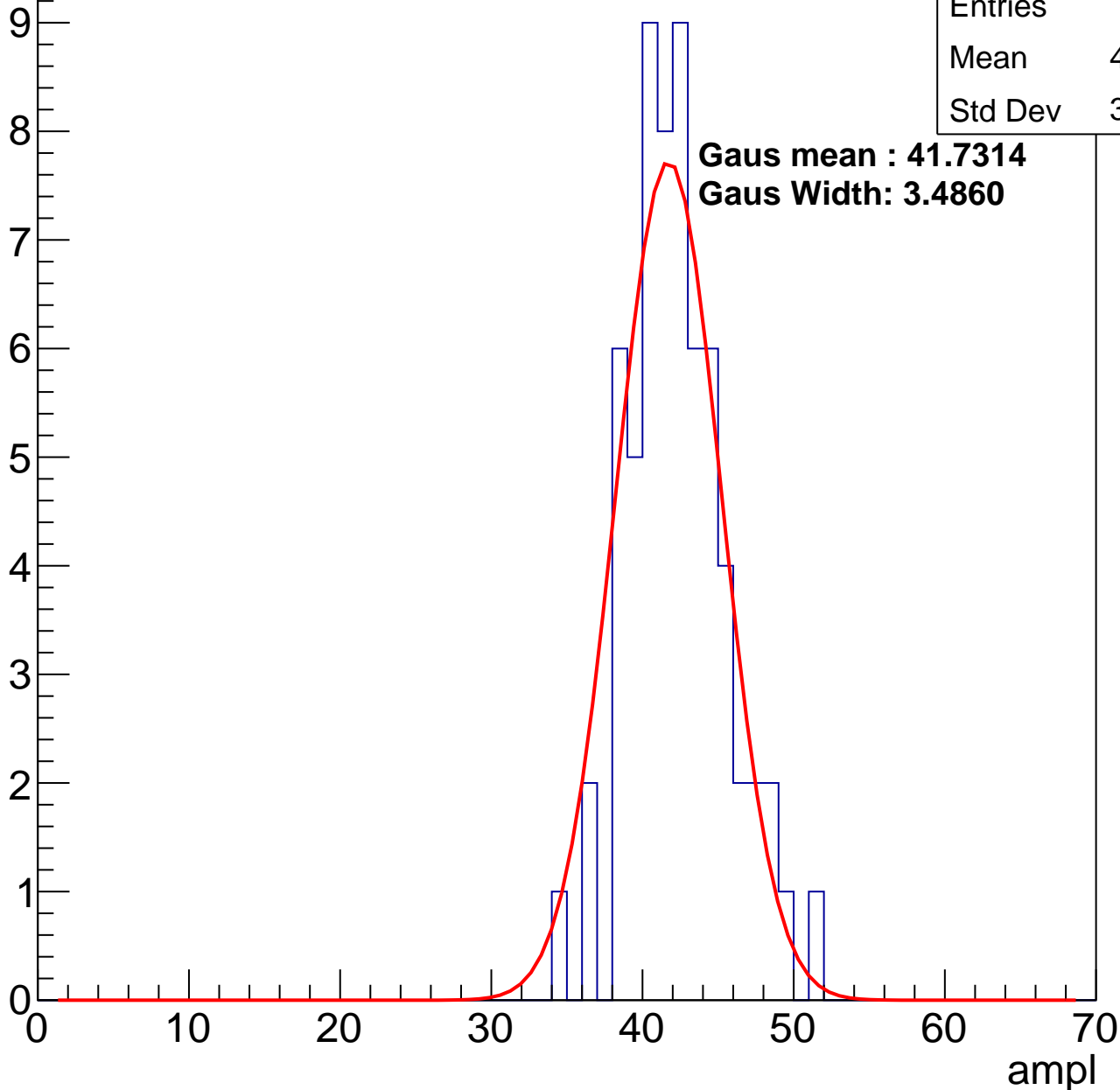
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.86
Std Dev	3.235

**Gaus mean : 41.7314**

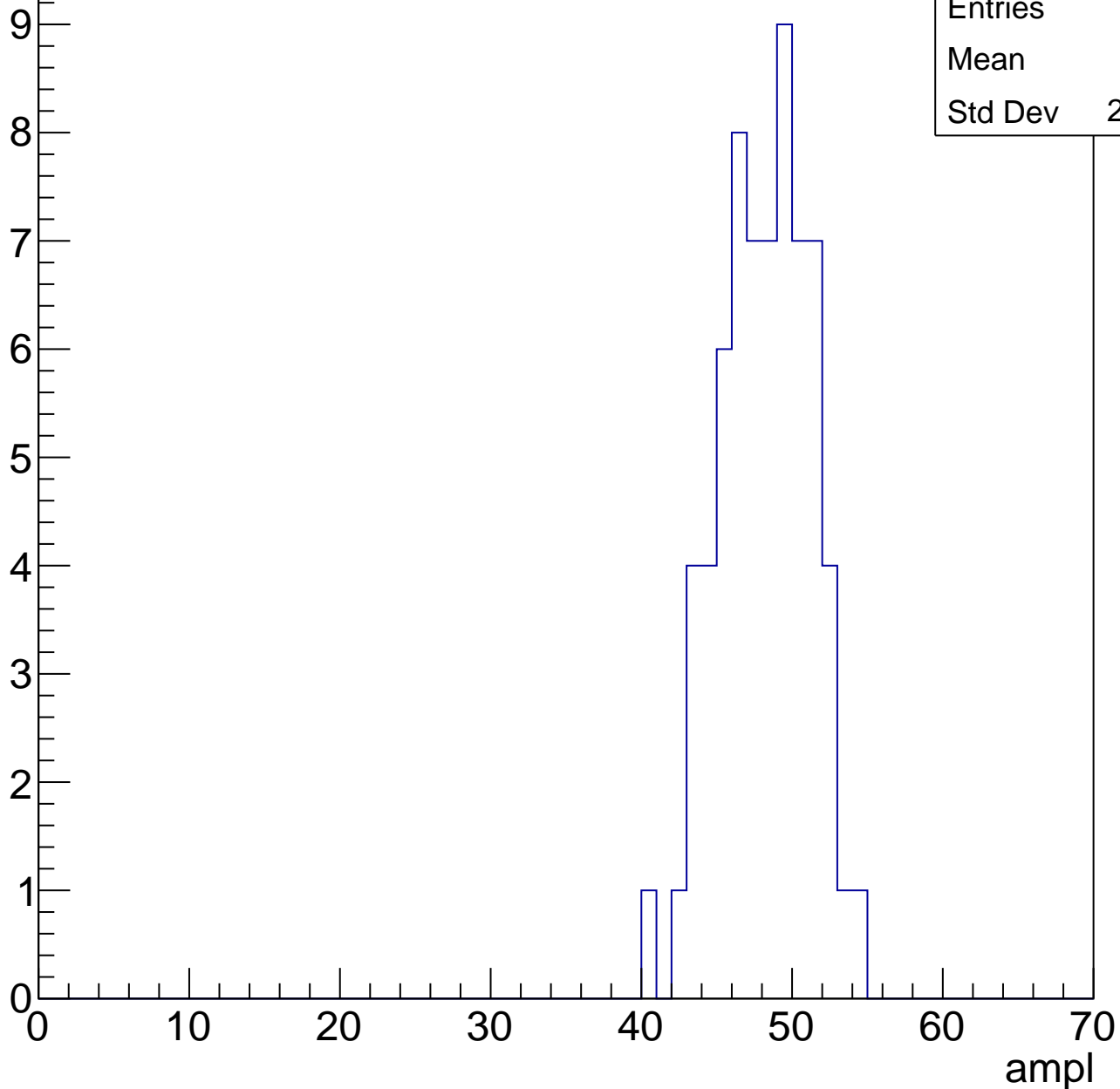
**Gaus Width: 3.4860**



# B1L000S, U23-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

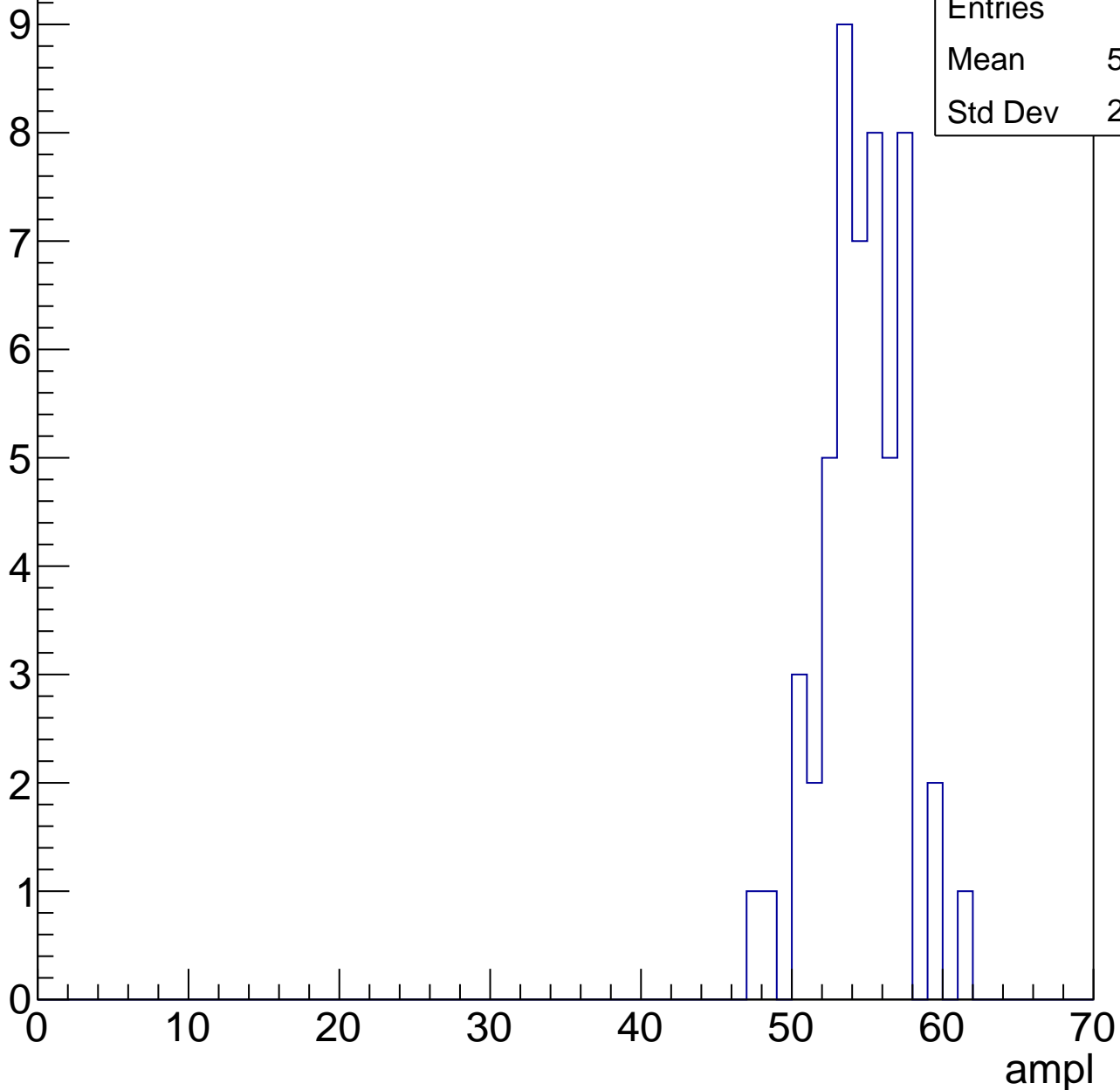


# B1L000S, U23-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	54.17
Std Dev	2.694

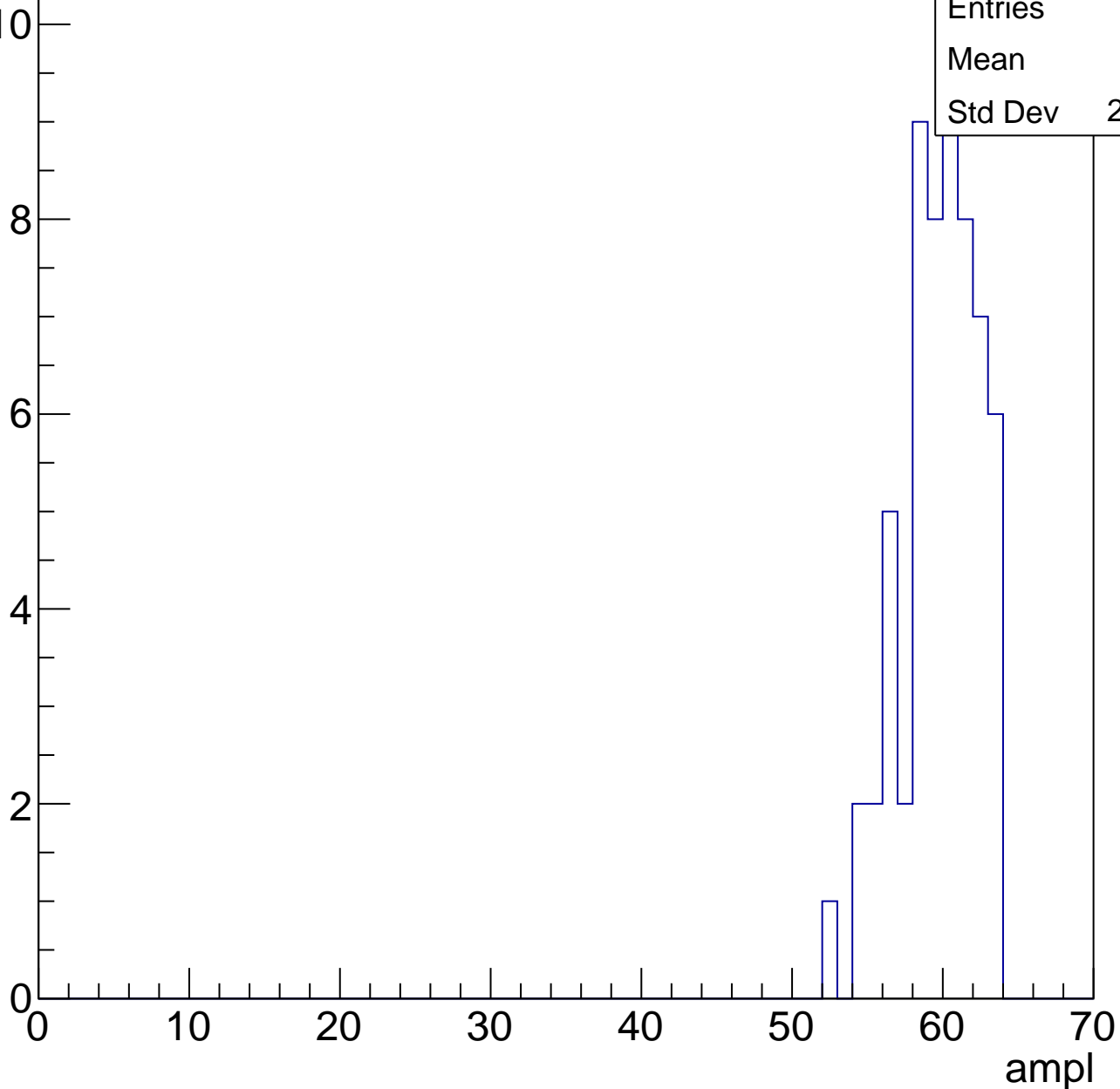


# B1L000S, U23-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

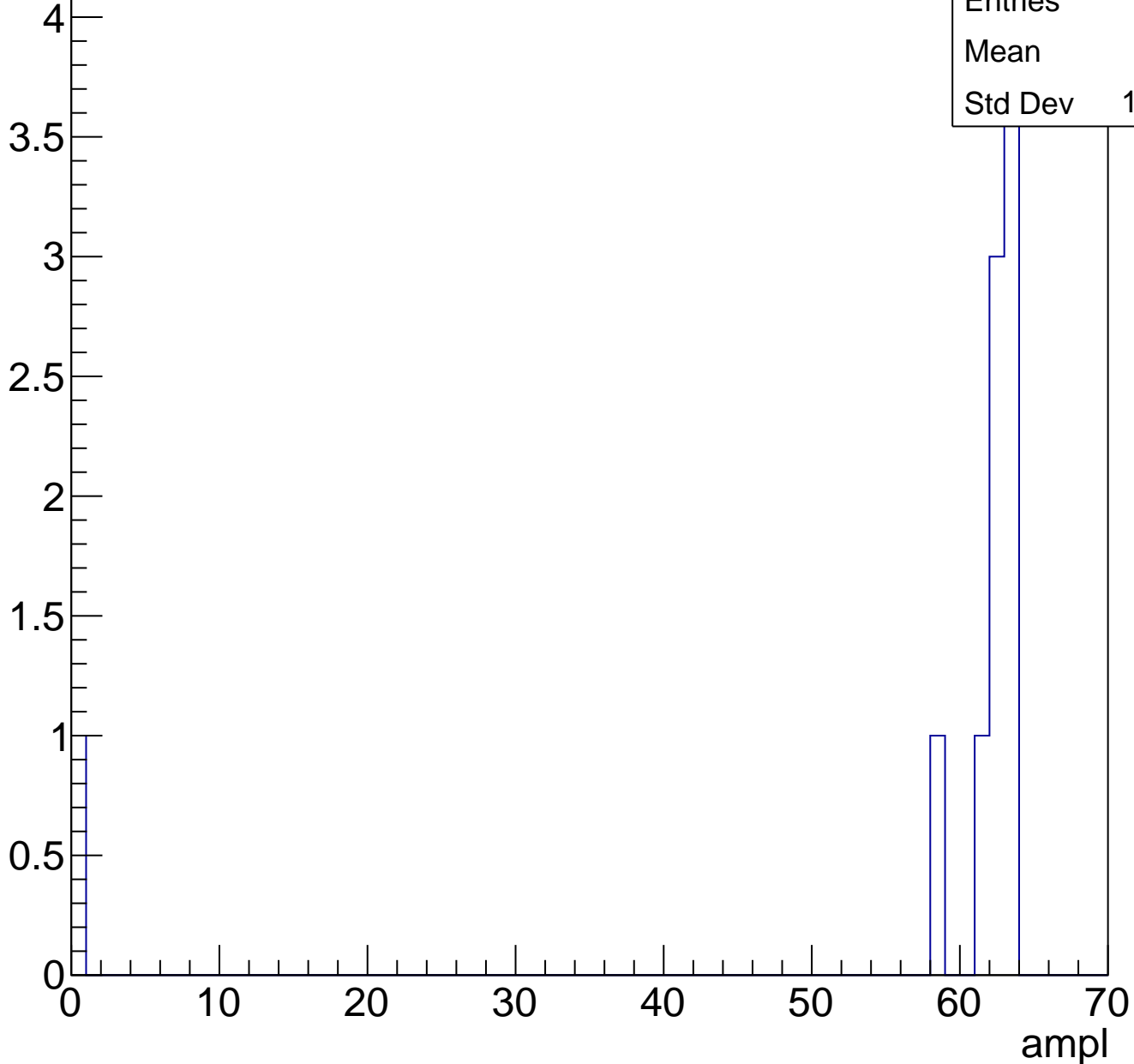
Entries	60
Mean	59.3
Std Dev	2.545



# B1L000S, U23-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U23-ch74, adc0

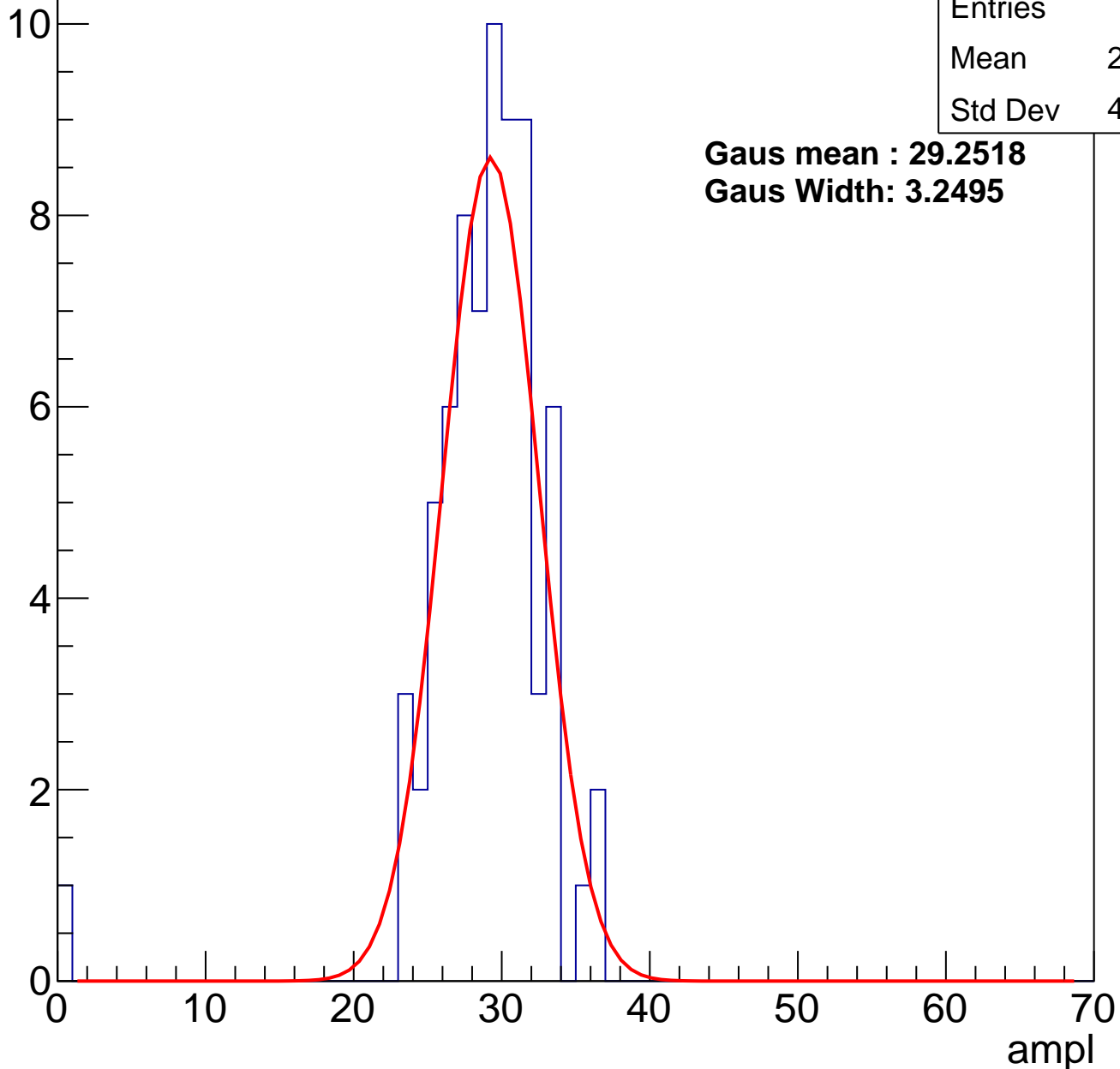
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	28.47
Std Dev	4.494

**Gaus mean : 29.2518**

**Gaus Width: 3.2495**

Entry



# B1L000S, U23-ch74, adc1

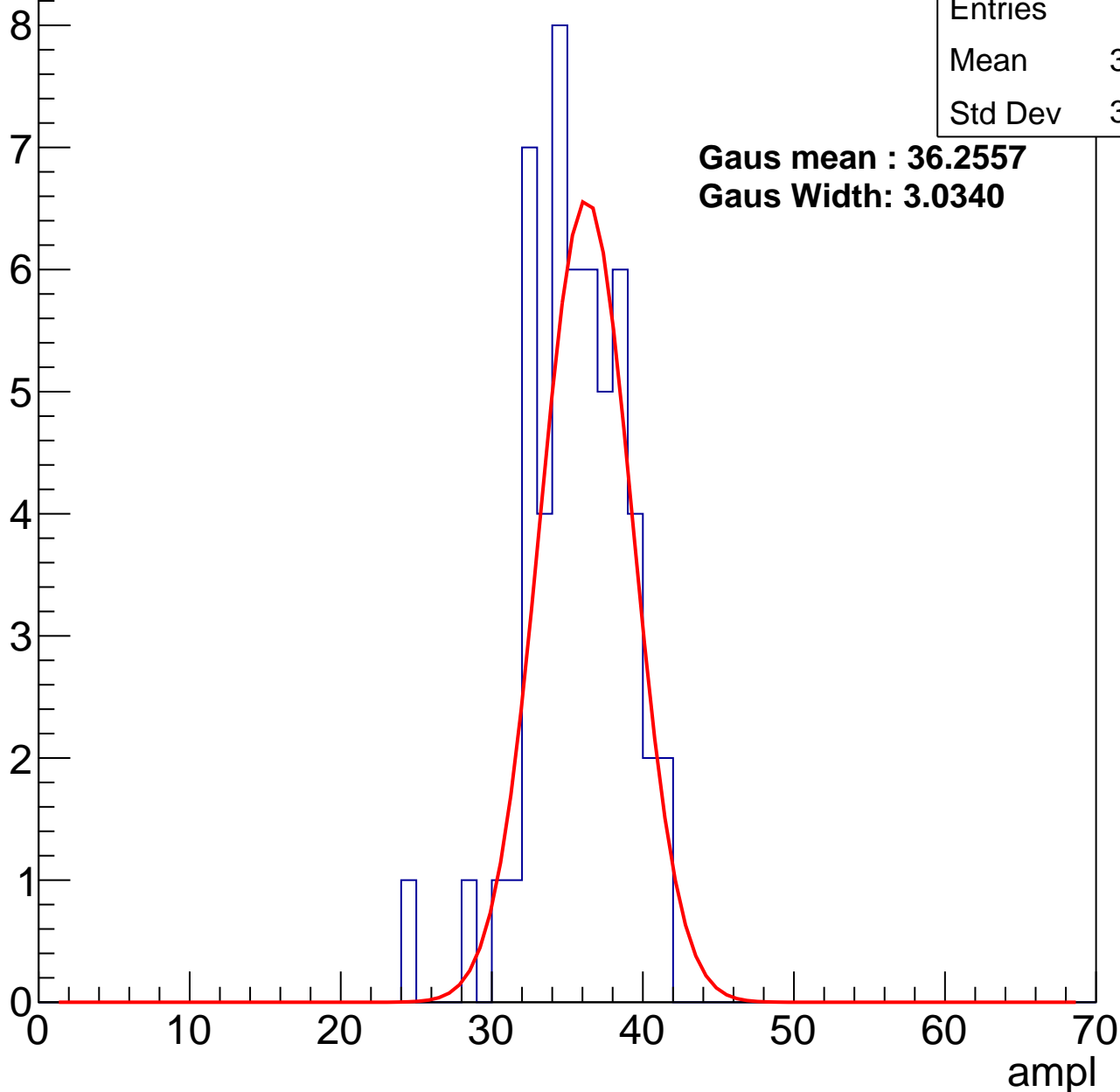
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35.15
Std Dev	3.228

**Gaus mean : 36.2557**

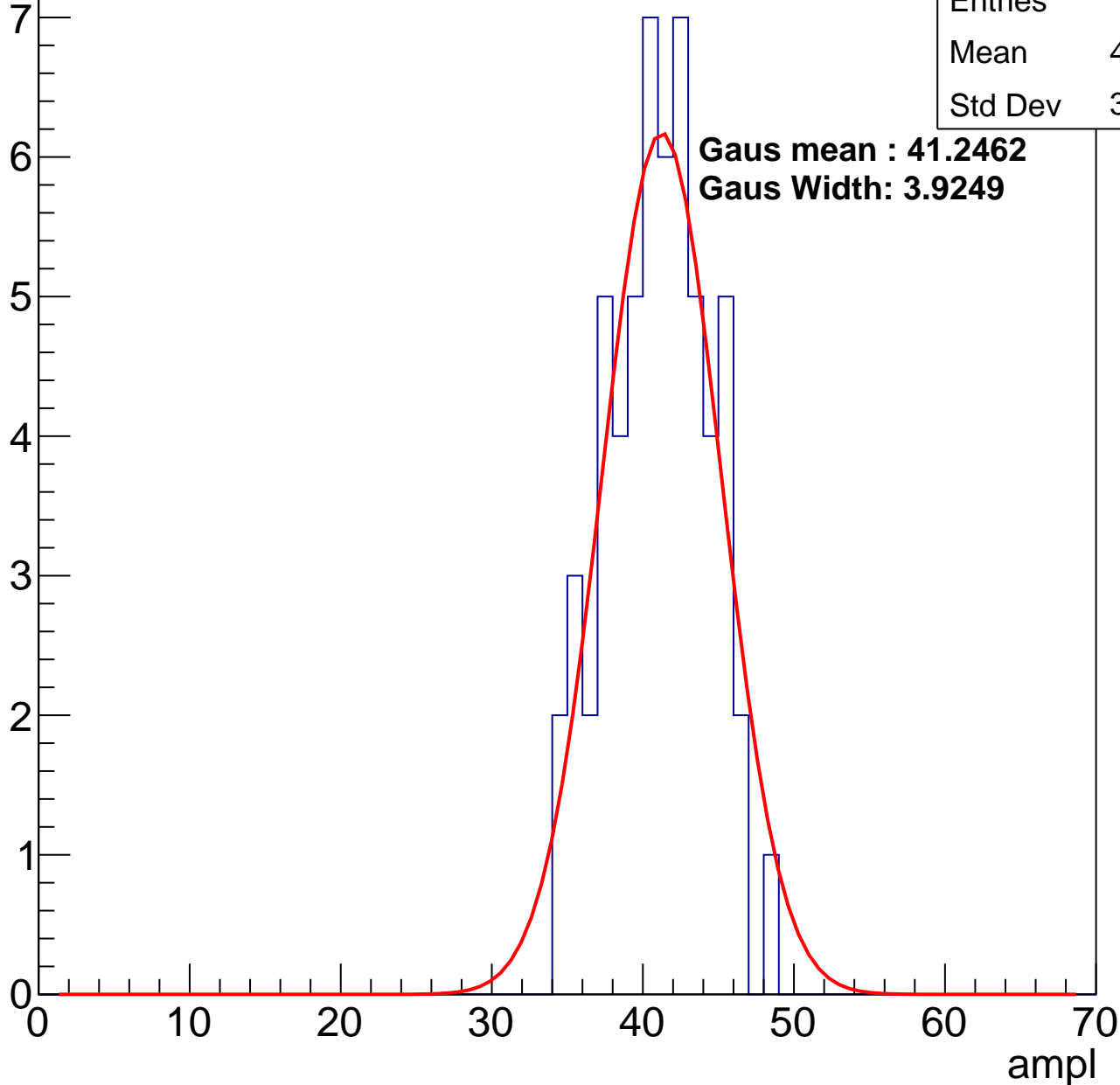
**Gaus Width: 3.0340**



# B1L000S, U23-ch74, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

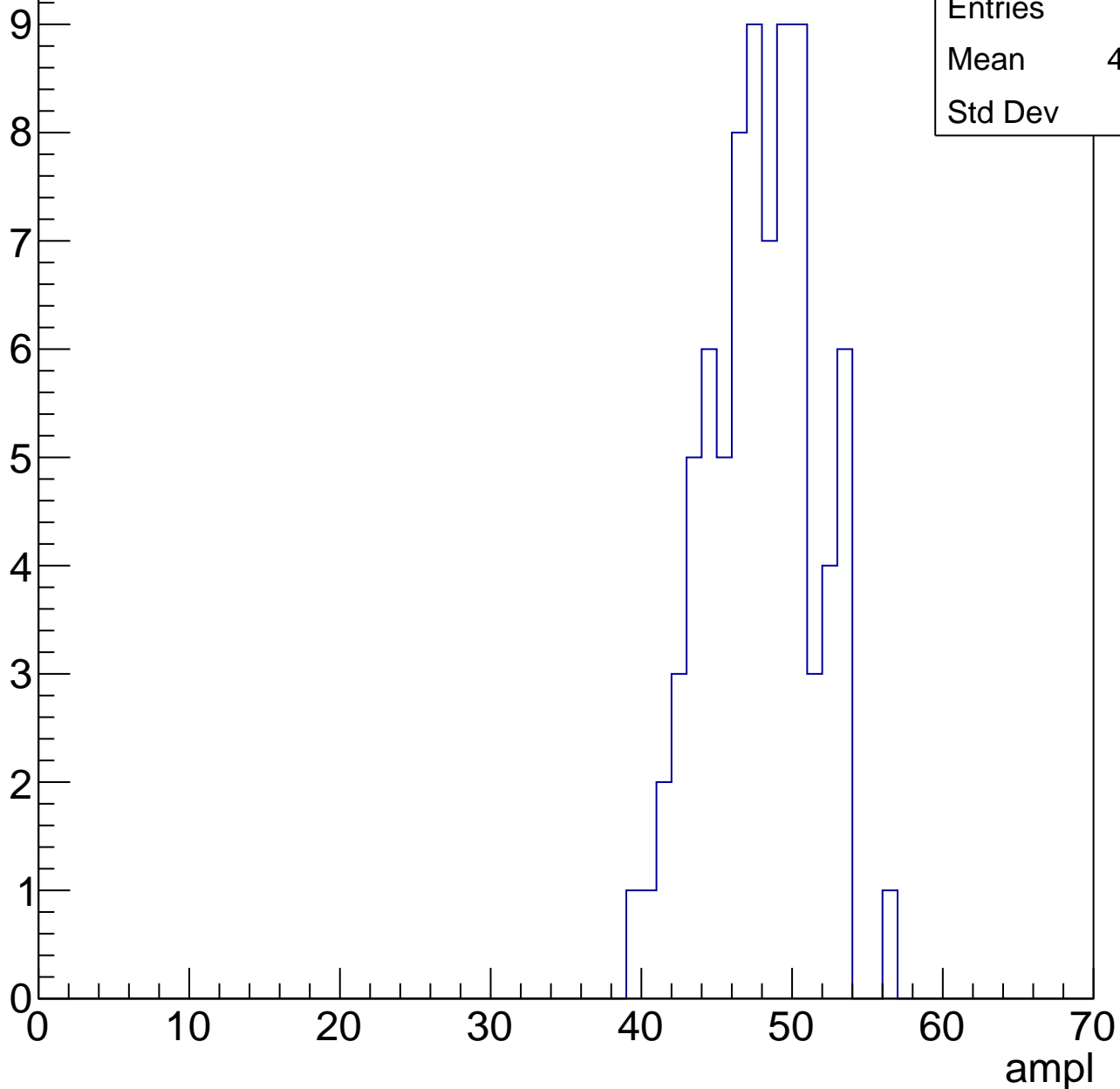
Entry



# B1L000S, U23-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

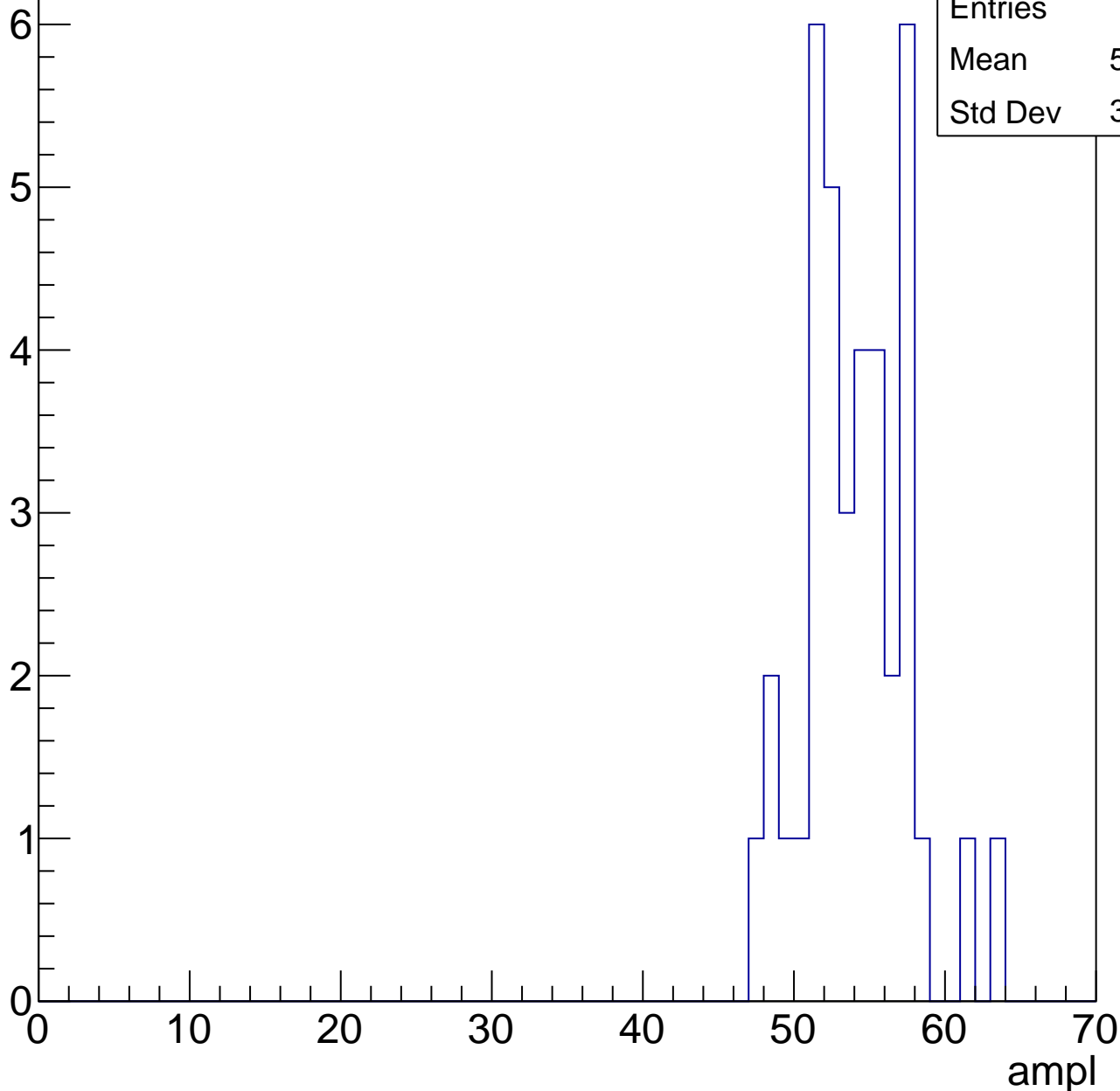


# B1L000S, U23-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	53.66
Std Dev	3.428

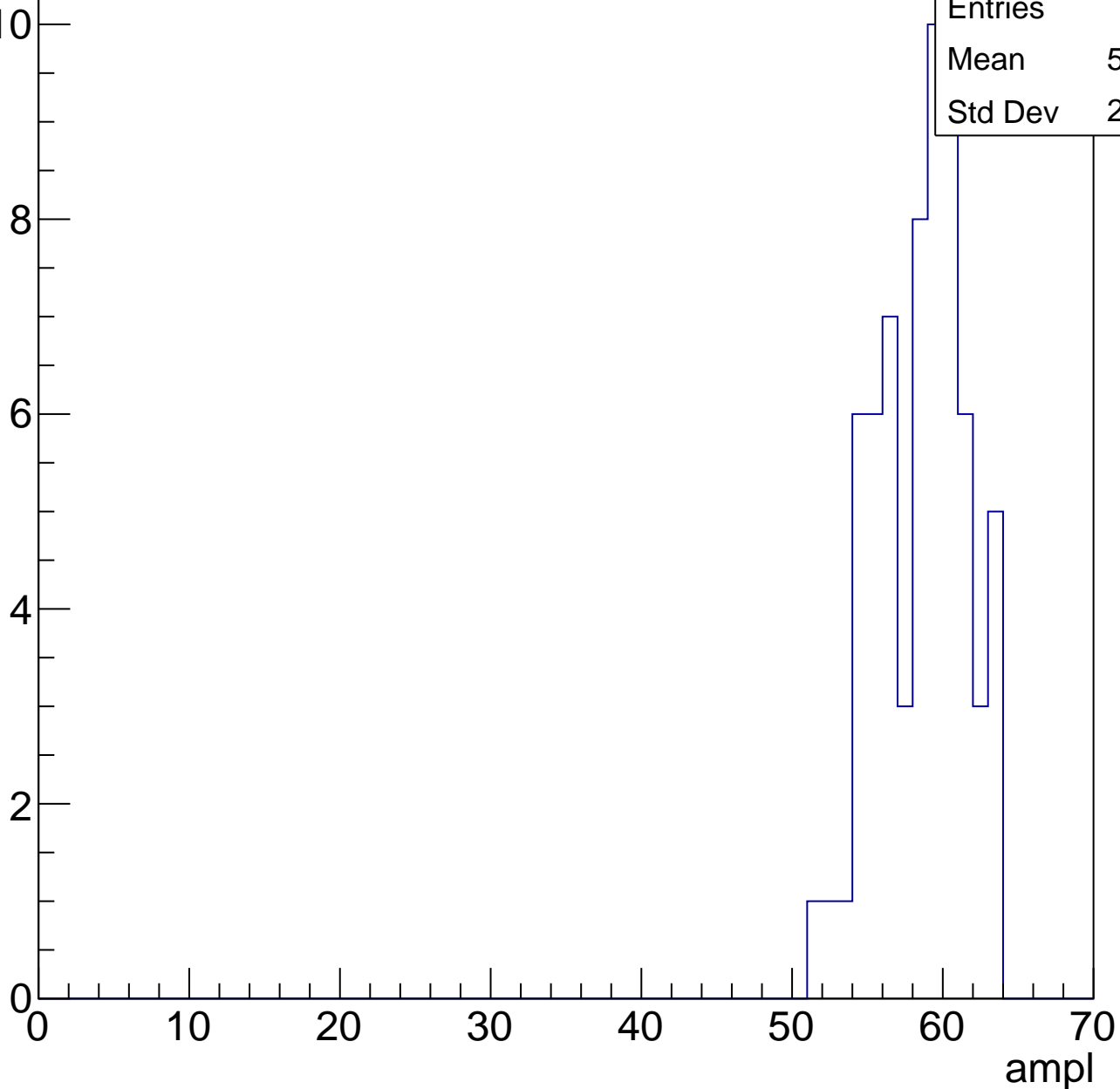


# B1L000S, U23-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	58.12
Std Dev	2.909

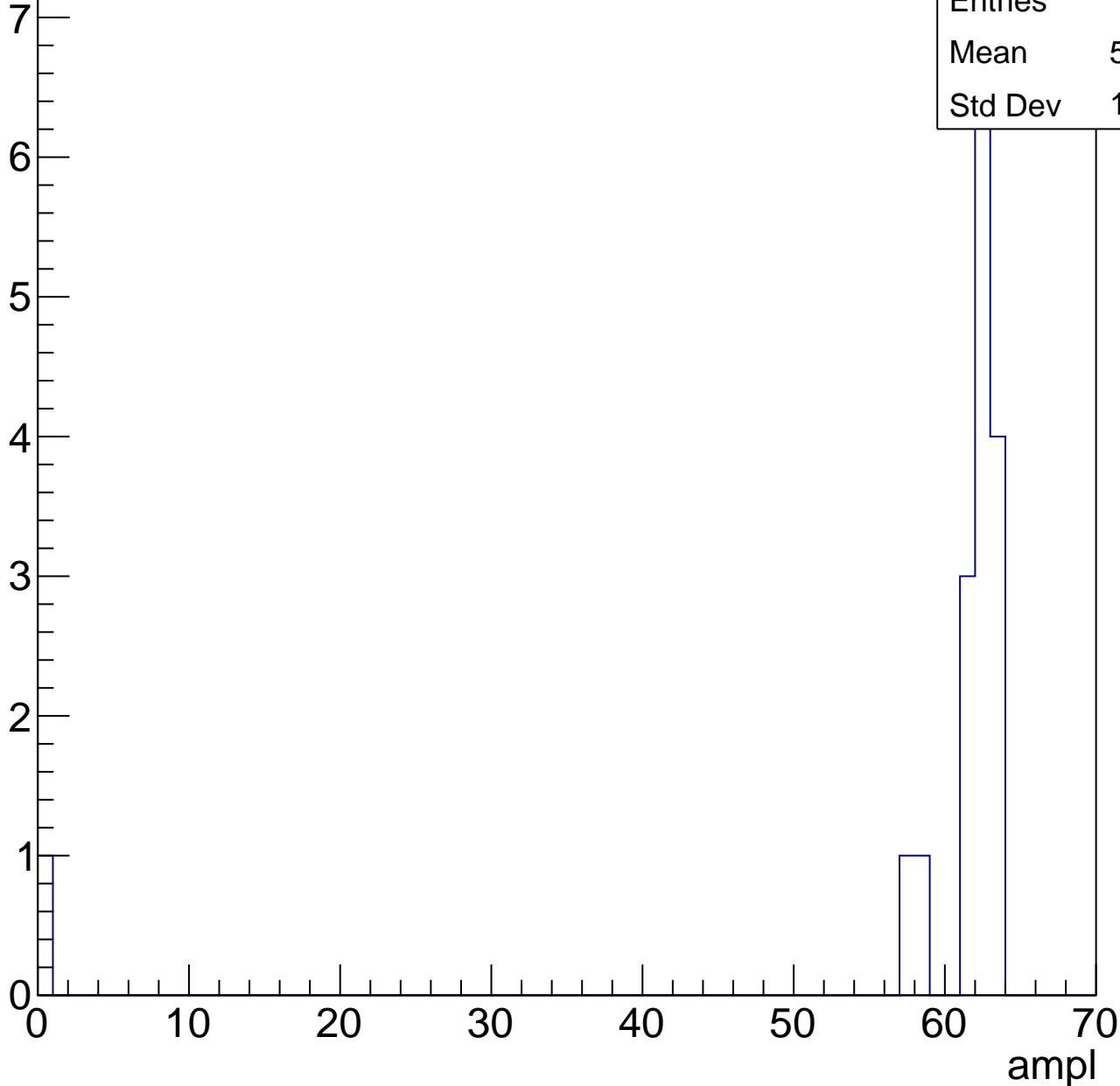


# B1L000S, U23-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.88
Std Dev	14.56





# B1L000S, U23-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U23-ch75, adc0

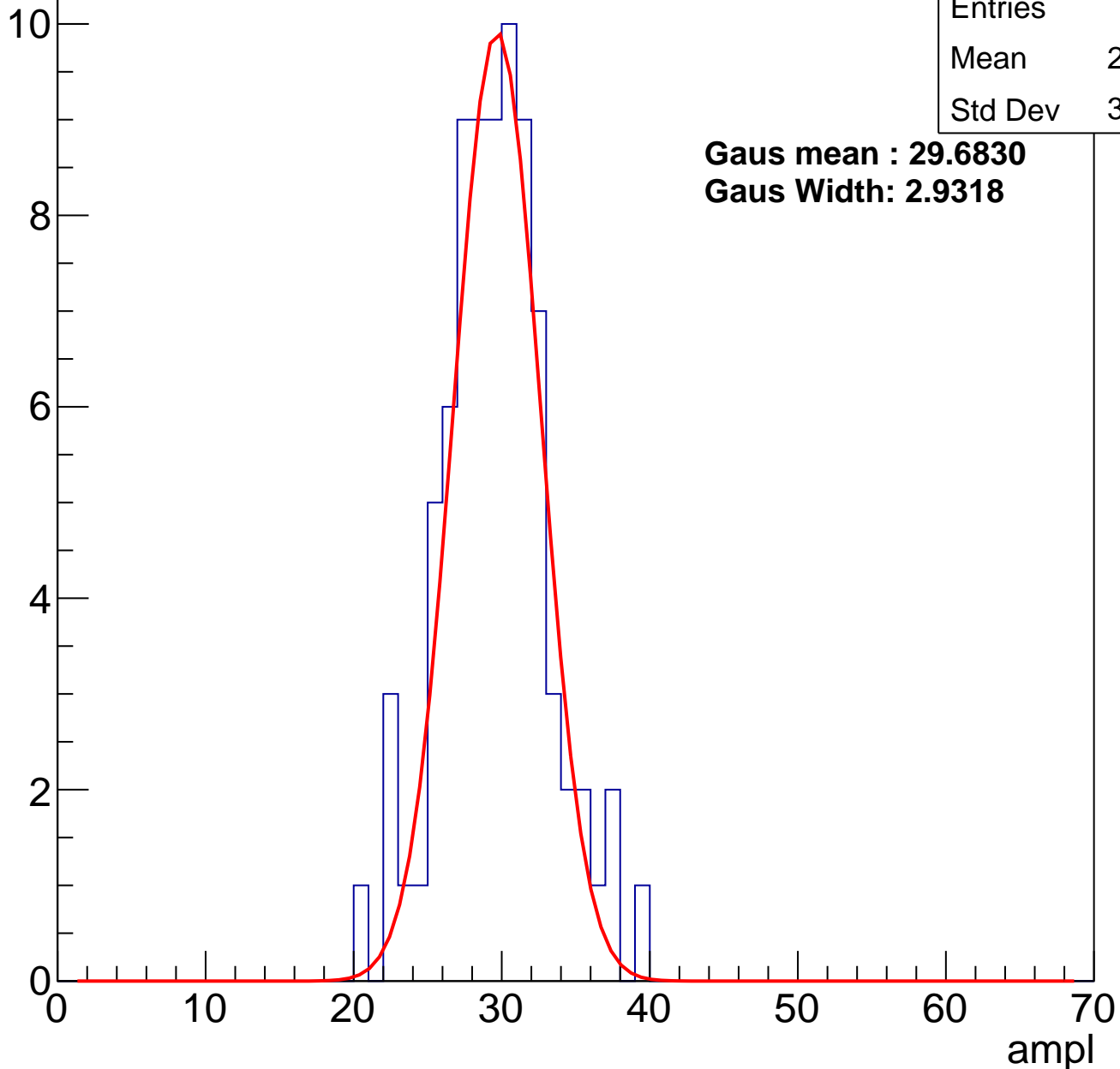
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	29.12
Std Dev	3.553

**Gaus mean : 29.6830**

**Gaus Width: 2.9318**

Entry



# B1L000S, U23-ch75, adc1

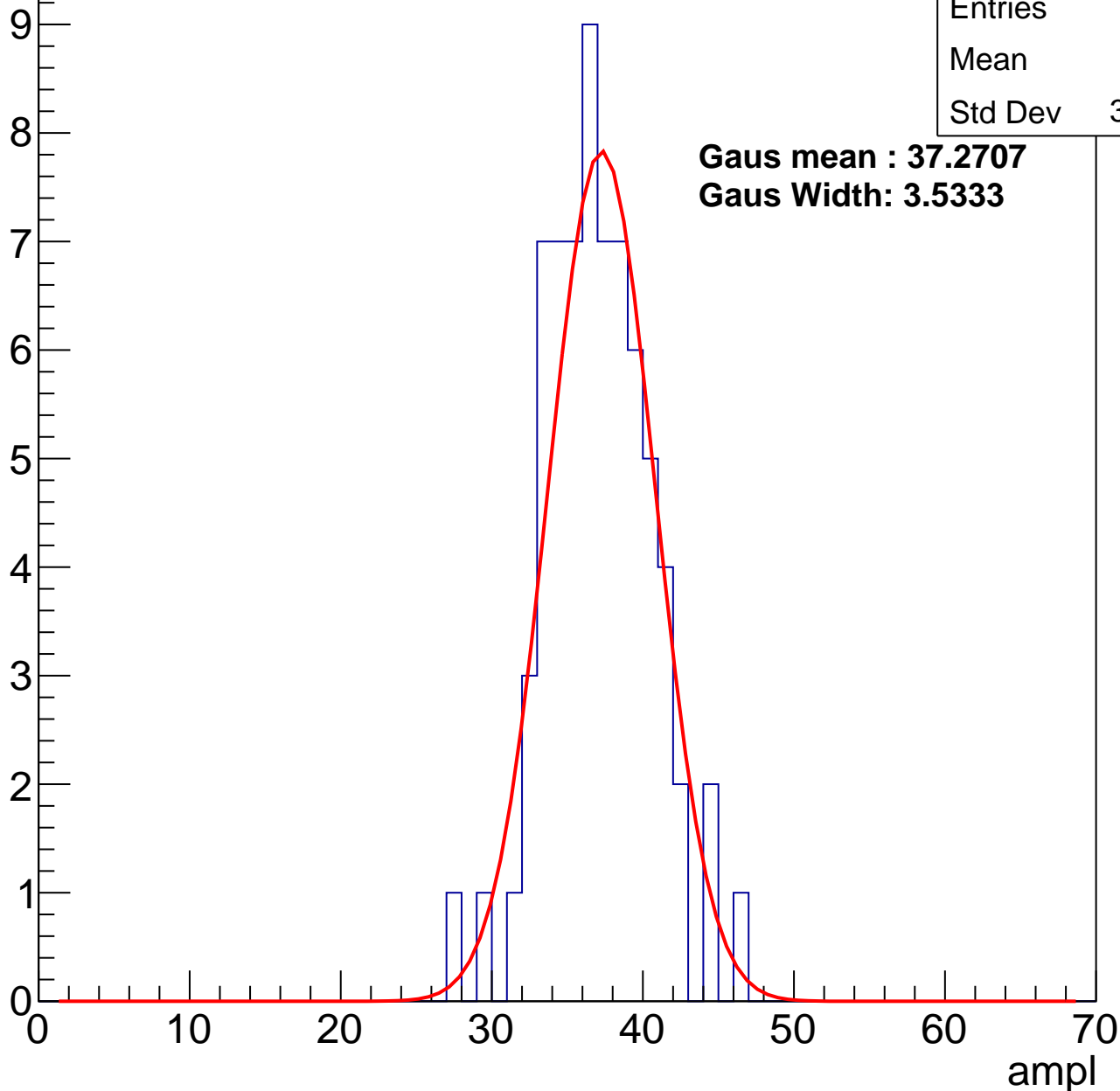
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.6
Std Dev	3.474

**Gaus mean : 37.2707**

**Gaus Width: 3.5333**



# B1L000S, U23-ch75, adc2

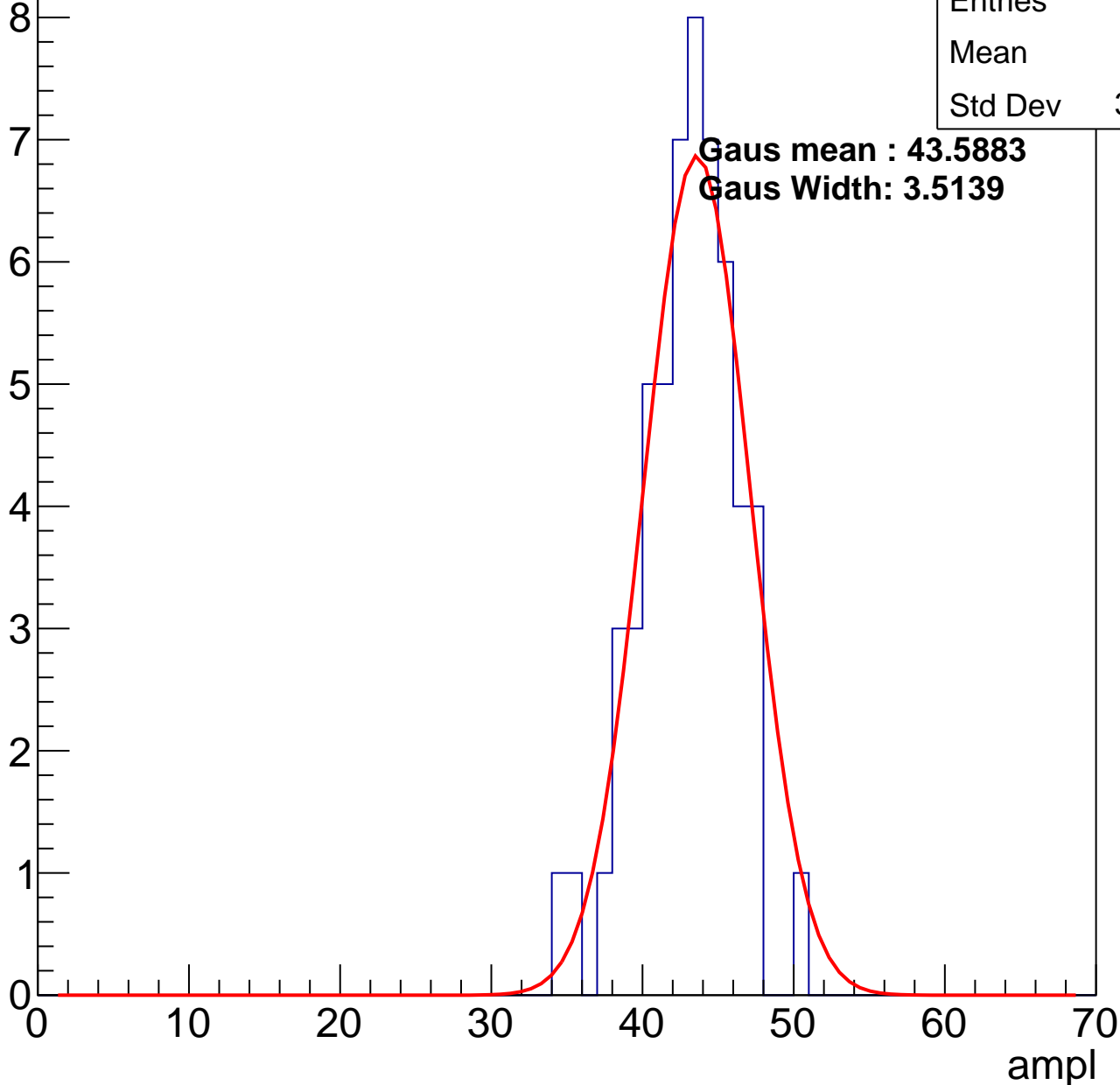
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.5
Std Dev	3.111

**Gaus mean : 43.5883**

**Gaus Width: 3.5139**



# B1L000S, U23-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

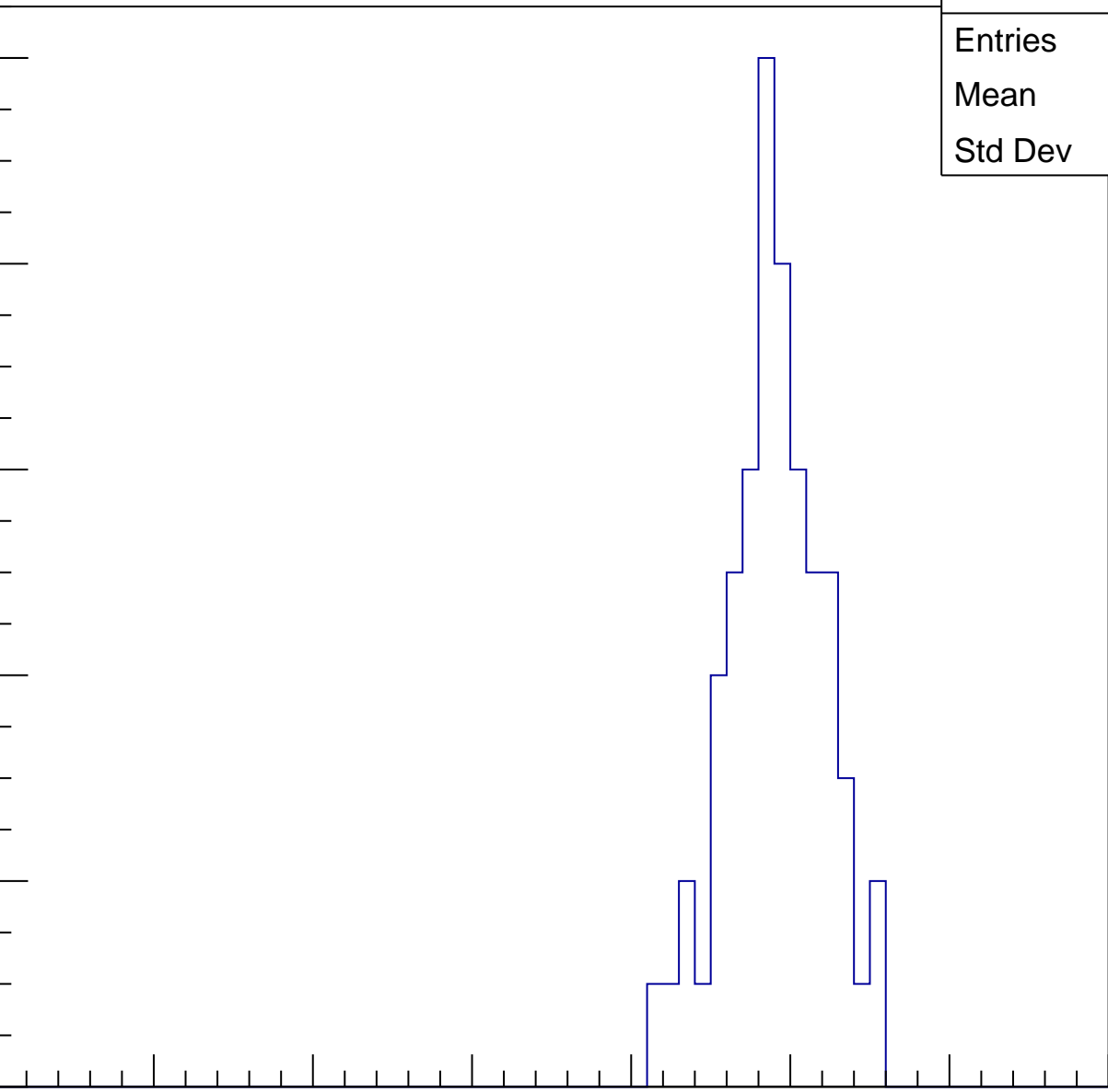
Entries	60
Mean	48.58
Std Dev	3.051

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

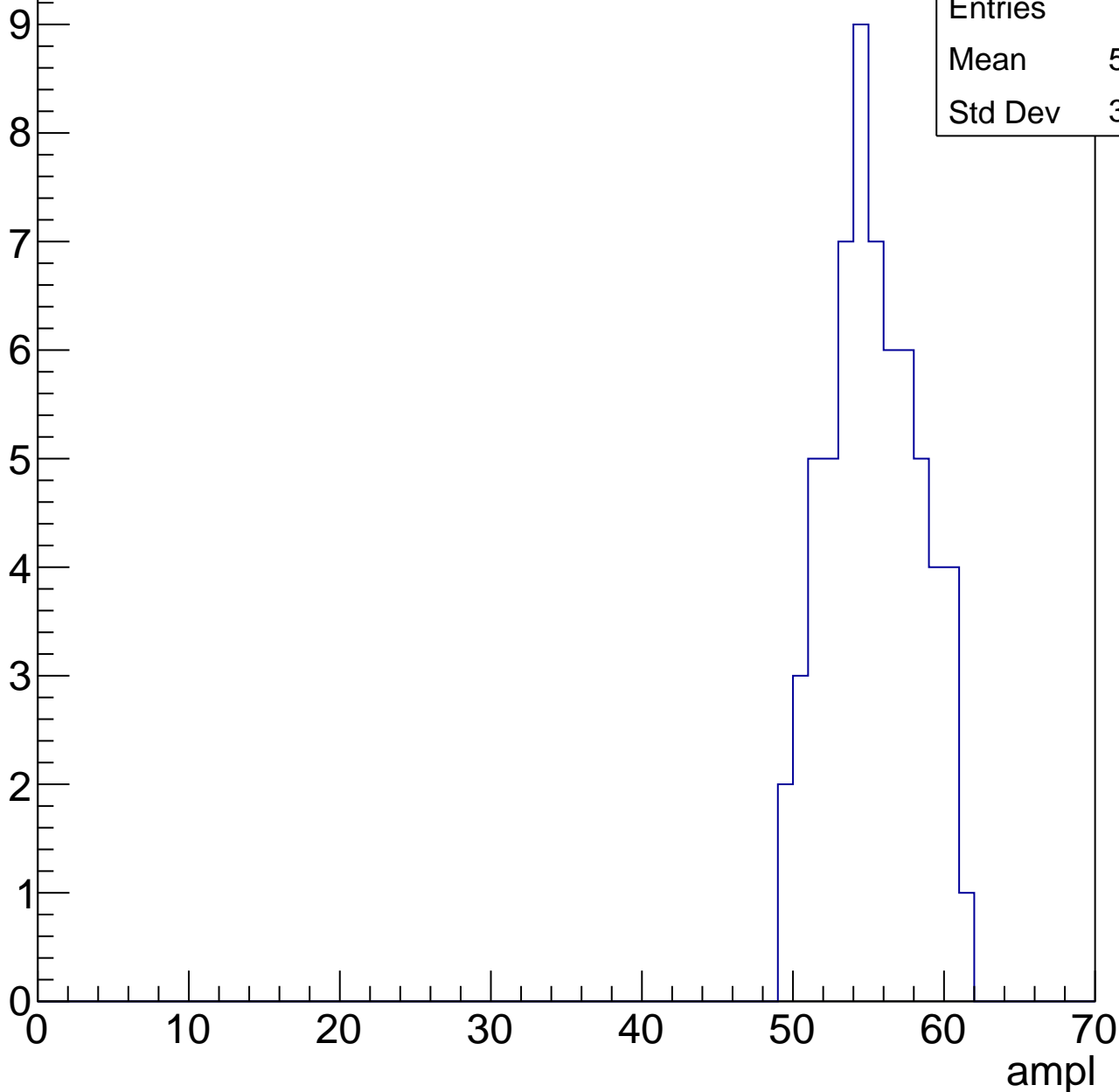


# B1L000S, U23-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	54.84
Std Dev	3.017



# B1L000S, U23-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries

40

Mean

59.5

Std Dev

2.225

ampl

0

10

20

30

40

50

60

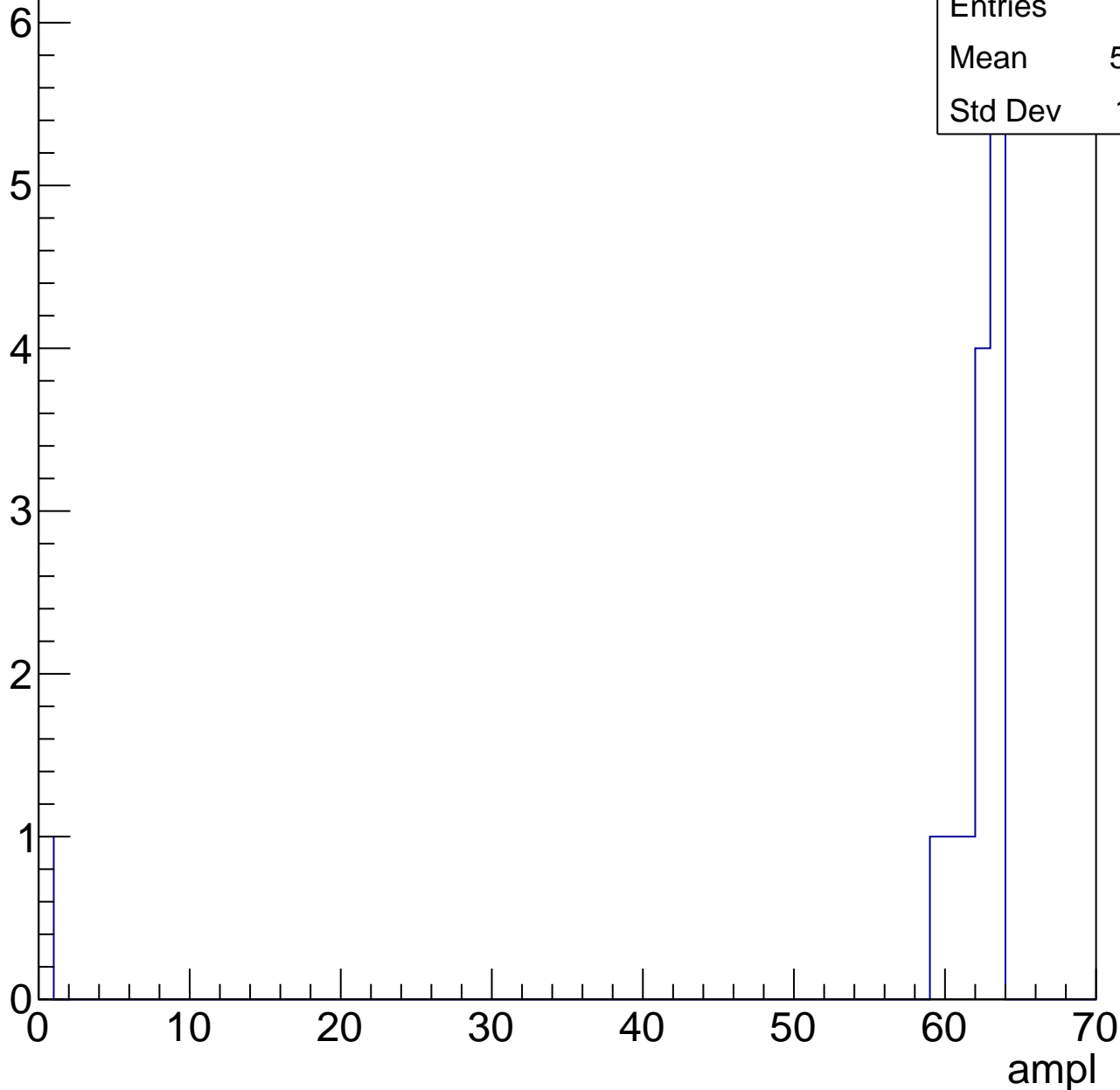
70

# B1L000S, U23-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.57
Std Dev	16.01

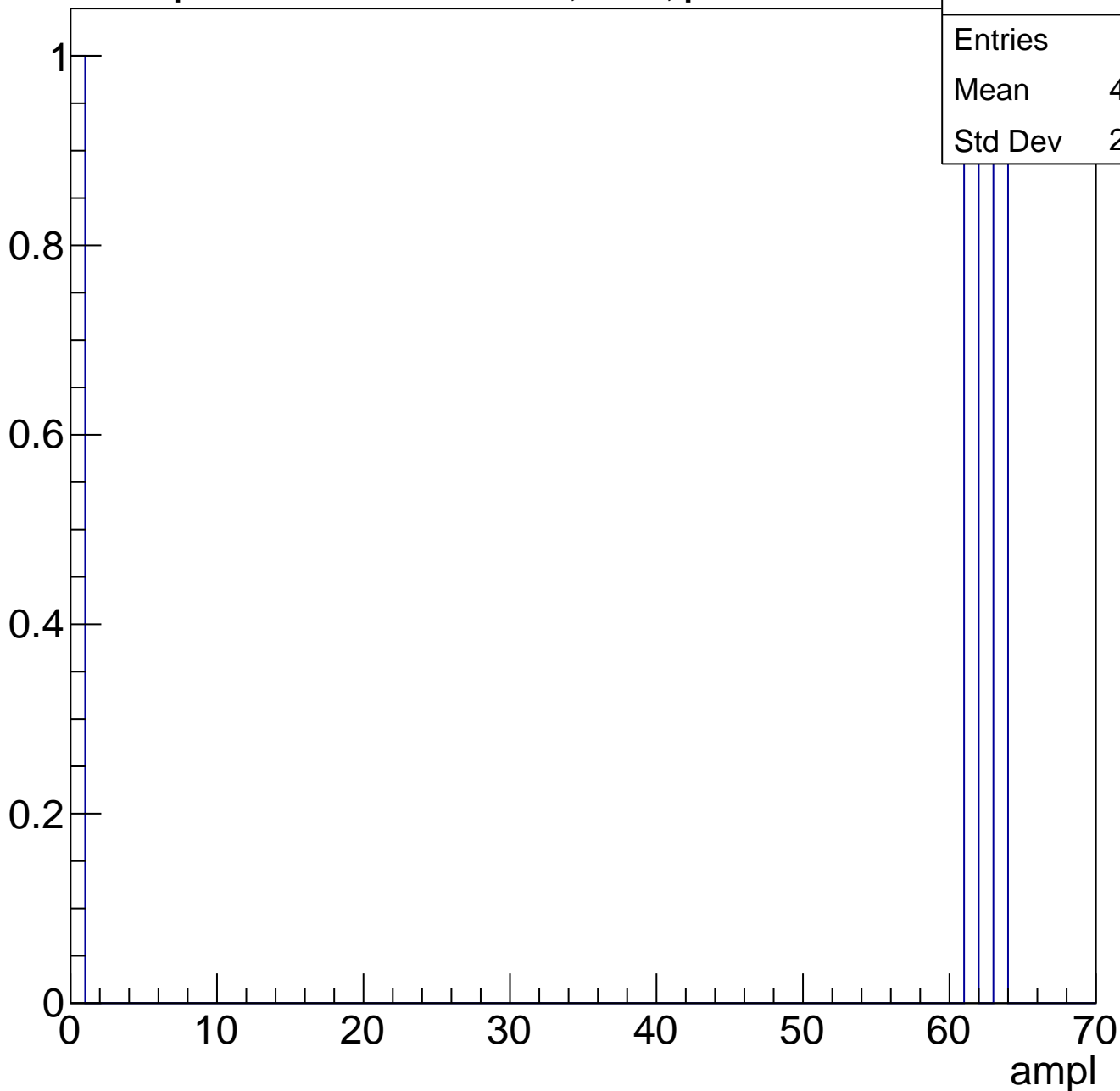




# B1L000S, U23-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch76, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.41
Std Dev	5.673

**Gaus mean : 29.7239**

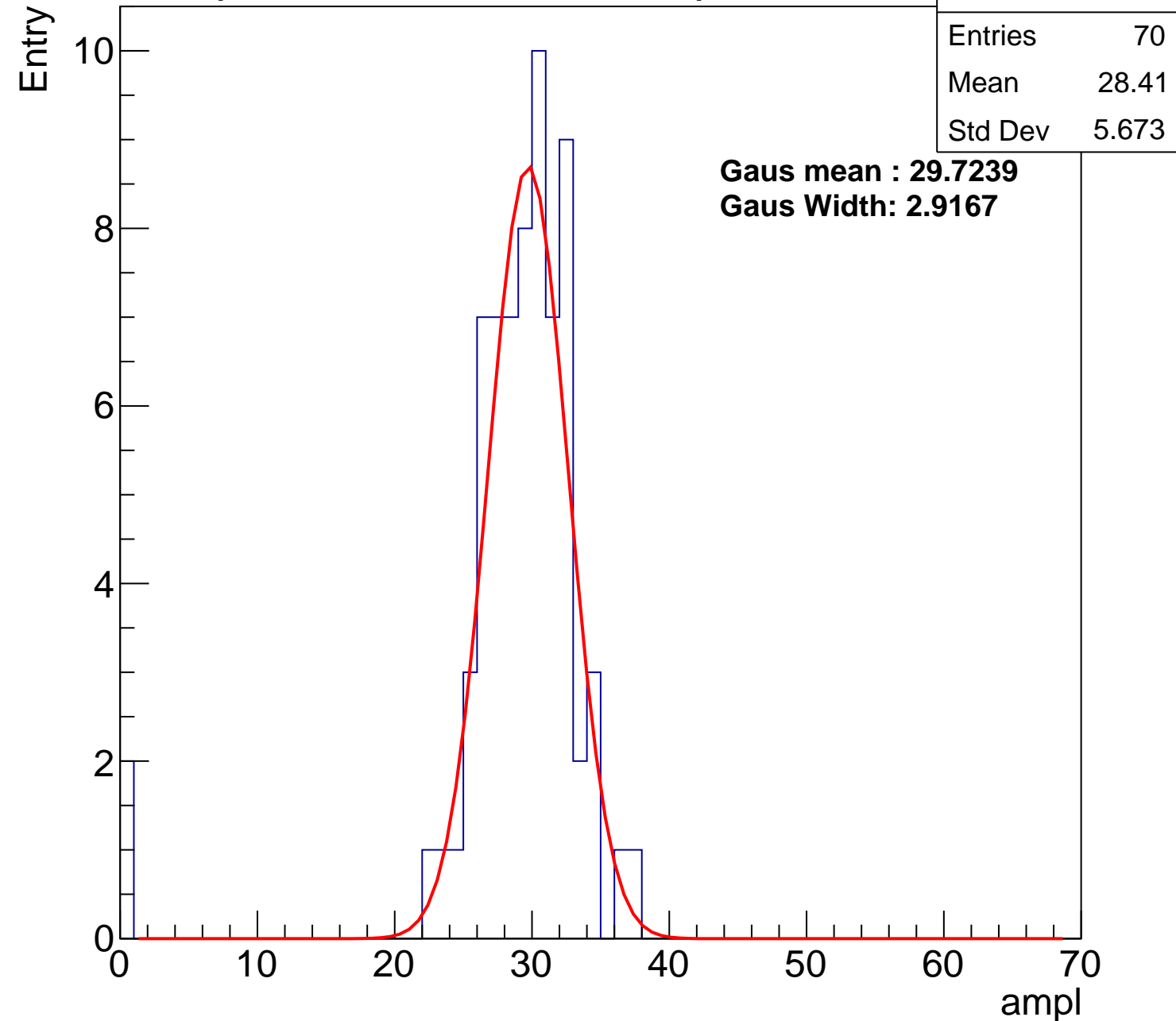
**Gaus Width: 2.9167**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch76, adc1

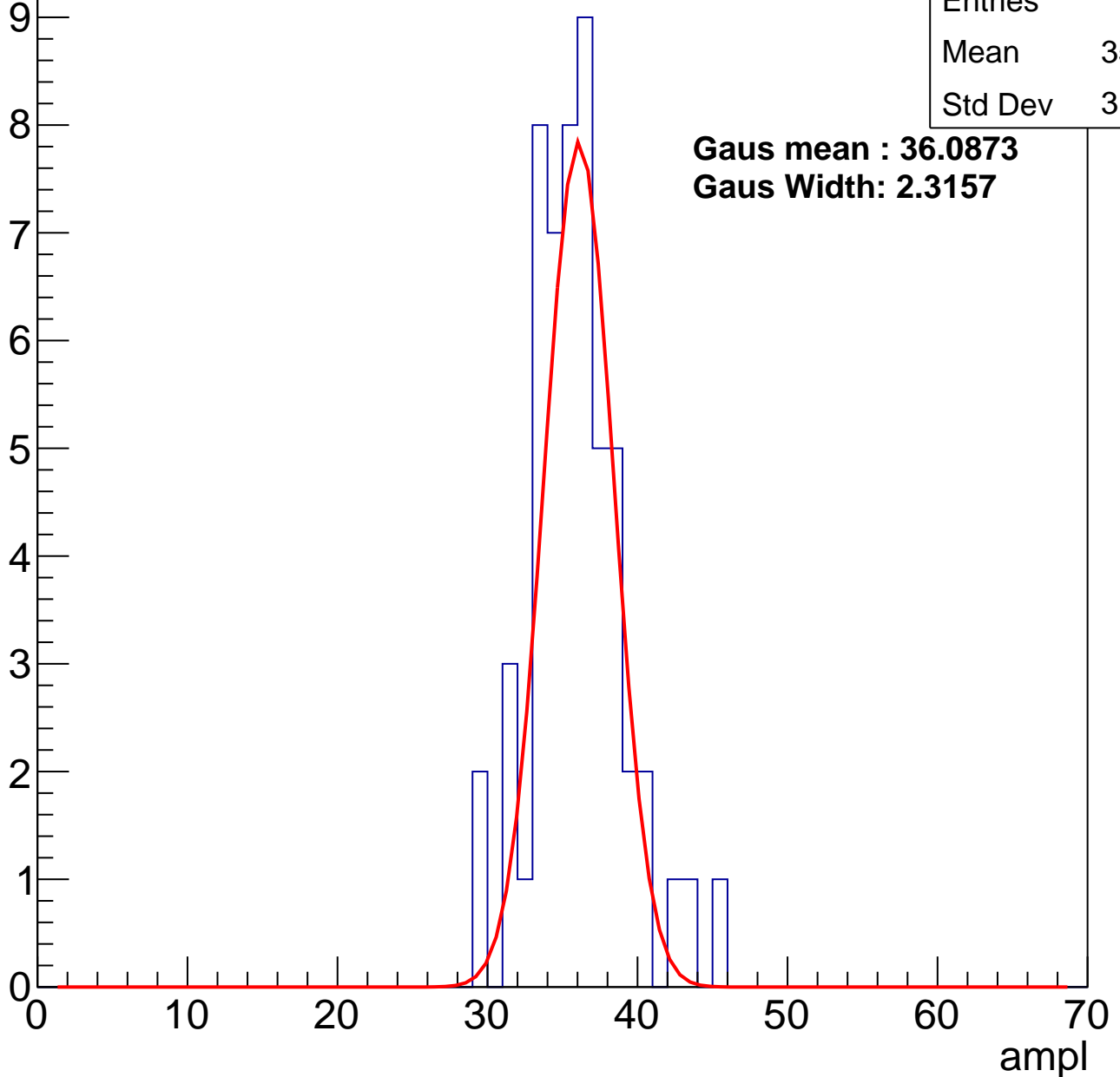
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	35.49
Std Dev	3.086

**Gaus mean : 36.0873**

**Gaus Width: 2.3157**



# B1L000S, U23-ch76, adc2

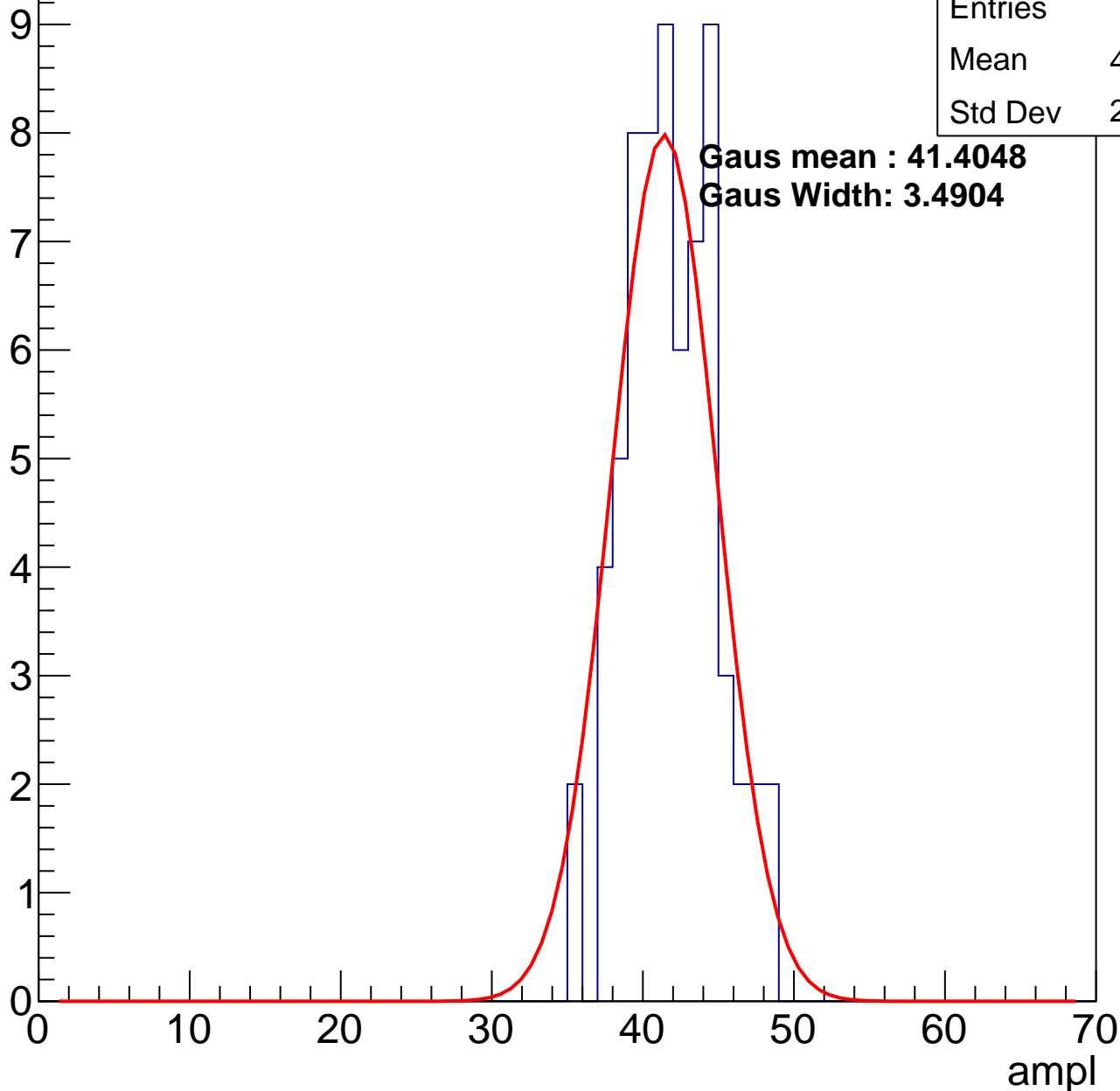
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.42
Std Dev	2.973

**Gaus mean : 41.4048**

**Gaus Width: 3.4904**

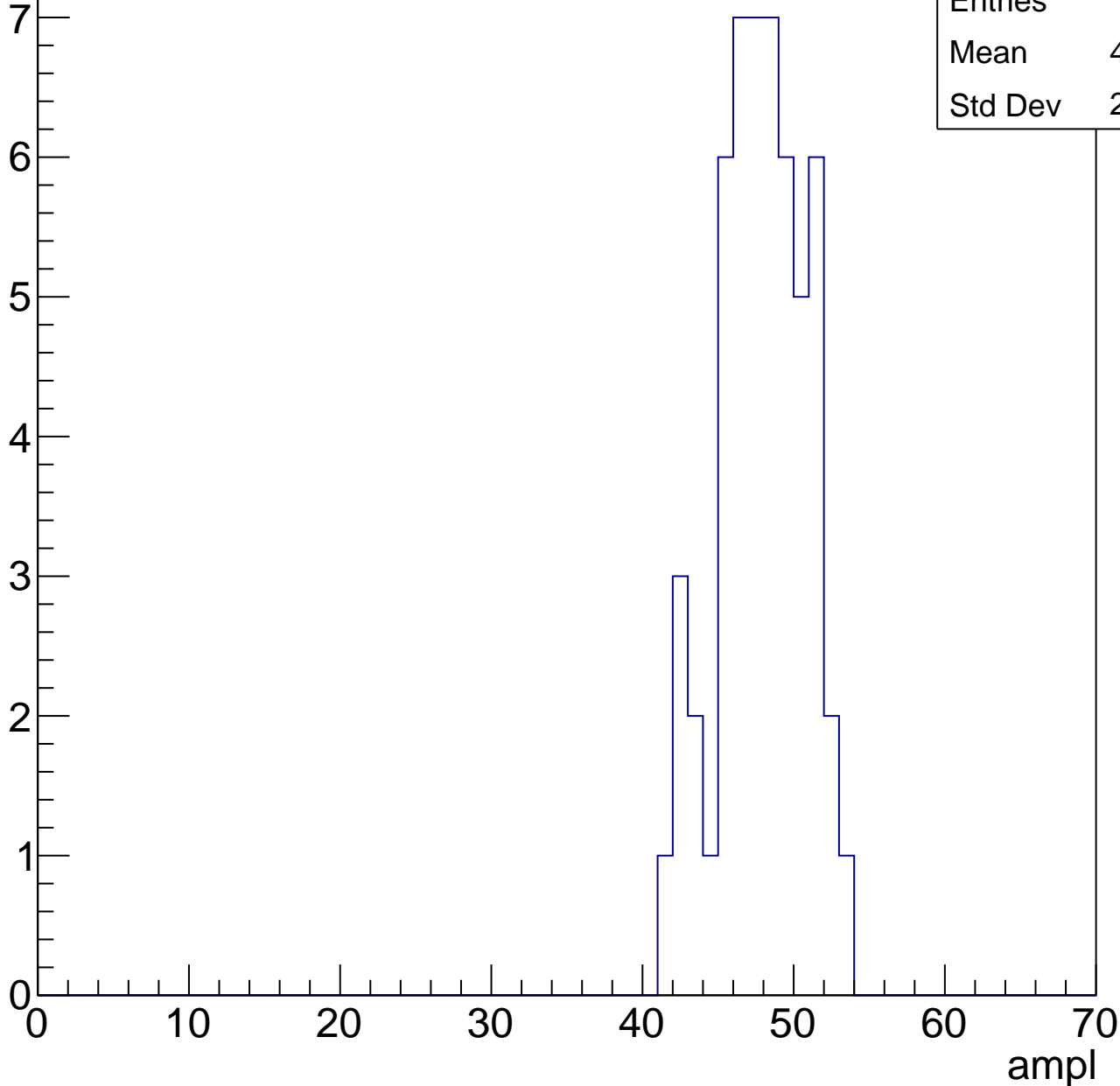


# B1L000S, U23-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	47.43
Std Dev	2.826

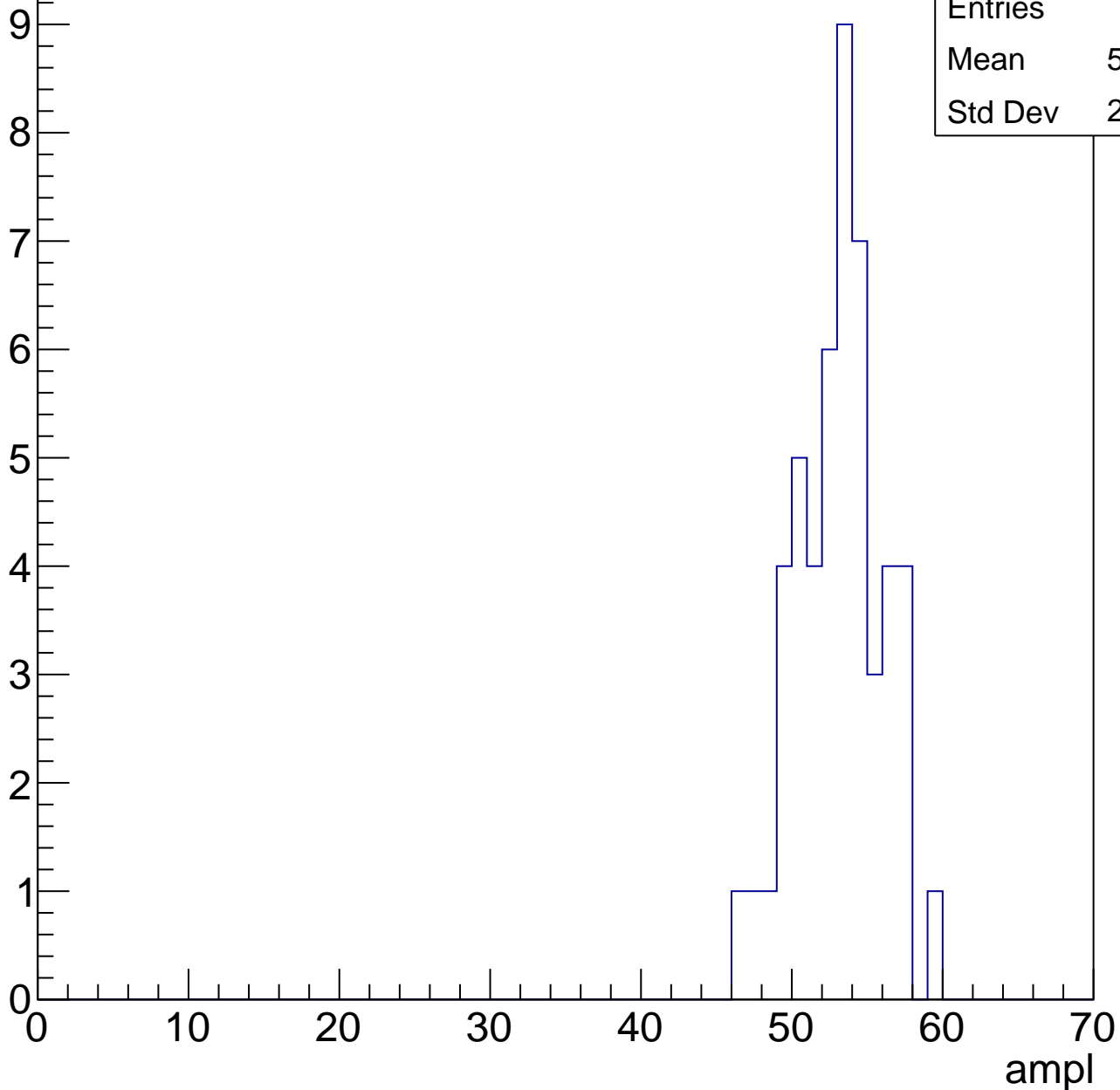


# B1L000S, U23-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	52.68
Std Dev	2.796



# B1L000S, U23-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	57.54
Std Dev	7.547

Entry

10

8

6

4

2

0

0

10

20

30

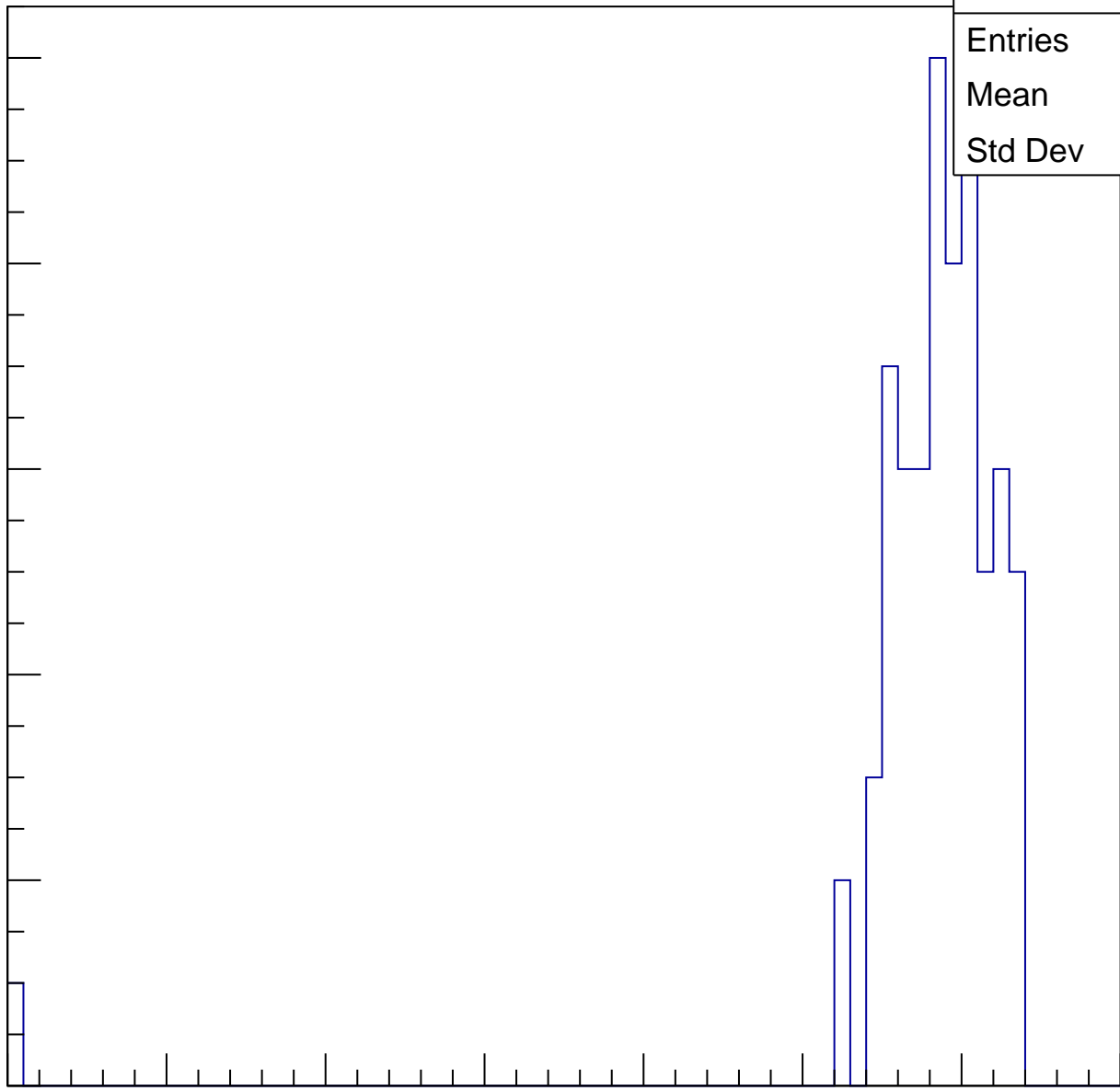
40

50

60

70

ampl

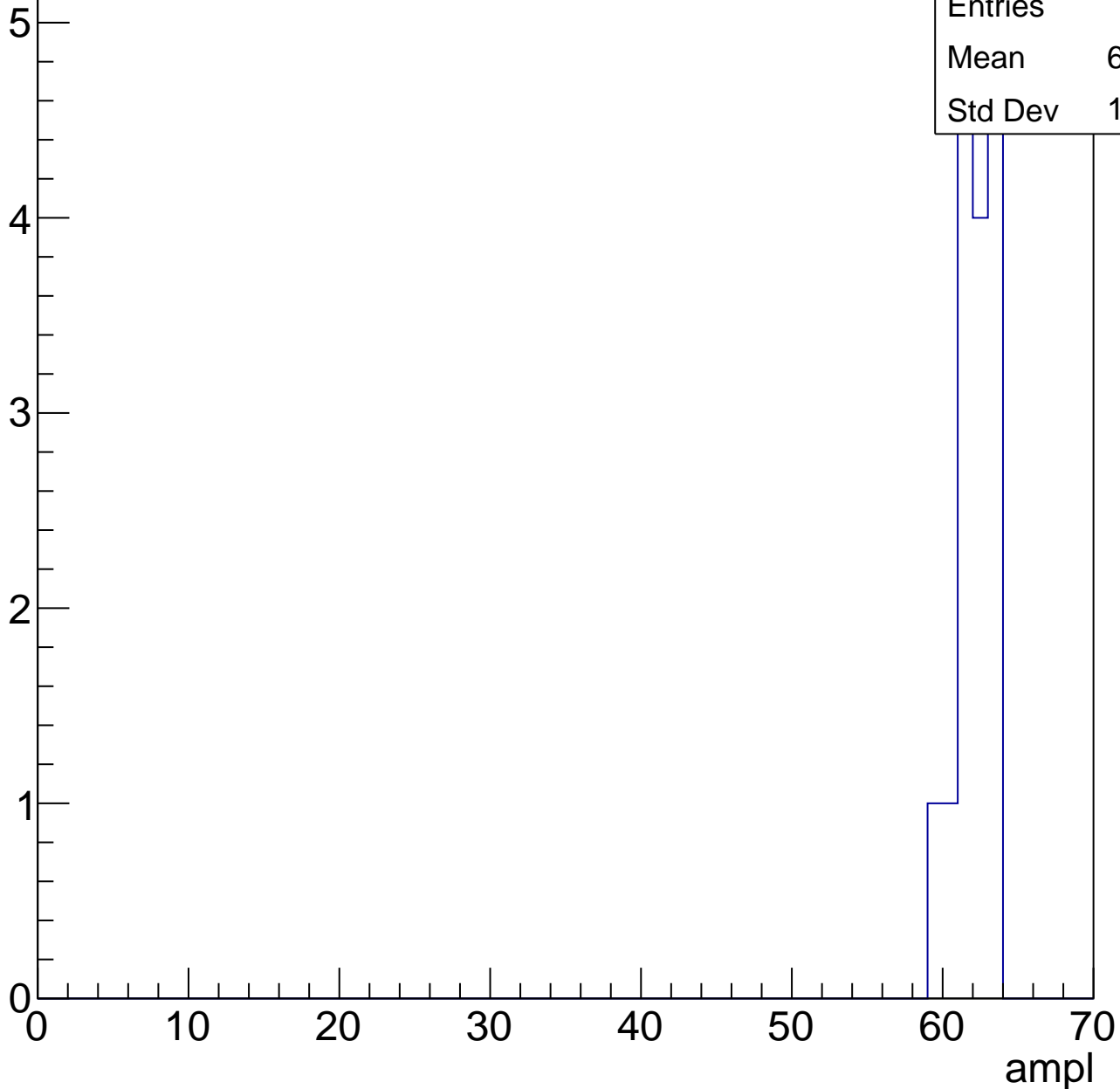


# B1L000S, U23-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.69
Std Dev	1.158





# B1L000S, U23-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch77, adc0

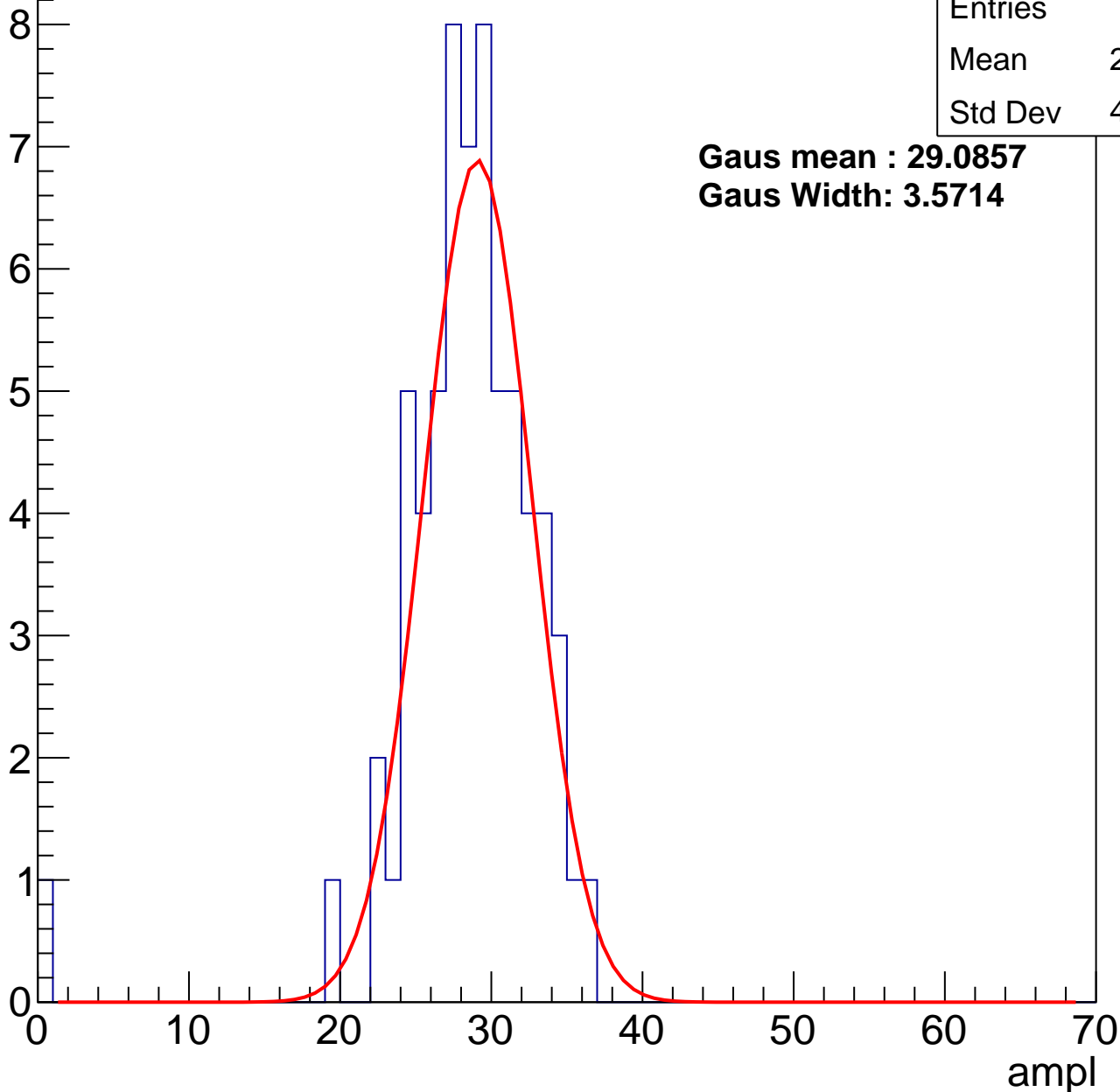
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	27.97
Std Dev	4.905

**Gaus mean : 29.0857**

**Gaus Width: 3.5714**



# B1L000S, U23-ch77, adc1

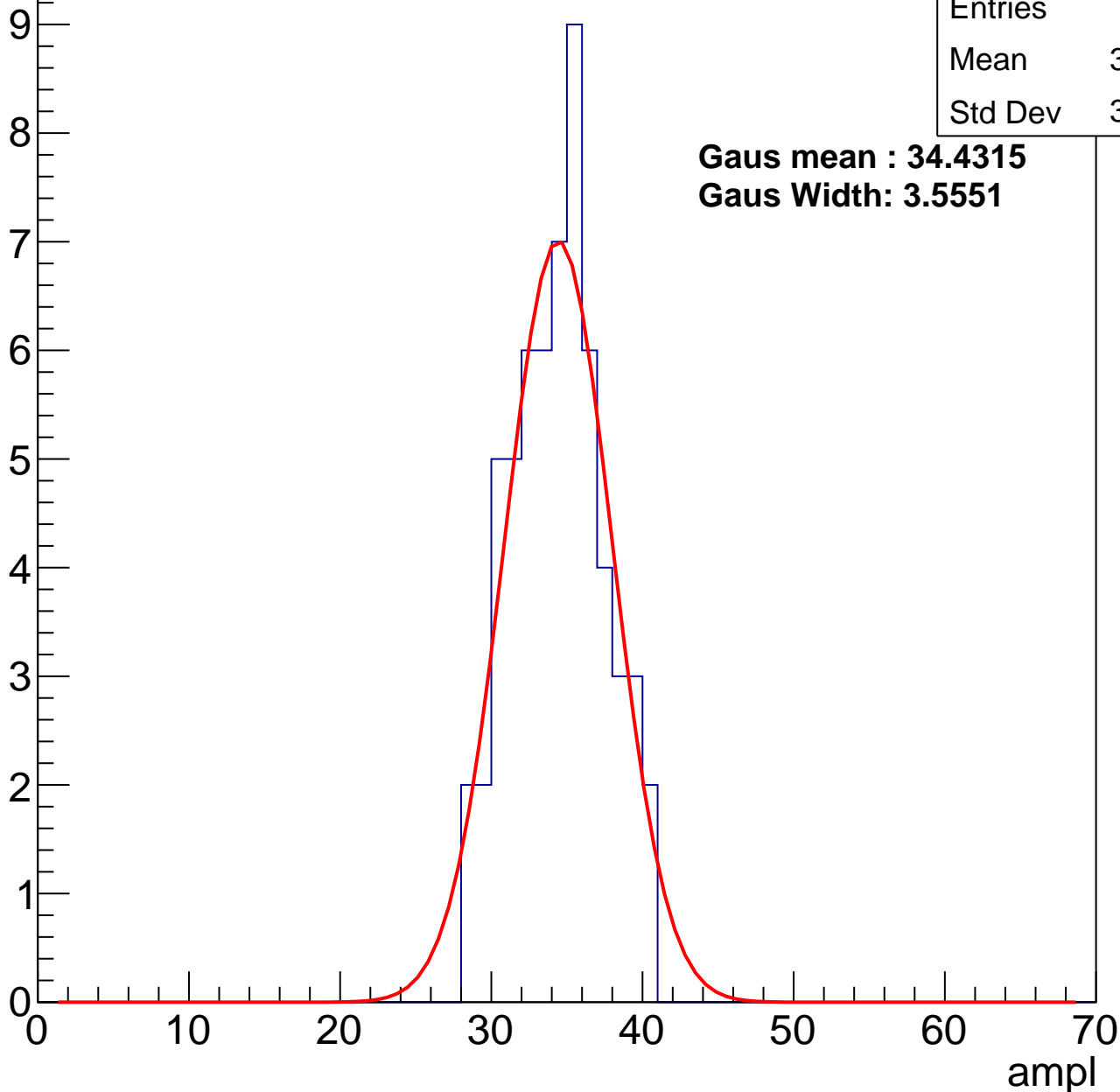
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	33.95
Std Dev	3.002

**Gaus mean : 34.4315**

**Gaus Width: 3.5551**



# B1L000S, U23-ch77, adc2

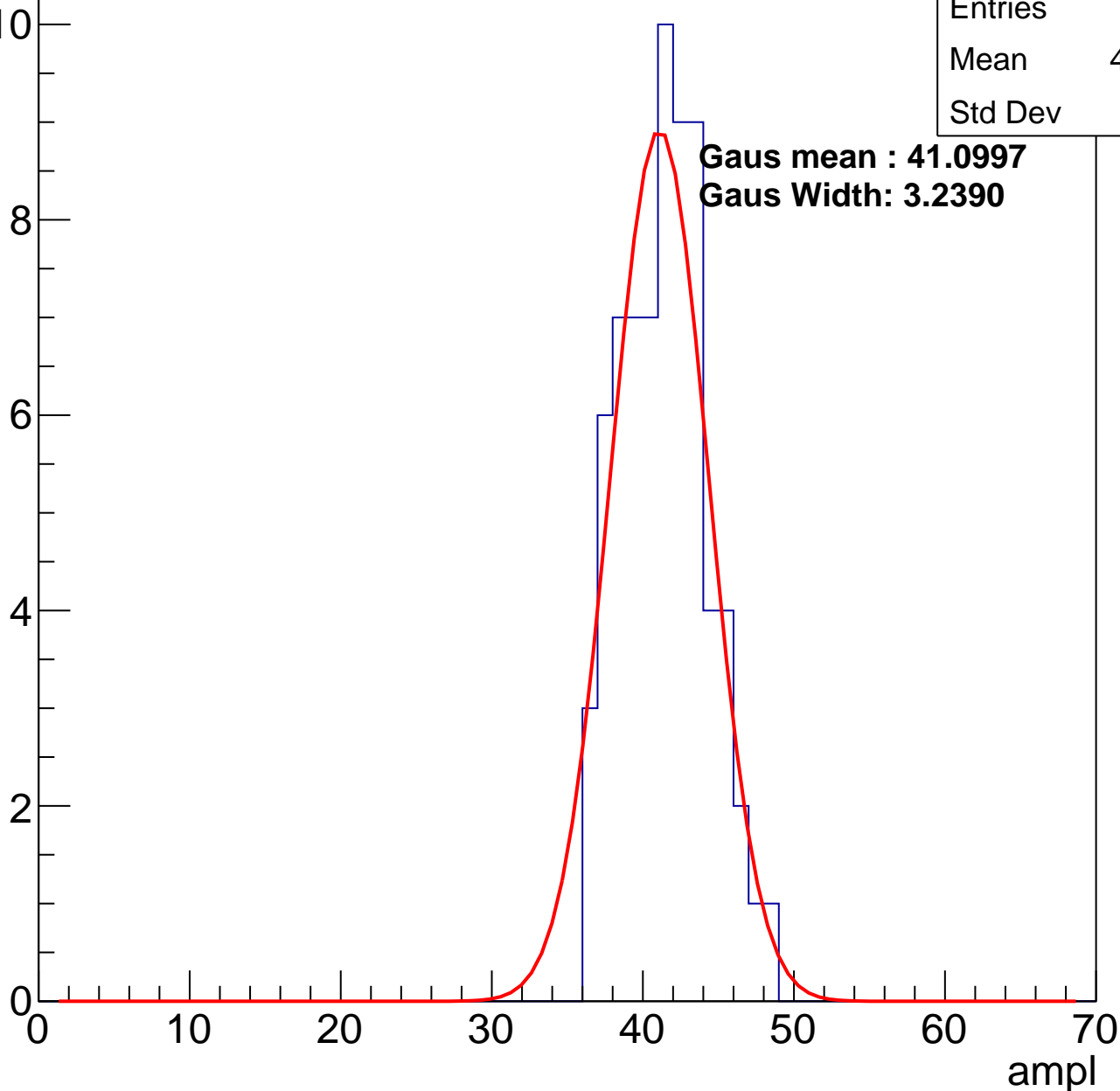
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	40.96
Std Dev	2.8

**Gaus mean : 41.0997**

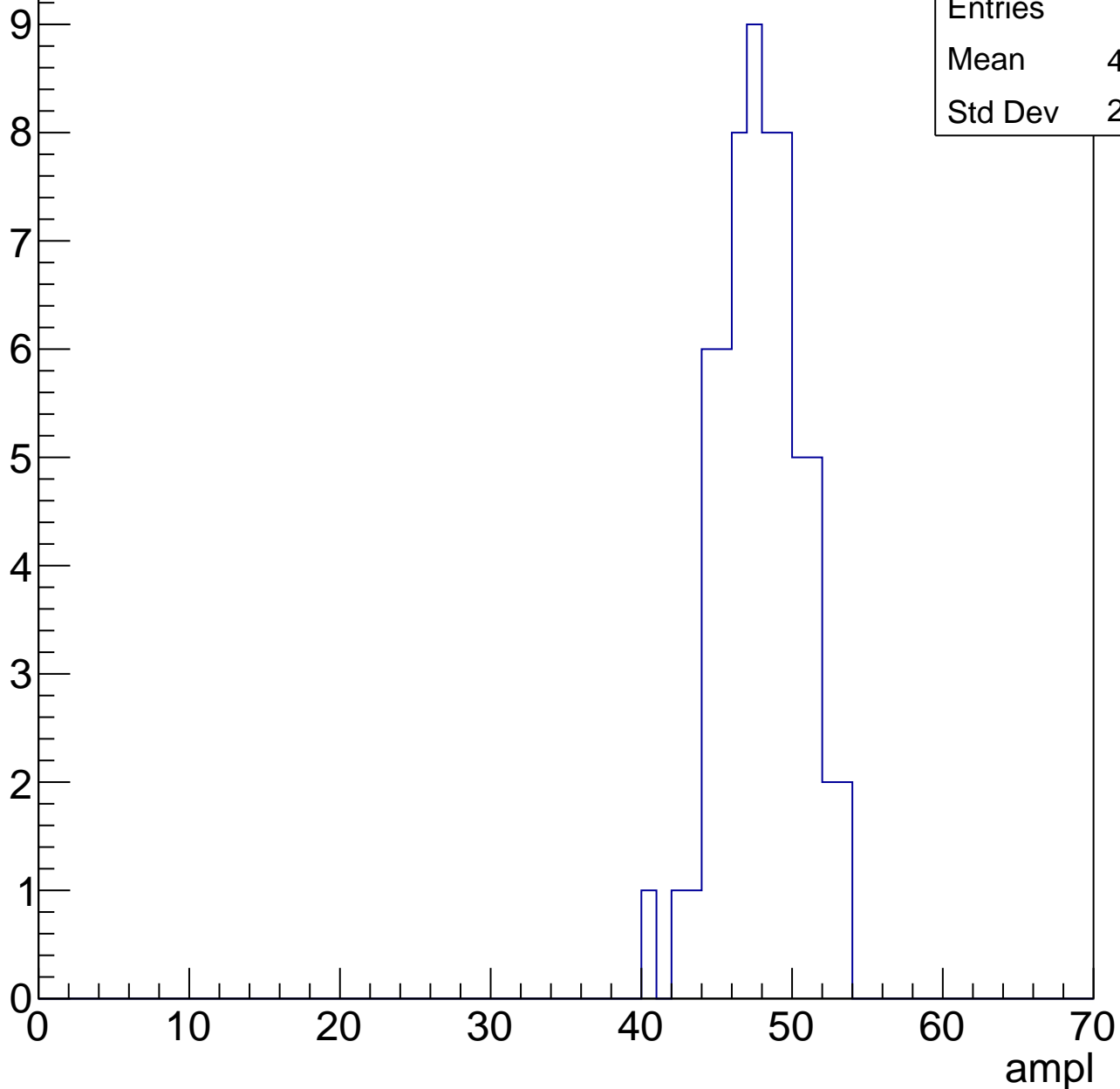
**Gaus Width: 3.2390**



# B1L000S, U23-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

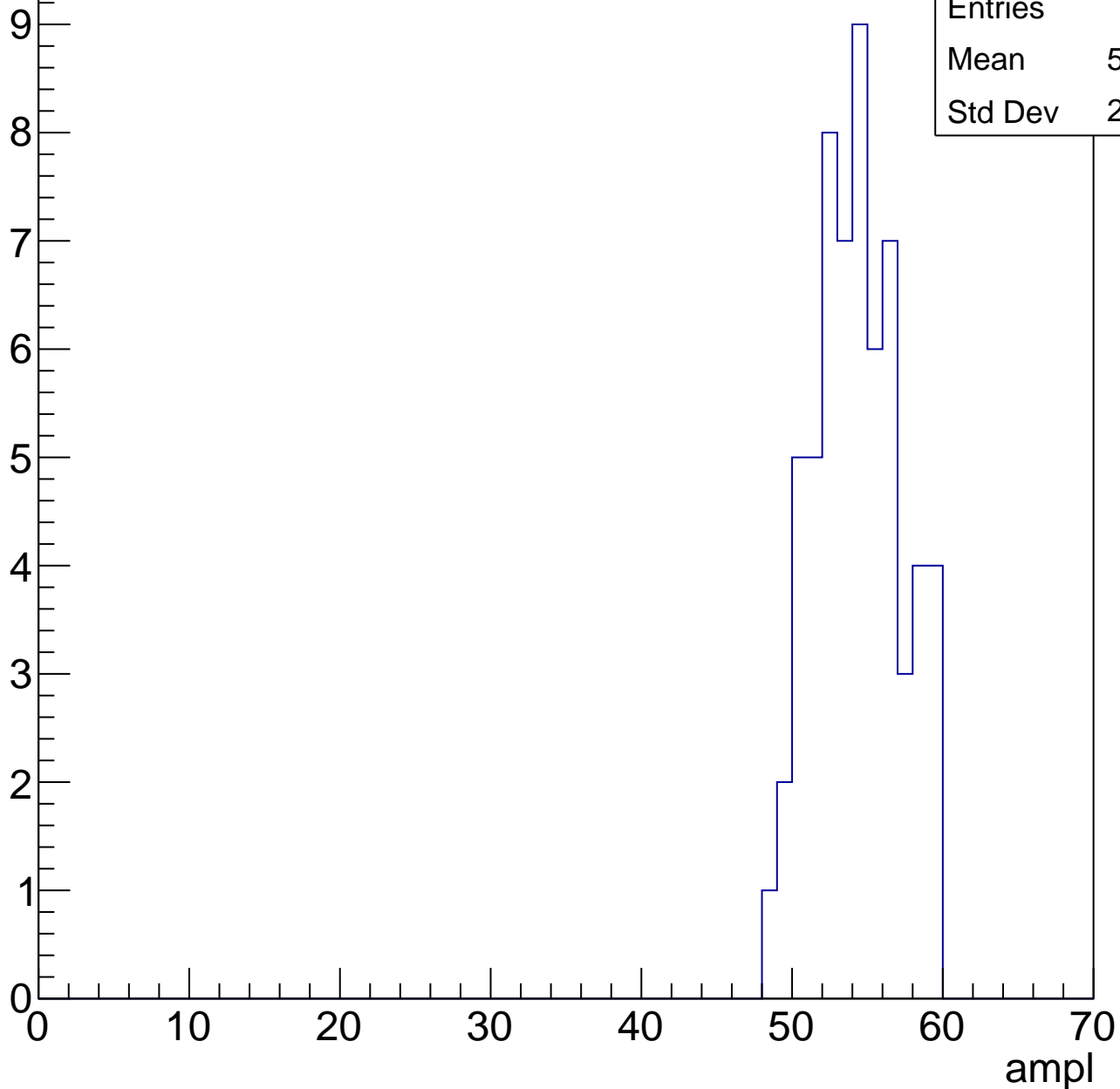
Entry



# B1L000S, U23-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

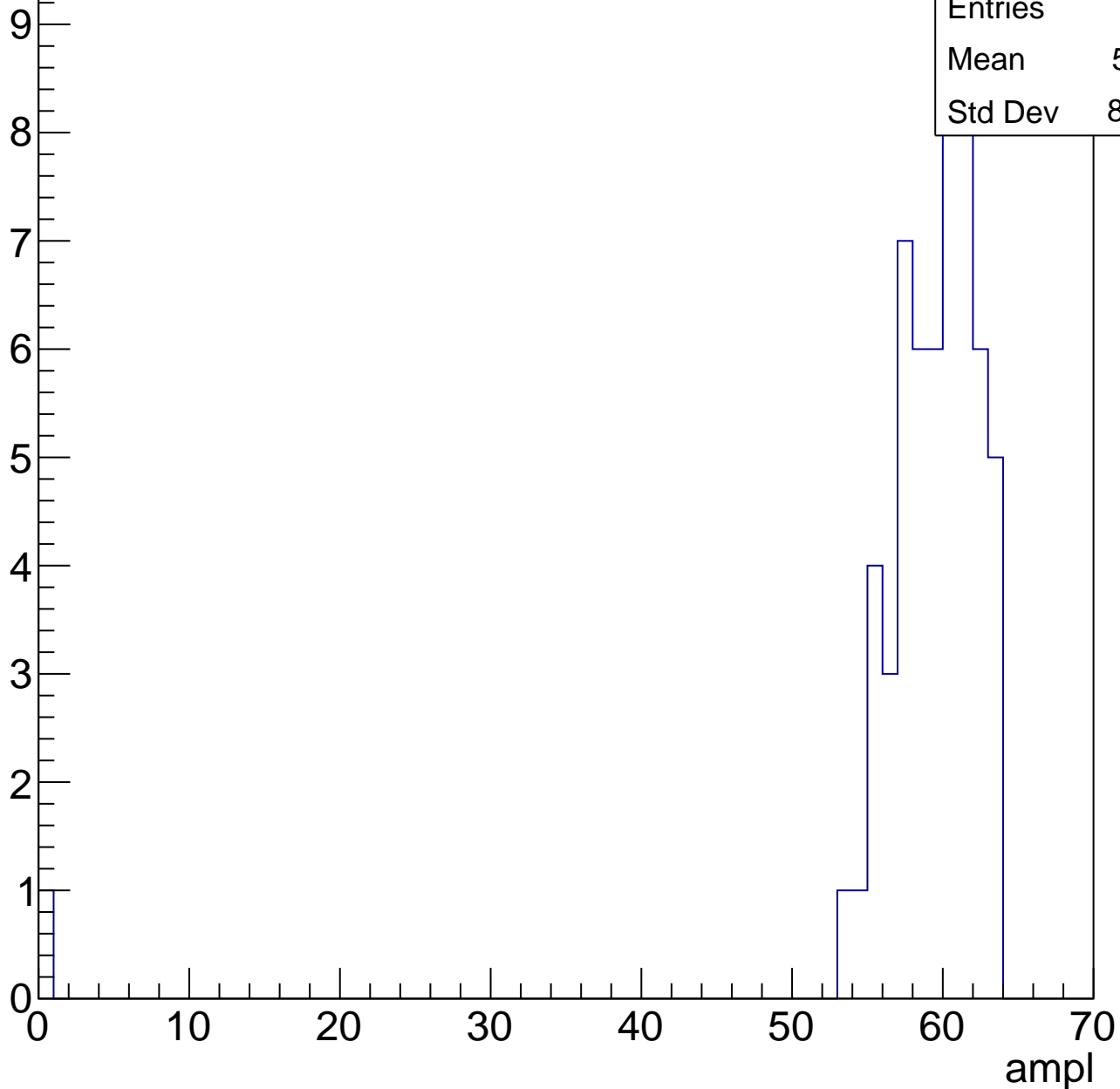
Entry



# B1L000S, U23-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



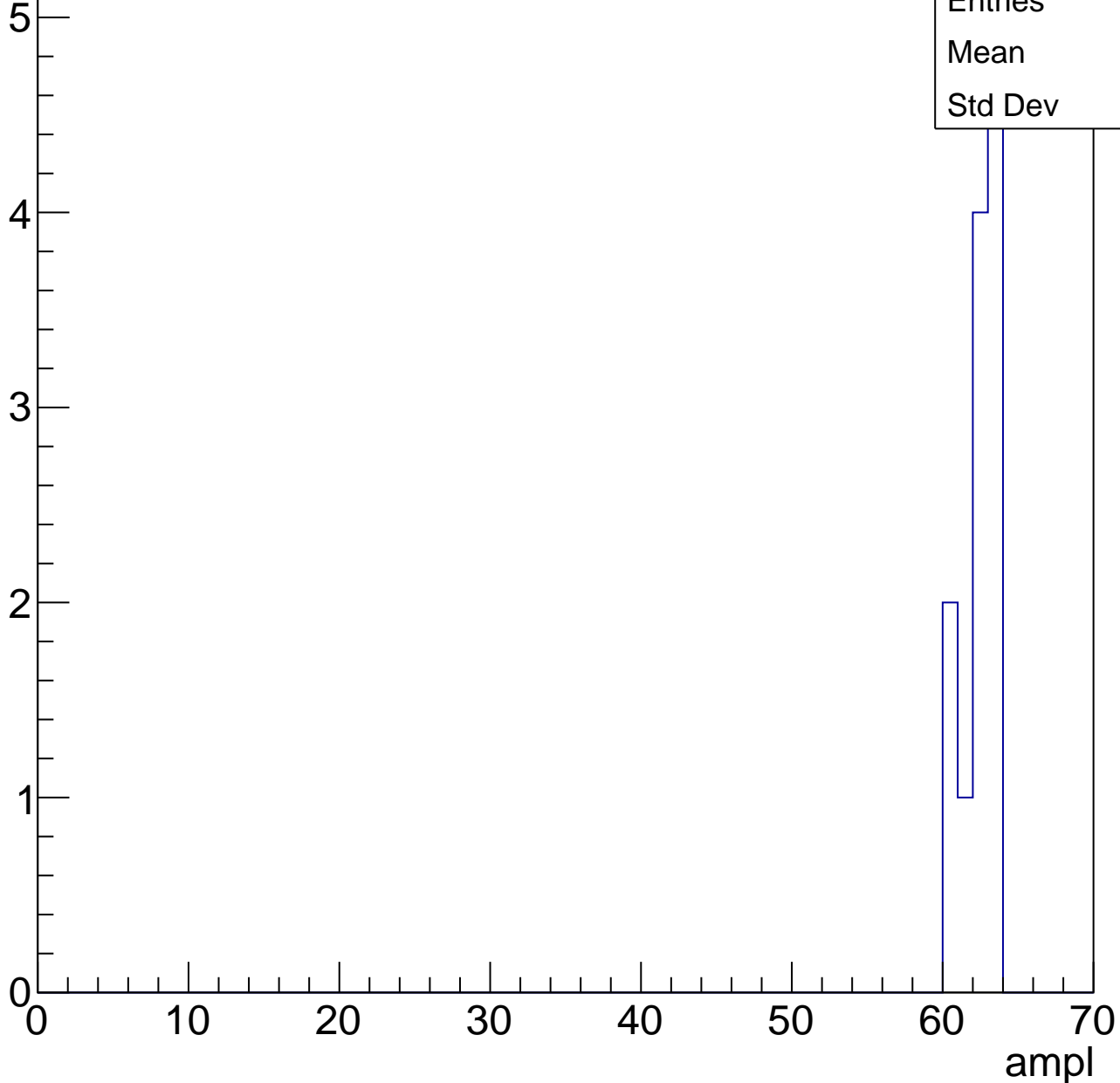
Entries	57
Mean	58.11
Std Dev	8.164

# B1L000S, U23-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62
Std Dev	1.08





# B1L000S, U23-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch78, adc0

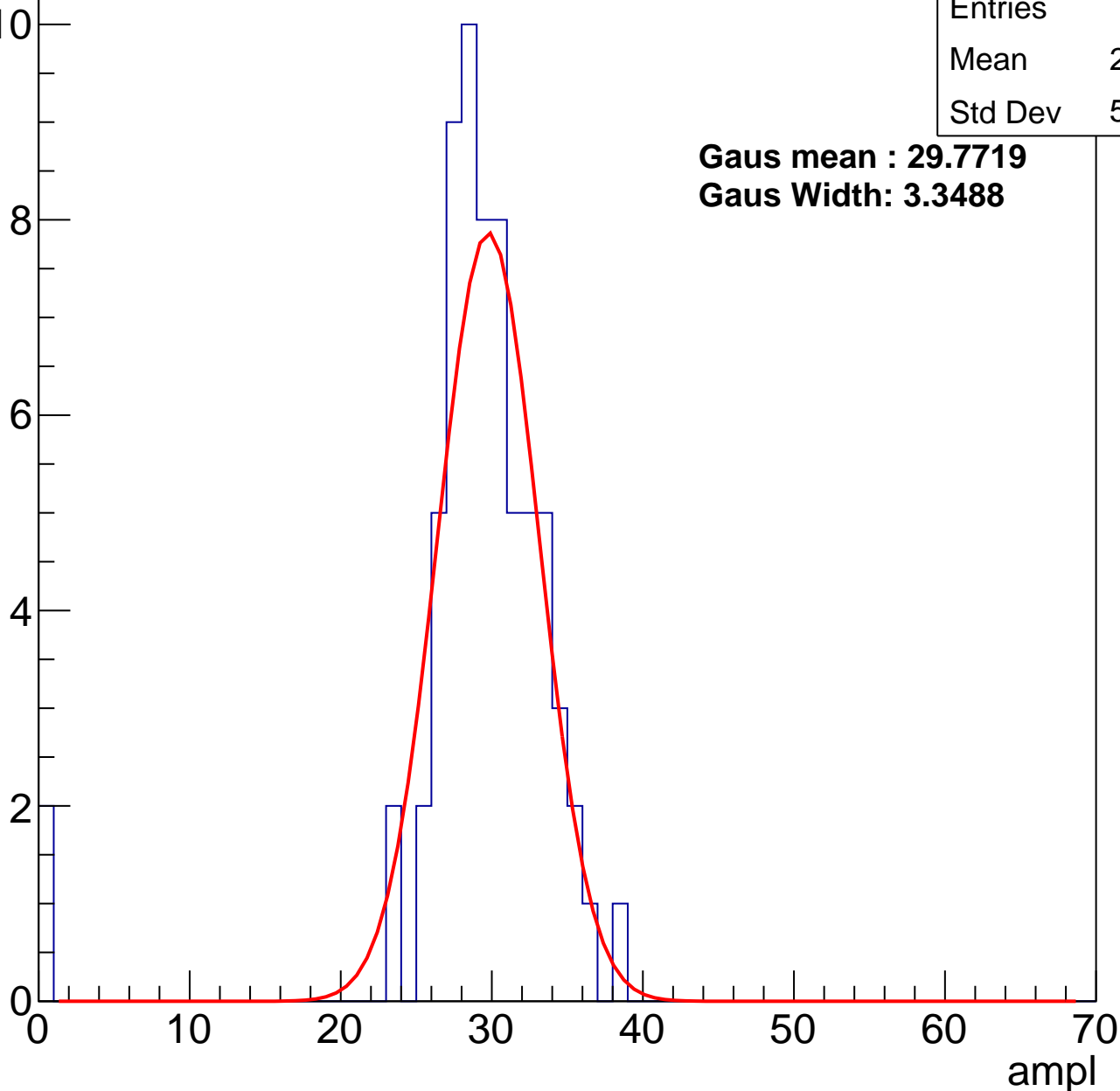
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.63
Std Dev	5.813

**Gaus mean : 29.7719**

**Gaus Width: 3.3488**



# B1L000S, U23-ch78, adc1

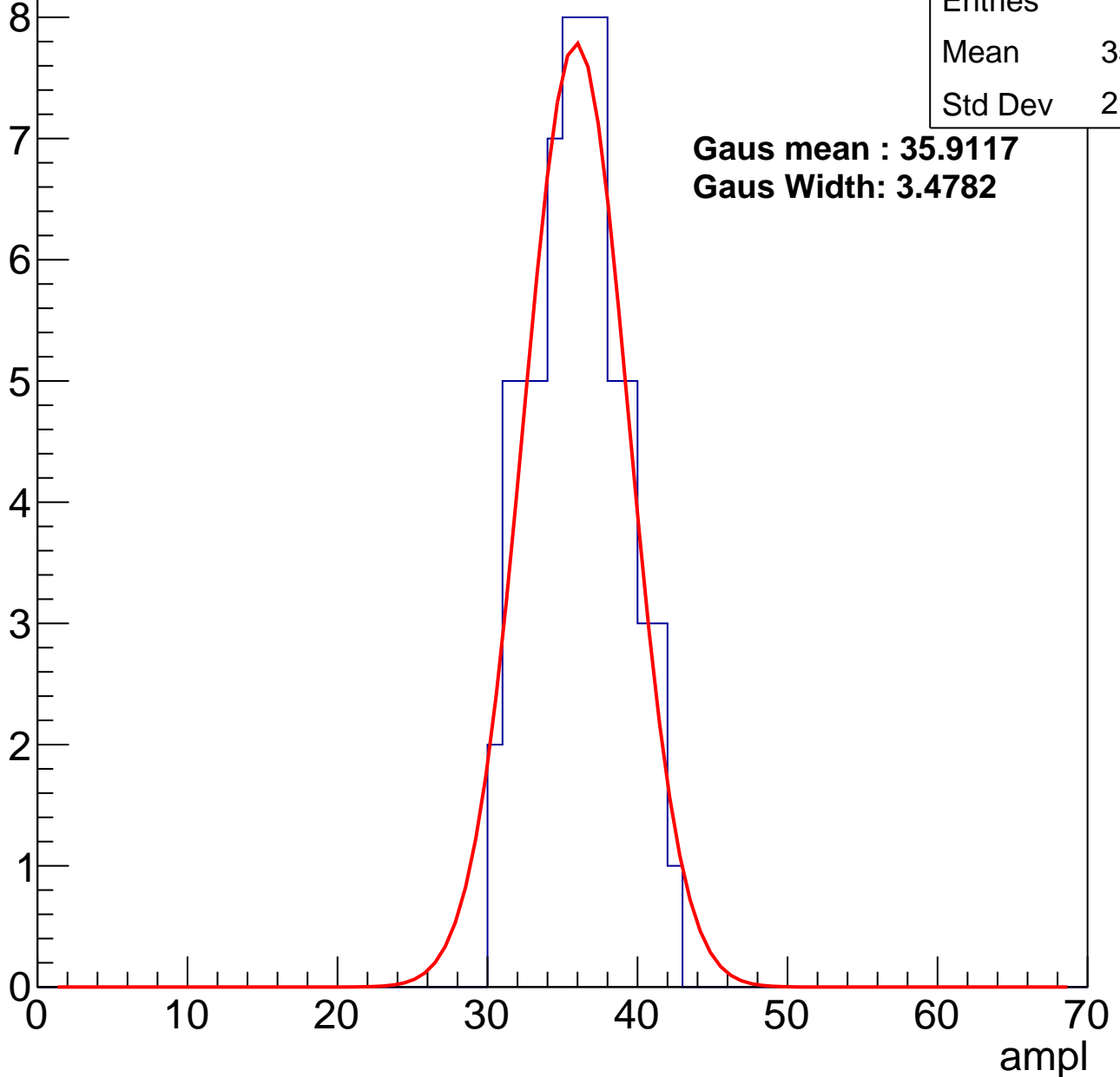
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.57
Std Dev	2.982

**Gaus mean : 35.9117**

**Gaus Width: 3.4782**



# B1L000S, U23-ch78, adc2

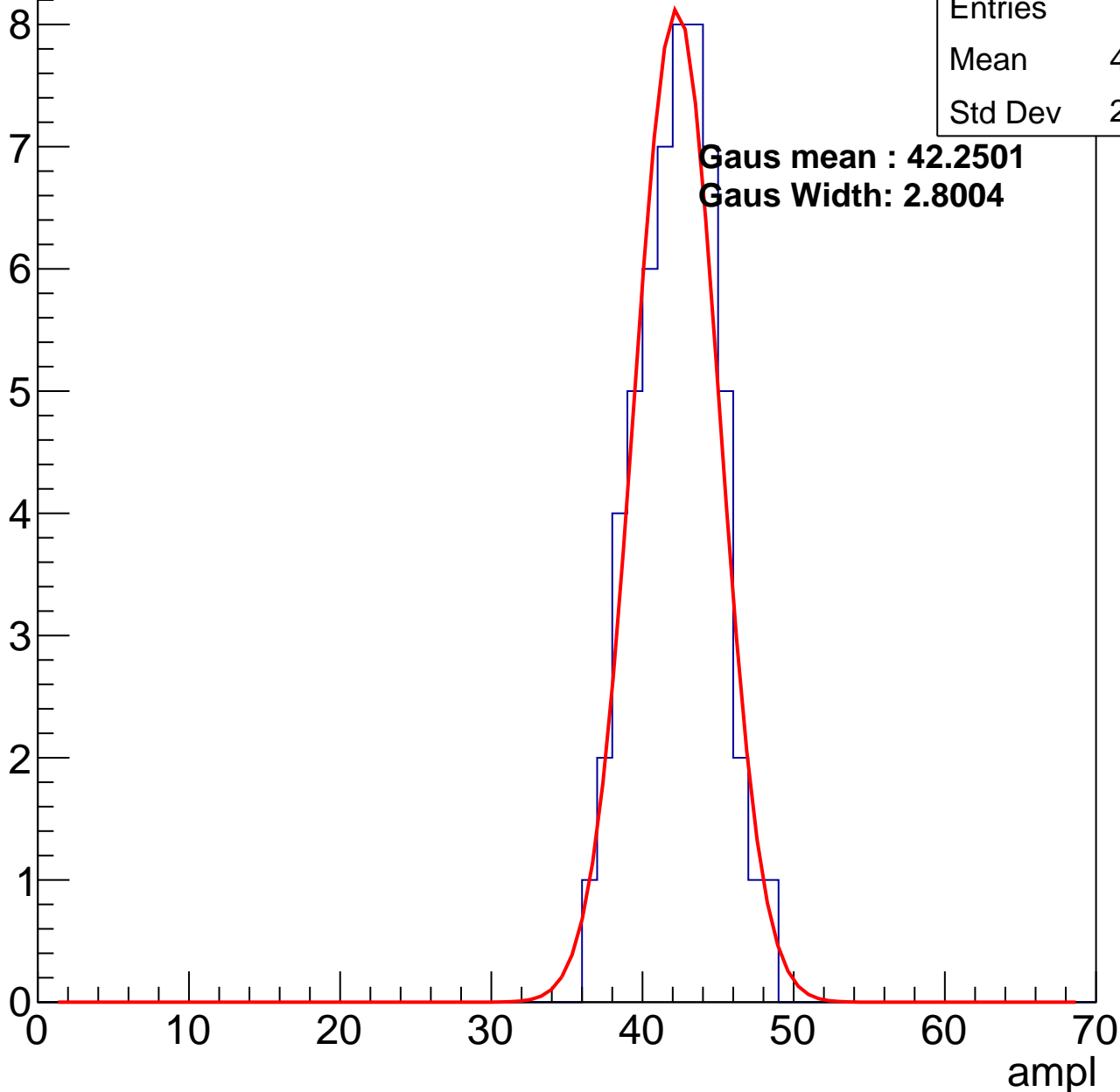
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.82
Std Dev	2.643

**Gaus mean : 42.2501**

**Gaus Width: 2.8004**

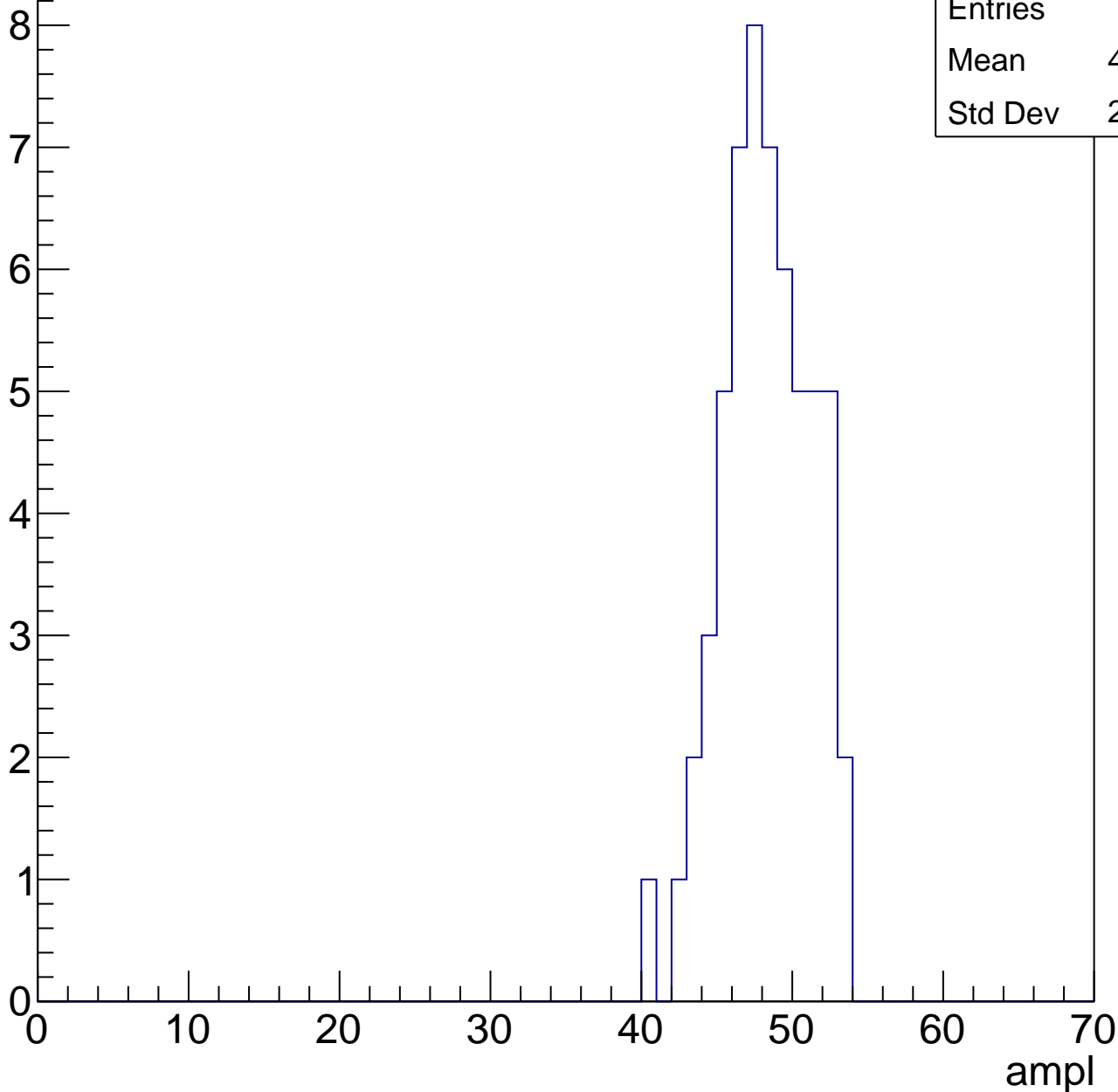


# B1L000S, U23-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

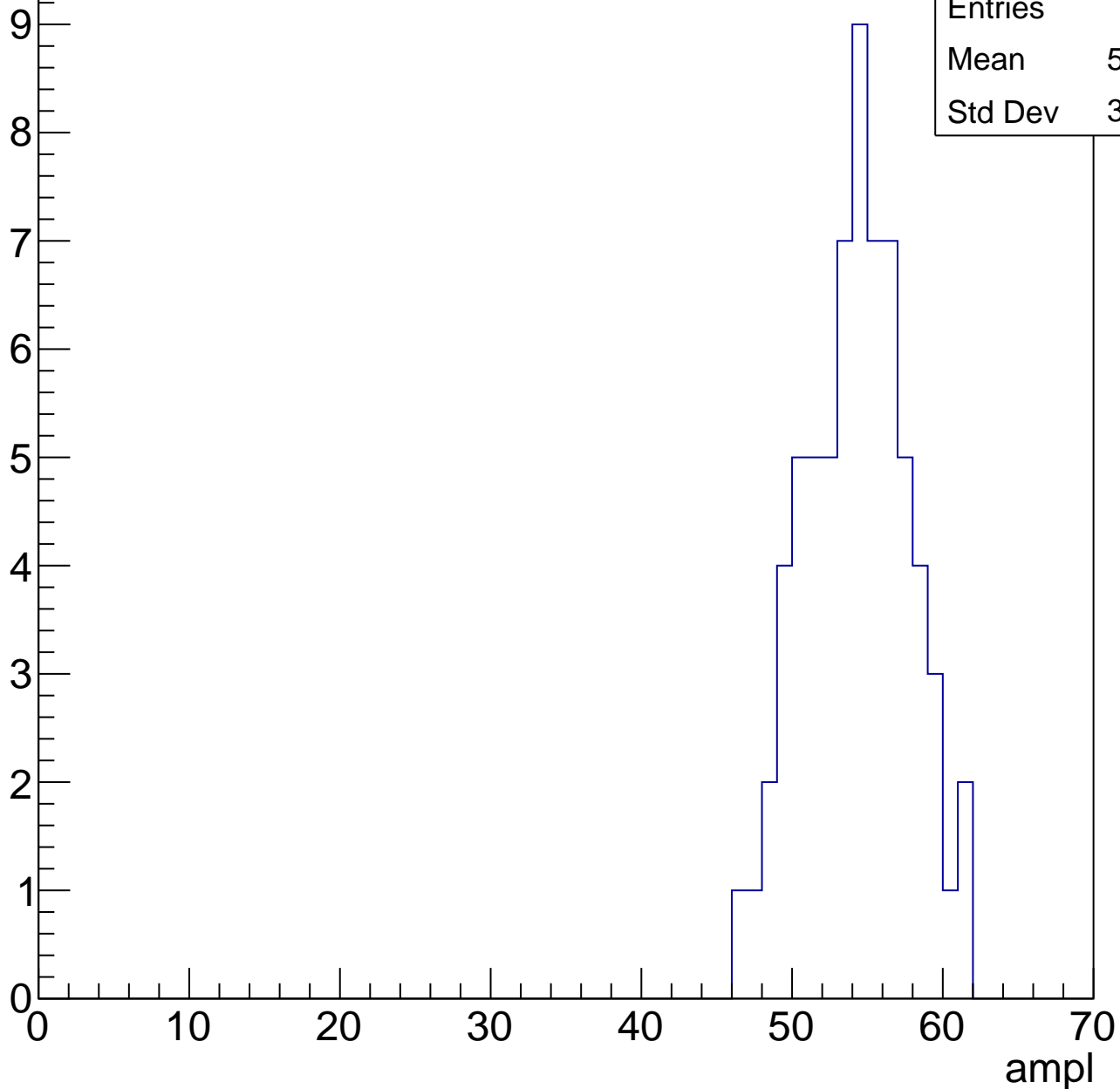
Entries	57
Mean	47.79
Std Dev	2.894



# B1L000S, U23-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	68
Mean	53.82
Std Dev	3.404

# B1L000S, U23-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 59.31

Std Dev 2.584

ampl

0

10

20

30

40

50

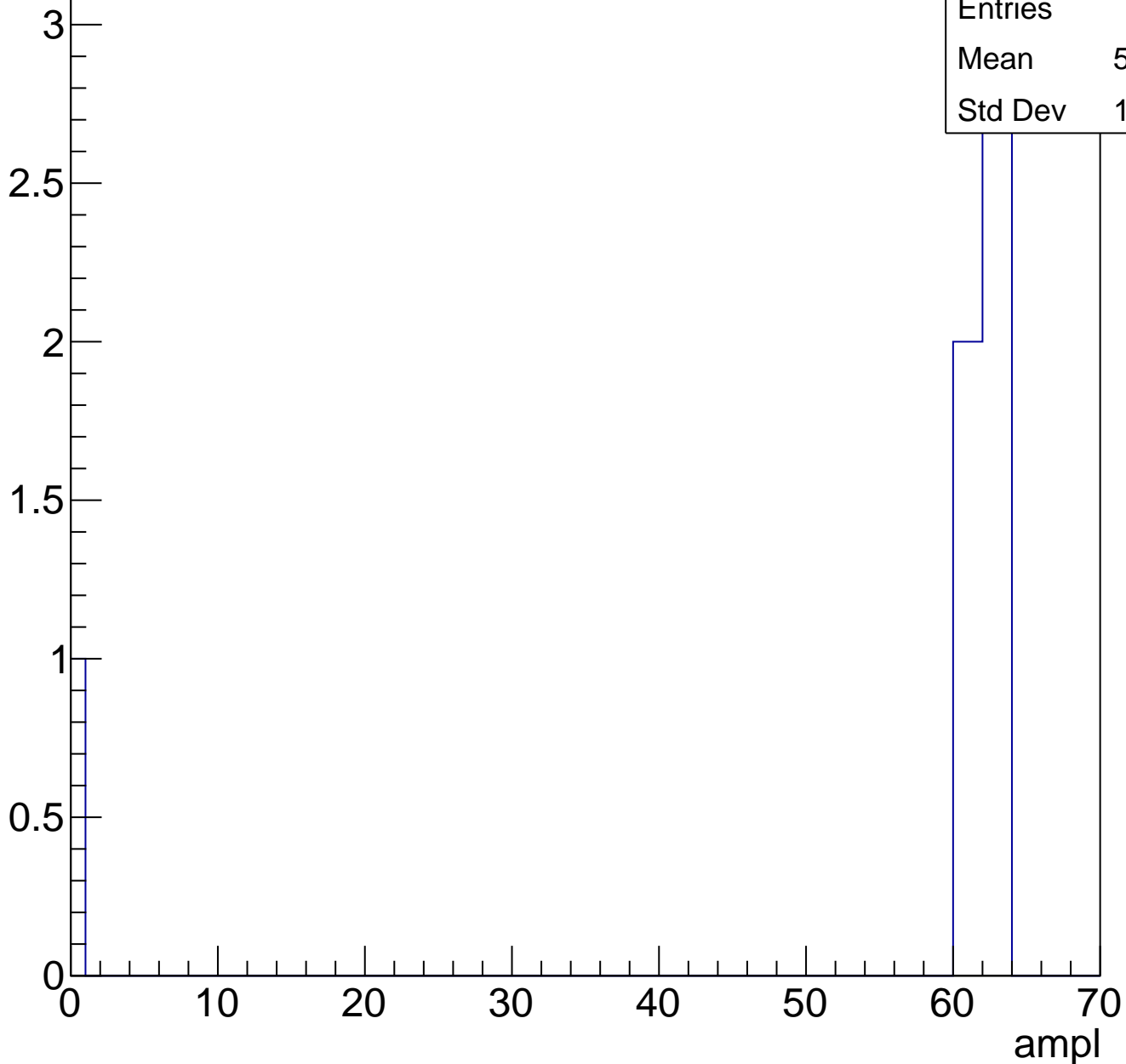
60

70

# B1L000S, U23-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U23-ch79, adc0

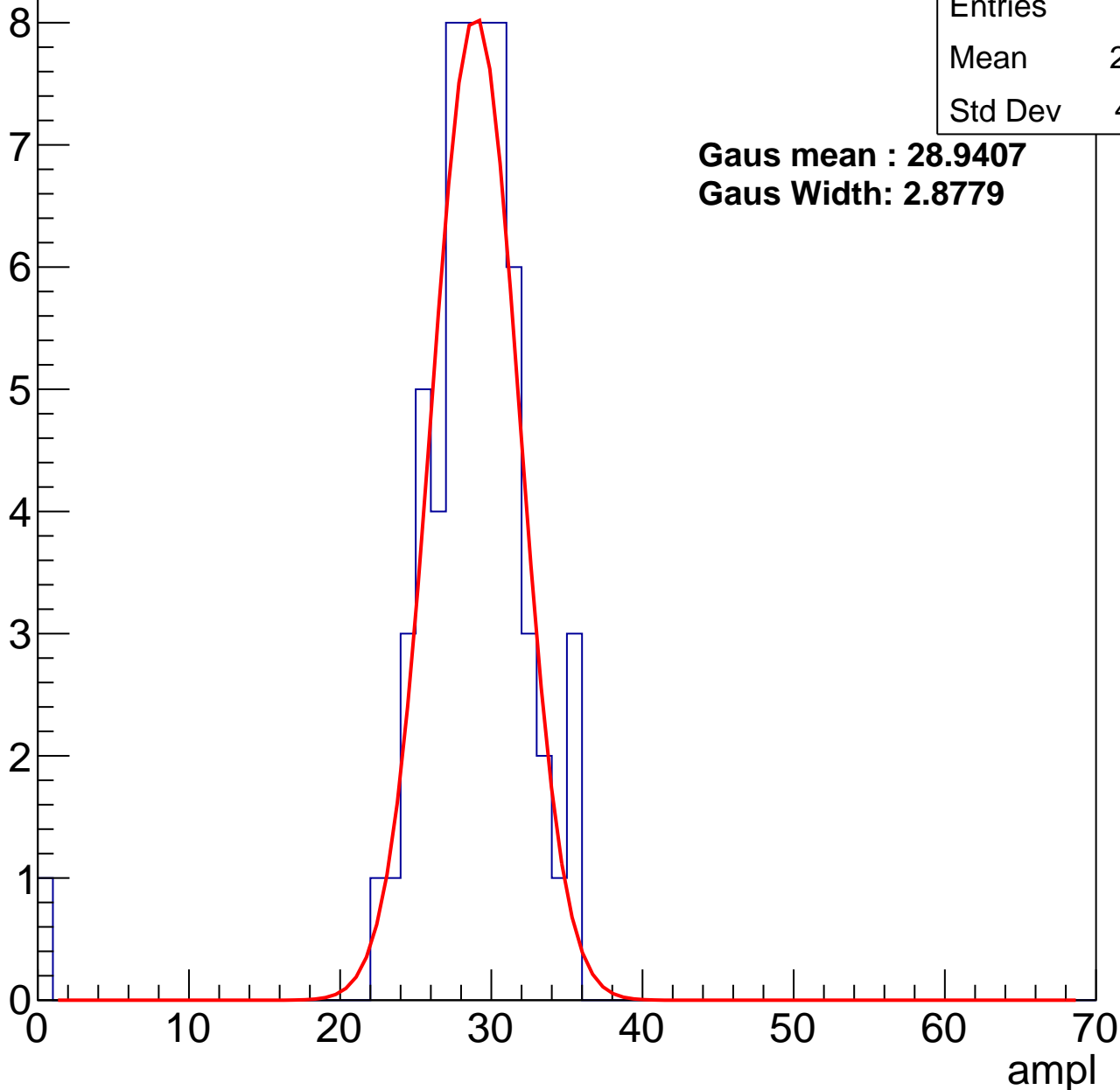
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.15
Std Dev	4.641

**Gaus mean : 28.9407**

**Gaus Width: 2.8779**



# B1L000S, U23-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	35.54
Std Dev	2.882

**Gaus mean : 36.1437**

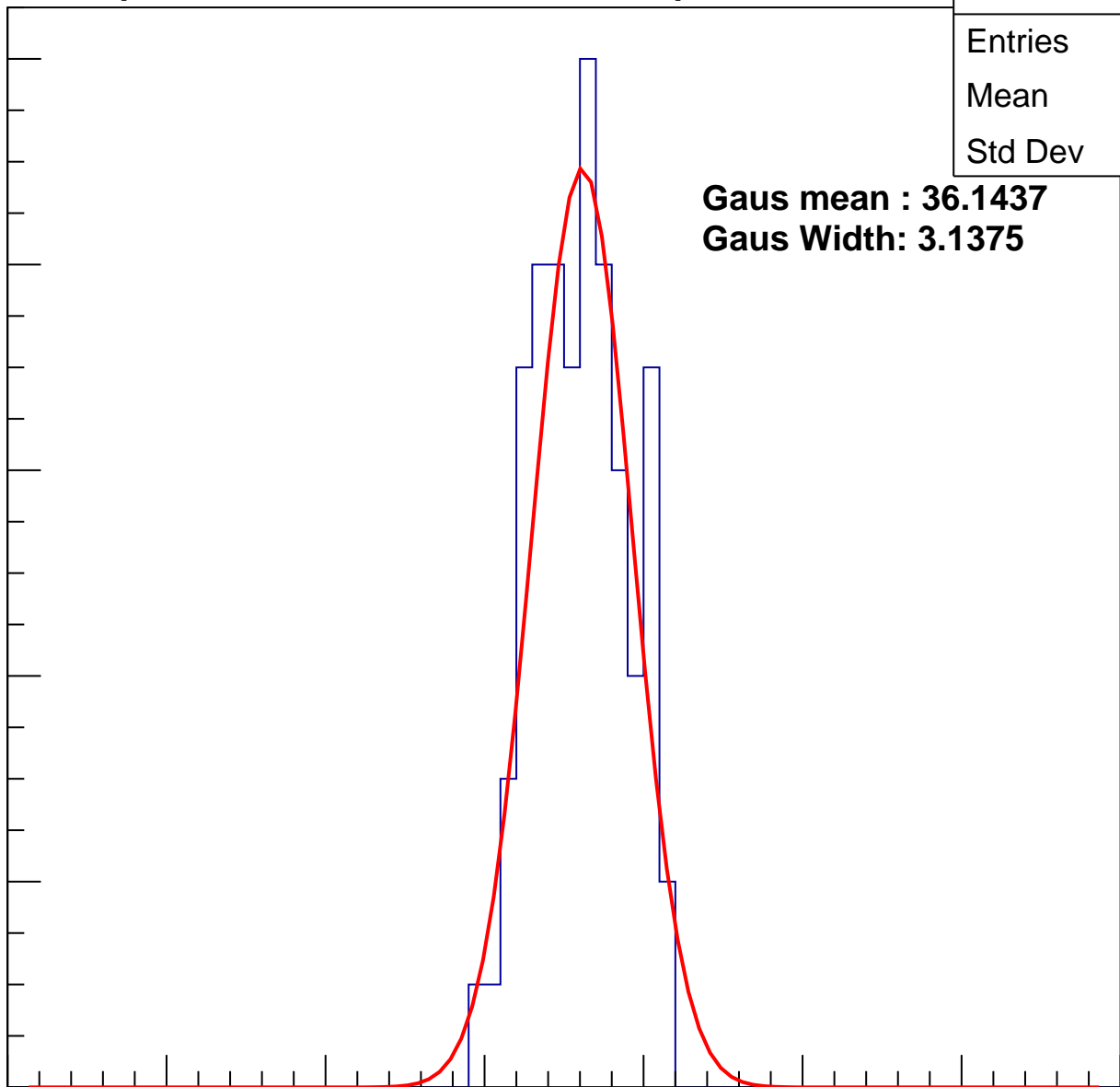
**Gaus Width: 3.1375**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch79, adc2

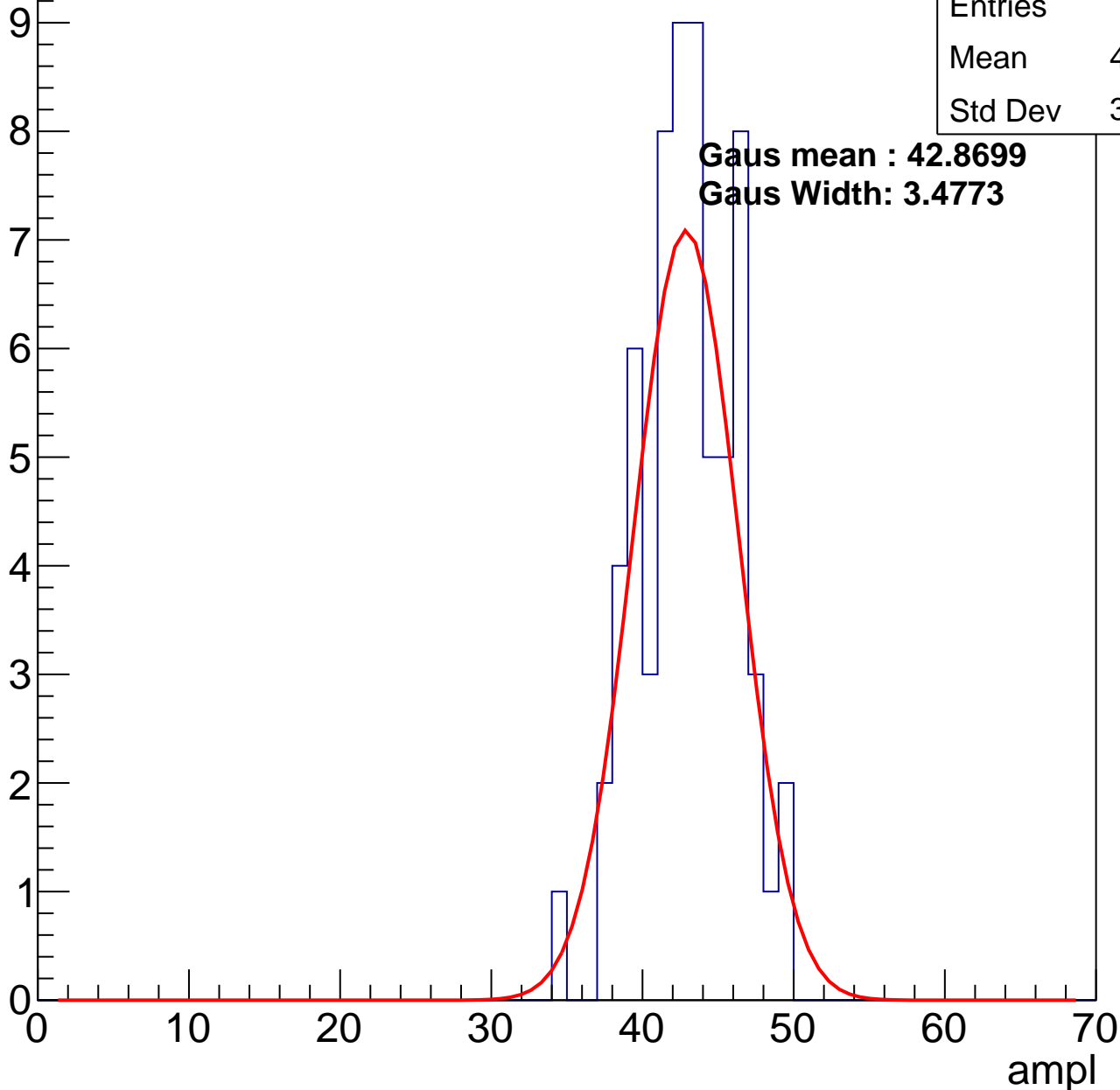
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	42.53
Std Dev	3.125

**Gaus mean : 42.8699**

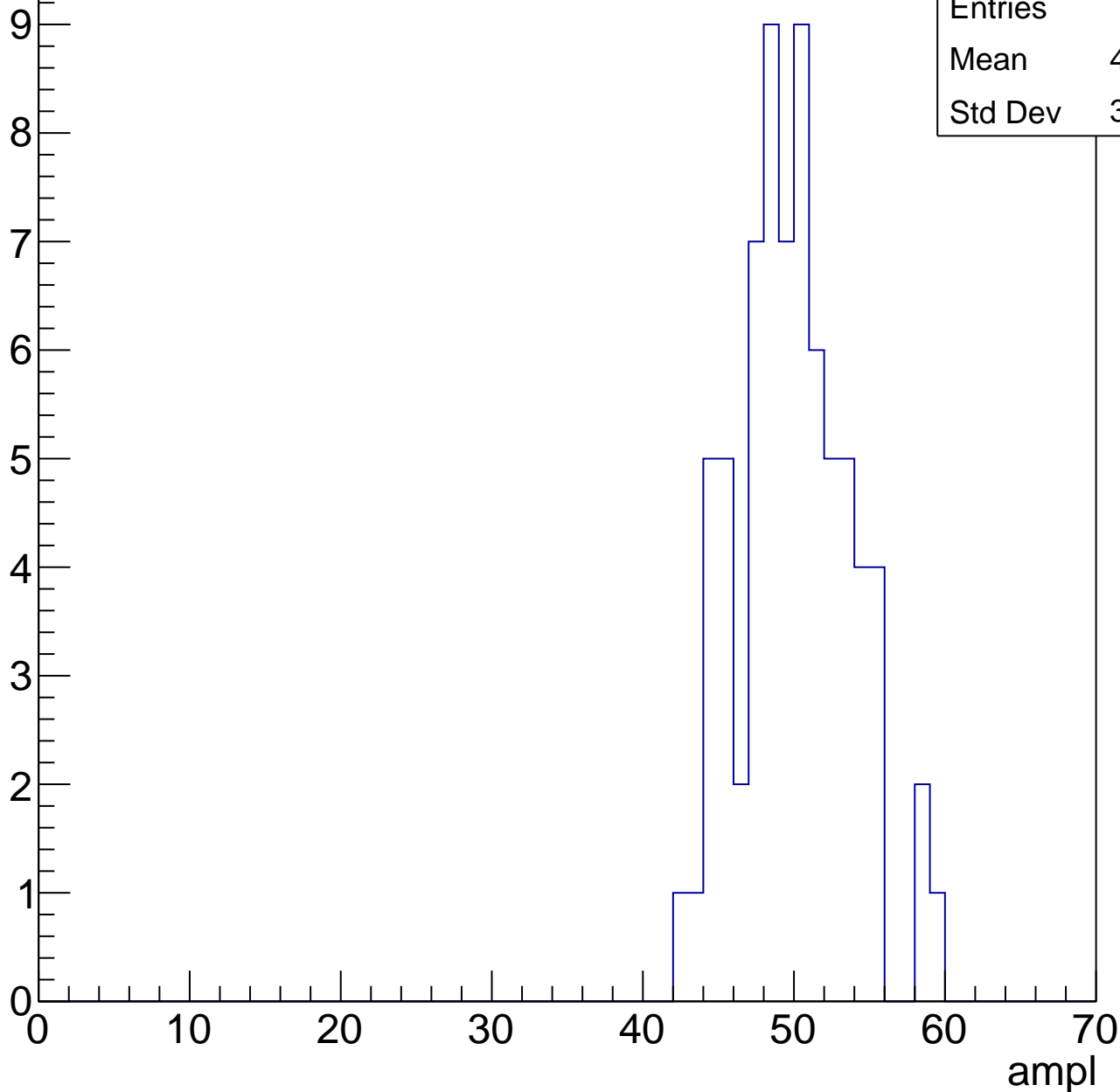
**Gaus Width: 3.4773**



# B1L000S, U23-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

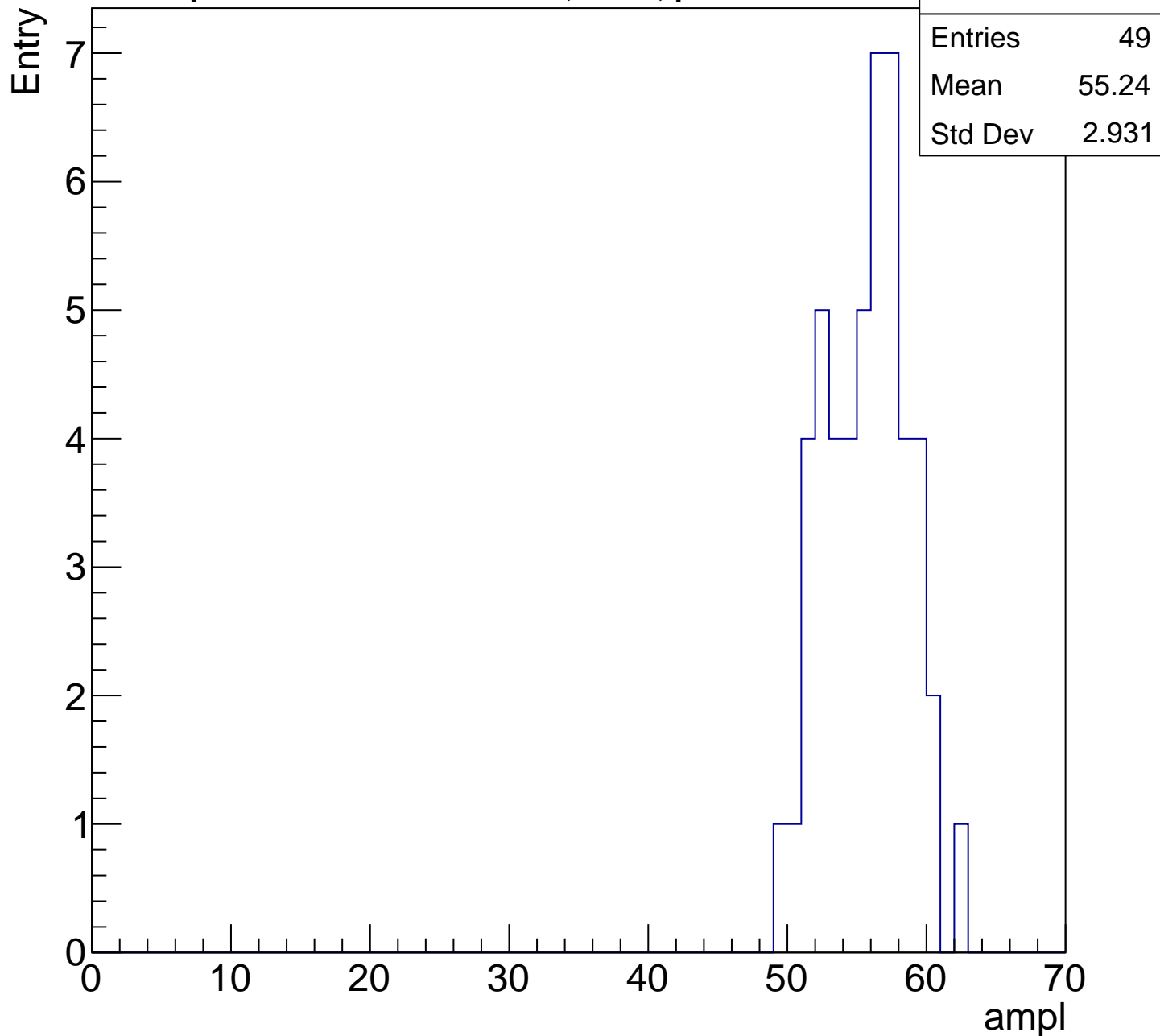
Entry



Entries	73
Mean	49.56
Std Dev	3.675

# B1L000S, U23-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

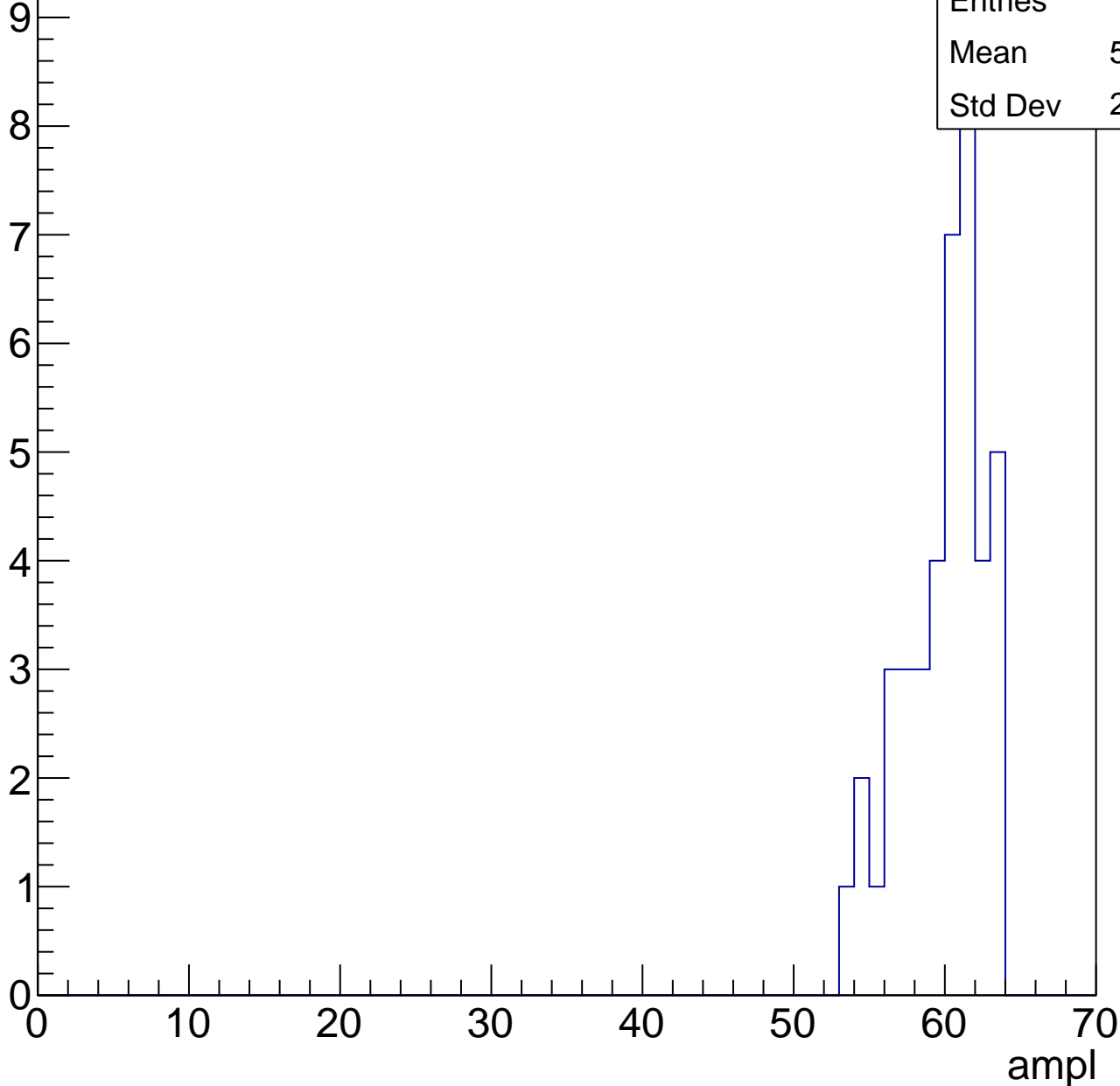


# B1L000S, U23-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	59.45
Std Dev	2.648

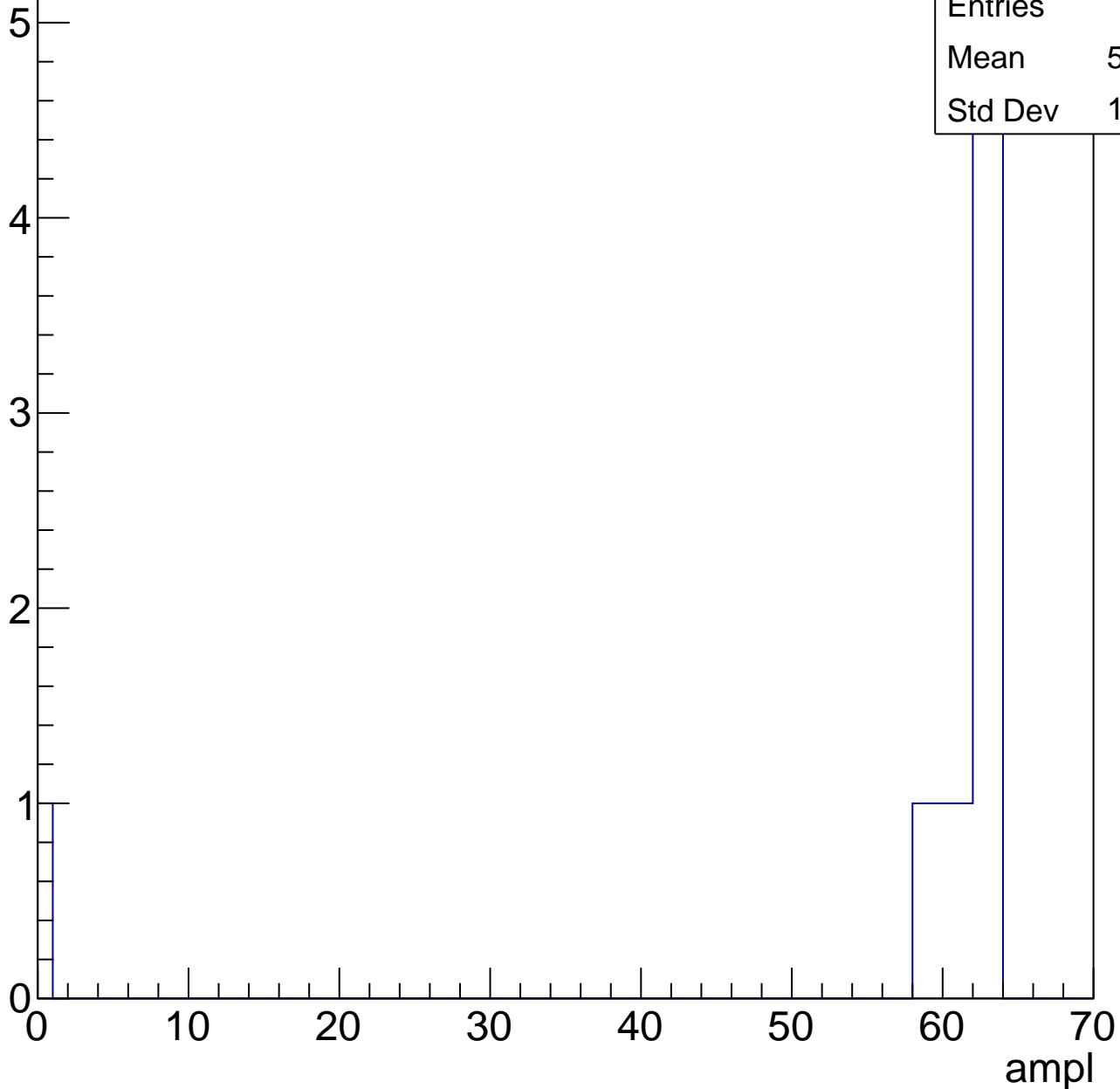


# B1L000S, U23-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.53
Std Dev	15.45





# B1L000S, U23-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U23-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	29.53
Std Dev	3.155

**Gaus mean : 30.2957**

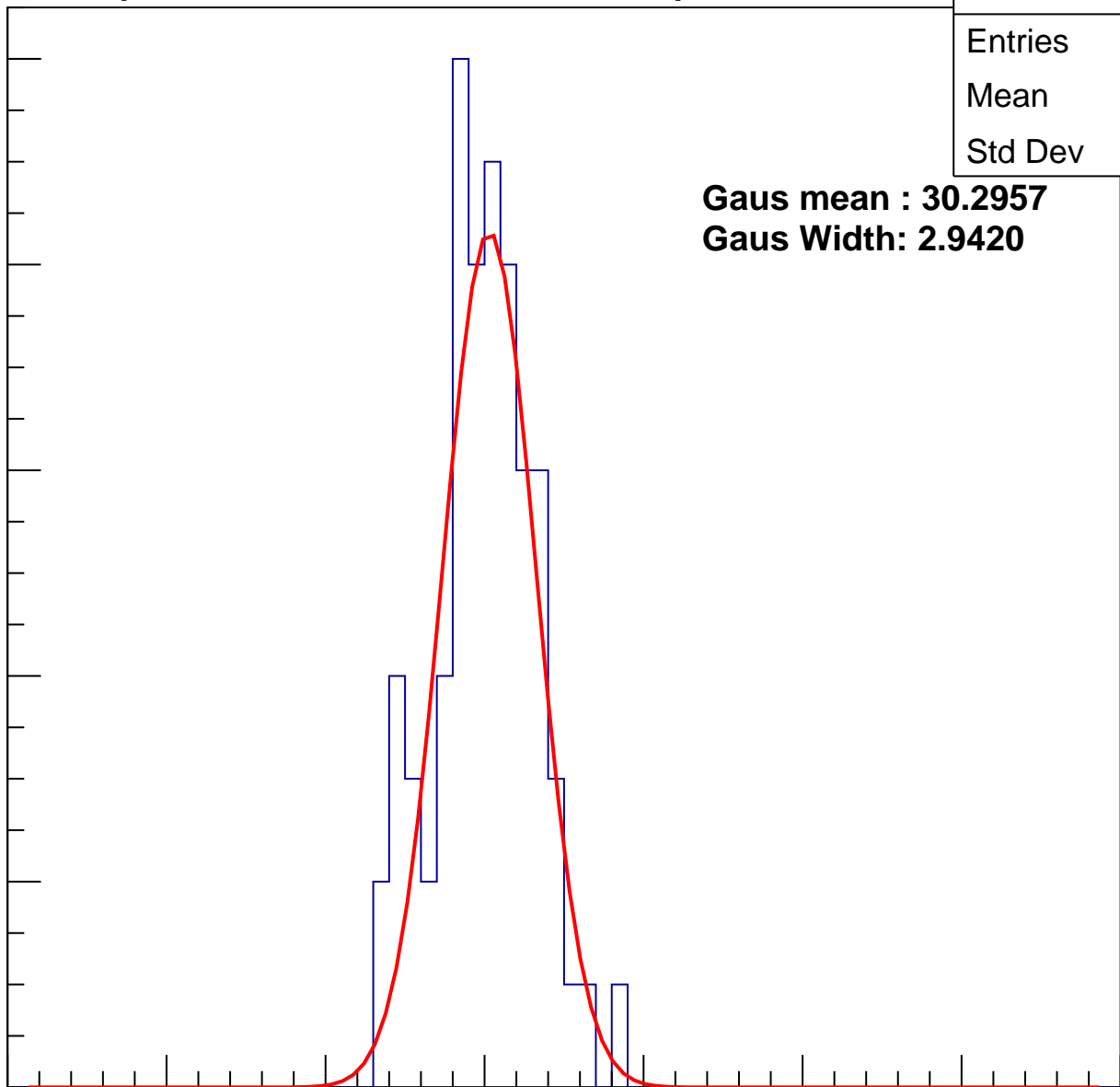
**Gaus Width: 2.9420**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch80, adc1

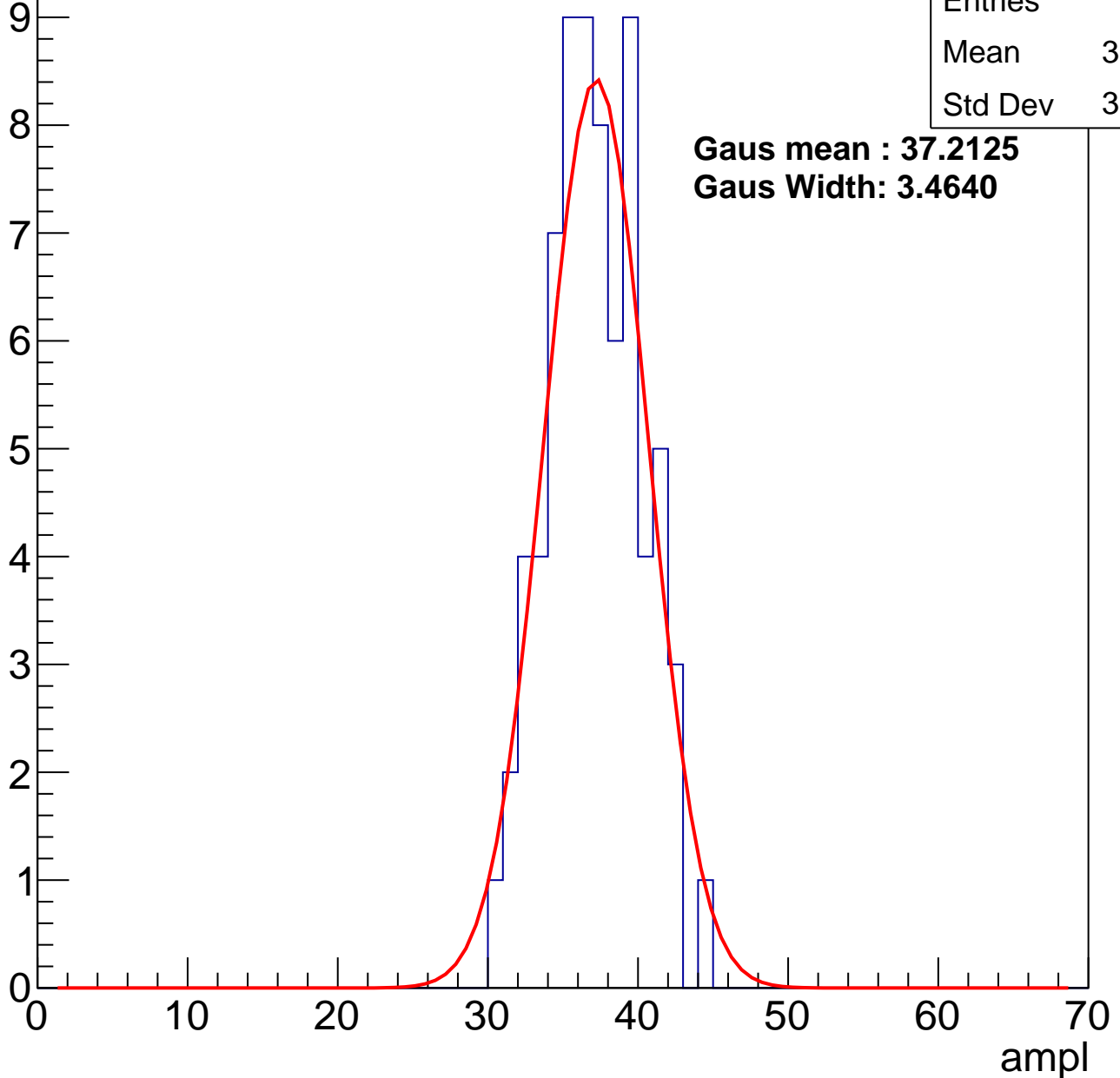
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	36.65
Std Dev	3.042

**Gaus mean : 37.2125**

**Gaus Width: 3.4640**



# B1L000S, U23-ch80, adc2

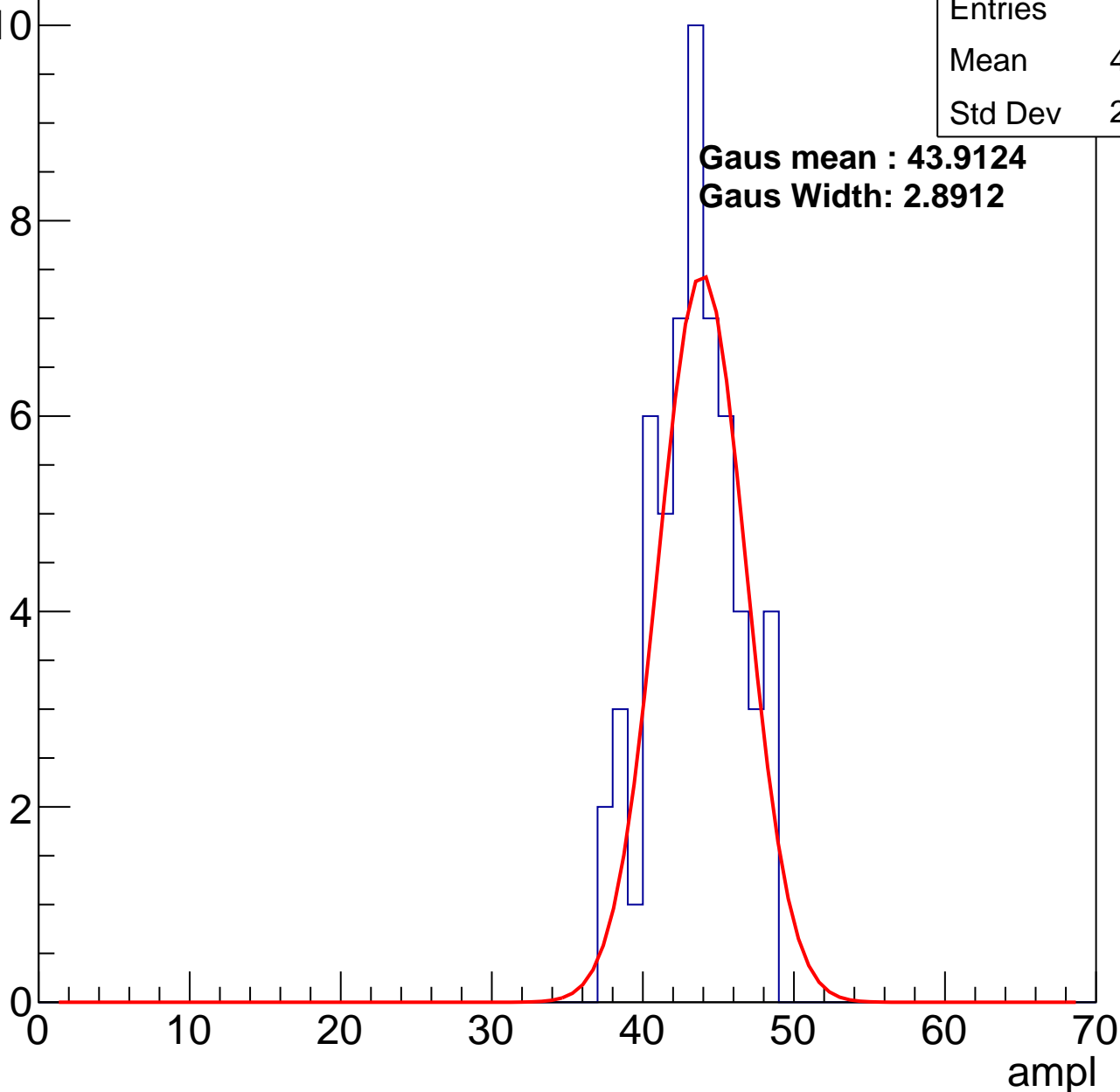
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.95
Std Dev	2.813

**Gaus mean : 43.9124**

**Gaus Width: 2.8912**

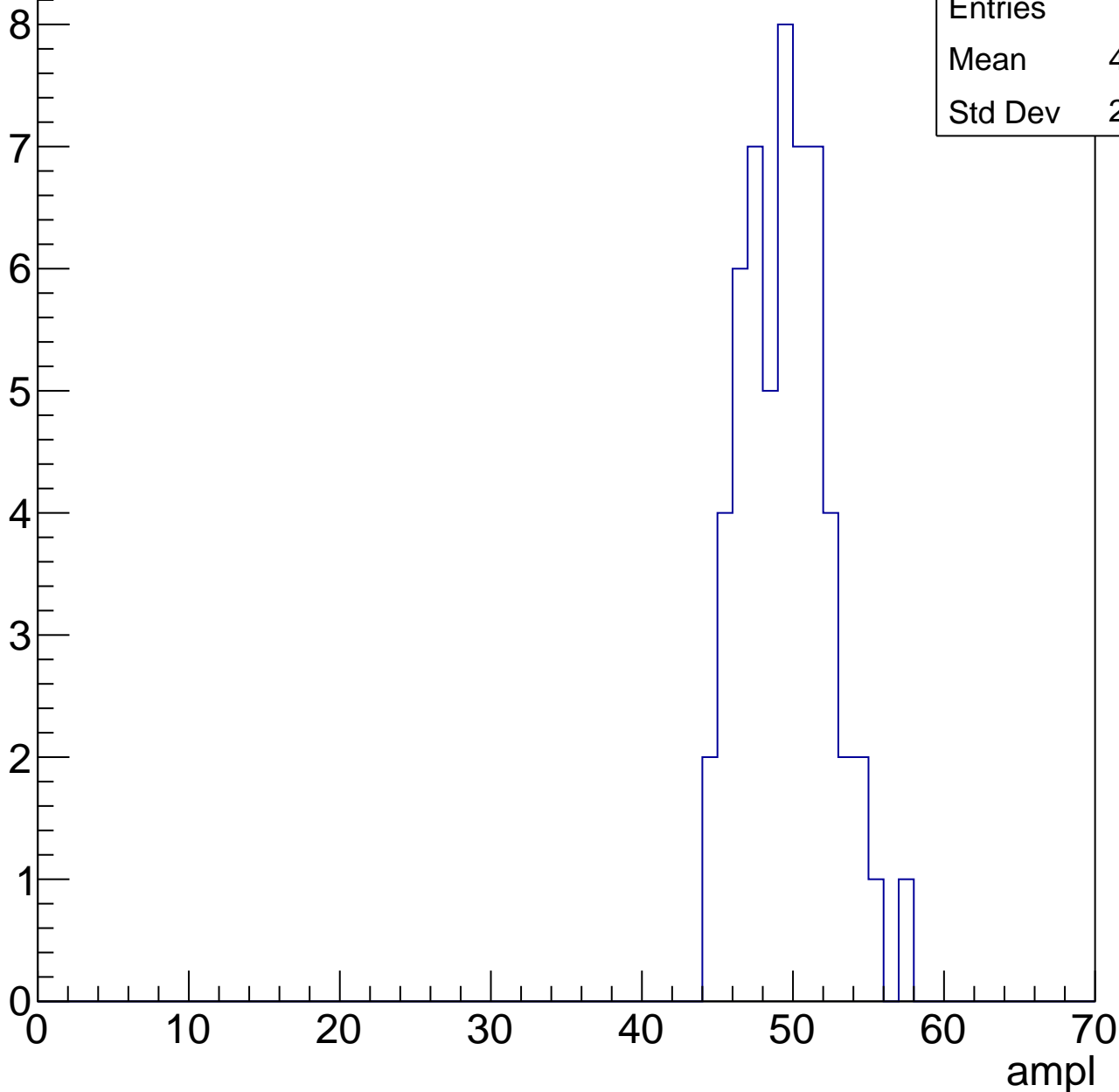


# B1L000S, U23-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

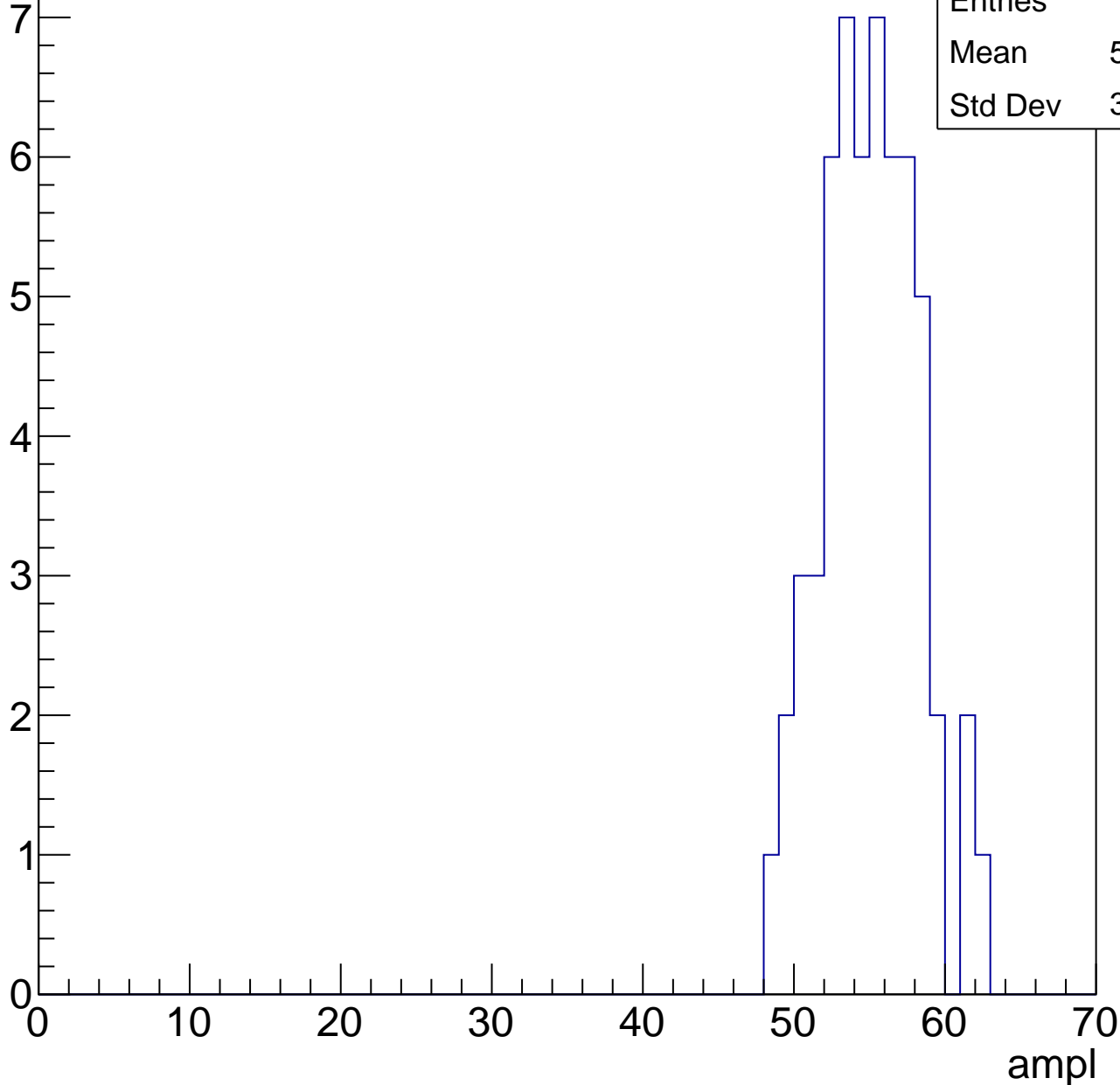
Entries	56
Mean	49.04
Std Dev	2.847



# B1L000S, U23-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	57
Mean	54.58
Std Dev	3.106

# B1L000S, U23-ch80, adc5

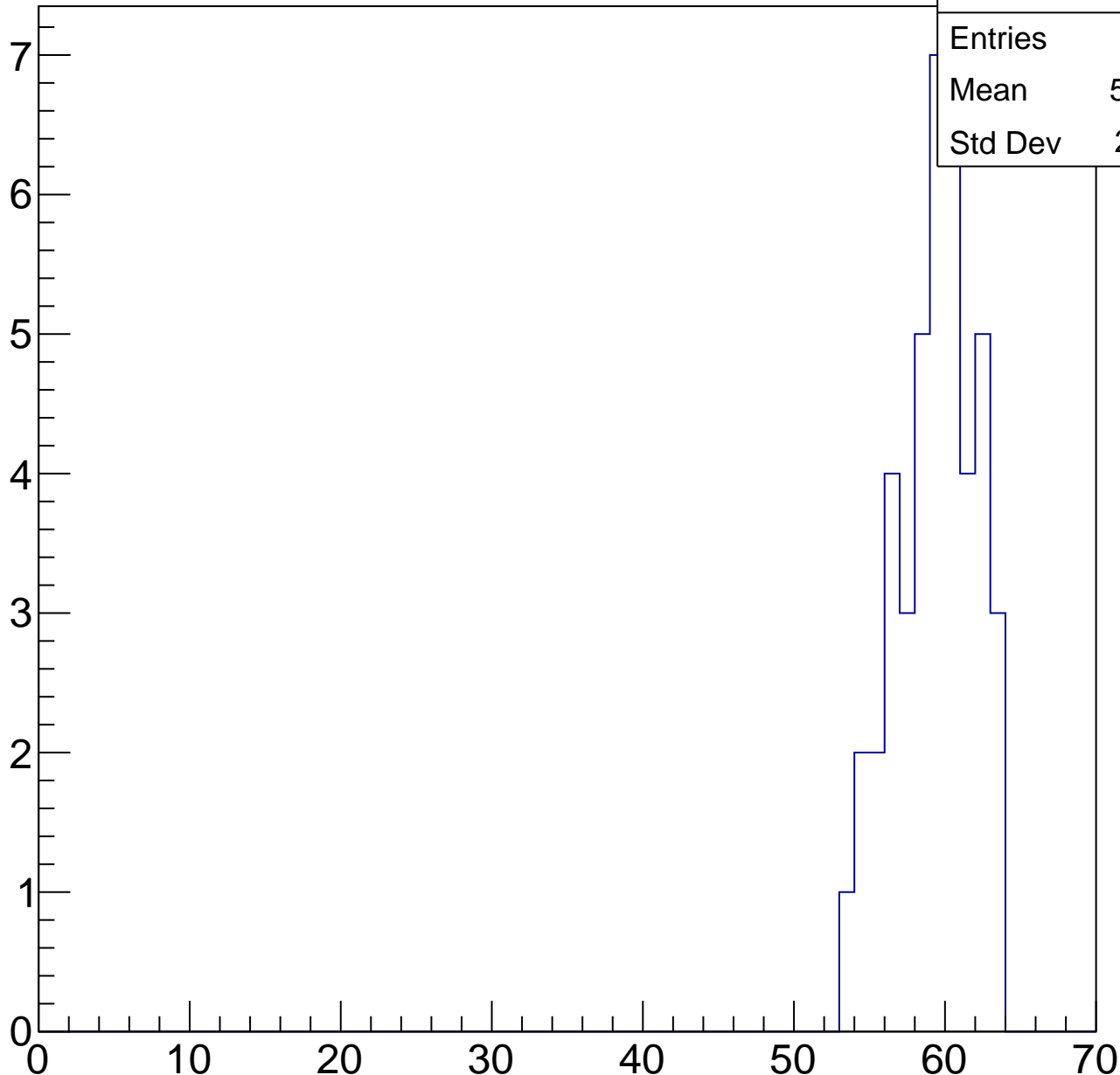
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	58.88
Std Dev	2.581

ampl

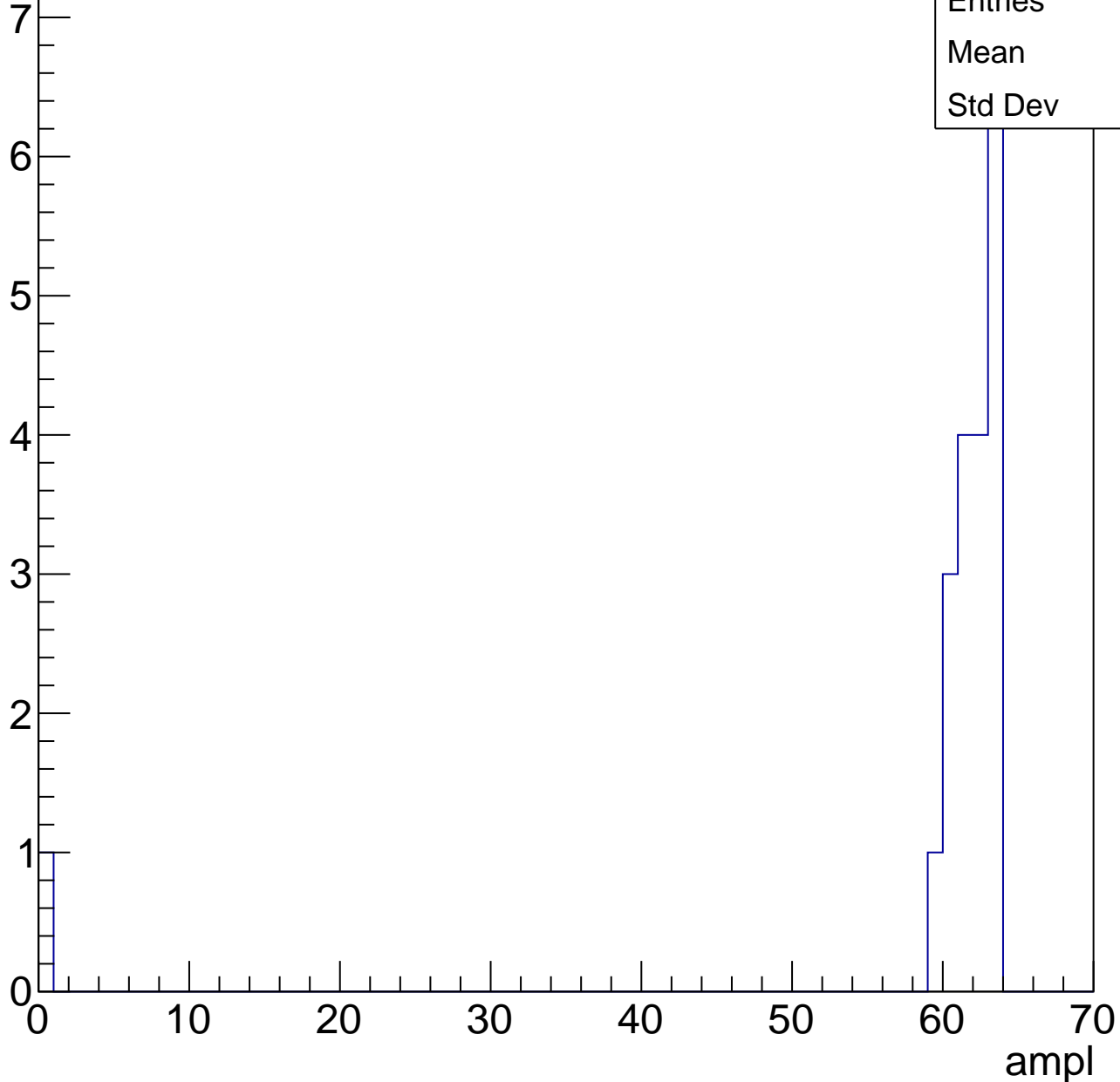


# B1L000S, U23-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.6
Std Dev	13.5





# B1L000S, U23-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch81, adc0

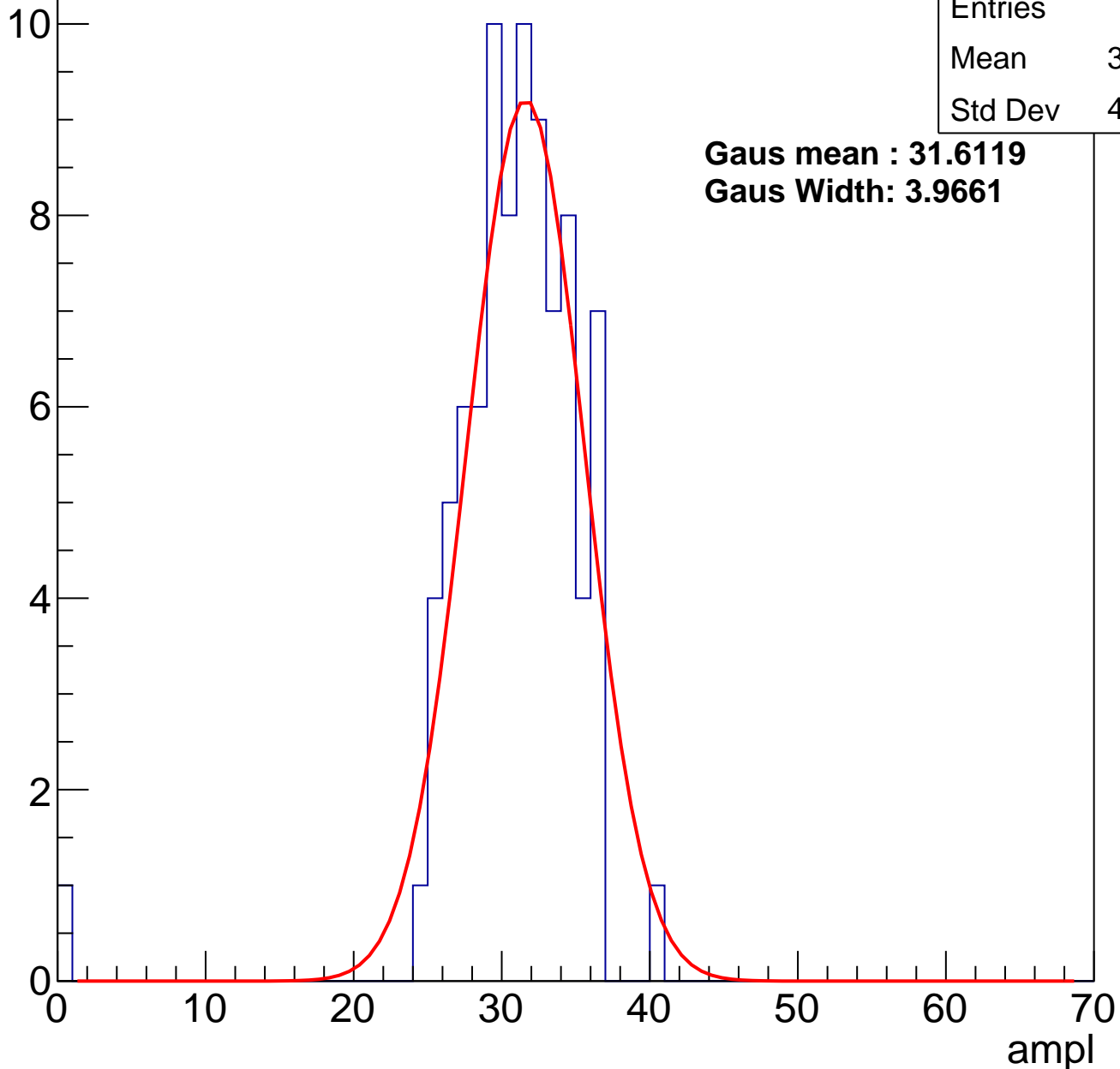
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	30.43
Std Dev	4.645

**Gaus mean : 31.6119**

**Gaus Width: 3.9661**

Entry



# B1L000S, U23-ch81, adc1

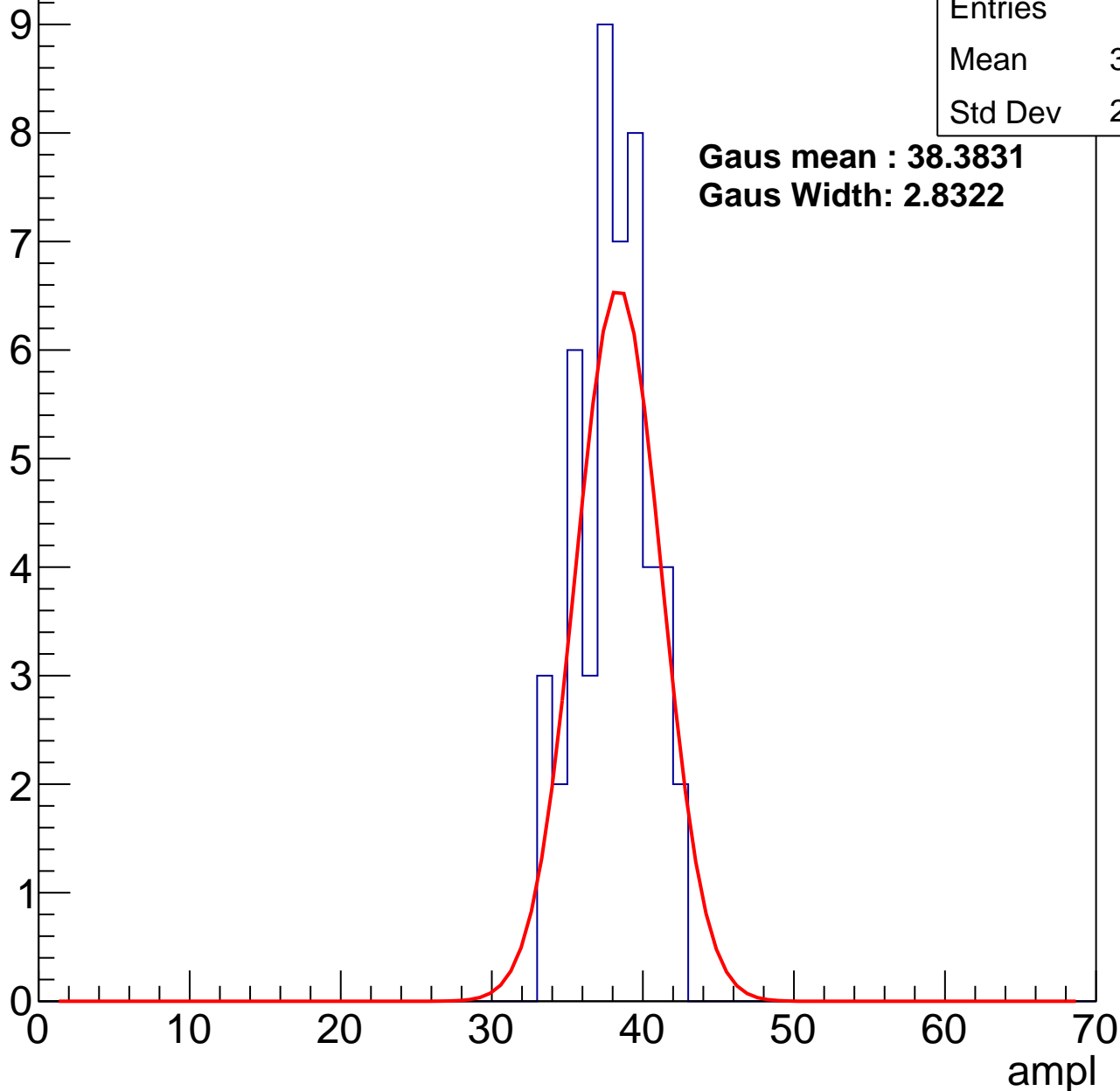
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	37.58
Std Dev	2.353

**Gaus mean : 38.3831**

**Gaus Width: 2.8322**



# B1L000S, U23-ch81, adc2

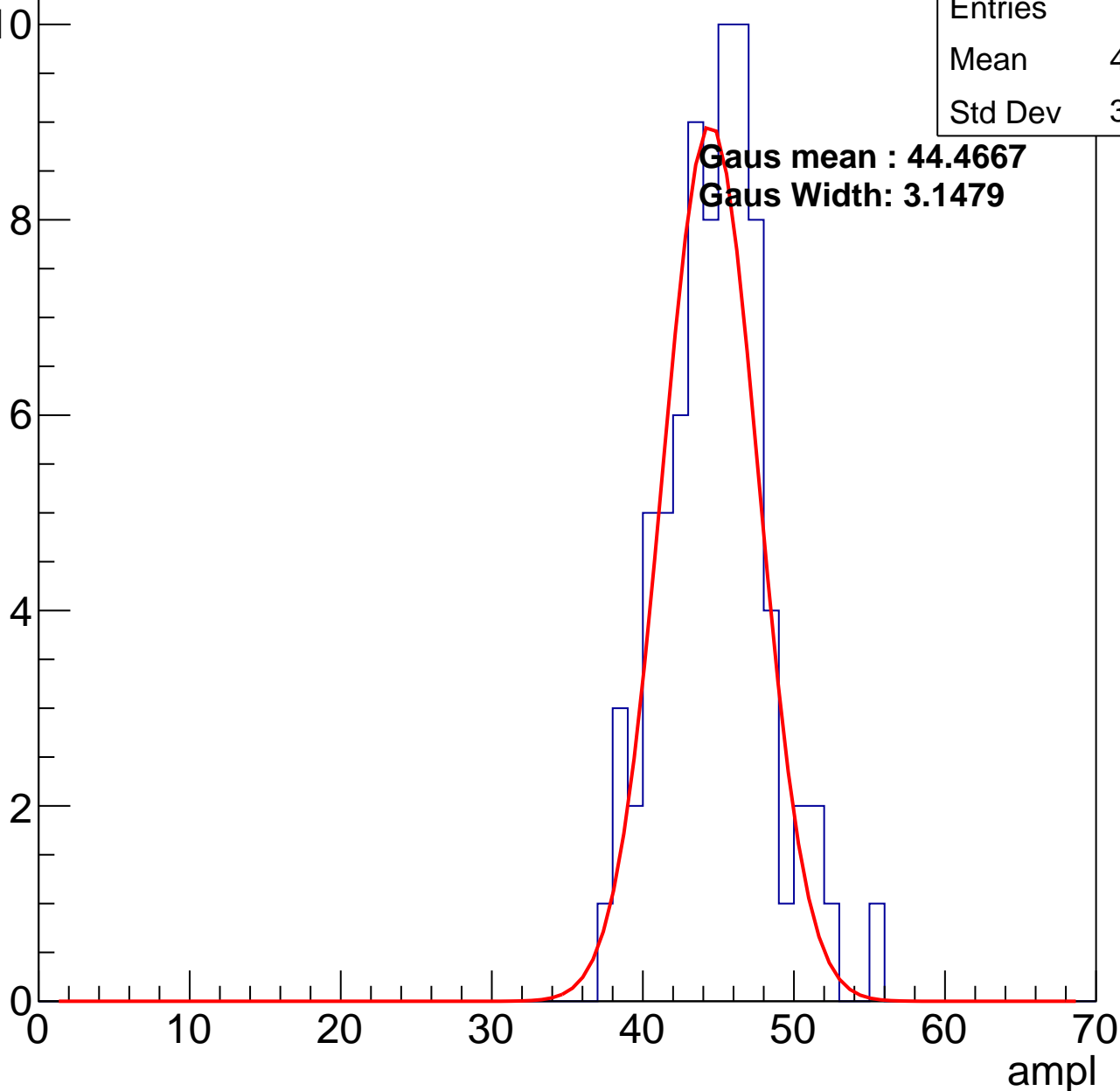
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	44.37
Std Dev	3.435

**Gaus mean : 44.4667**

**Gaus Width: 3.1479**

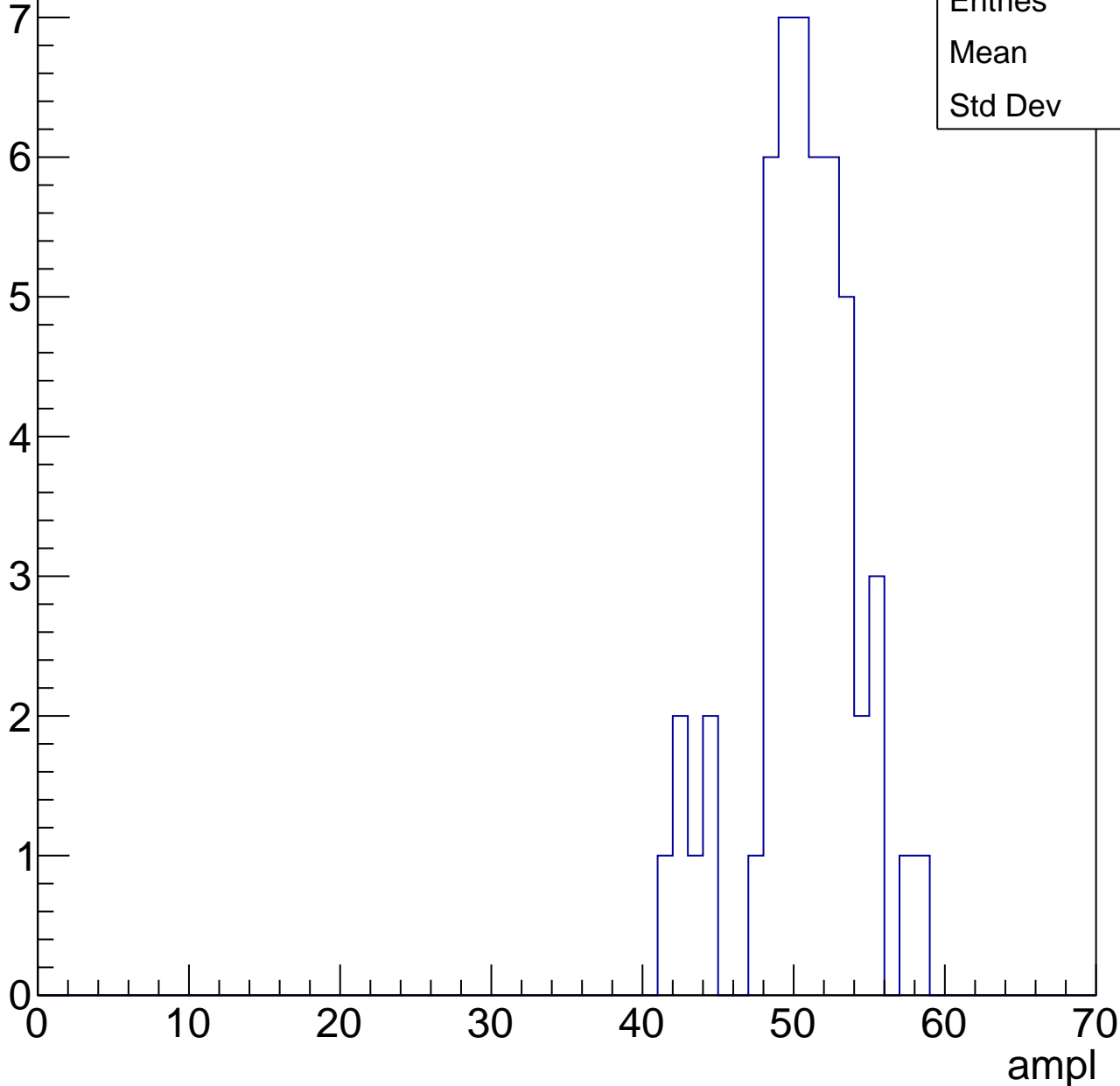


# B1L000S, U23-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	50.1
Std Dev	3.61

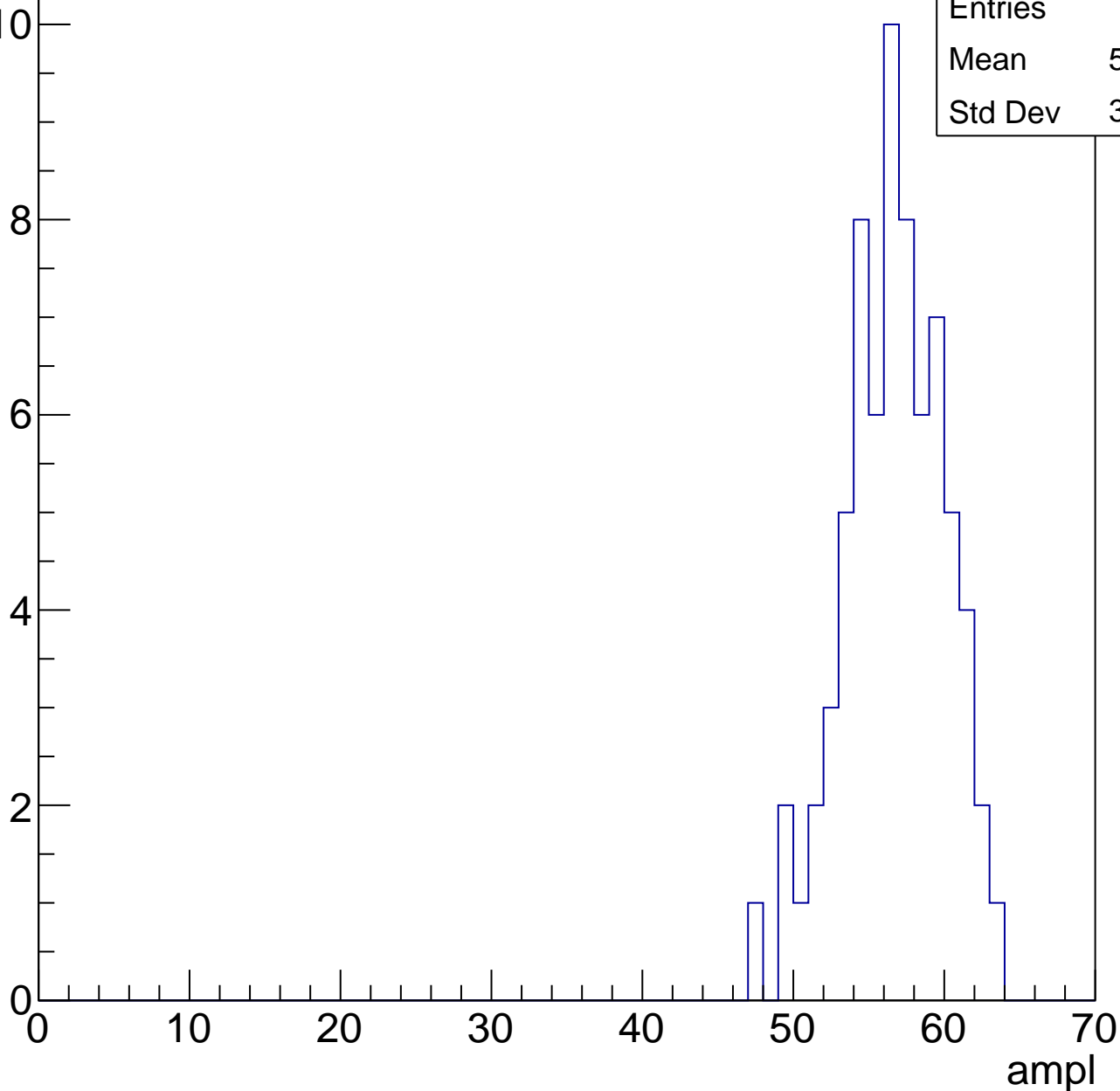


# B1L000S, U23-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	56.17
Std Dev	3.336

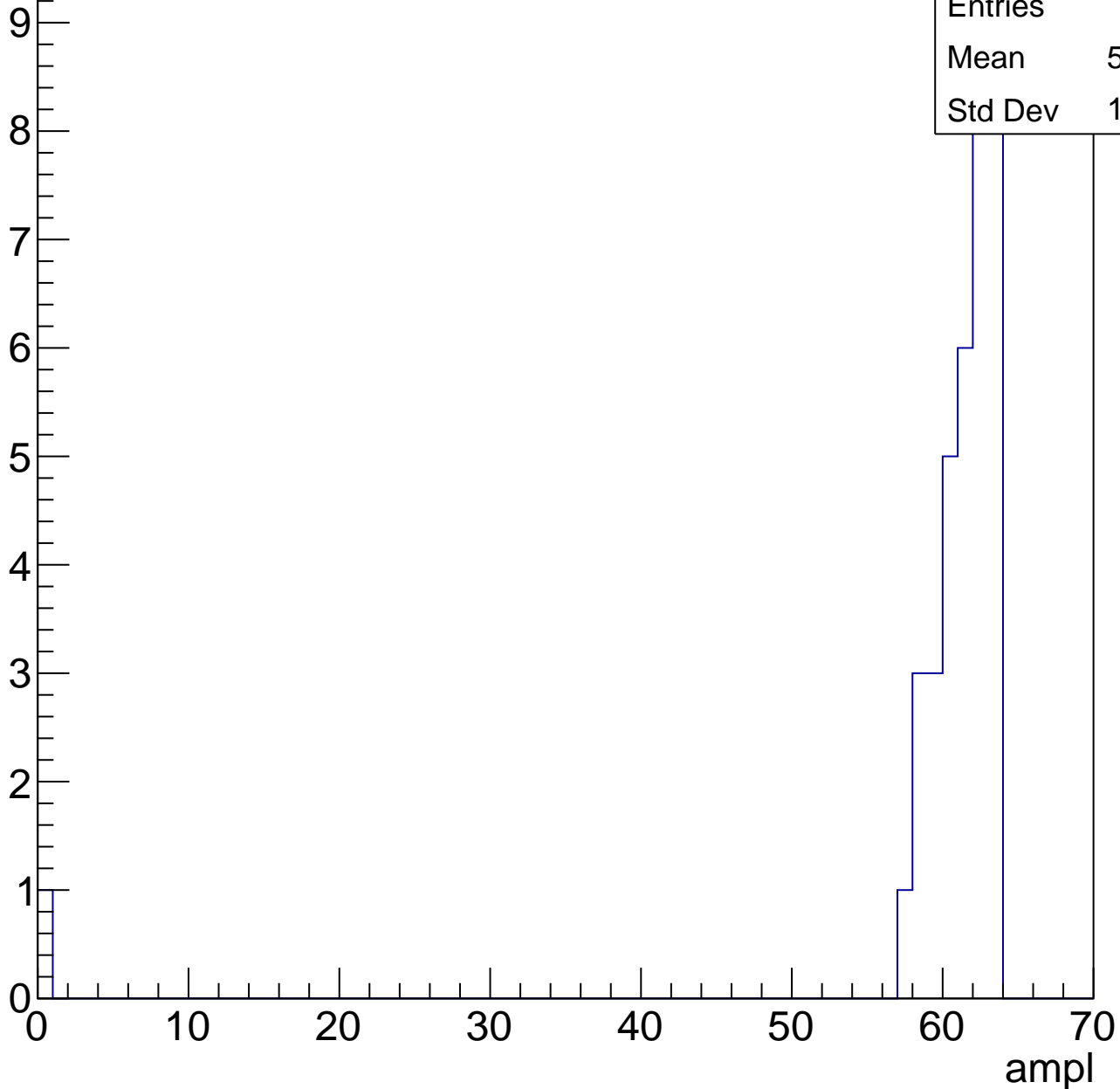


# B1L000S, U23-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	59.36
Std Dev	10.18



# B1L000S, U23-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	28.25
Std Dev	4.493

**Gaus mean : 29.1472**

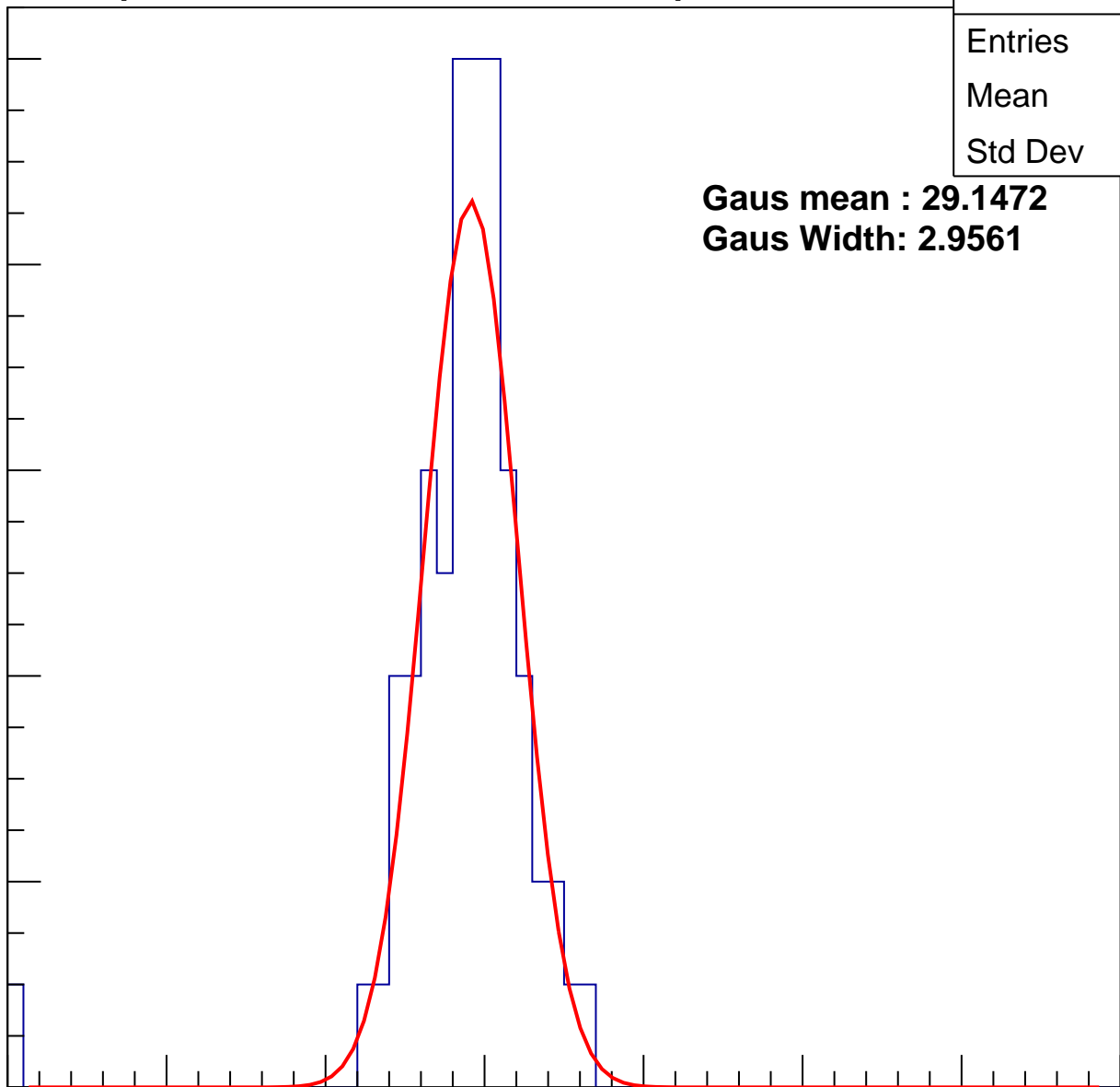
**Gaus Width: 2.9561**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch82, adc1

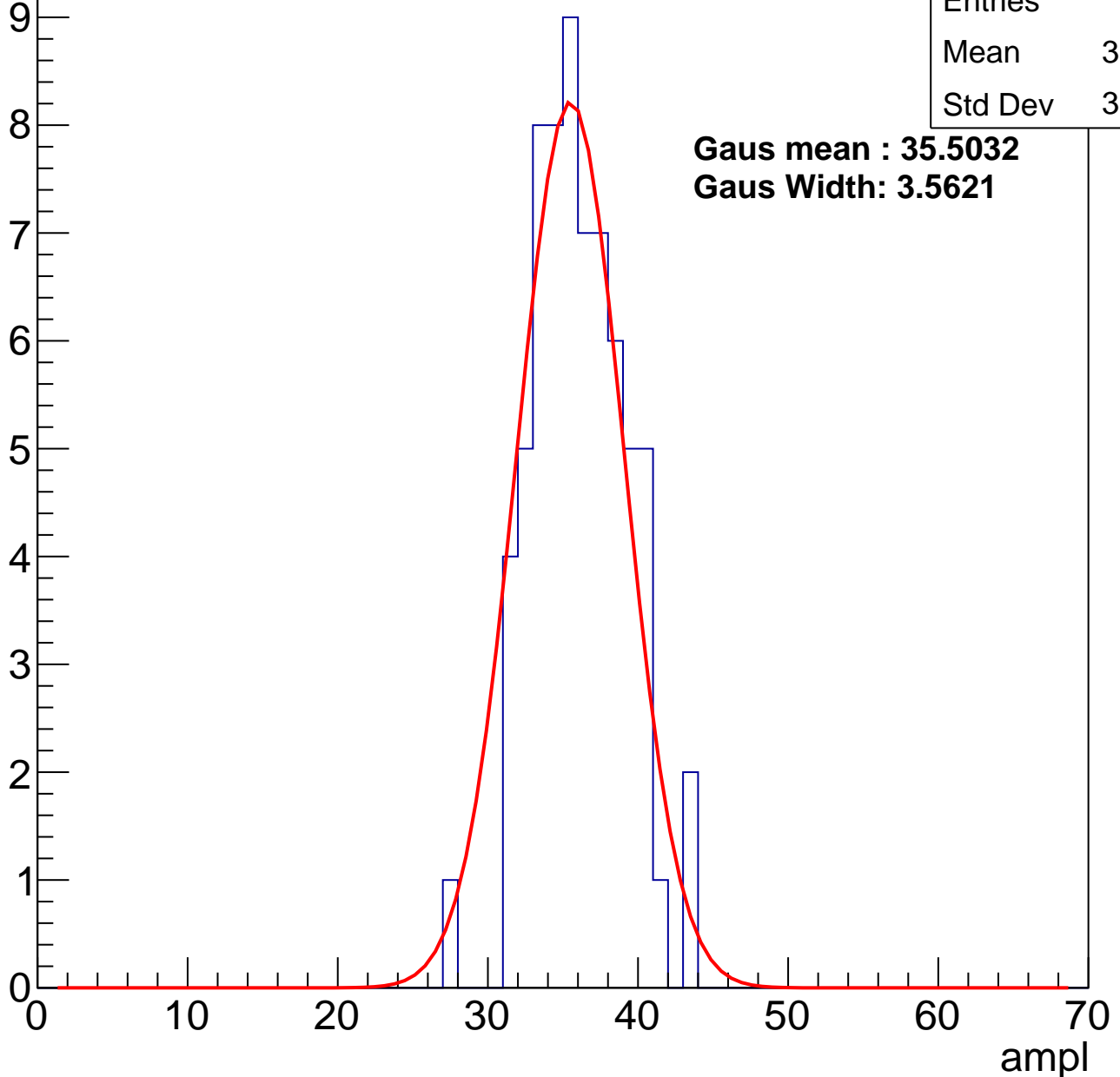
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.63
Std Dev	3.077

**Gaus mean : 35.5032**

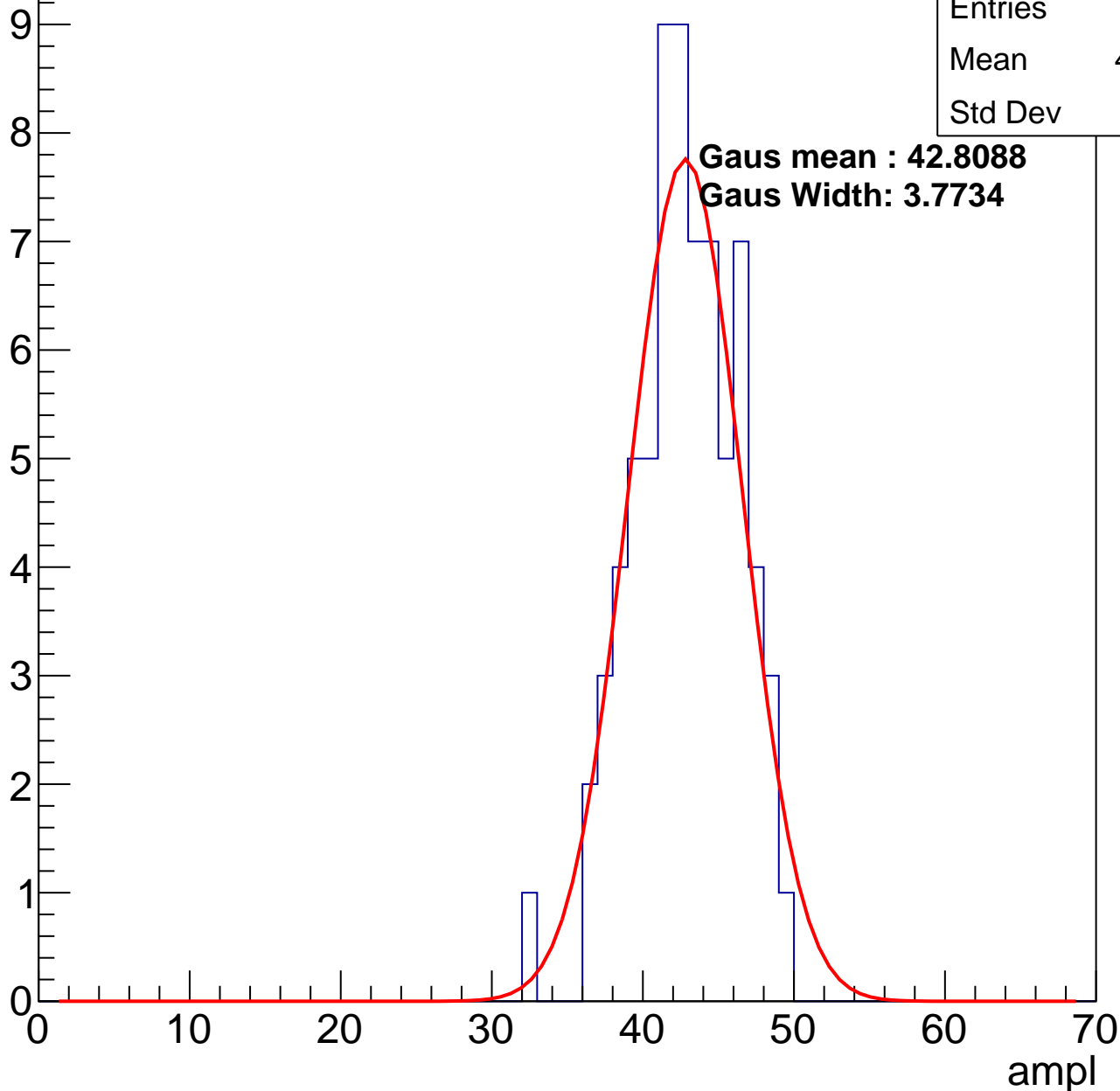
**Gaus Width: 3.5621**



# B1L000S, U23-ch82, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

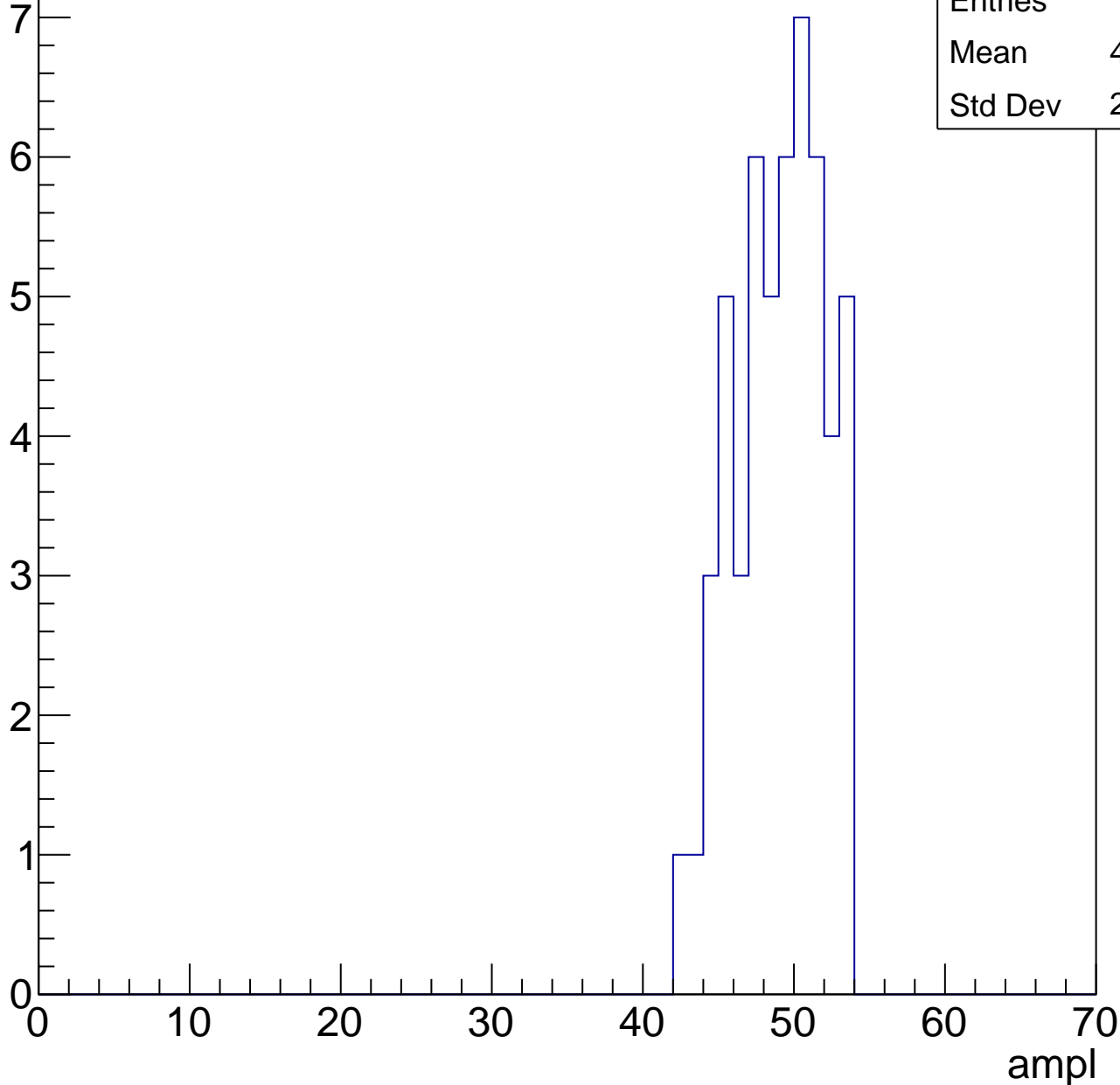
Entry



# B1L000S, U23-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

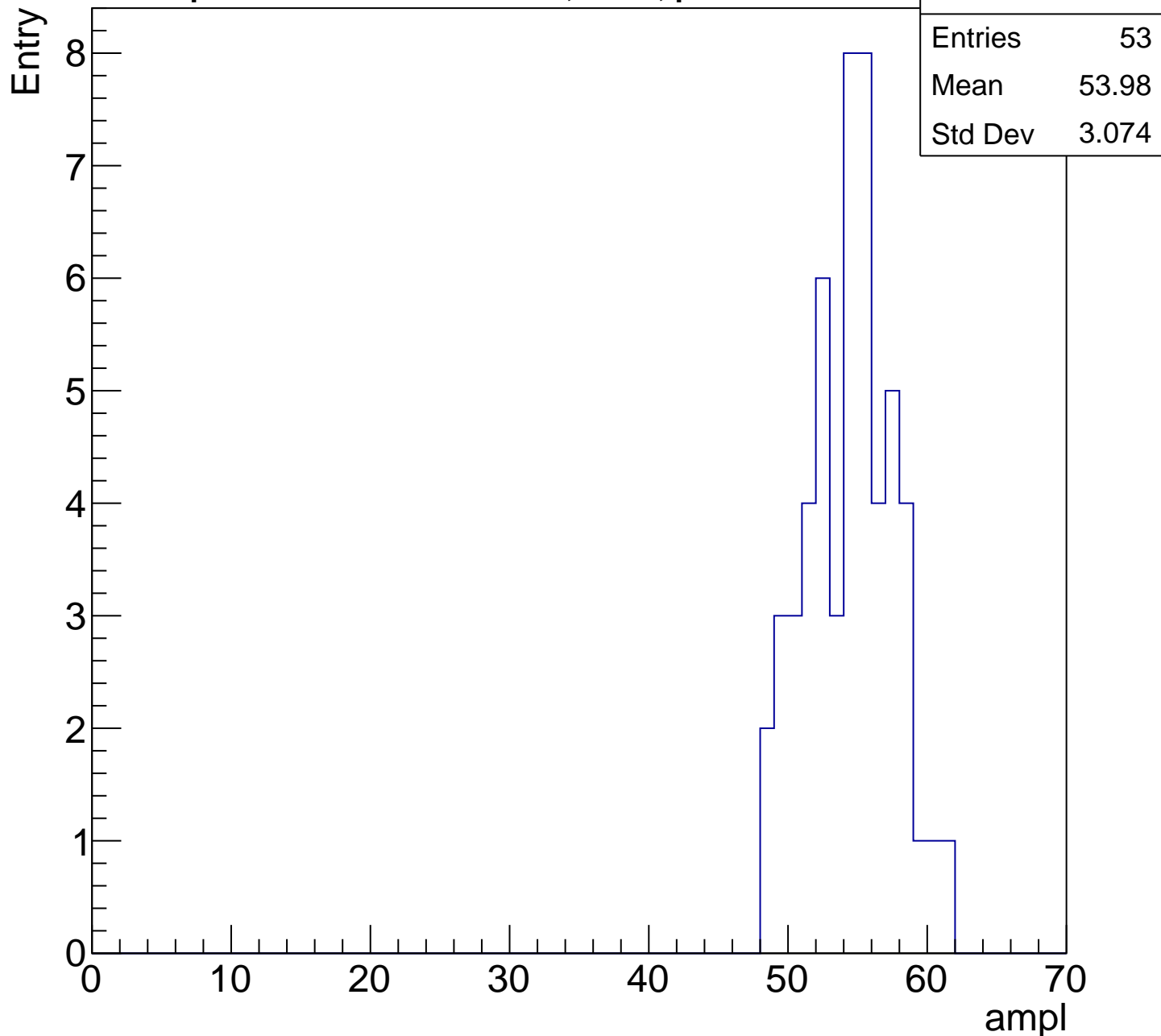
Entry



Entries	52
Mean	48.56
Std Dev	2.885

# B1L000S, U23-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch82, adc5

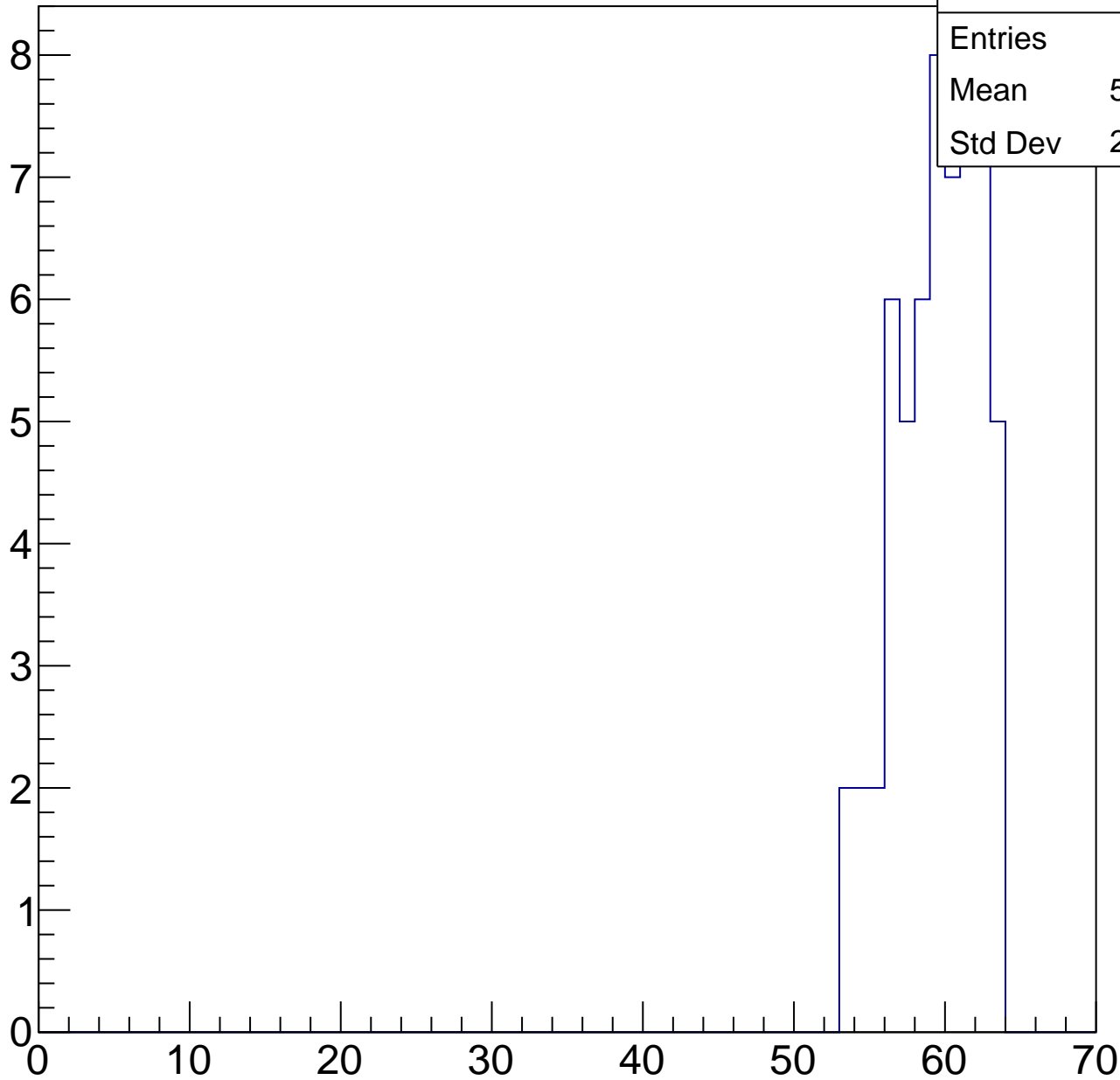
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	59.05
Std Dev	2.683

ampl

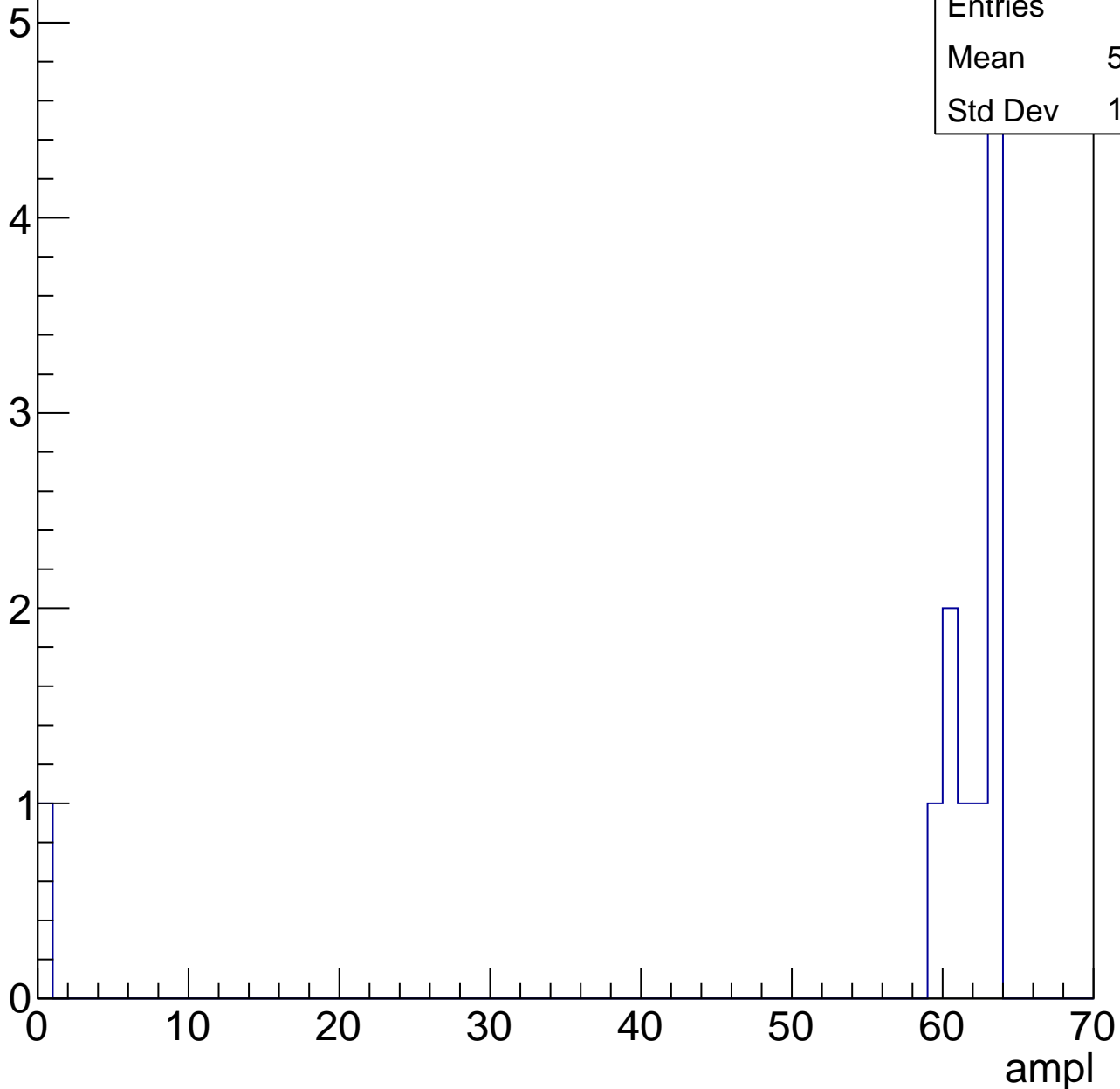


# B1L000S, U23-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	56.09
Std Dev	17.79

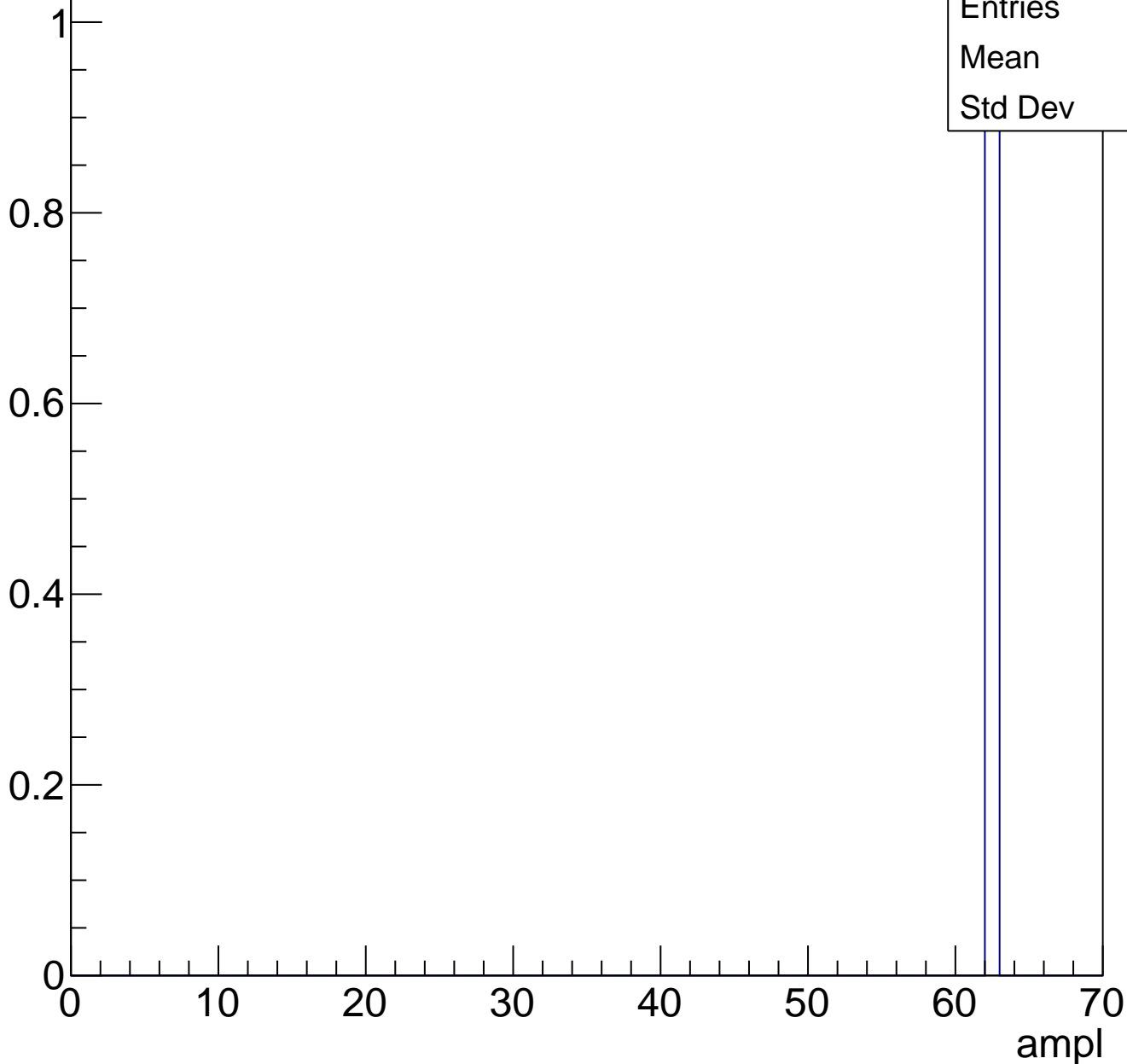




# B1L000S, U23-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch83, adc0

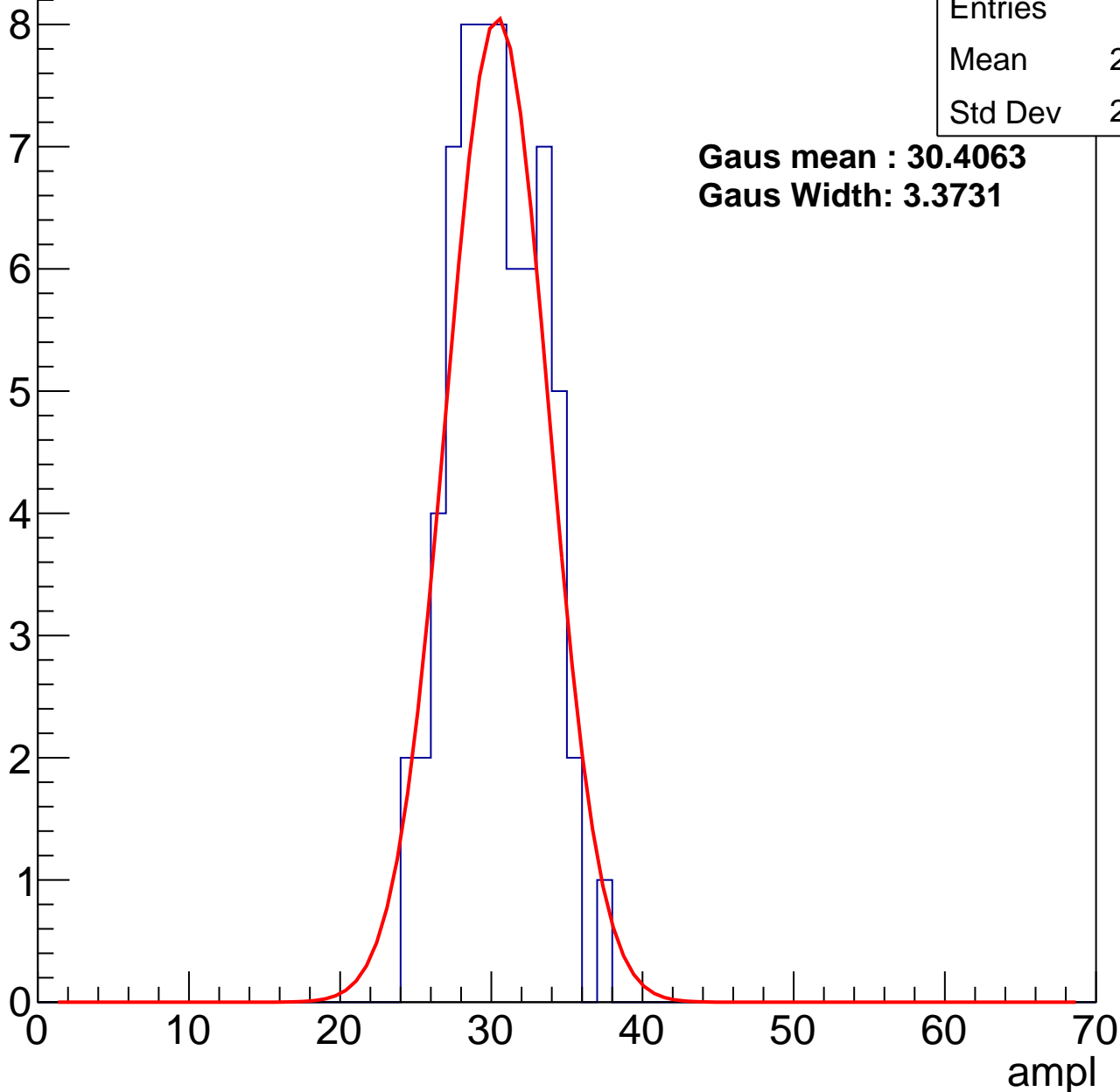
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.89
Std Dev	2.914

**Gaus mean : 30.4063**

**Gaus Width: 3.3731**



# B1L000S, U23-ch83, adc1

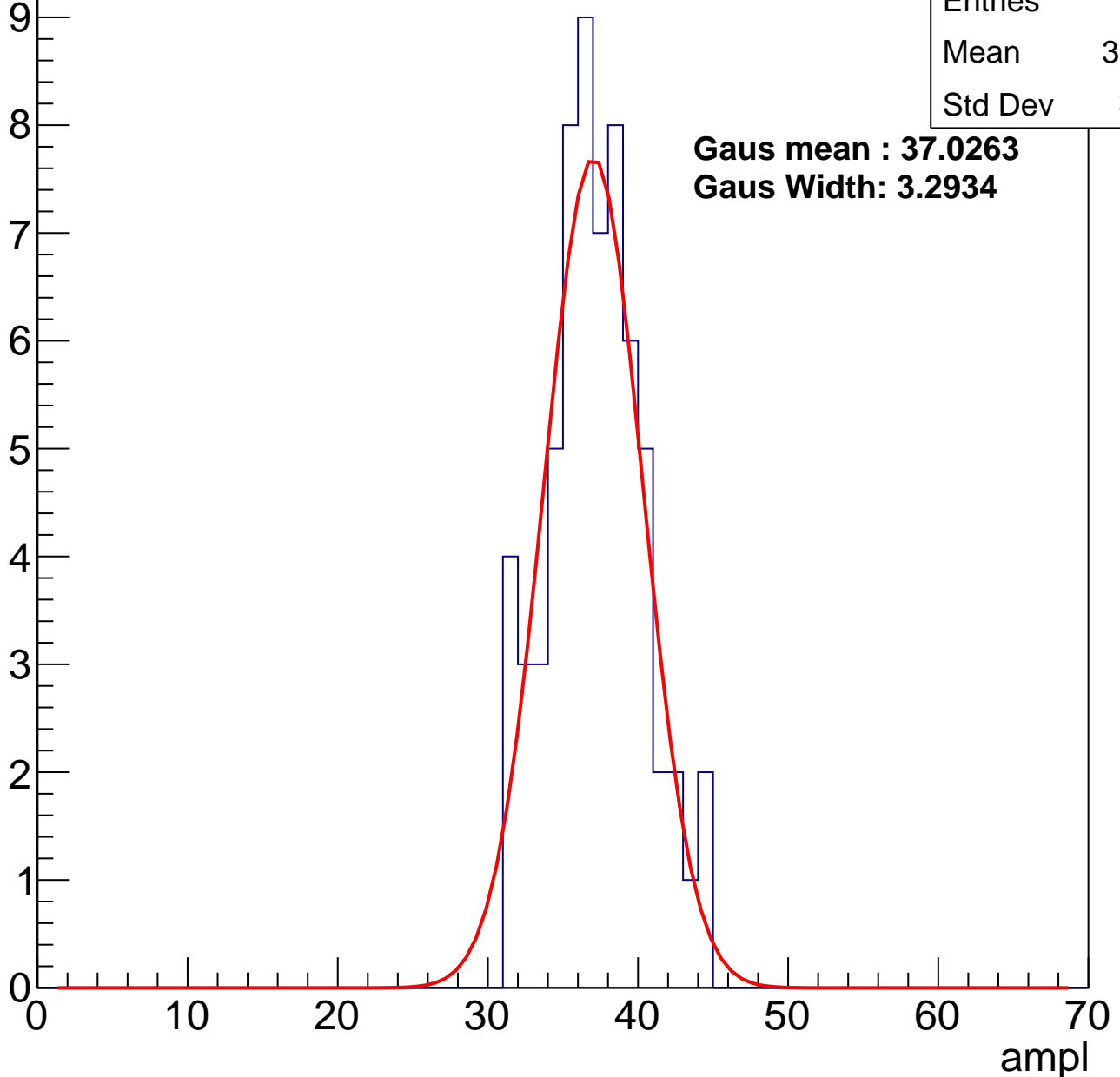
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	36.72
Std Dev	3.14

**Gaus mean : 37.0263**

**Gaus Width: 3.2934**



# B1L000S, U23-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 55

Mean 42.71

Std Dev 3.178

**Gaus mean : 43.0107**

**Gaus Width: 3.3907**

ampl

0

10

20

30

40

50

60

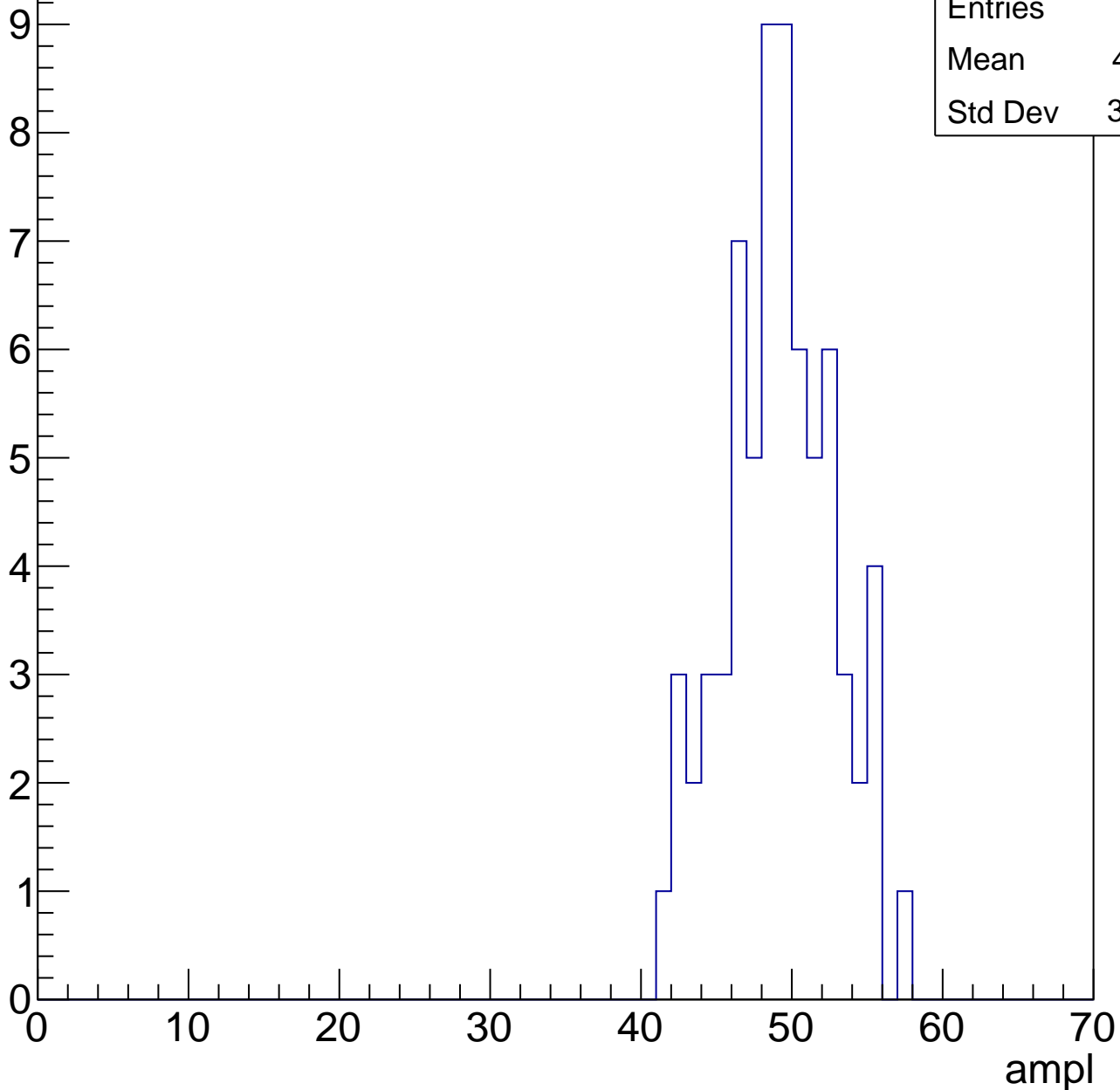
70

# B1L000S, U23-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.71
Std Dev	3.568

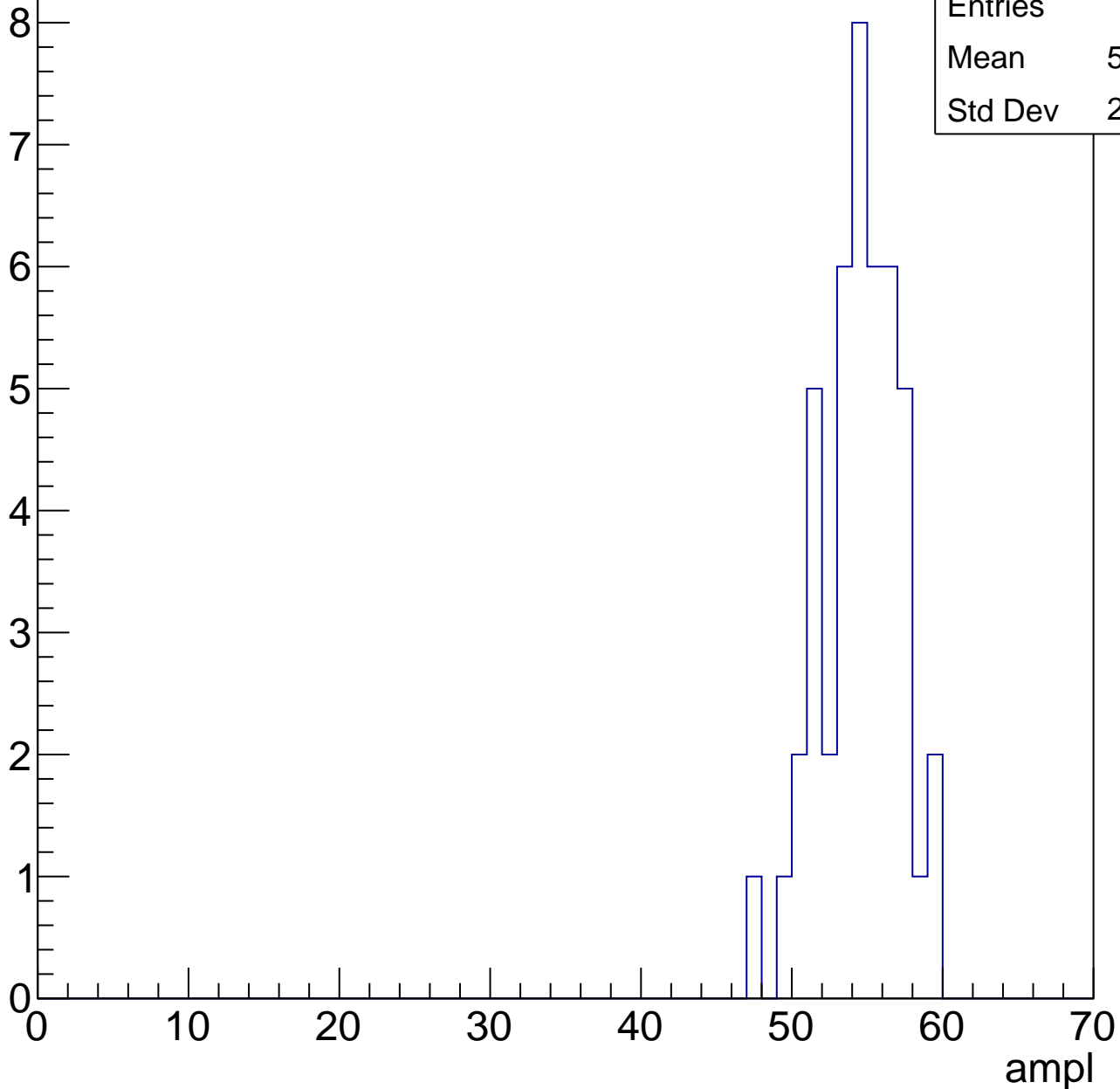


# B1L000S, U23-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	54.04
Std Dev	2.607

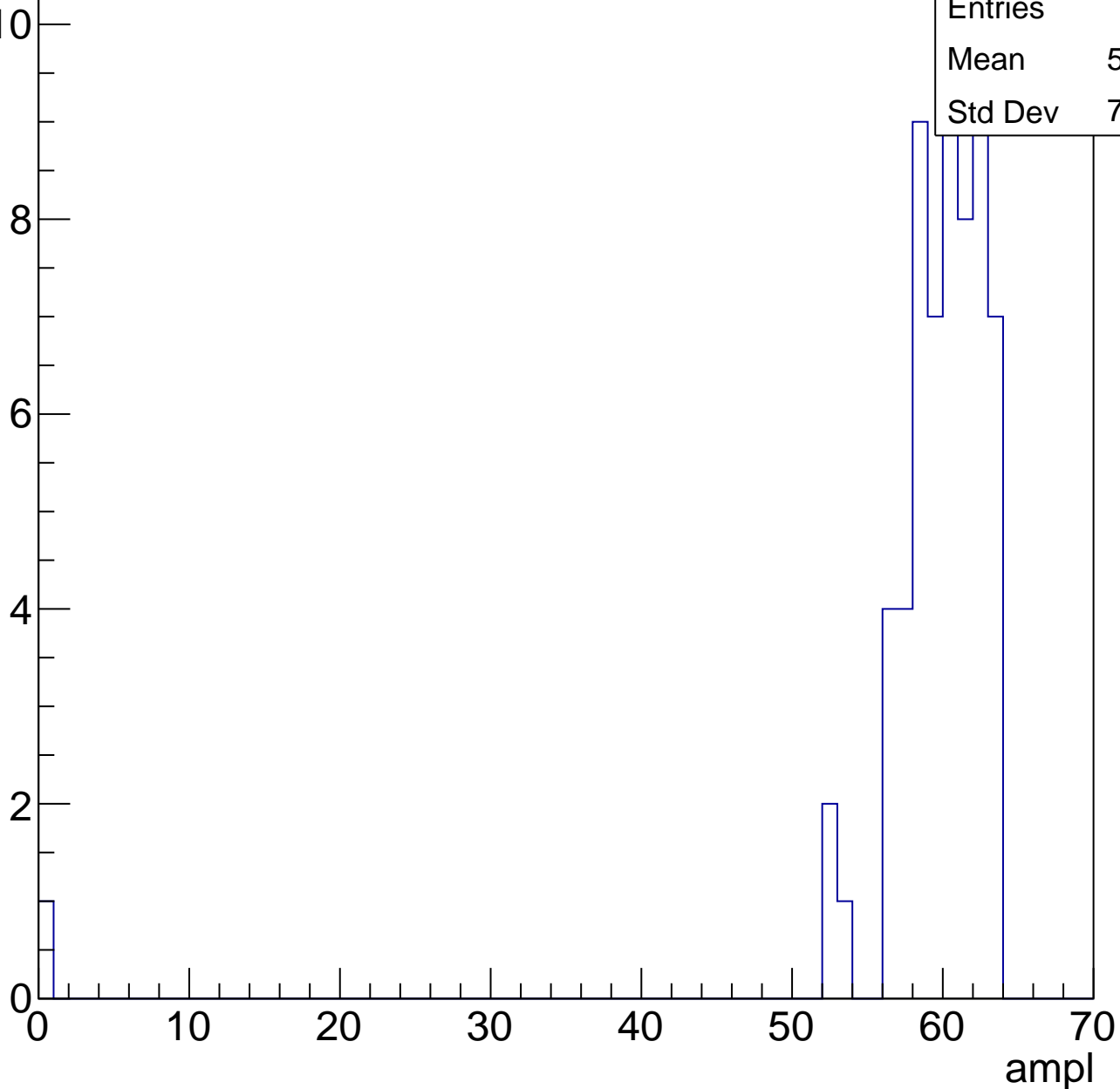


# B1L000S, U23-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

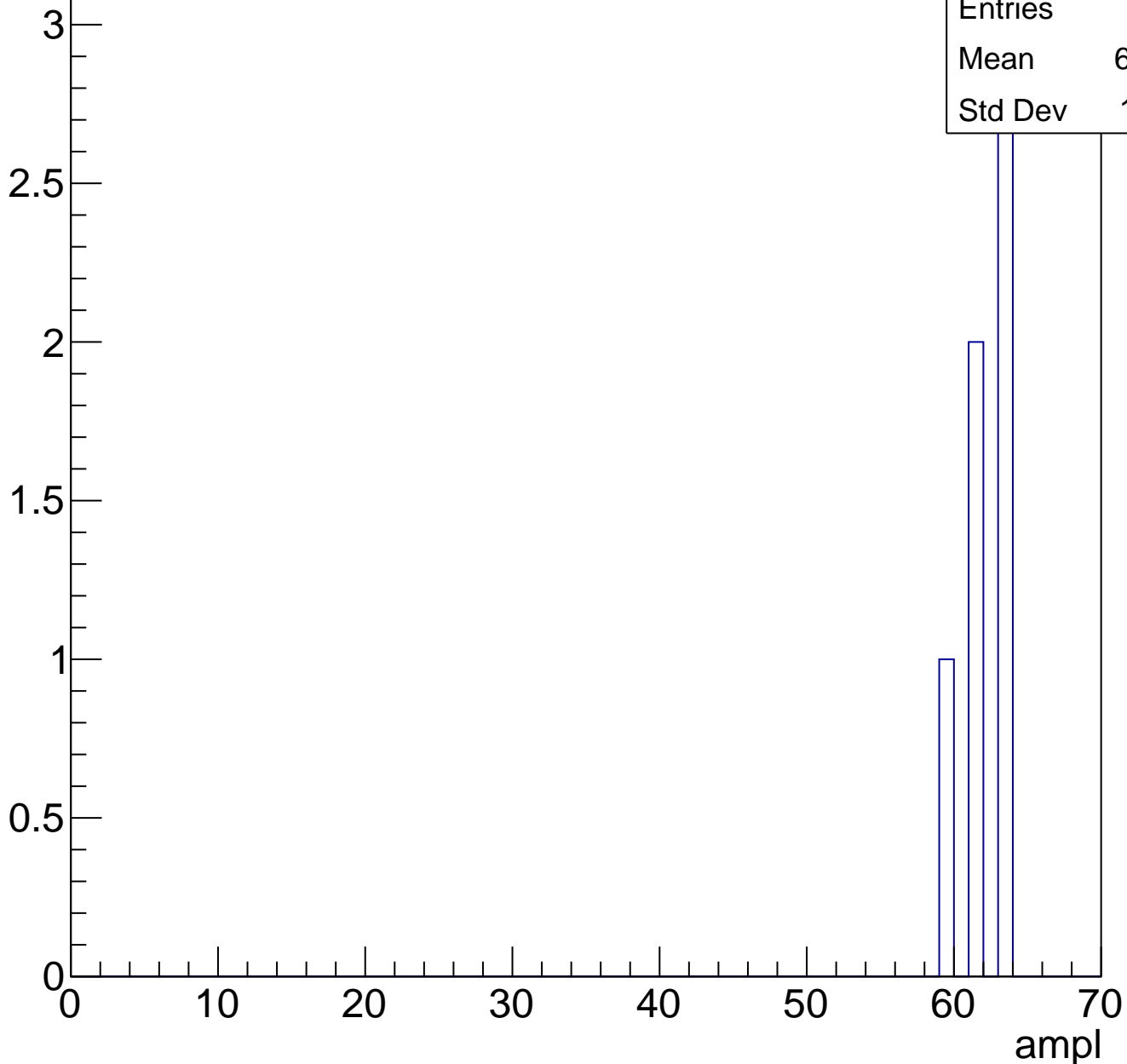
Entries	63
Mean	58.62
Std Dev	7.879



# B1L000S, U23-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch84, adc0

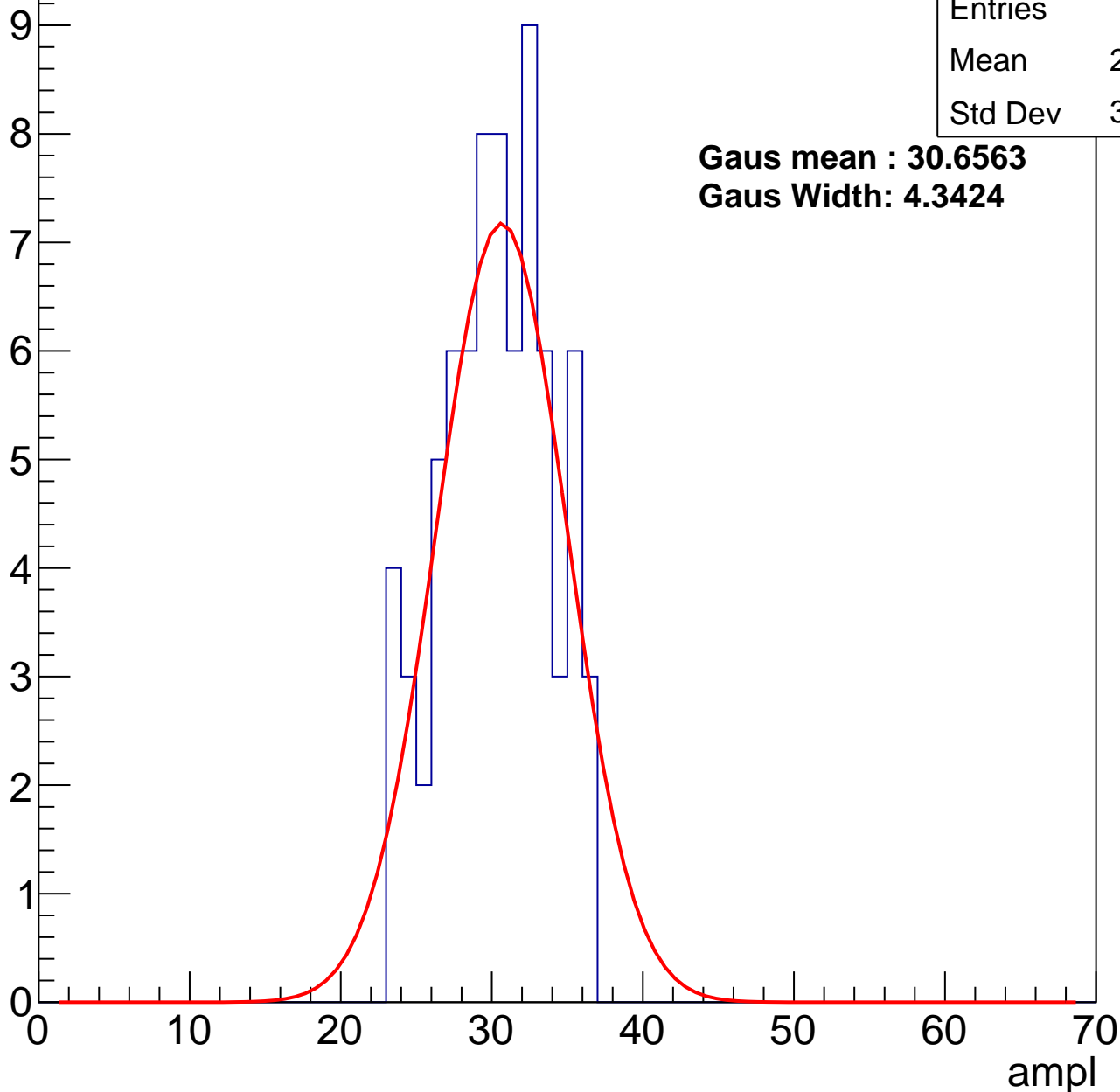
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	29.84
Std Dev	3.503

**Gaus mean : 30.6563**

**Gaus Width: 4.3424**



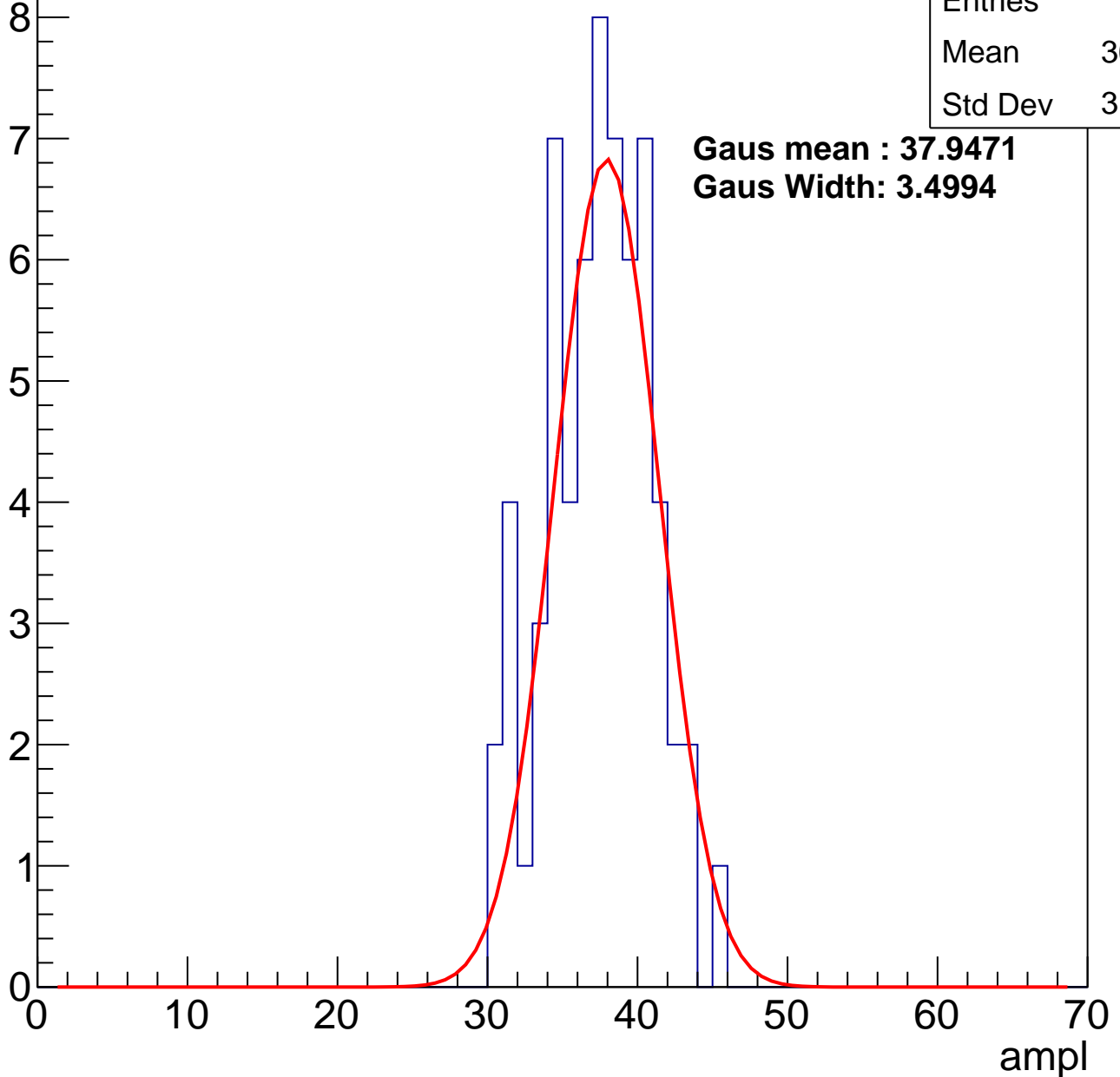
# B1L000S, U23-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.94
Std Dev	3.409

**Gaus mean : 37.9471**  
**Gaus Width: 3.4994**



# B1L000S, U23-ch84, adc2

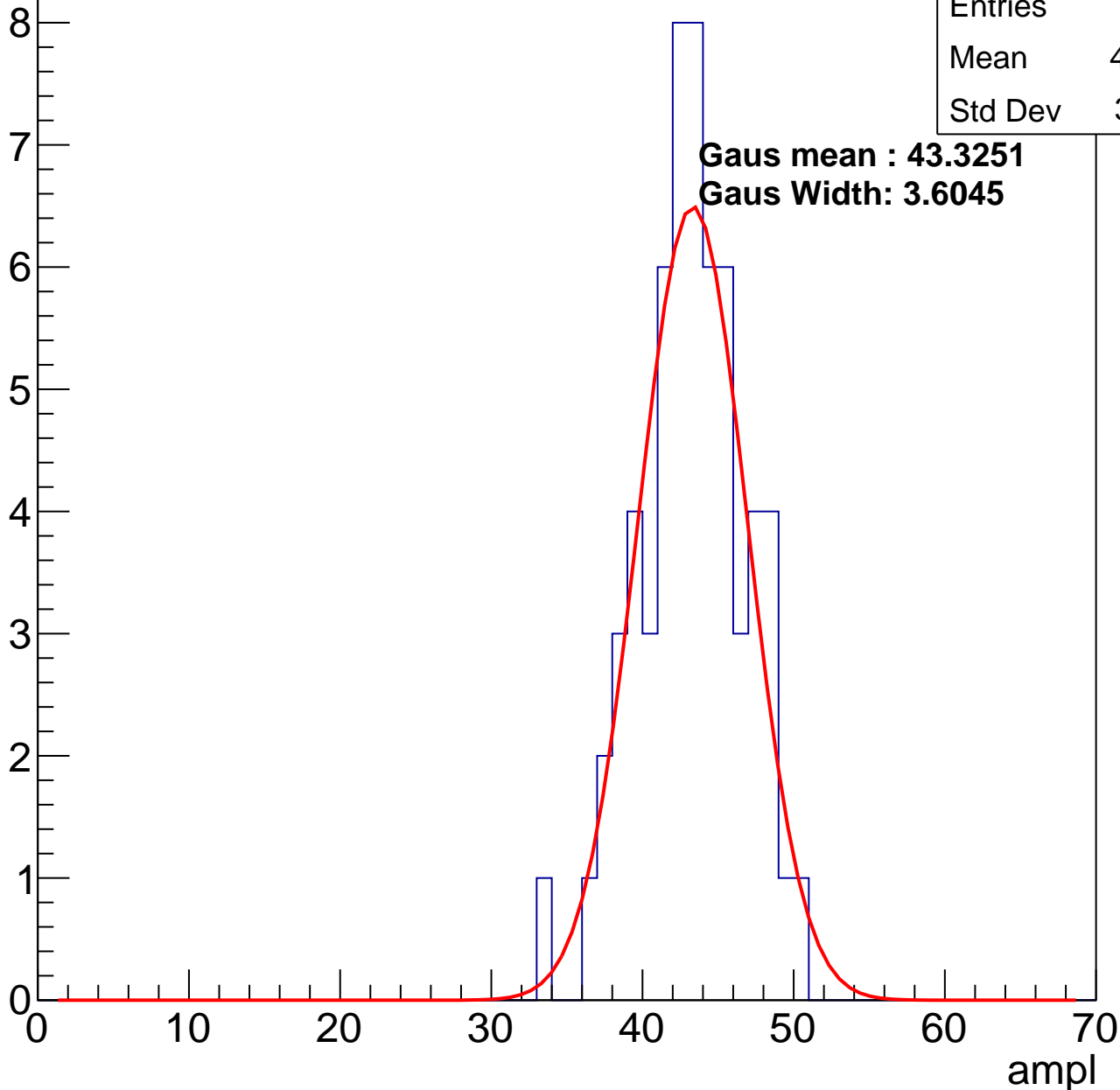
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.79
Std Dev	3.441

**Gaus mean : 43.3251**

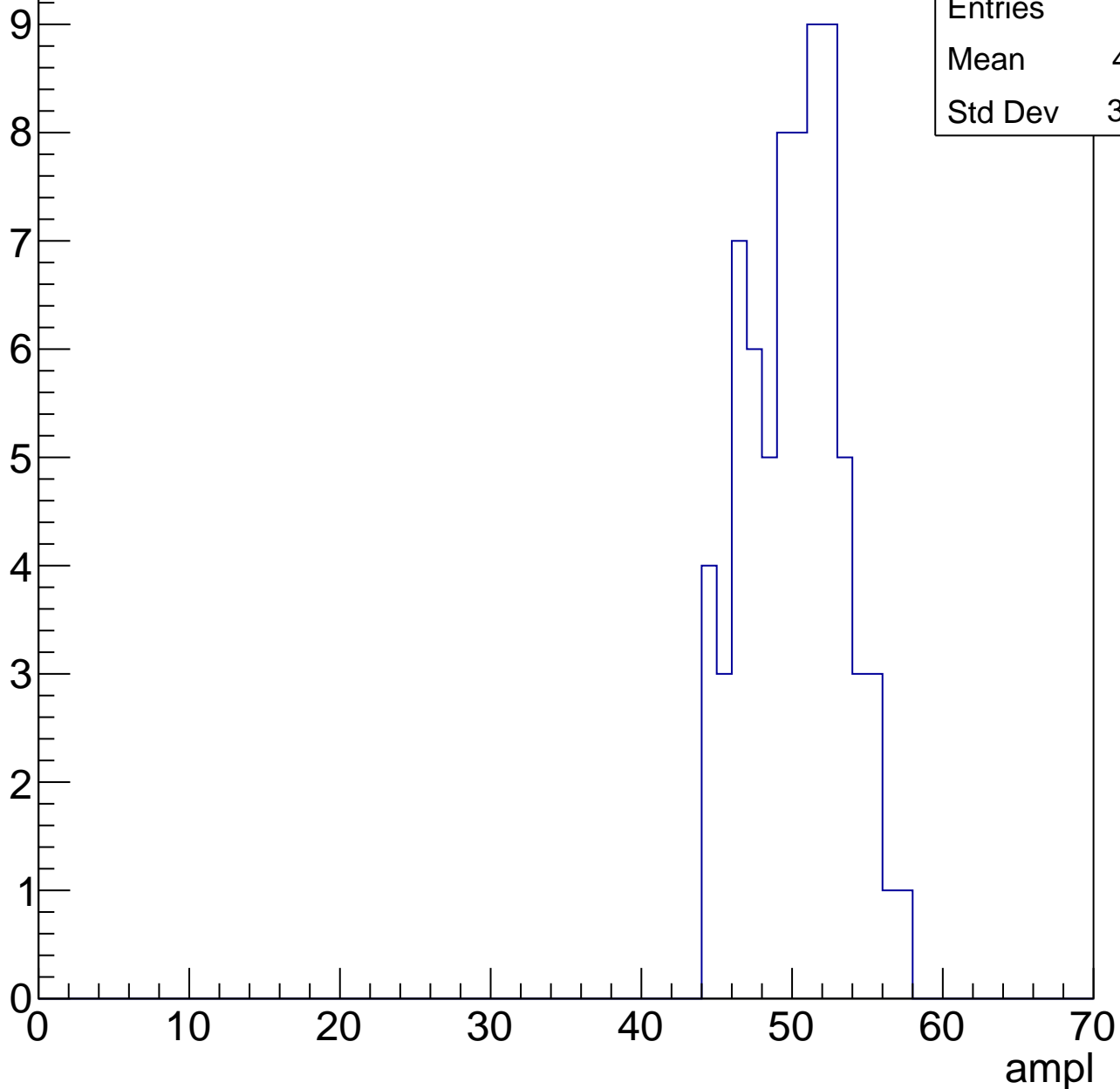
**Gaus Width: 3.6045**



# B1L000S, U23-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

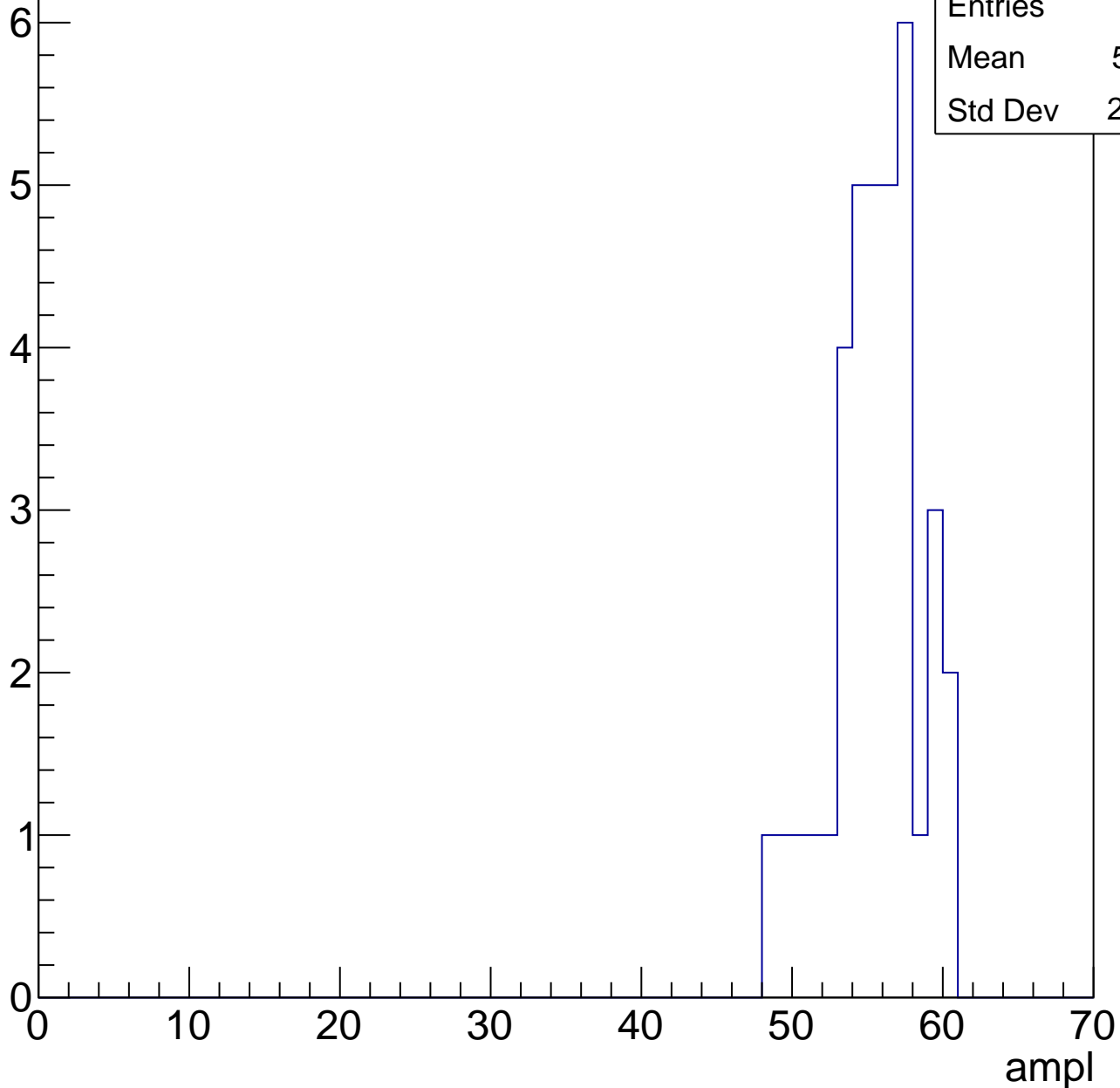


Entries	72
Mean	49.71
Std Dev	3.129

# B1L000S, U23-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch84, adc5

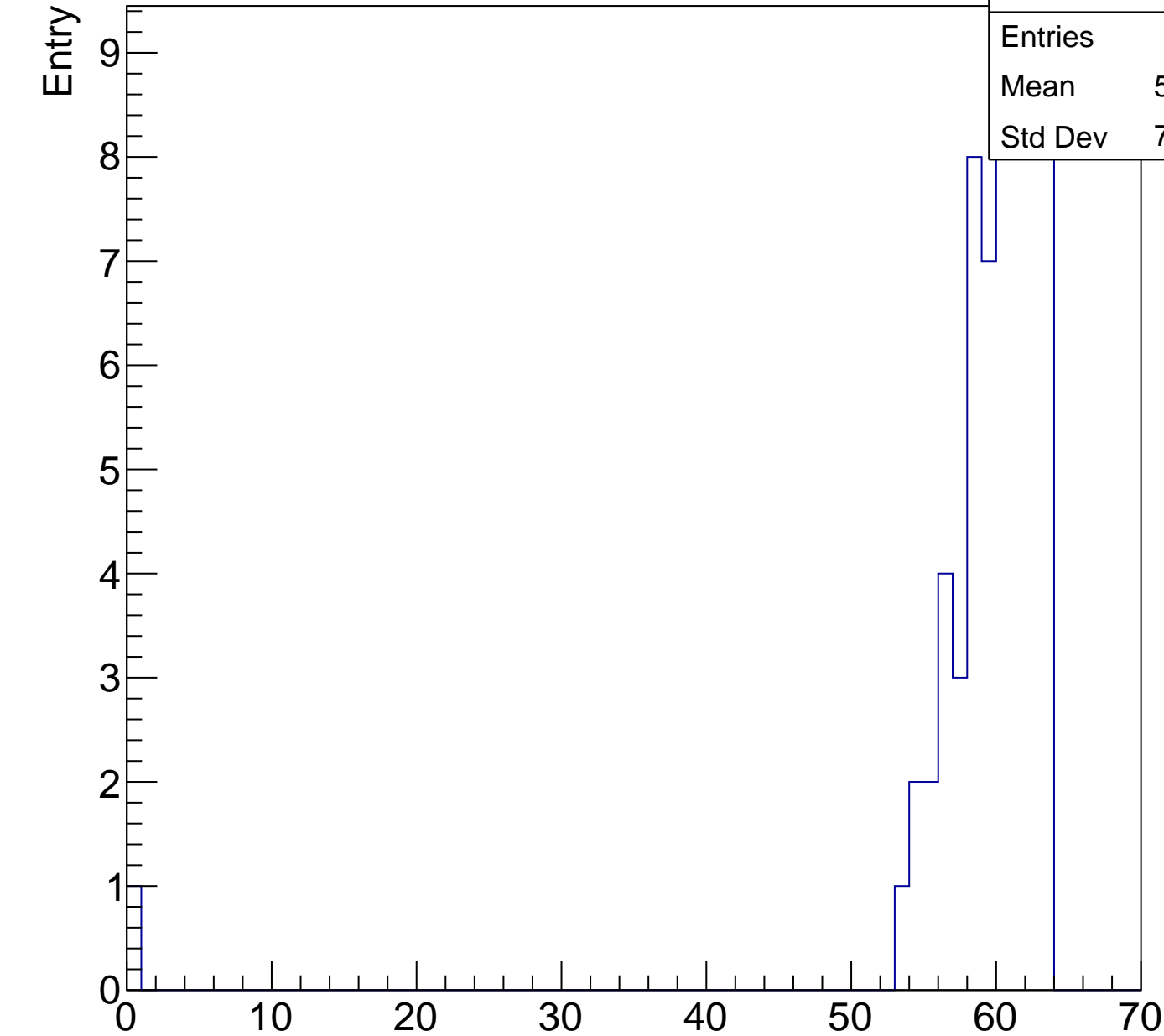
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	58.56
Std Dev	7.983

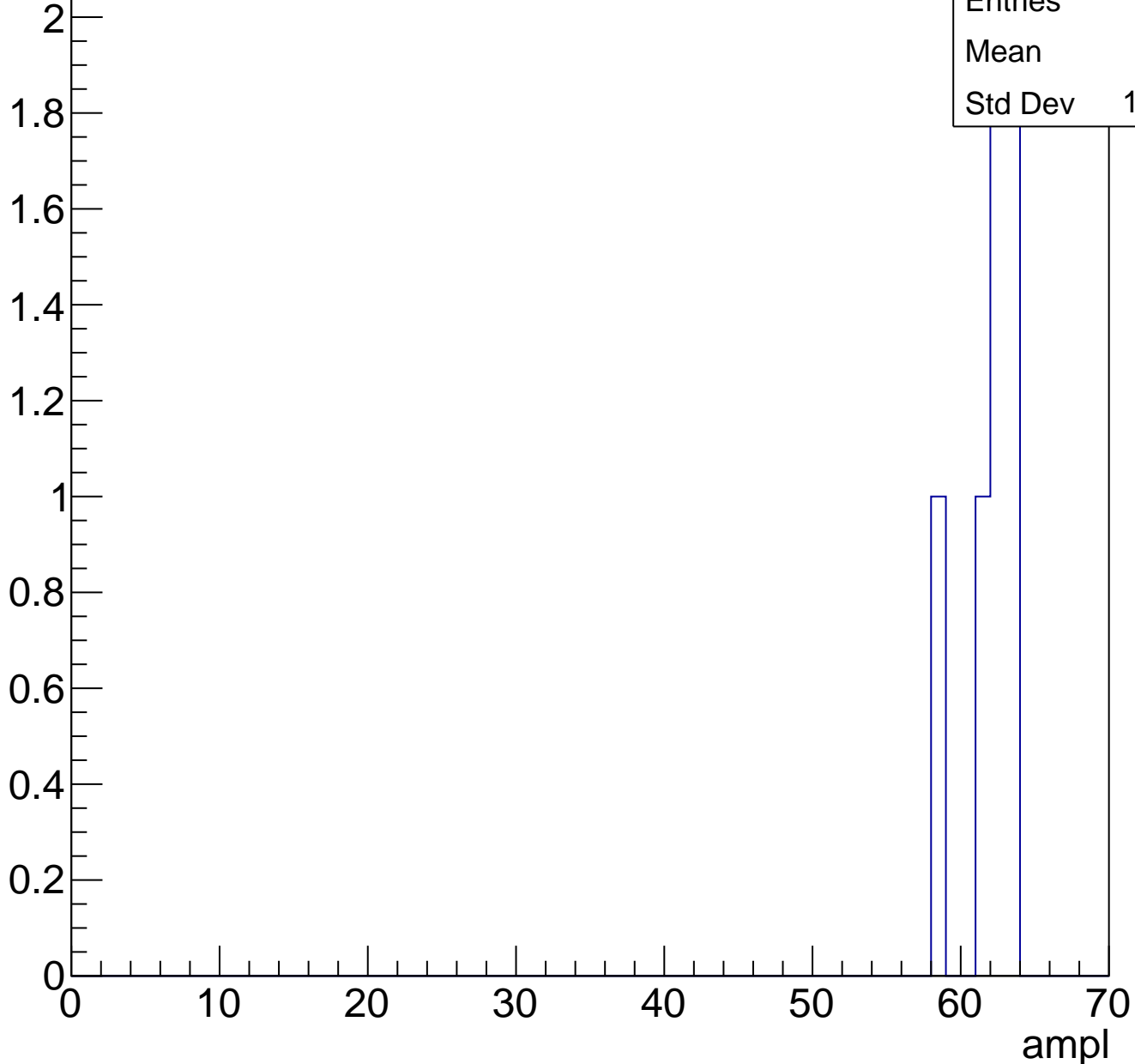
ampl



# B1L000S, U23-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch85, adc0

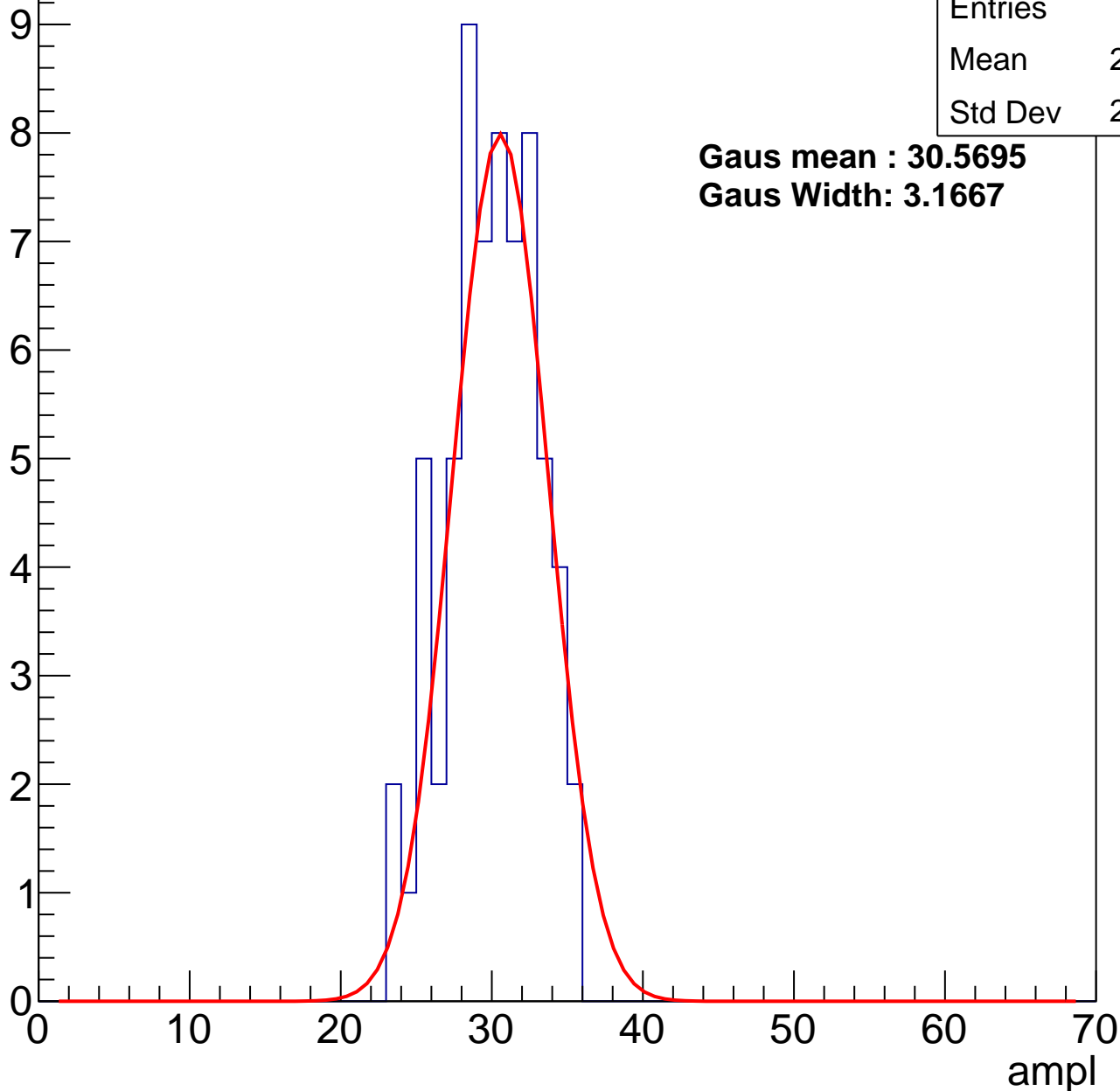
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.55
Std Dev	2.946

**Gaus mean : 30.5695**

**Gaus Width: 3.1667**



# B1L000S, U23-ch85, adc1

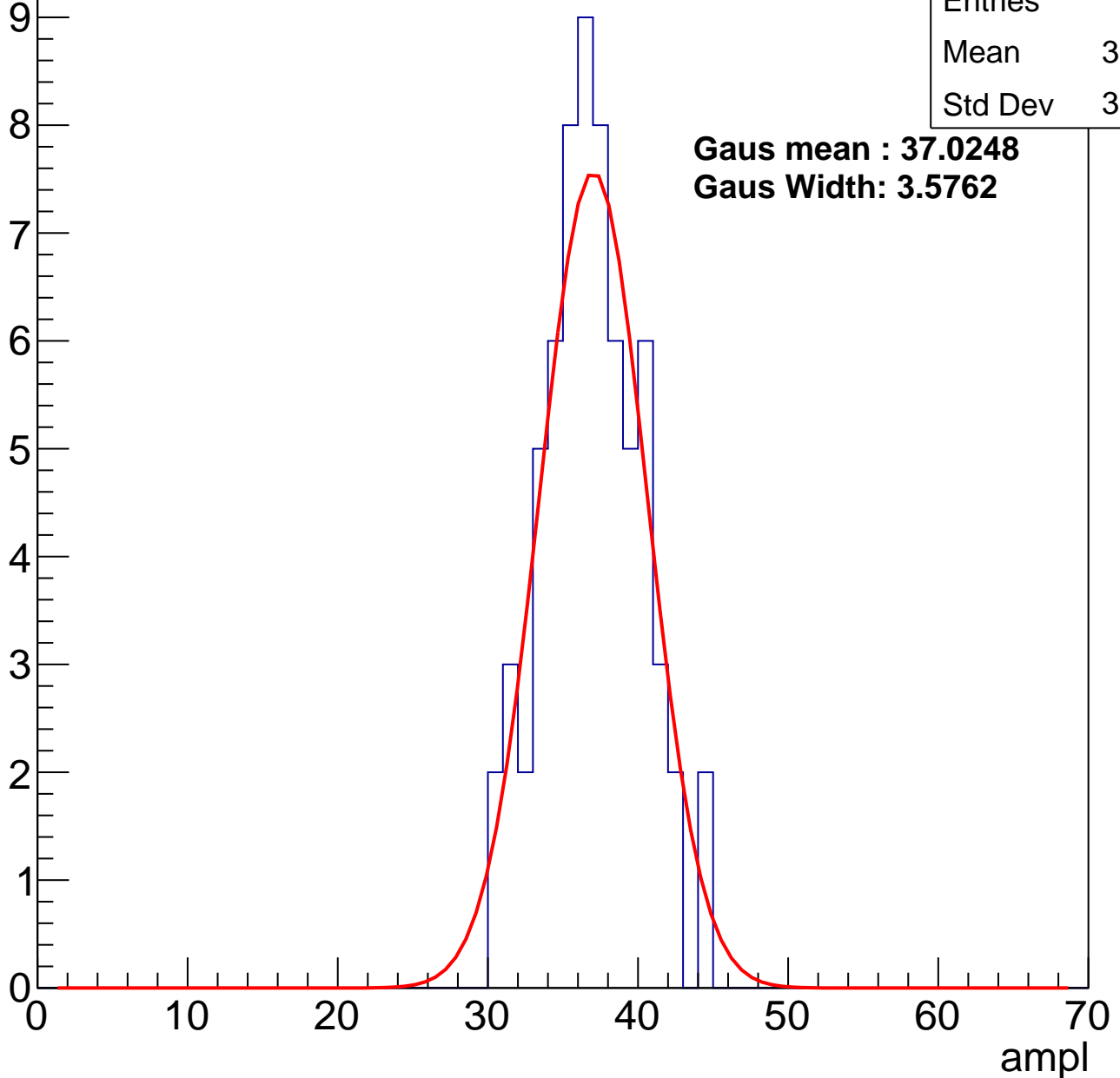
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.48
Std Dev	3.206

**Gaus mean : 37.0248**

**Gaus Width: 3.5762**



# B1L000S, U23-ch85, adc2

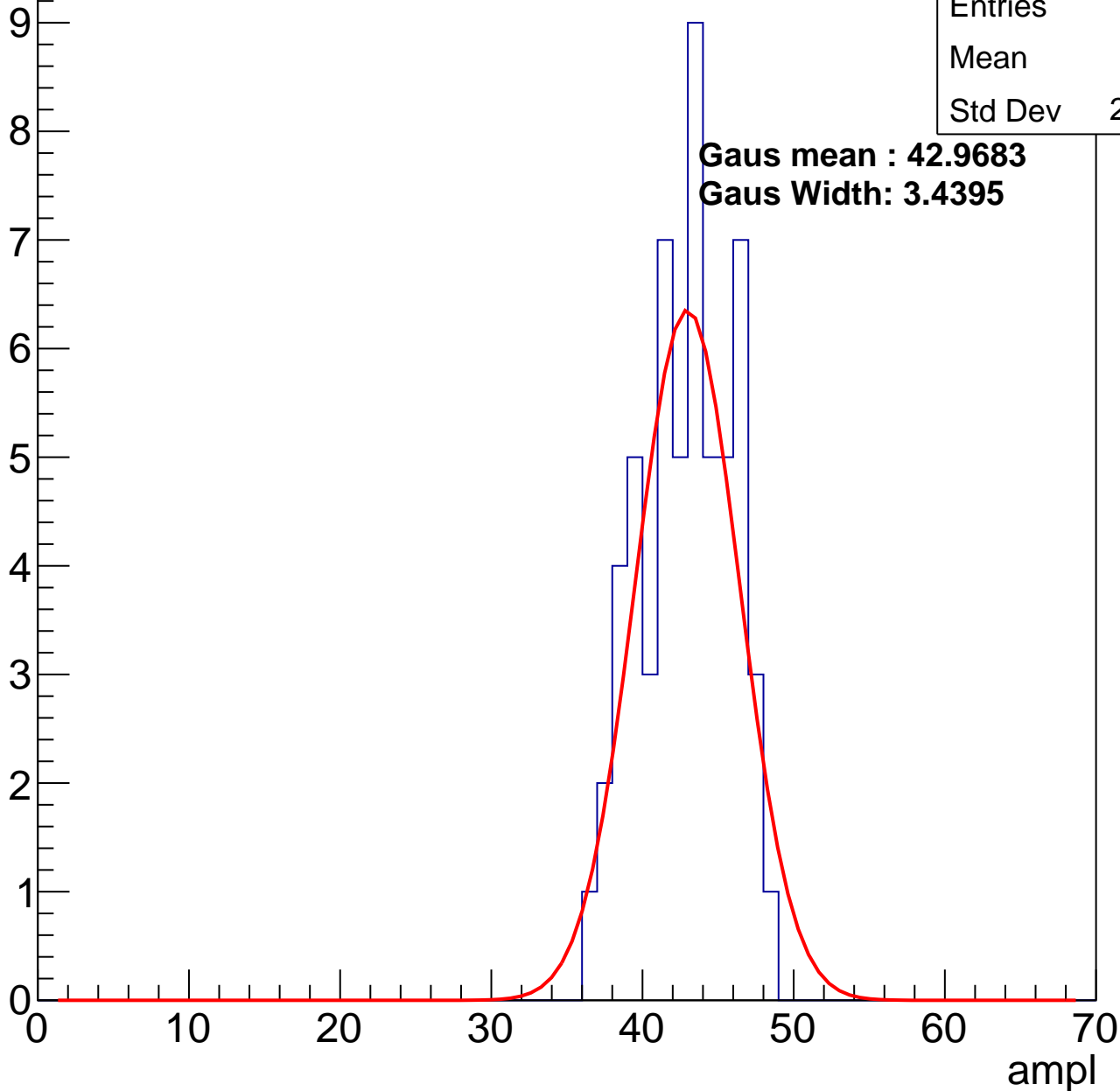
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42.4
Std Dev	2.967

**Gaus mean : 42.9683**

**Gaus Width: 3.4395**

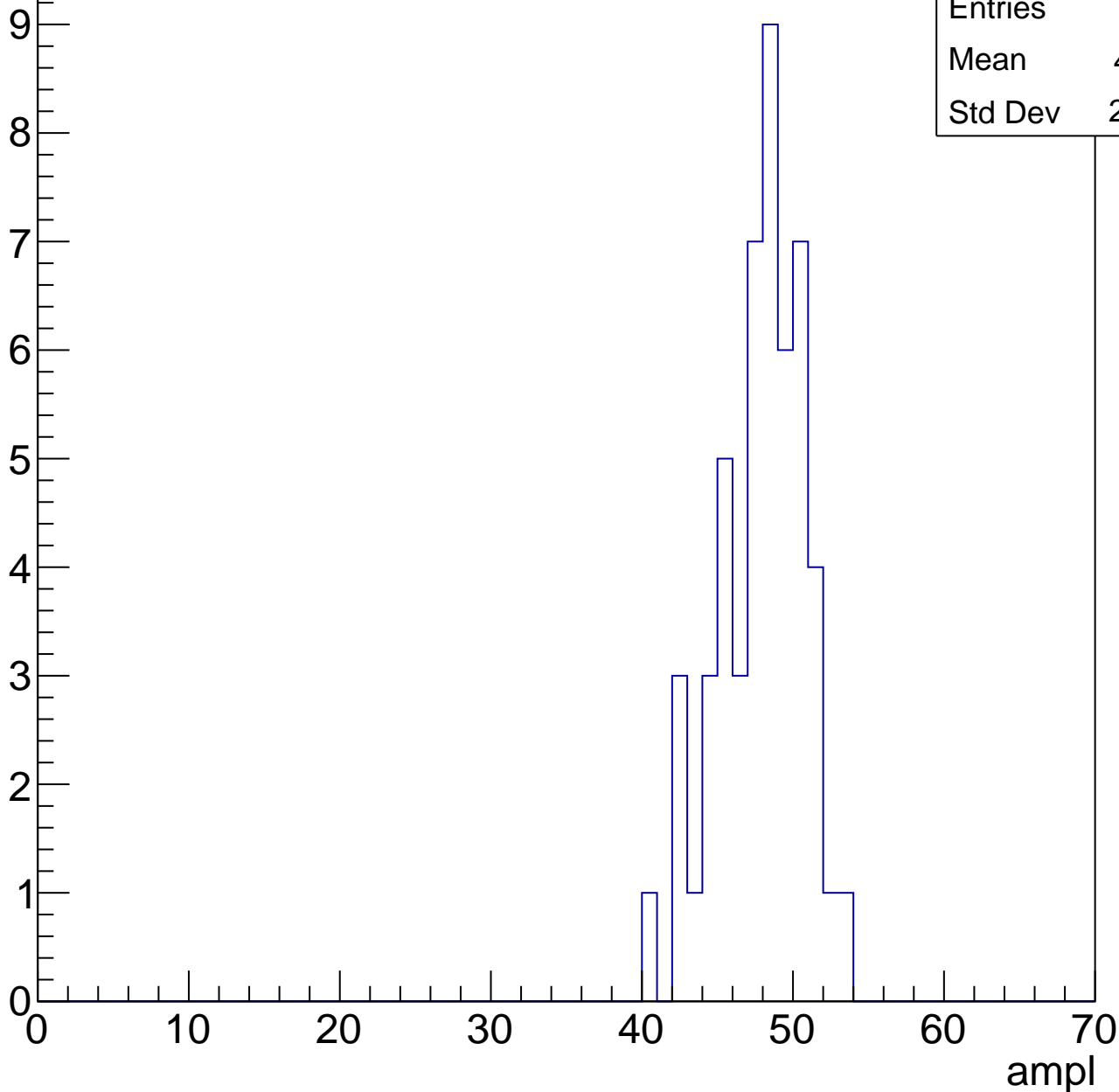


# B1L000S, U23-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	47.41
Std Dev	2.809

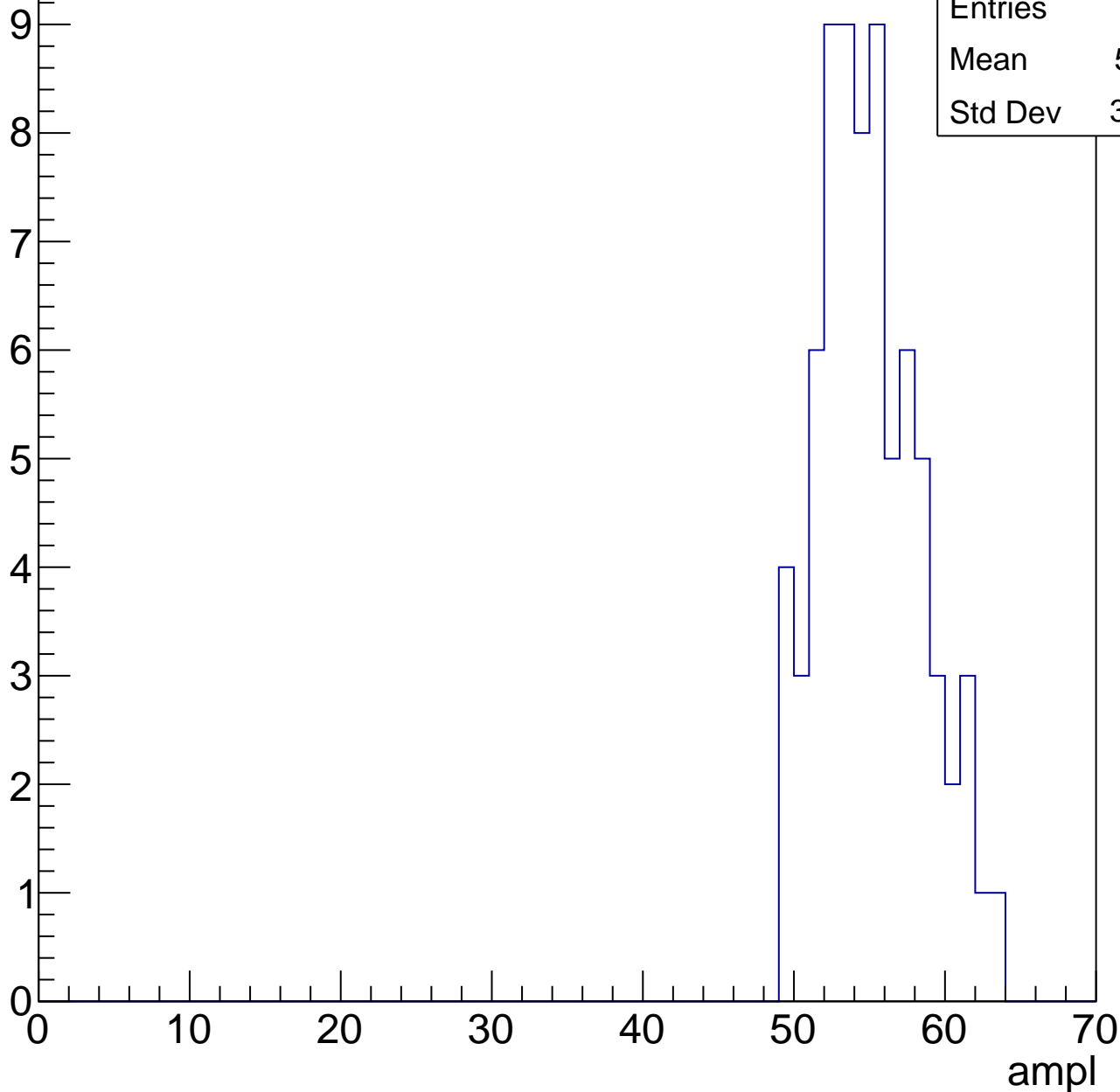


# B1L000S, U23-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	54.61
Std Dev	3.332



# B1L000S, U23-ch85, adc5

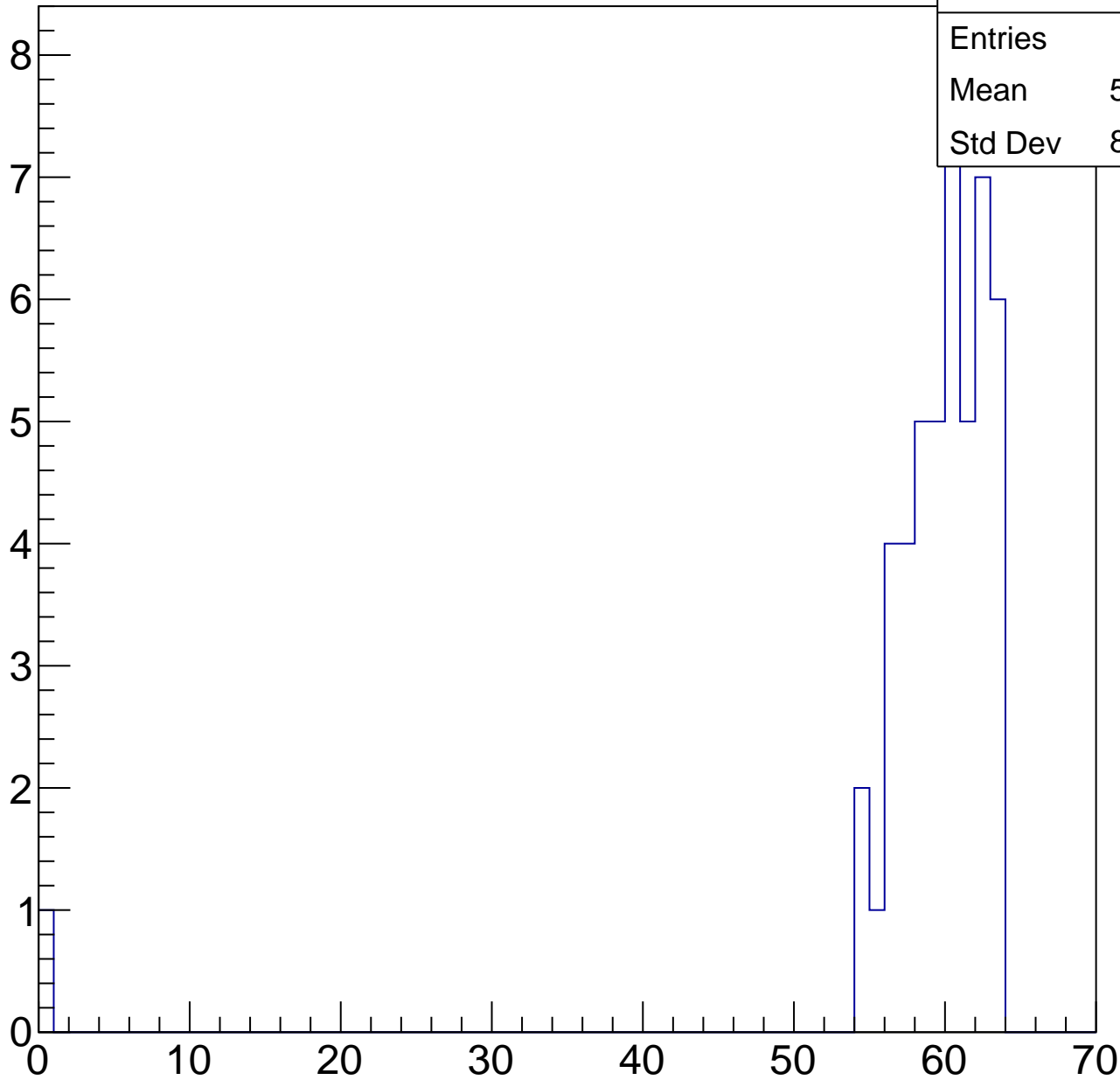
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	58.27
Std Dev	8.857

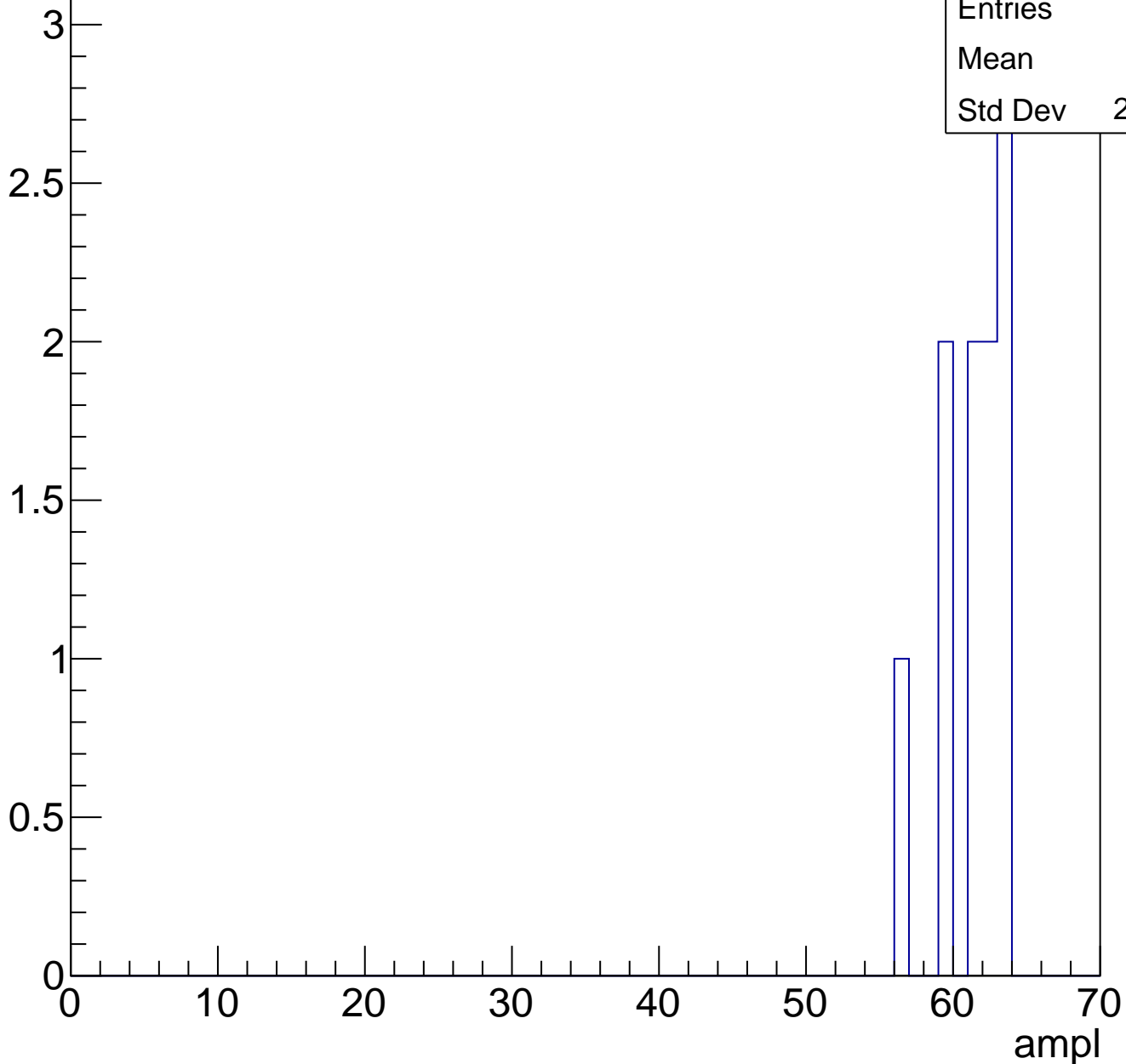
ampl



# B1L000S, U23-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U23-ch86, adc0

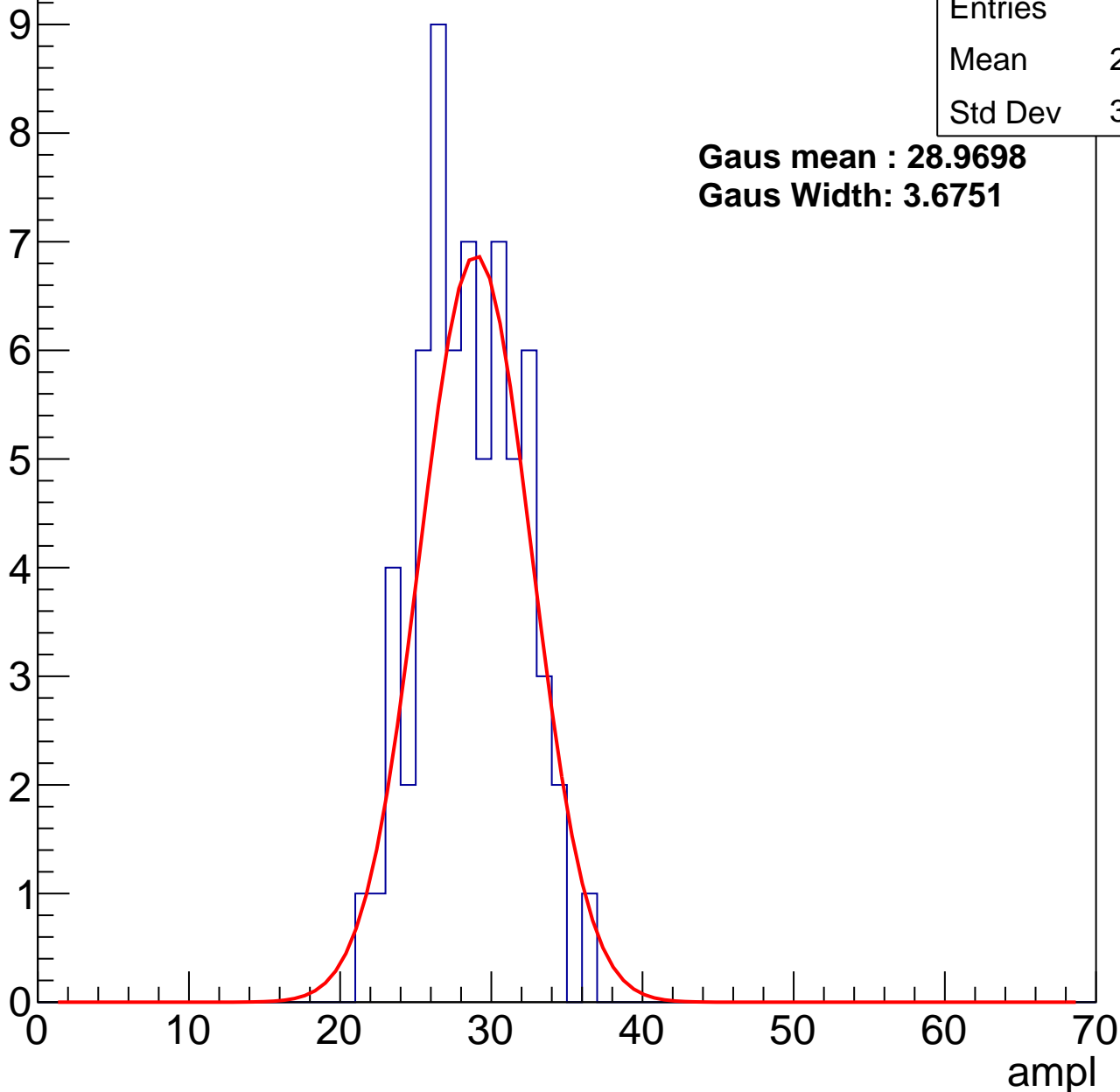
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.15
Std Dev	3.273

**Gaus mean : 28.9698**

**Gaus Width: 3.6751**



# B1L000S, U23-ch86, adc1

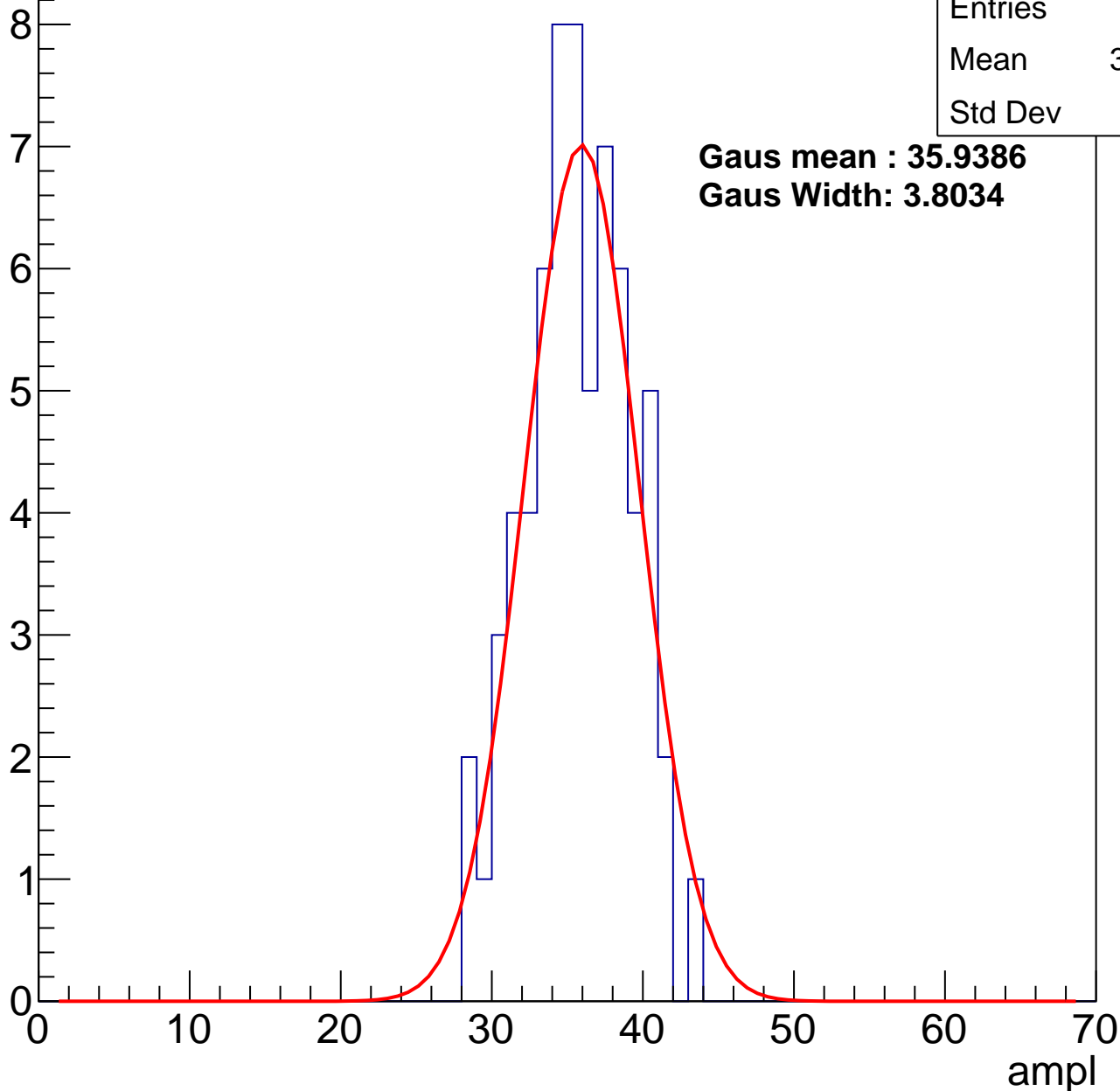
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.23
Std Dev	3.37

**Gaus mean : 35.9386**

**Gaus Width: 3.8034**



# B1L000S, U23-ch86, adc2

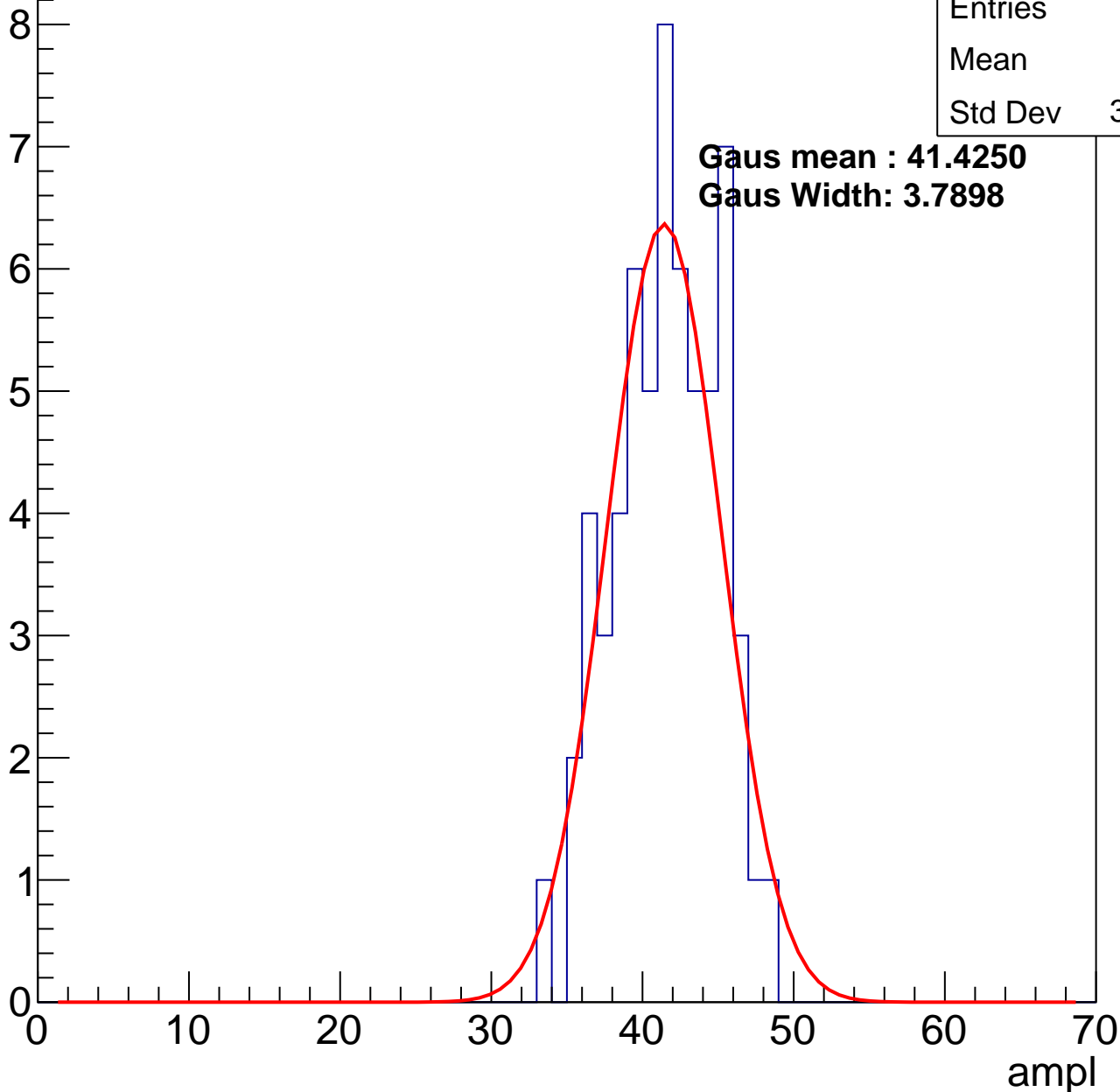
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.1
Std Dev	3.367

**Gaus mean : 41.4250**

**Gaus Width: 3.7898**

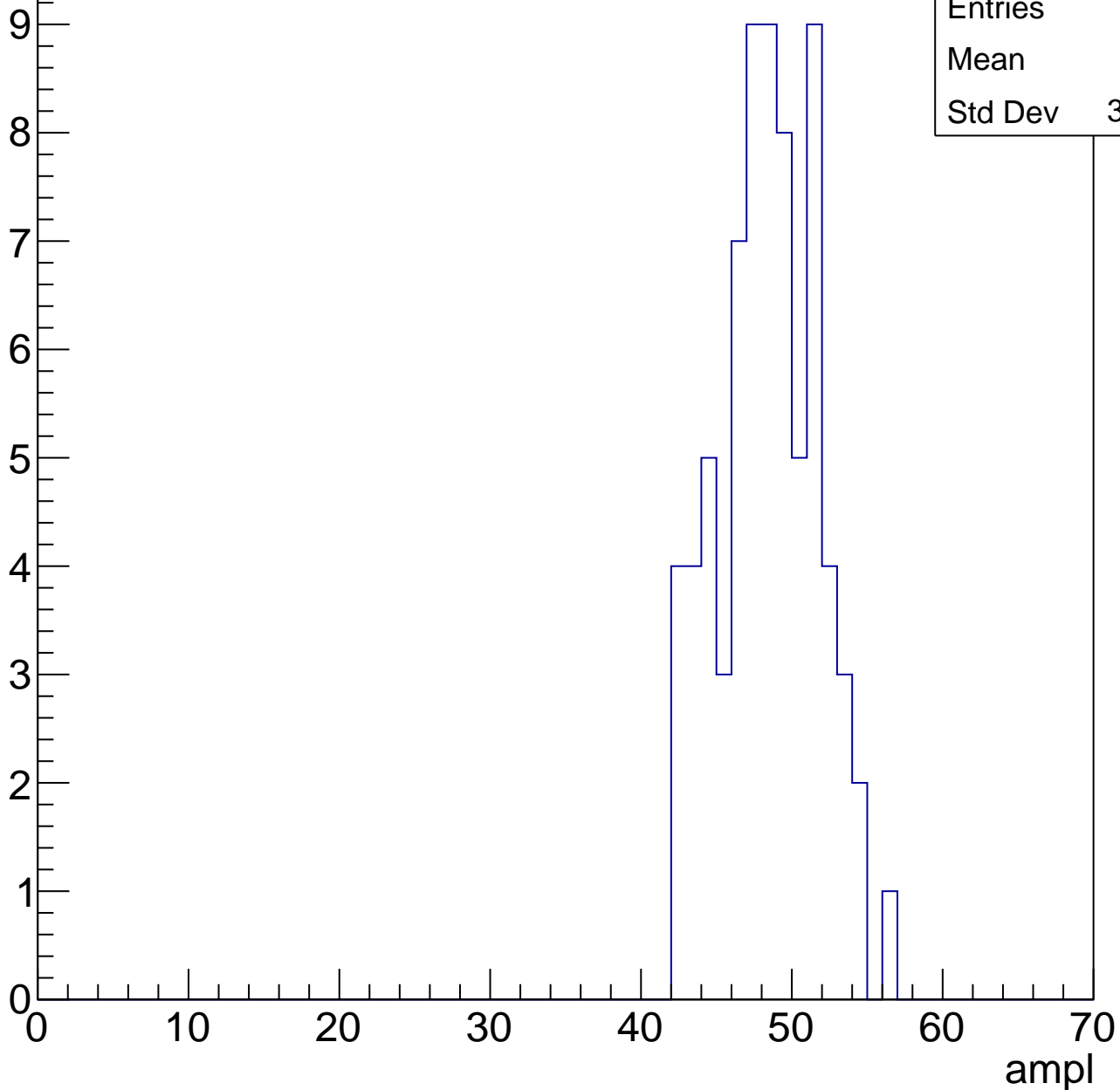


# B1L000S, U23-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	48
Std Dev	3.252

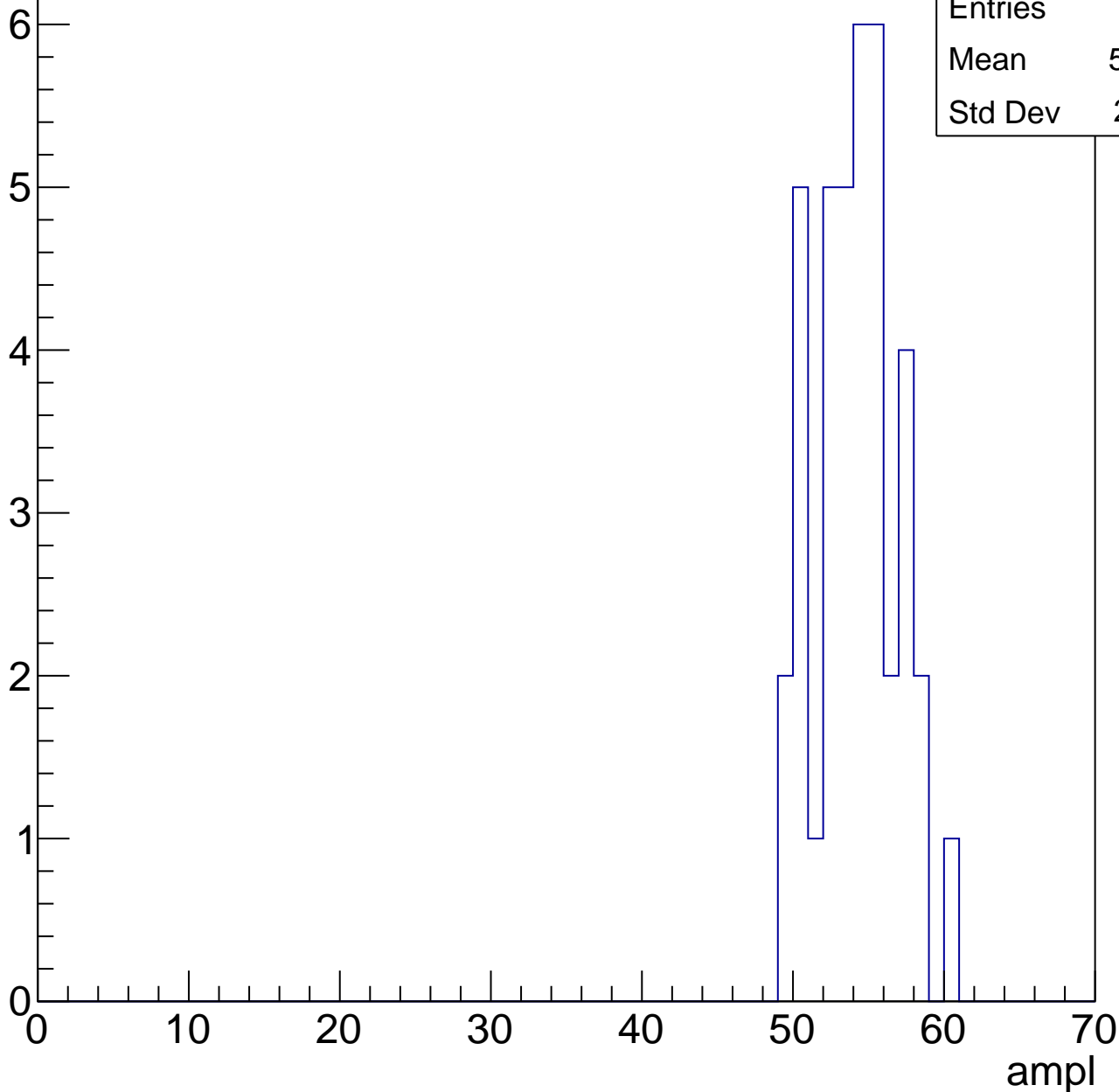


# B1L000S, U23-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	53.69
Std Dev	2.671

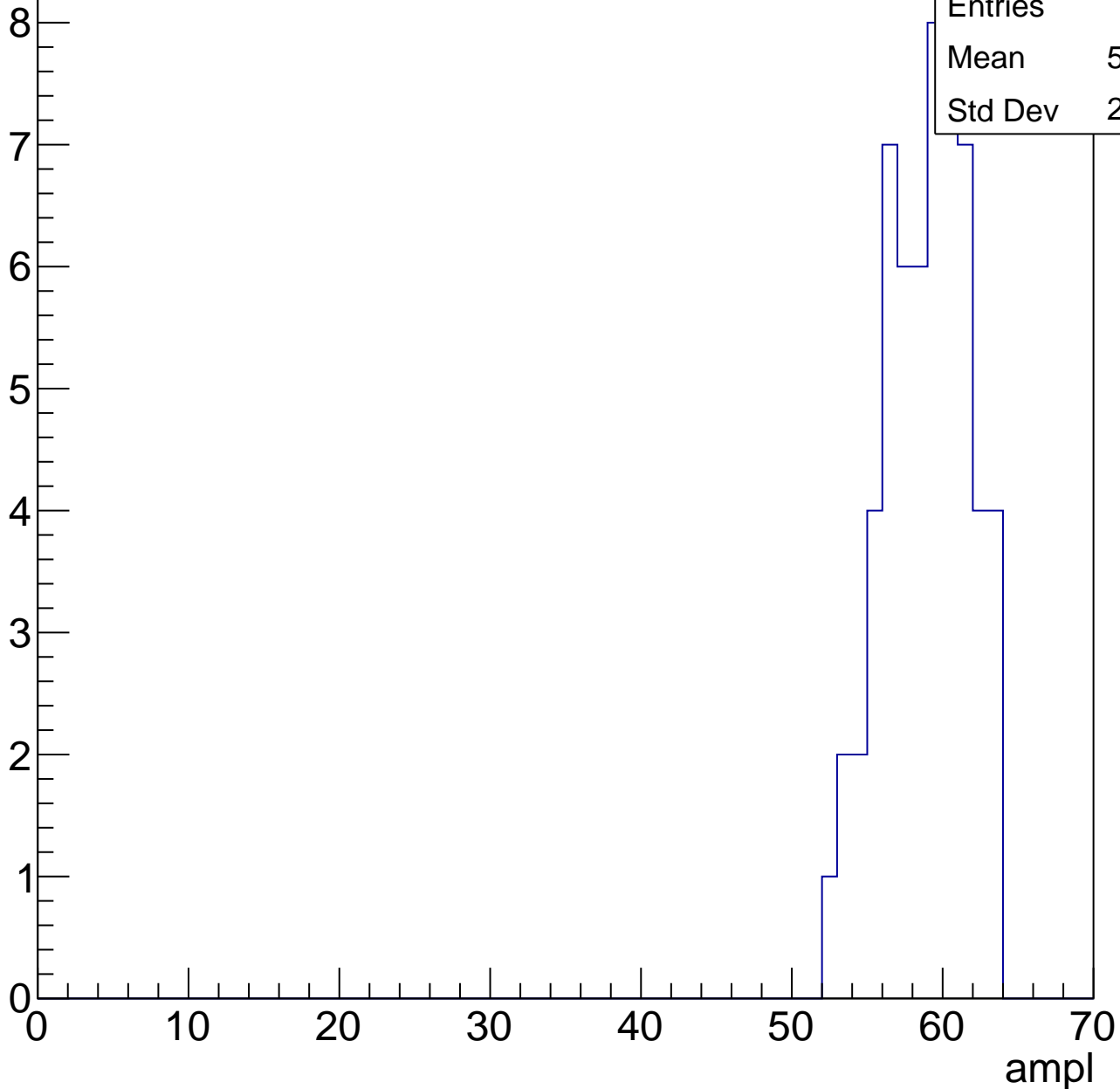


# B1L000S, U23-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	58.42
Std Dev	2.745

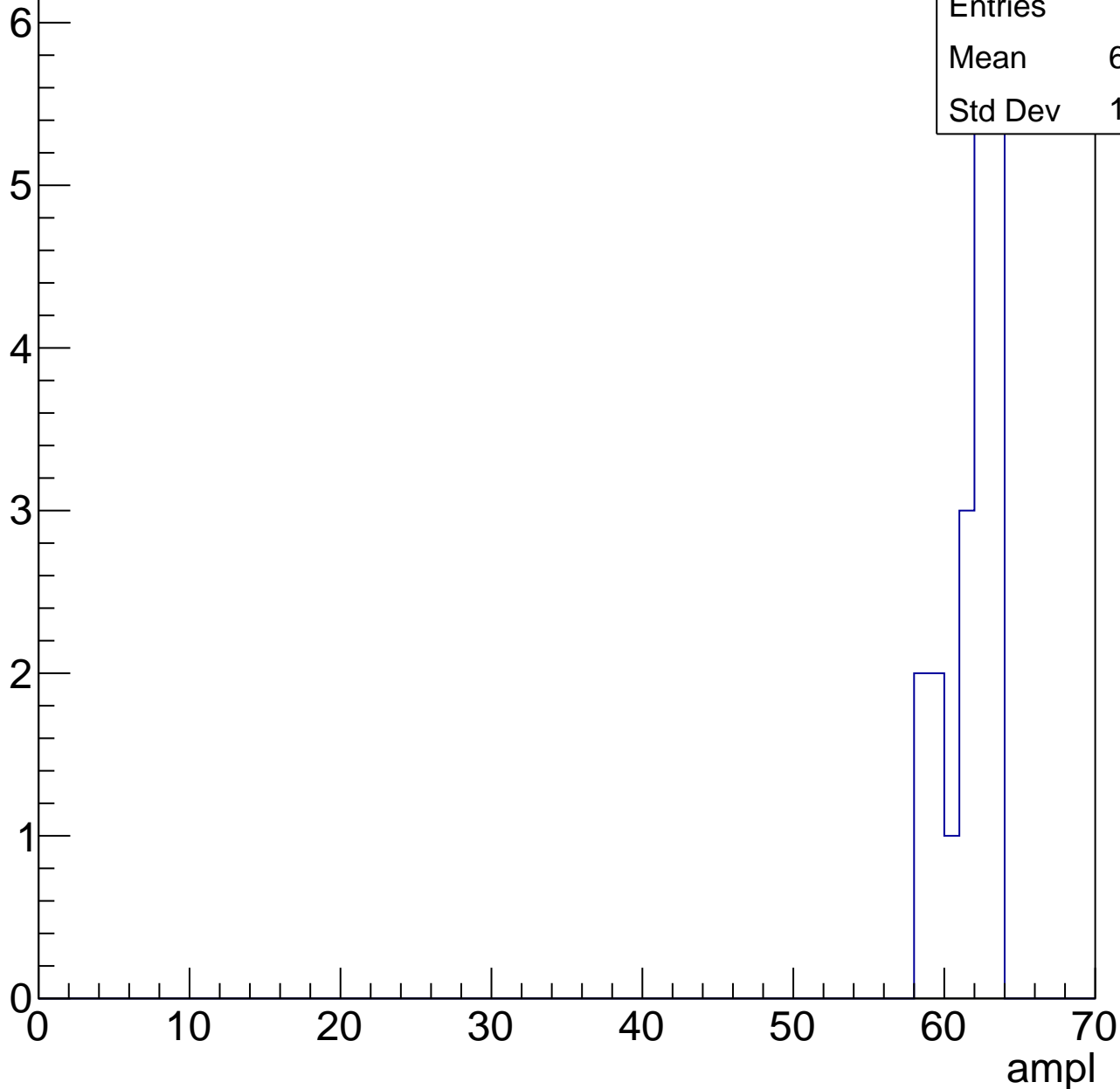


# B1L000S, U23-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.35
Std Dev	1.652





# B1L000S, U23-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch87, adc0

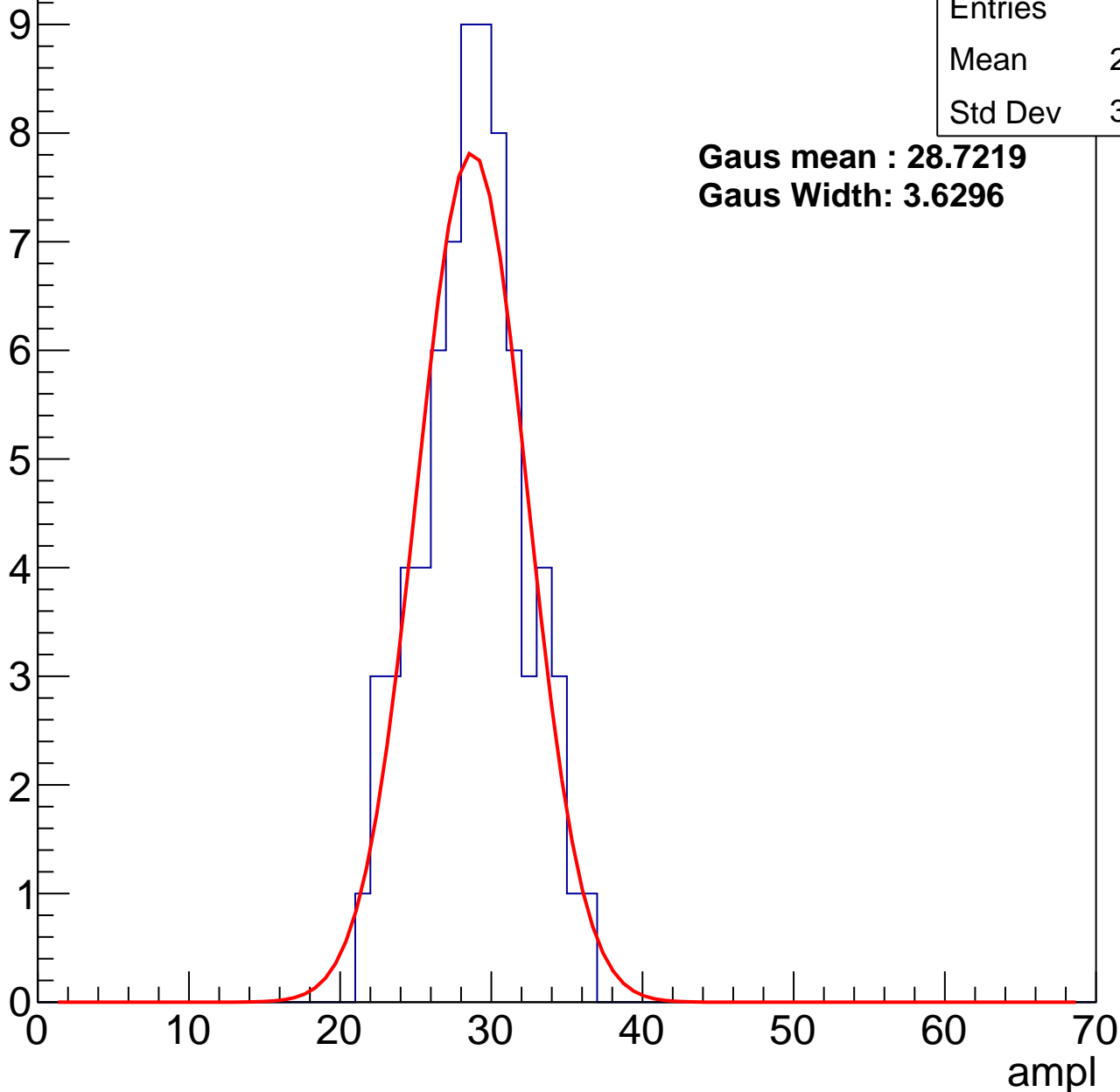
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	28.29
Std Dev	3.377

**Gaus mean : 28.7219**

**Gaus Width: 3.6296**



# B1L000S, U23-ch87, adc1

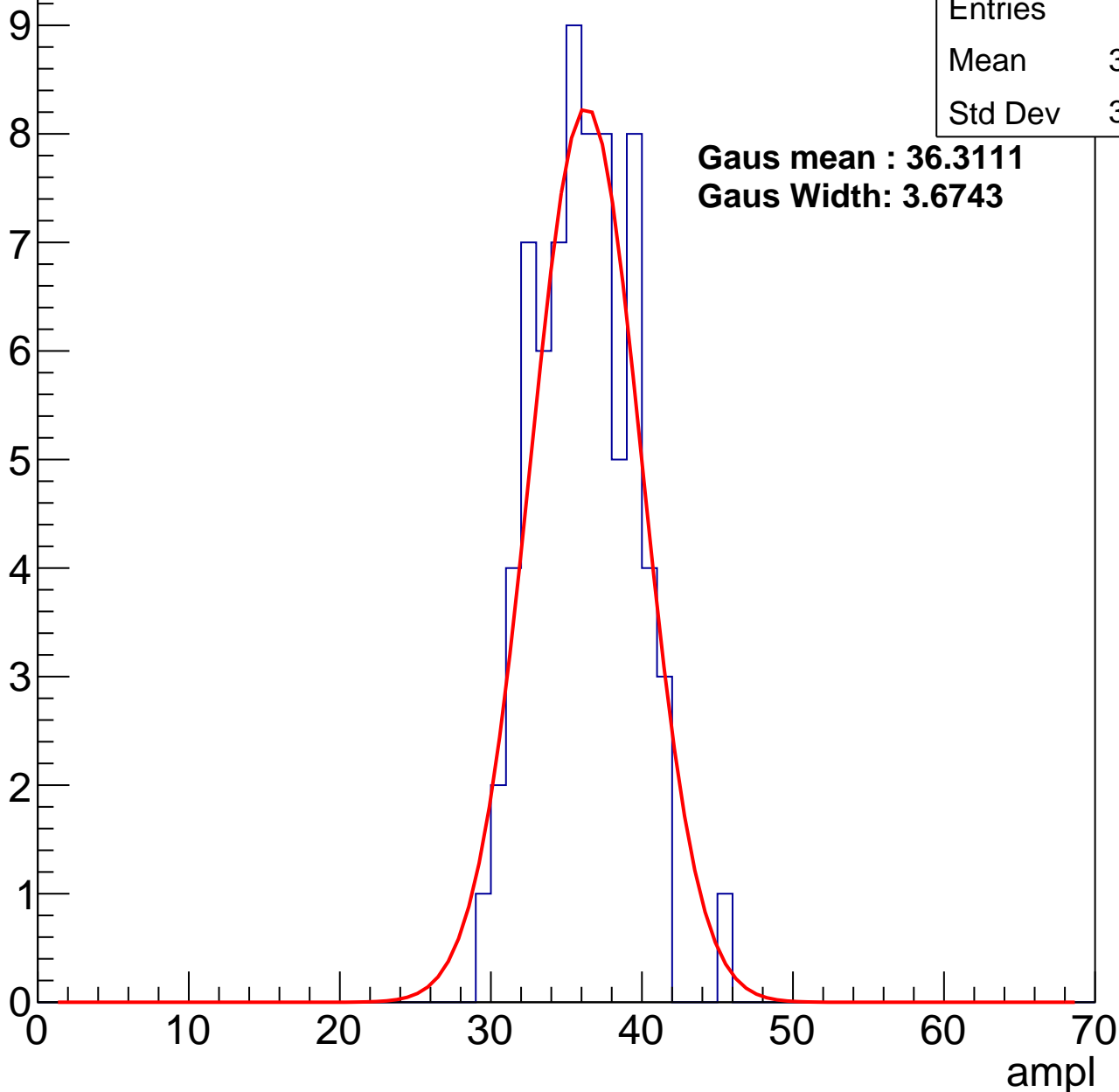
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	35.64
Std Dev	3.173

**Gaus mean : 36.3111**

**Gaus Width: 3.6743**



# B1L000S, U23-ch87, adc2

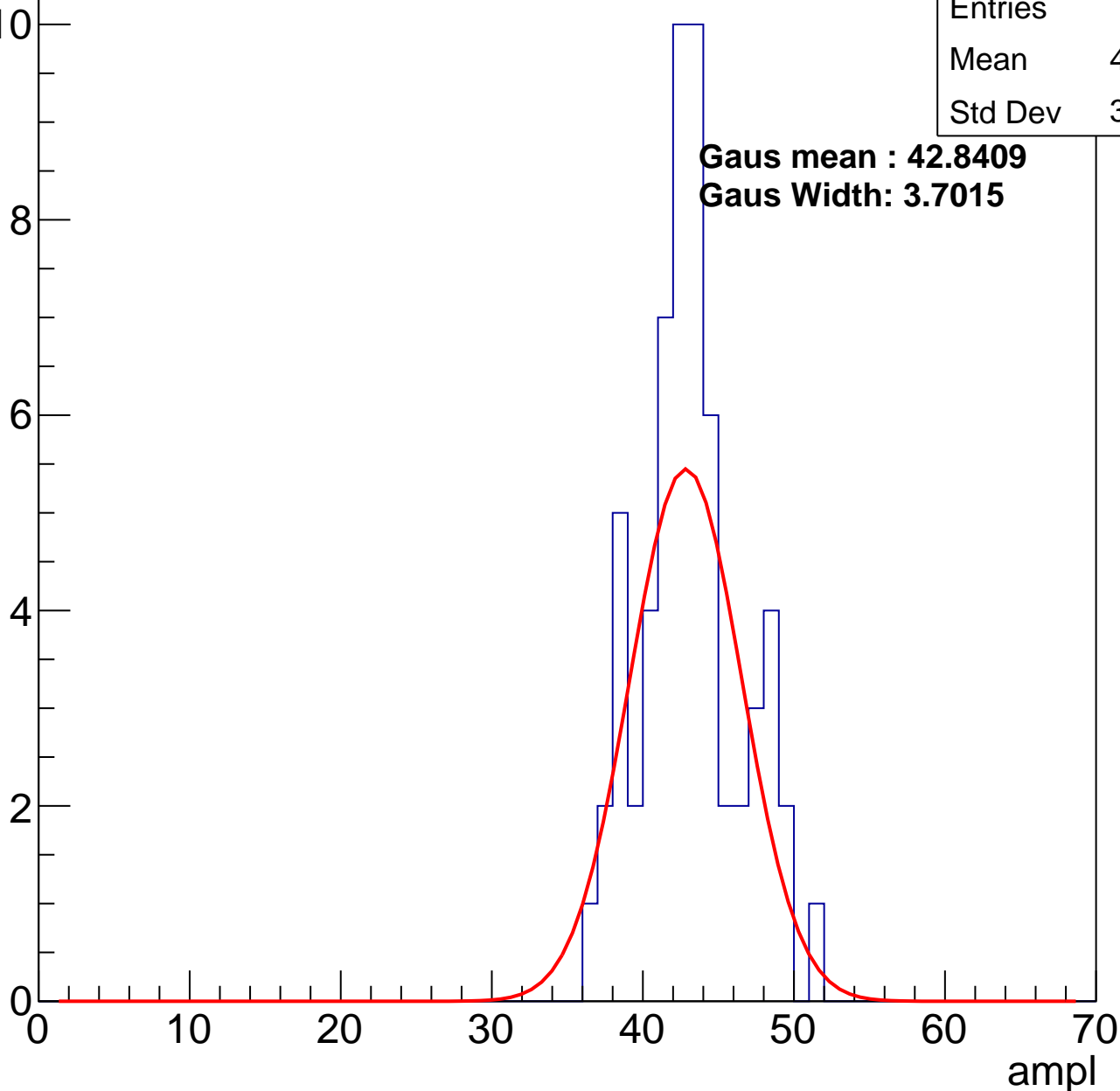
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.67
Std Dev	3.293

**Gaus mean : 42.8409**

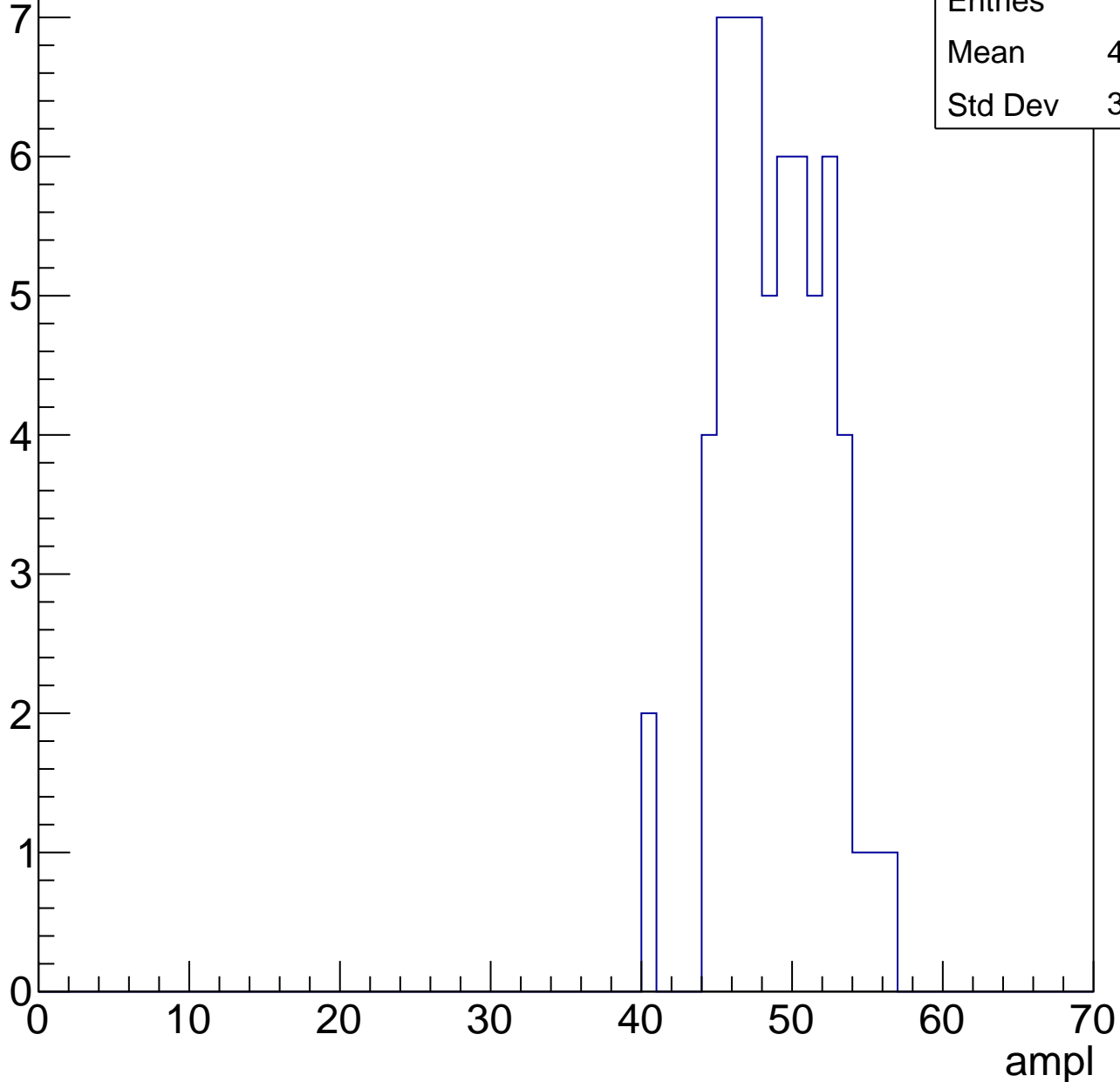
**Gaus Width: 3.7015**



# B1L000S, U23-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



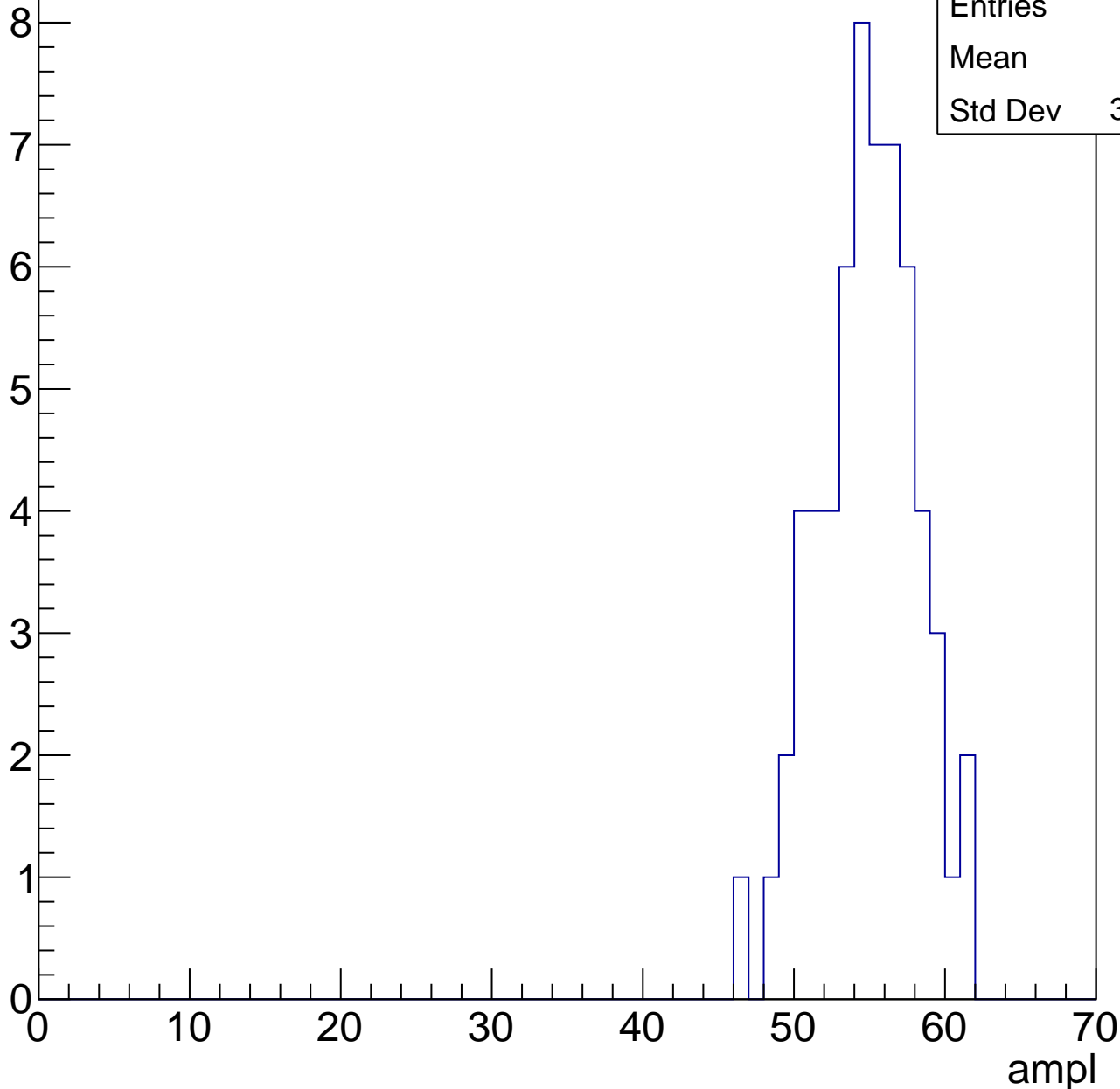
Entries	62
Mean	48.39
Std Dev	3.362

# B1L000S, U23-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.4
Std Dev	3.226

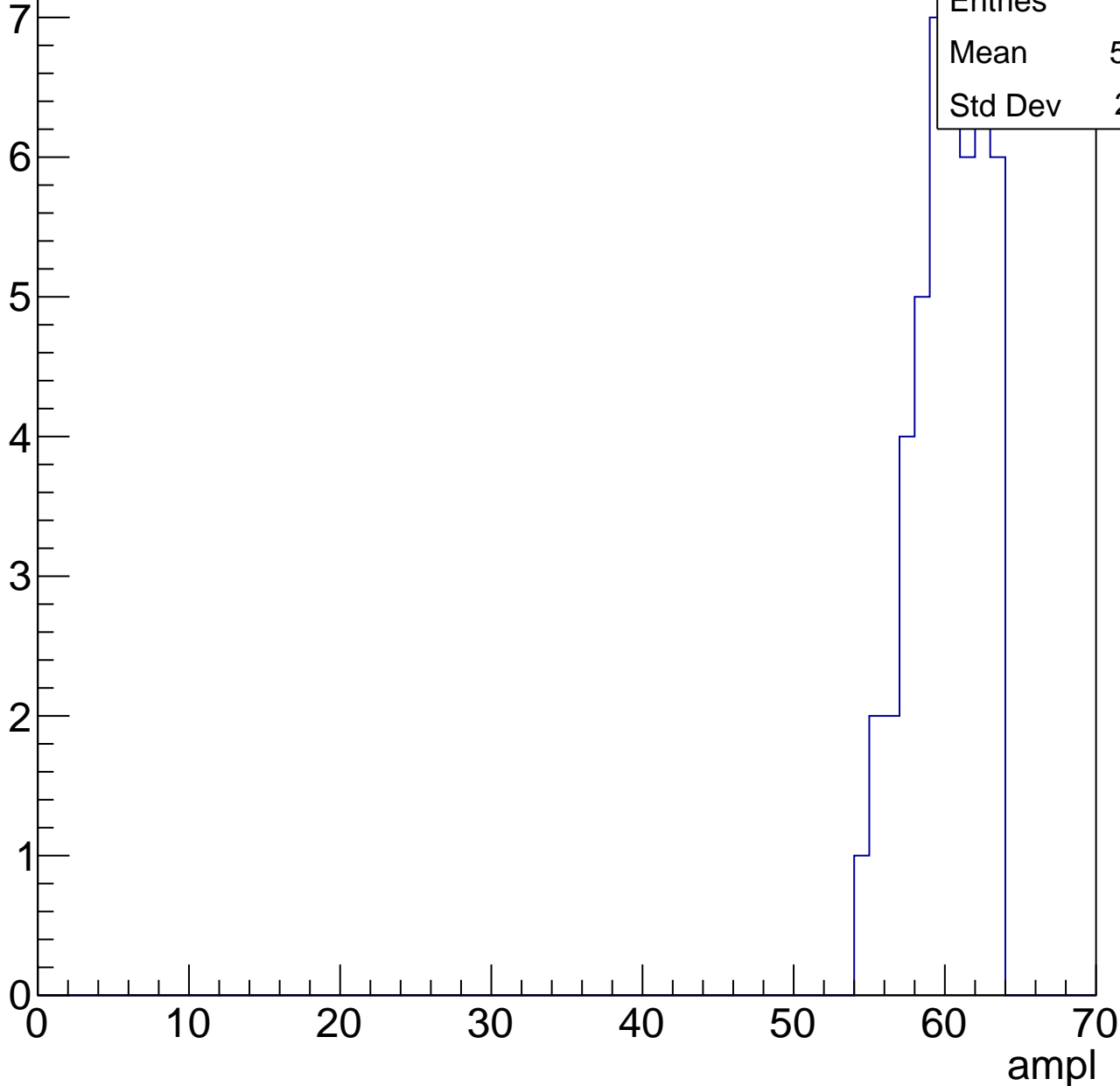


# B1L000S, U23-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

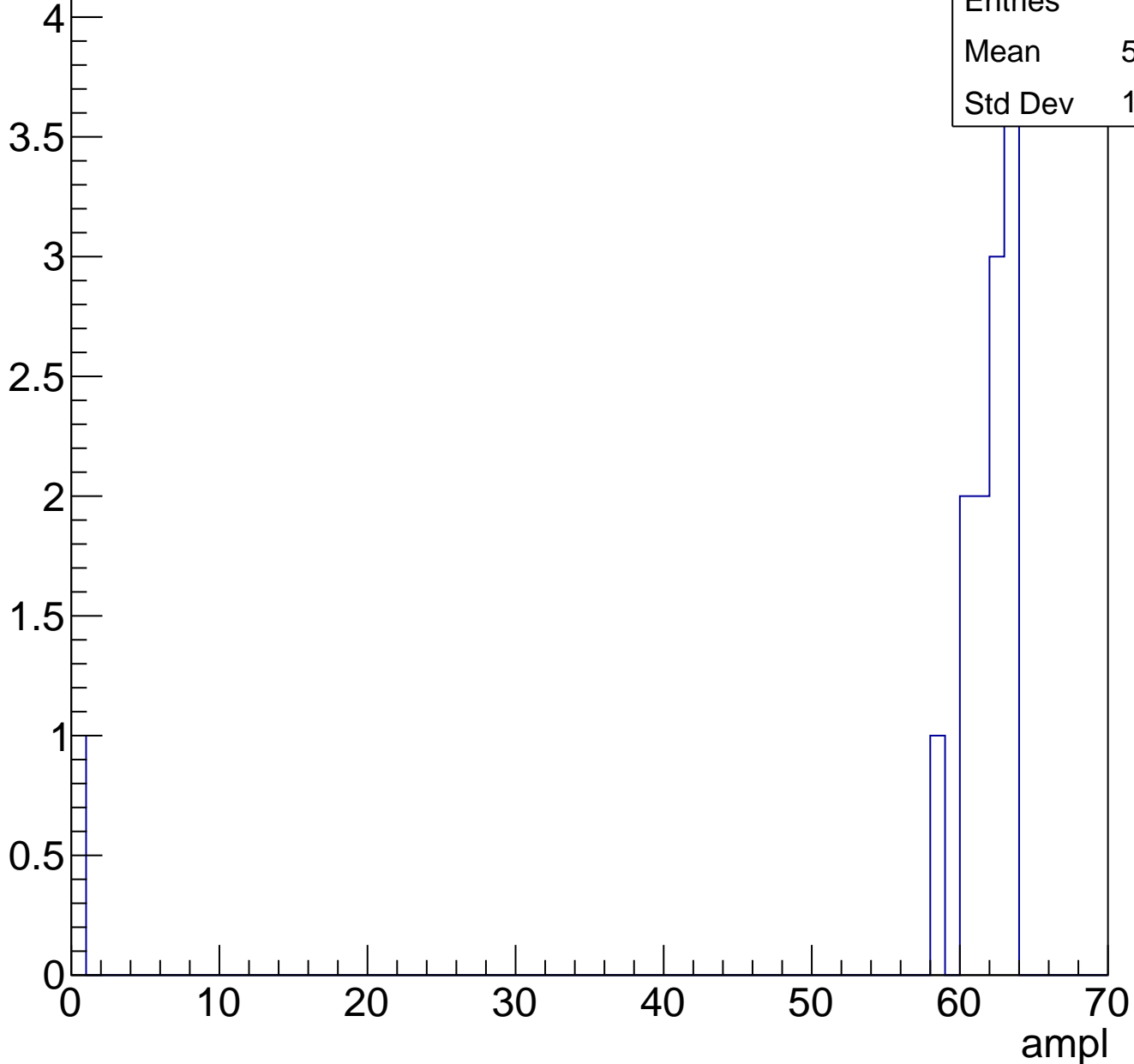
Entries	47
Mean	59.68
Std Dev	2.371



# B1L000S, U23-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch88, adc0

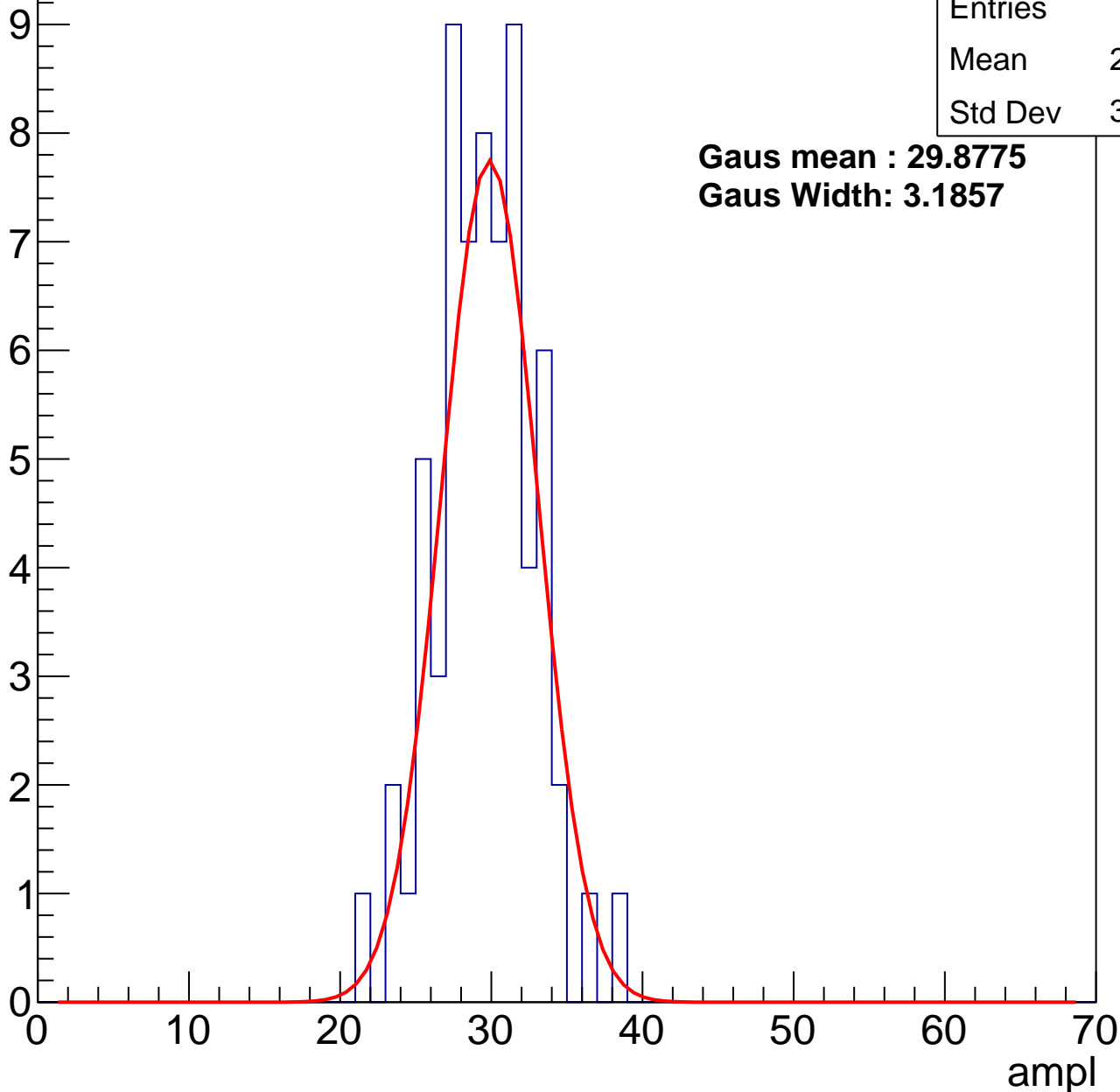
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.12
Std Dev	3.174

**Gaus mean : 29.8775**

**Gaus Width: 3.1857**



# B1L000S, U23-ch88, adc1

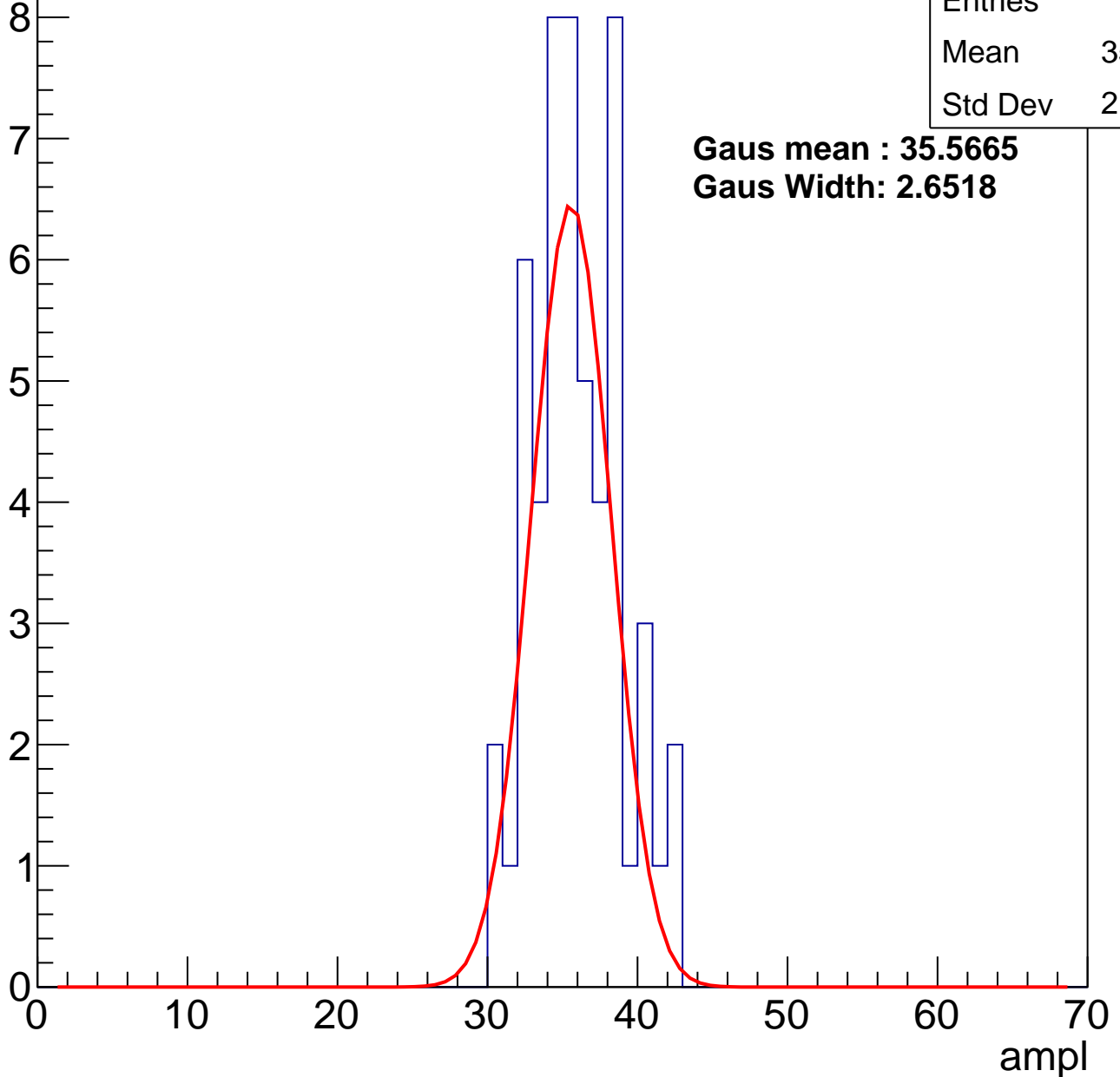
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	35.53
Std Dev	2.905

**Gaus mean : 35.5665**

**Gaus Width: 2.6518**



# B1L000S, U23-ch88, adc2

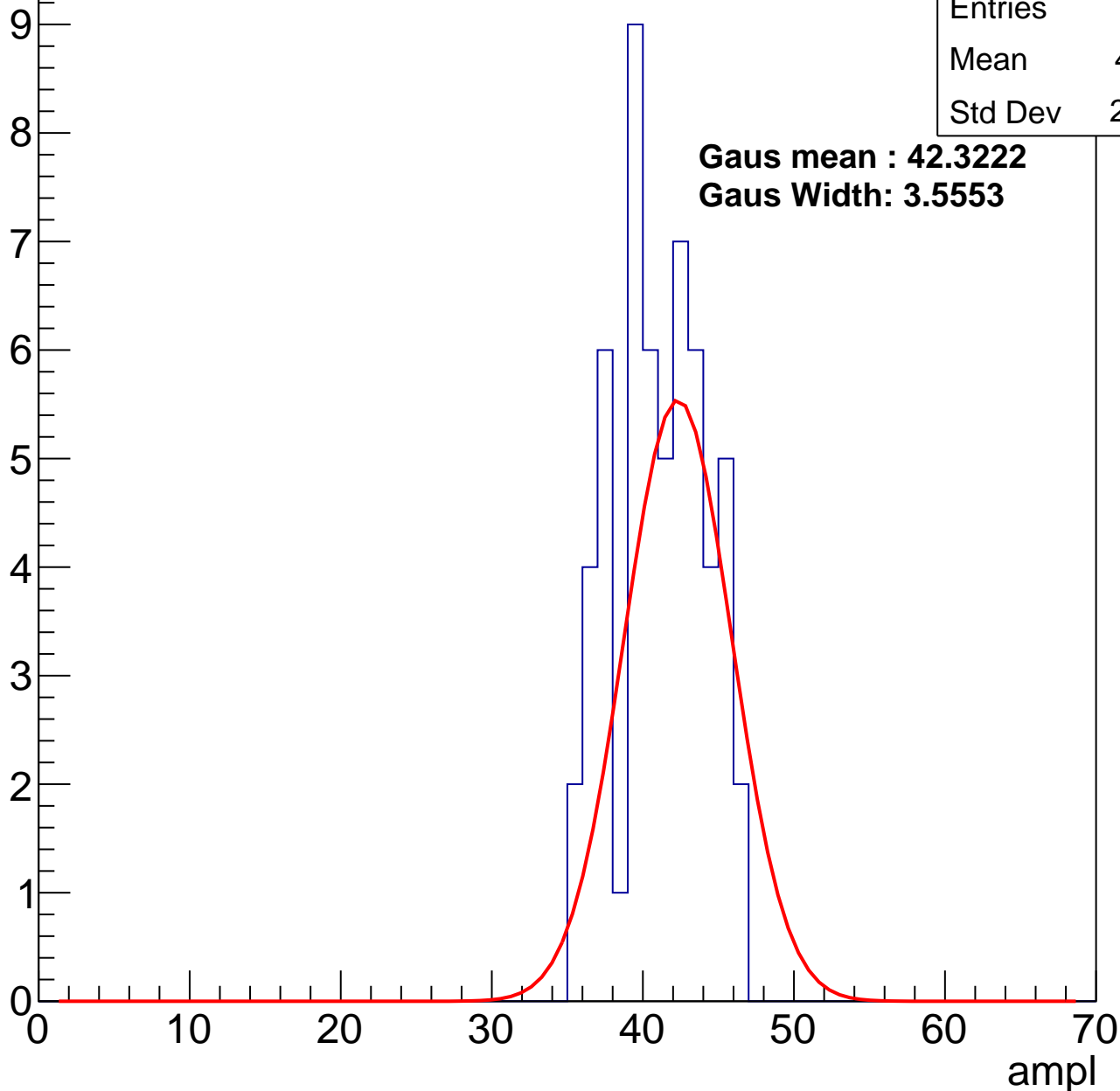
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.61
Std Dev	2.984

**Gaus mean : 42.3222**

**Gaus Width: 3.5553**

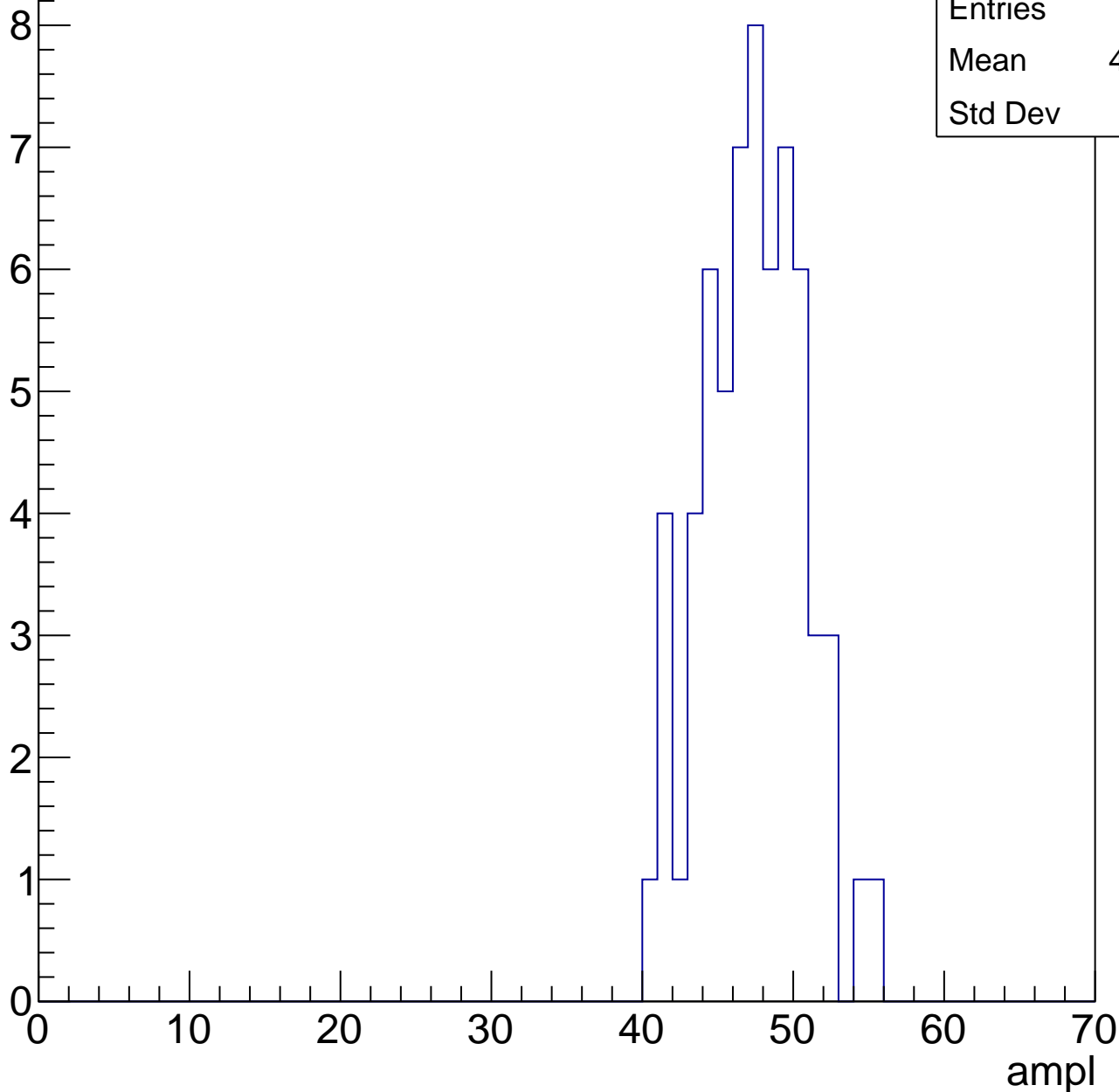


# B1L000S, U23-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

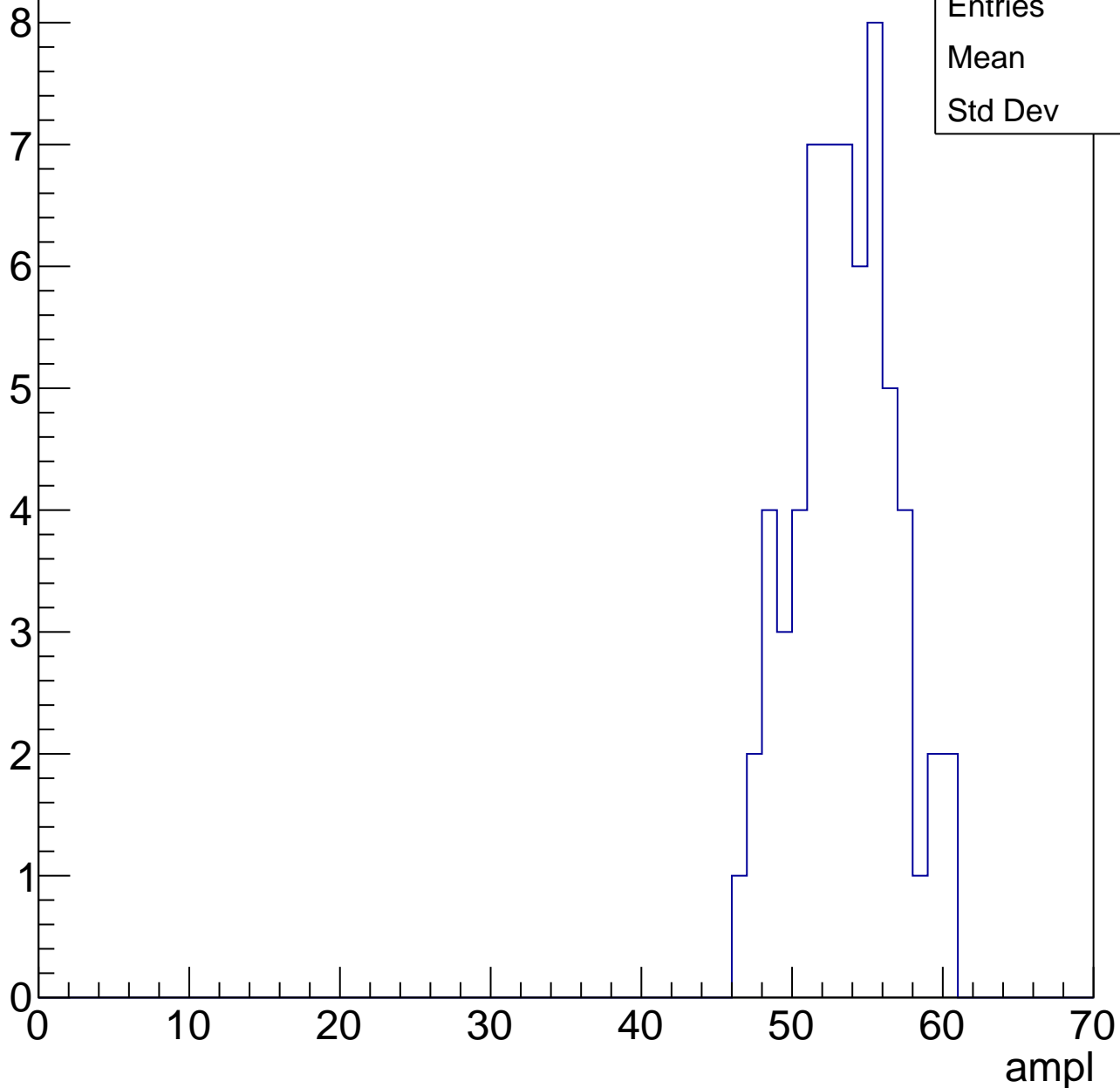
Entries	63
Mean	46.89
Std Dev	3.3



# B1L000S, U23-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

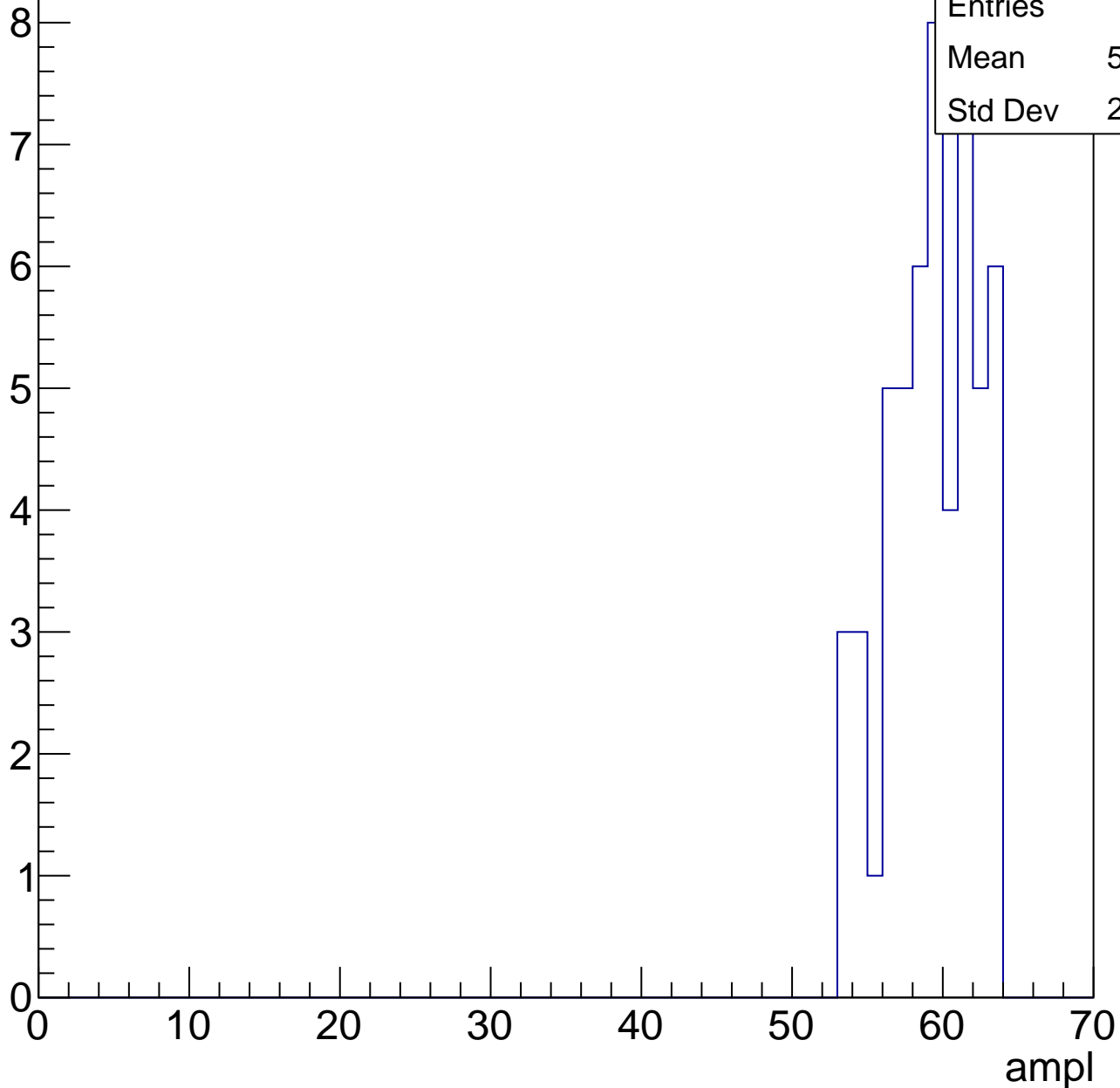
Entry



# B1L000S, U23-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

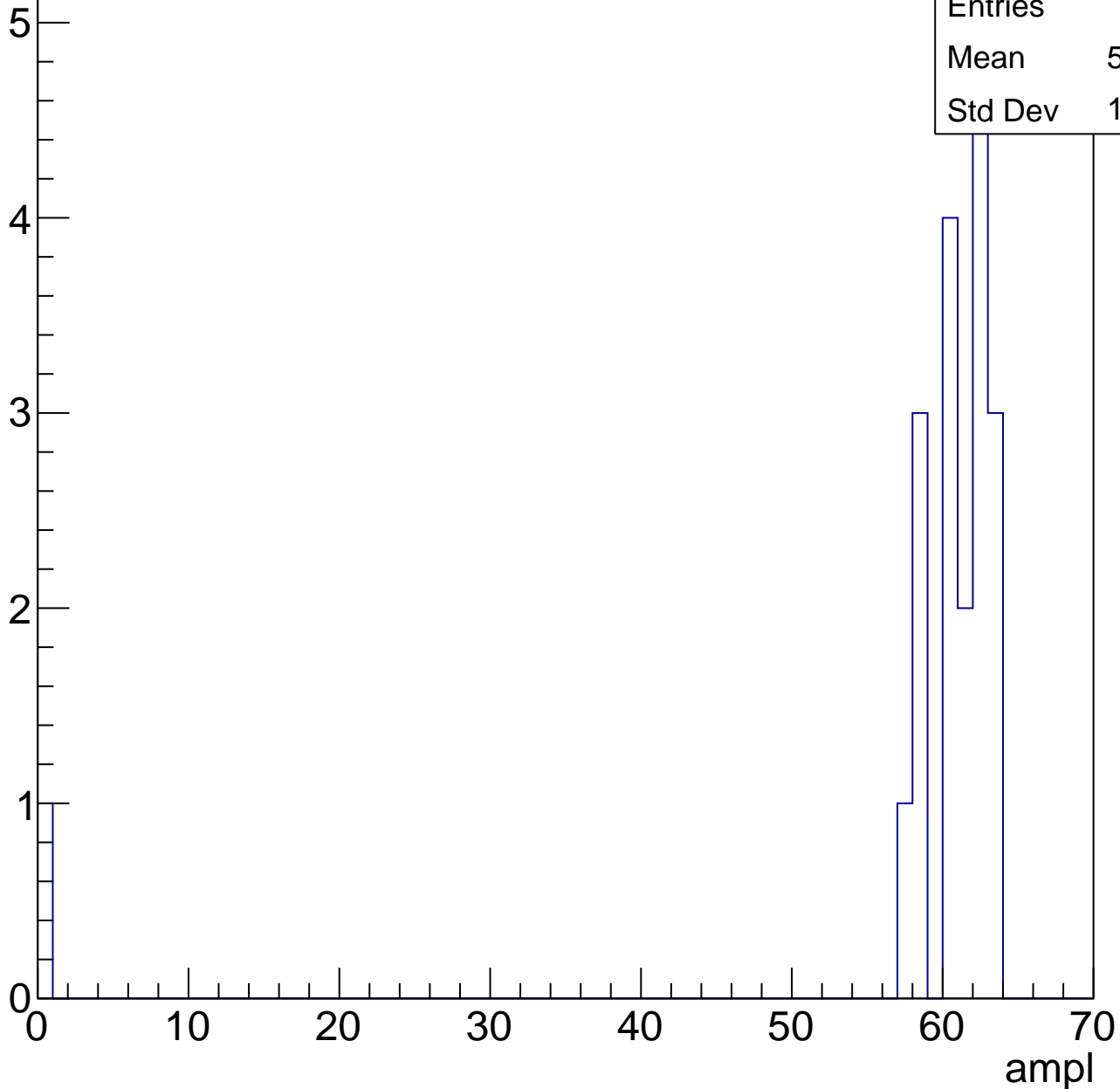


# B1L000S, U23-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	57.47
Std Dev	13.67





# B1L000S, U23-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	27.25
Std Dev	4.212

**Gaus mean : 27.9361**

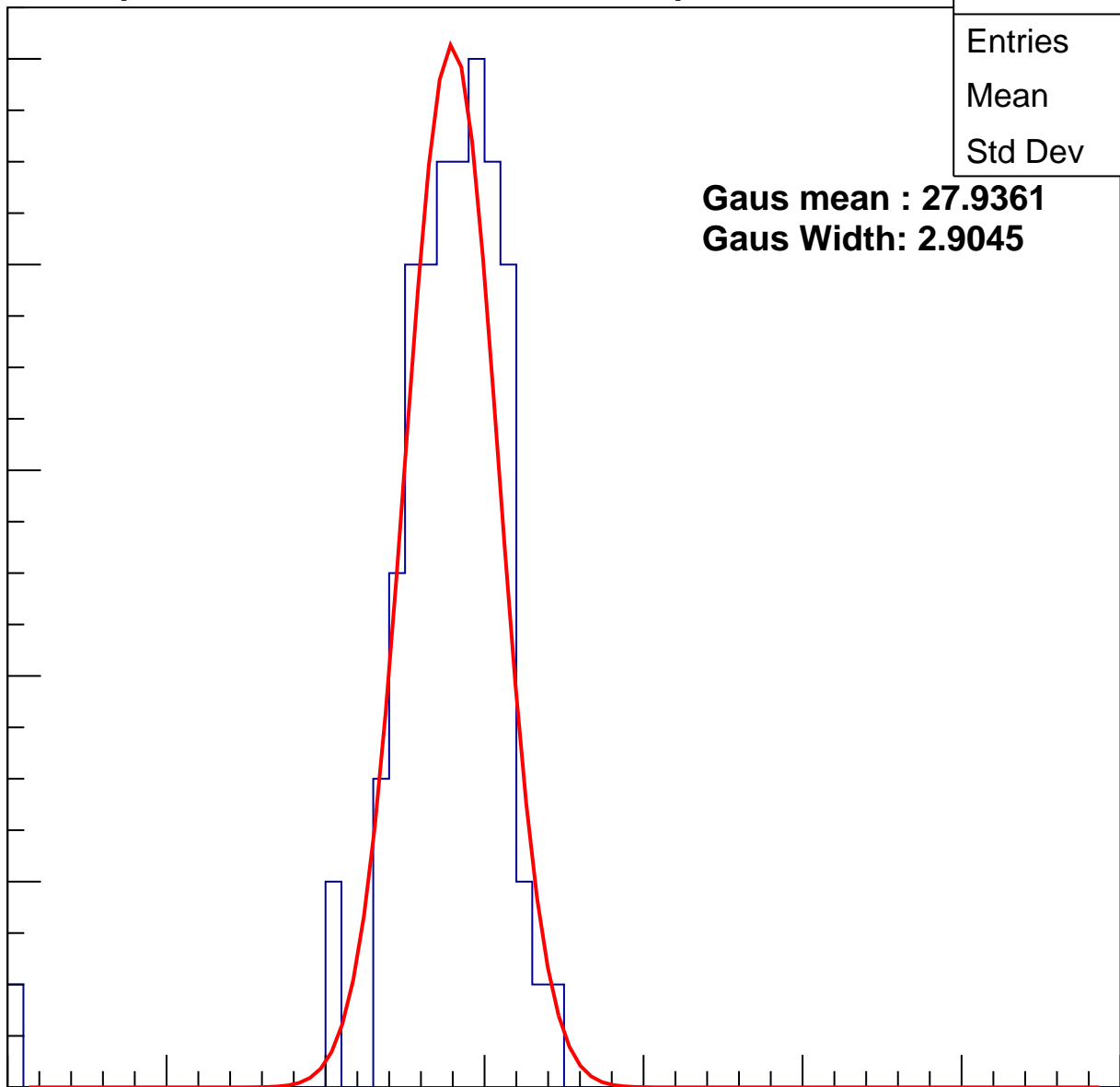
**Gaus Width: 2.9045**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	35.3
Std Dev	2.902

**Gaus mean : 35.8700**

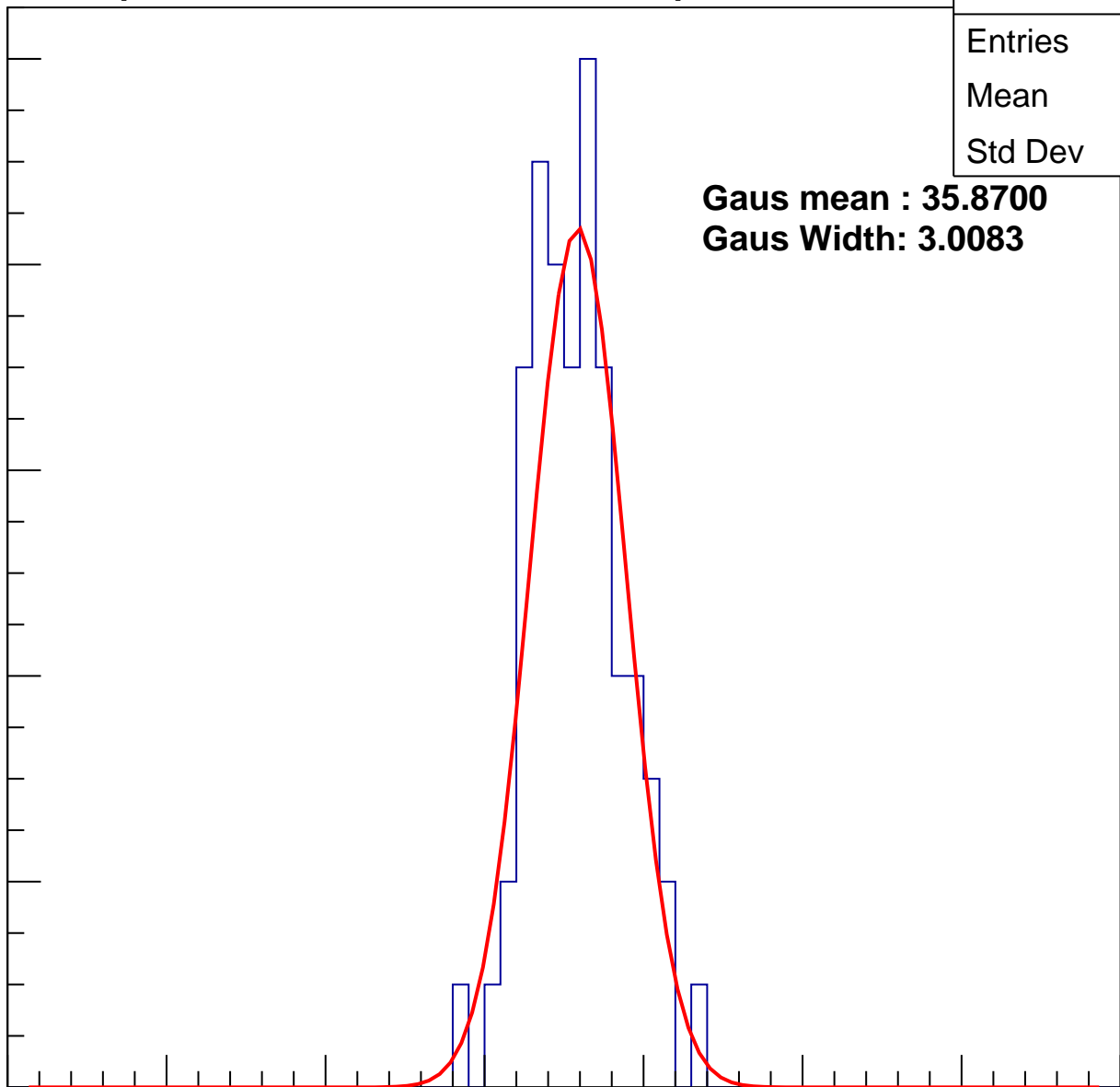
**Gaus Width: 3.0083**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch89, adc2

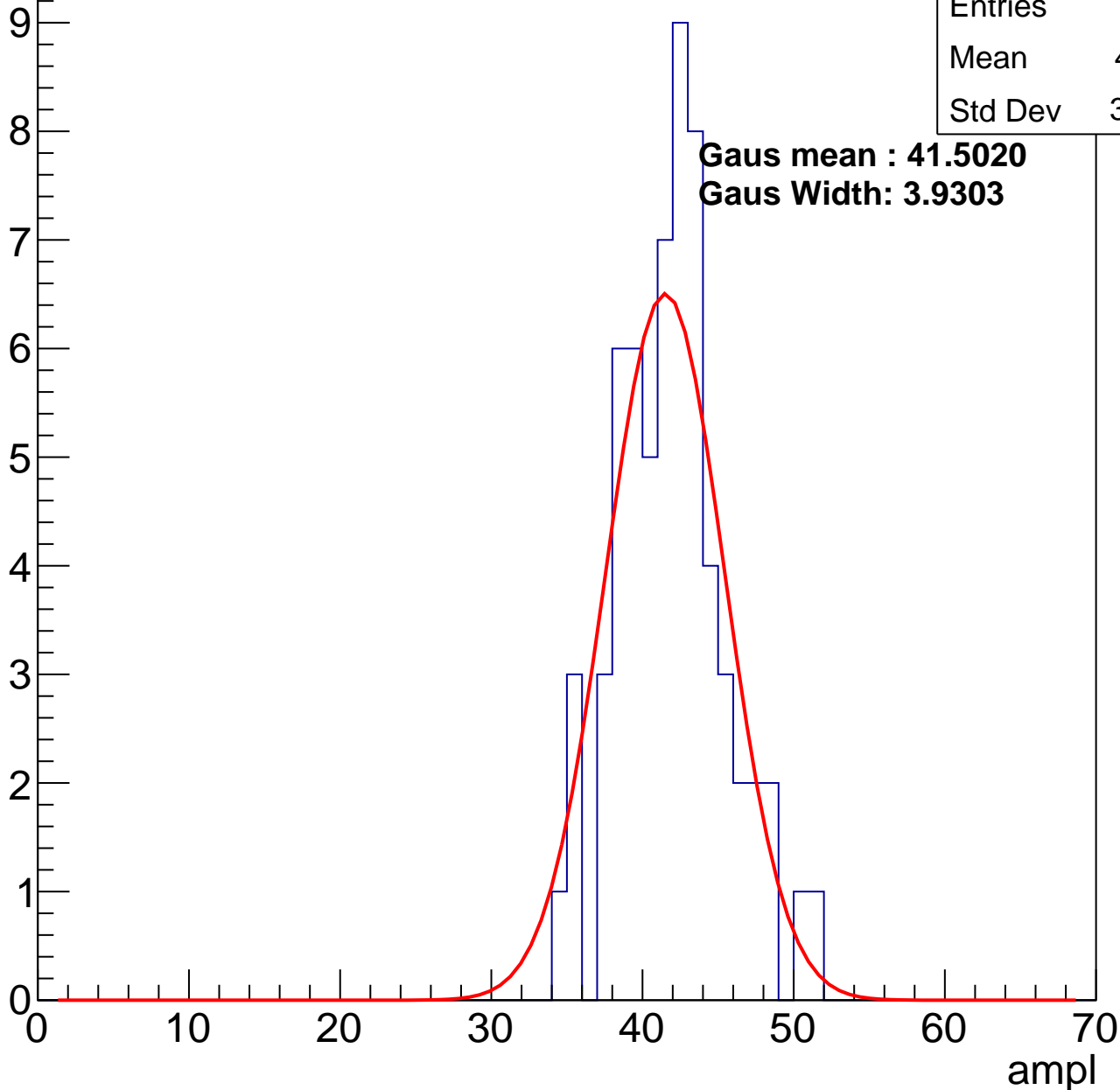
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.51
Std Dev	3.558

**Gaus mean : 41.5020**

**Gaus Width: 3.9303**

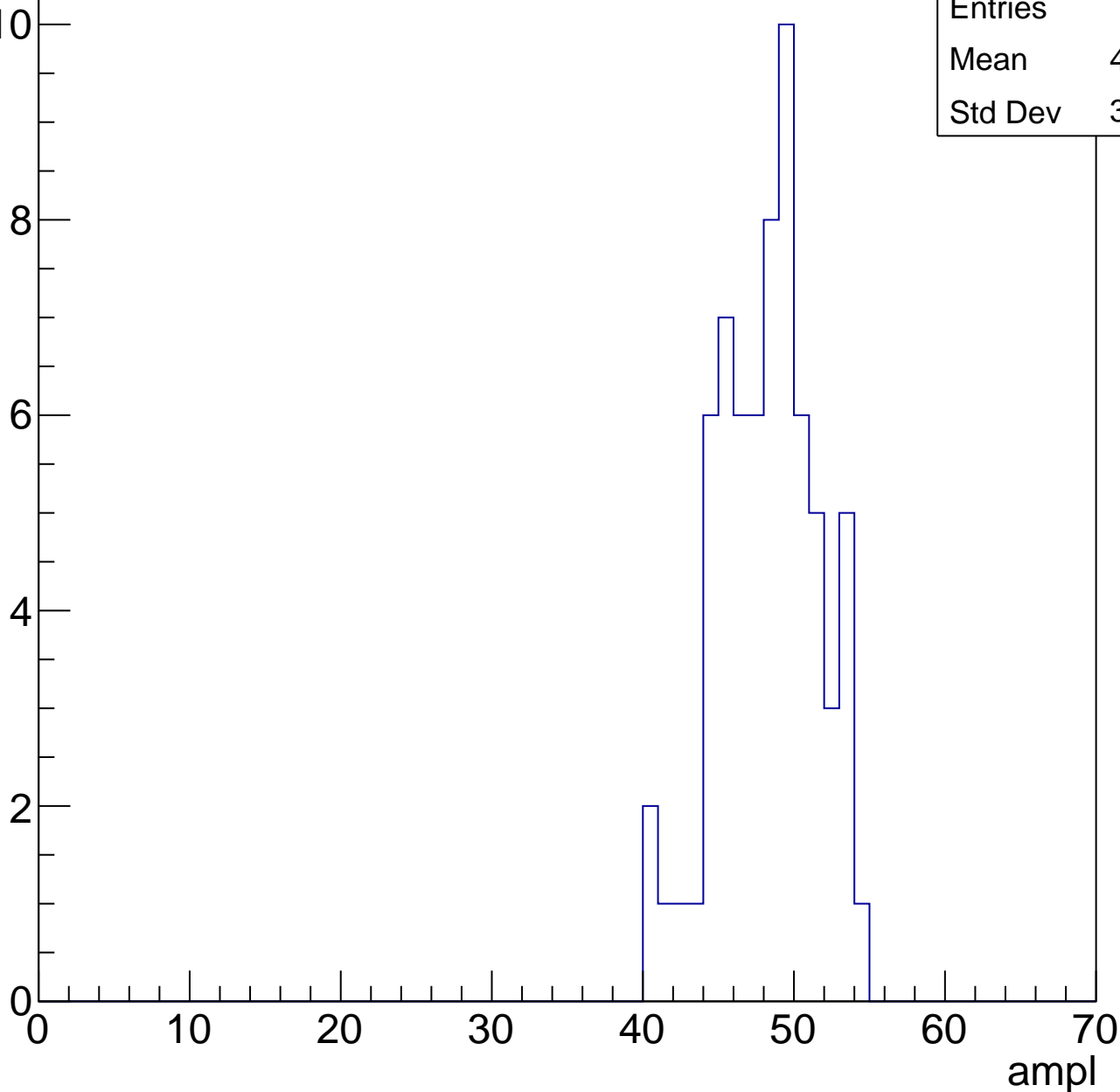


# B1L000S, U23-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

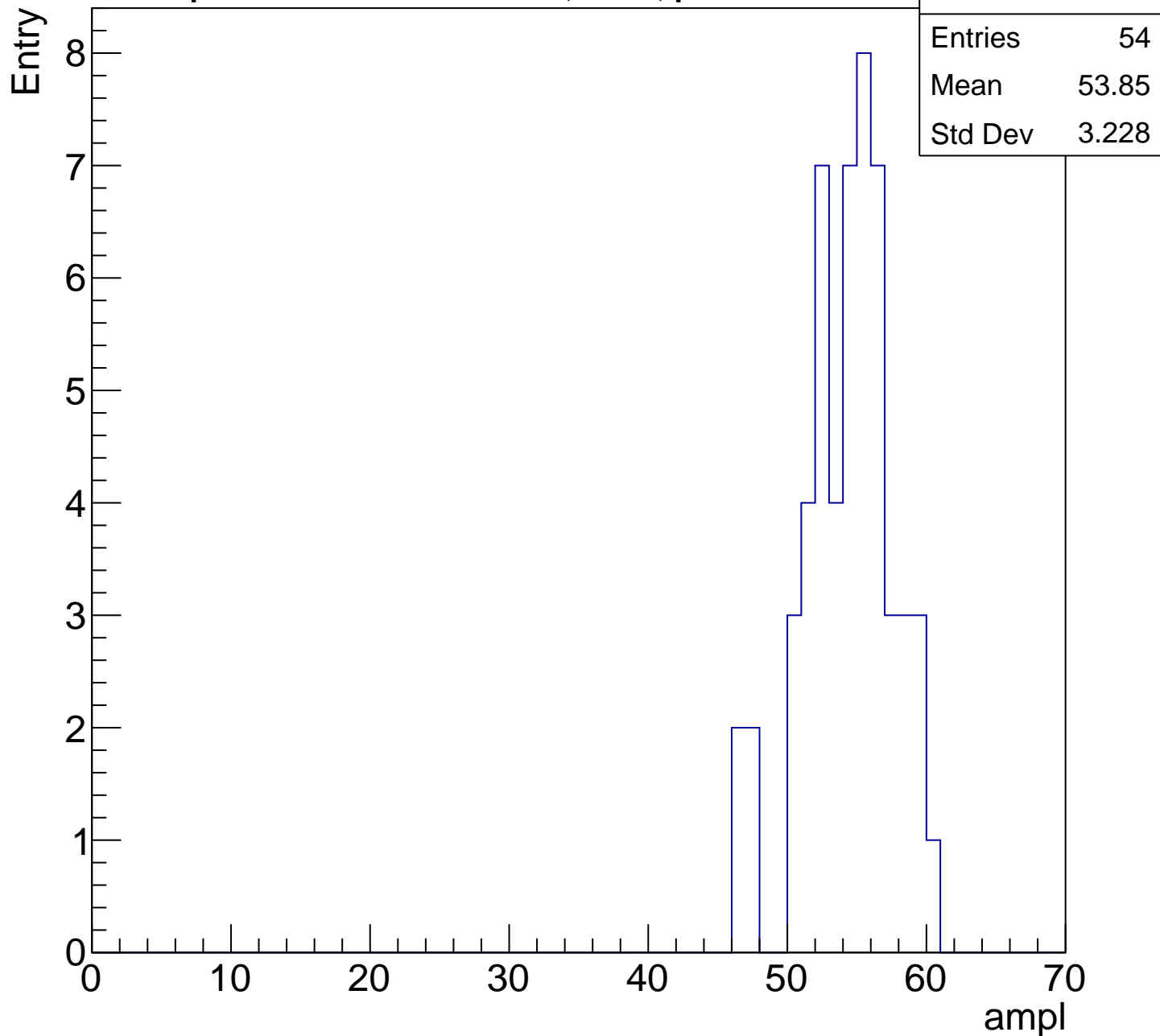
Entry

Entries	68
Mean	47.75
Std Dev	3.233



# B1L000S, U23-ch89, adc4

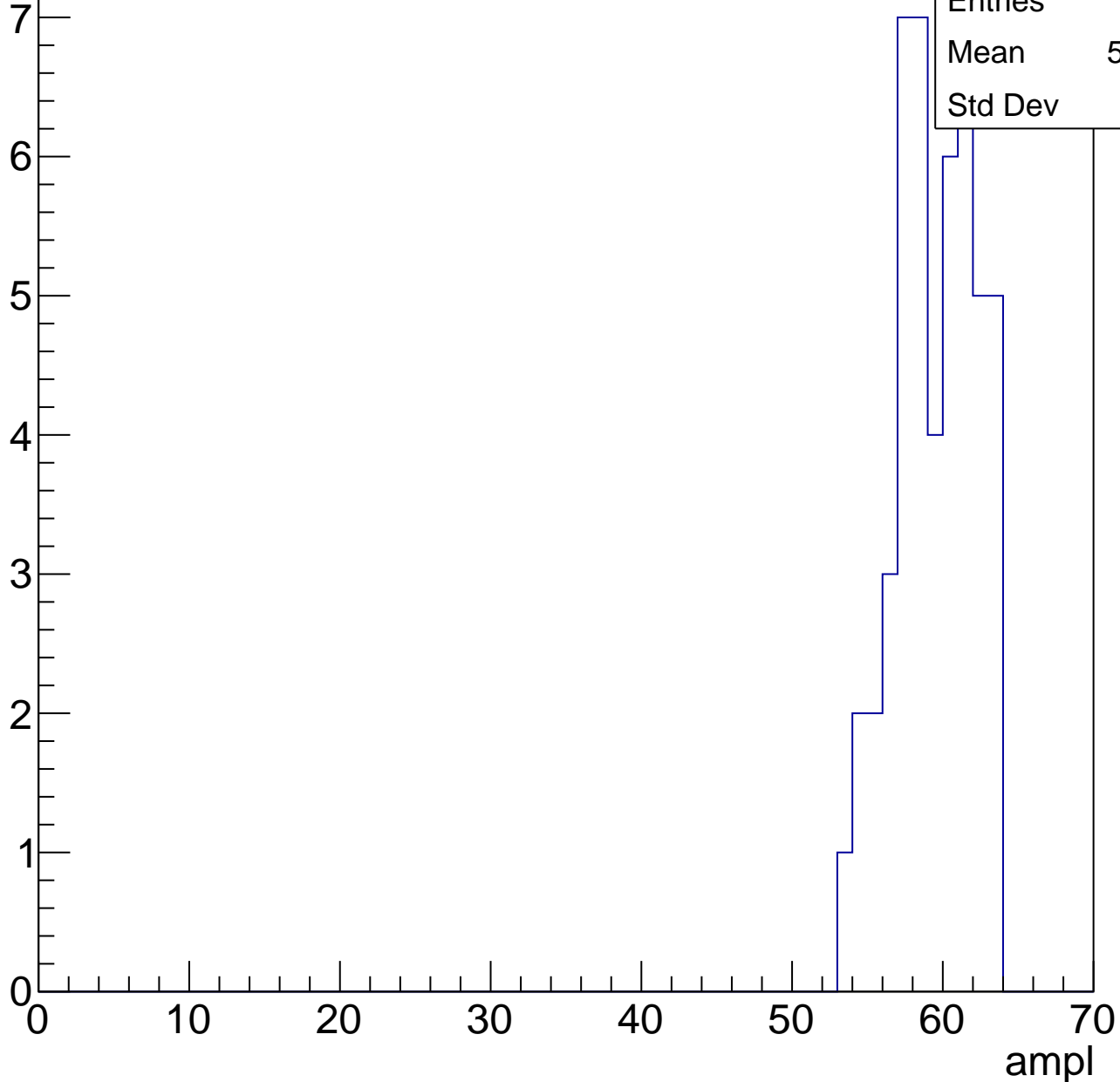
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



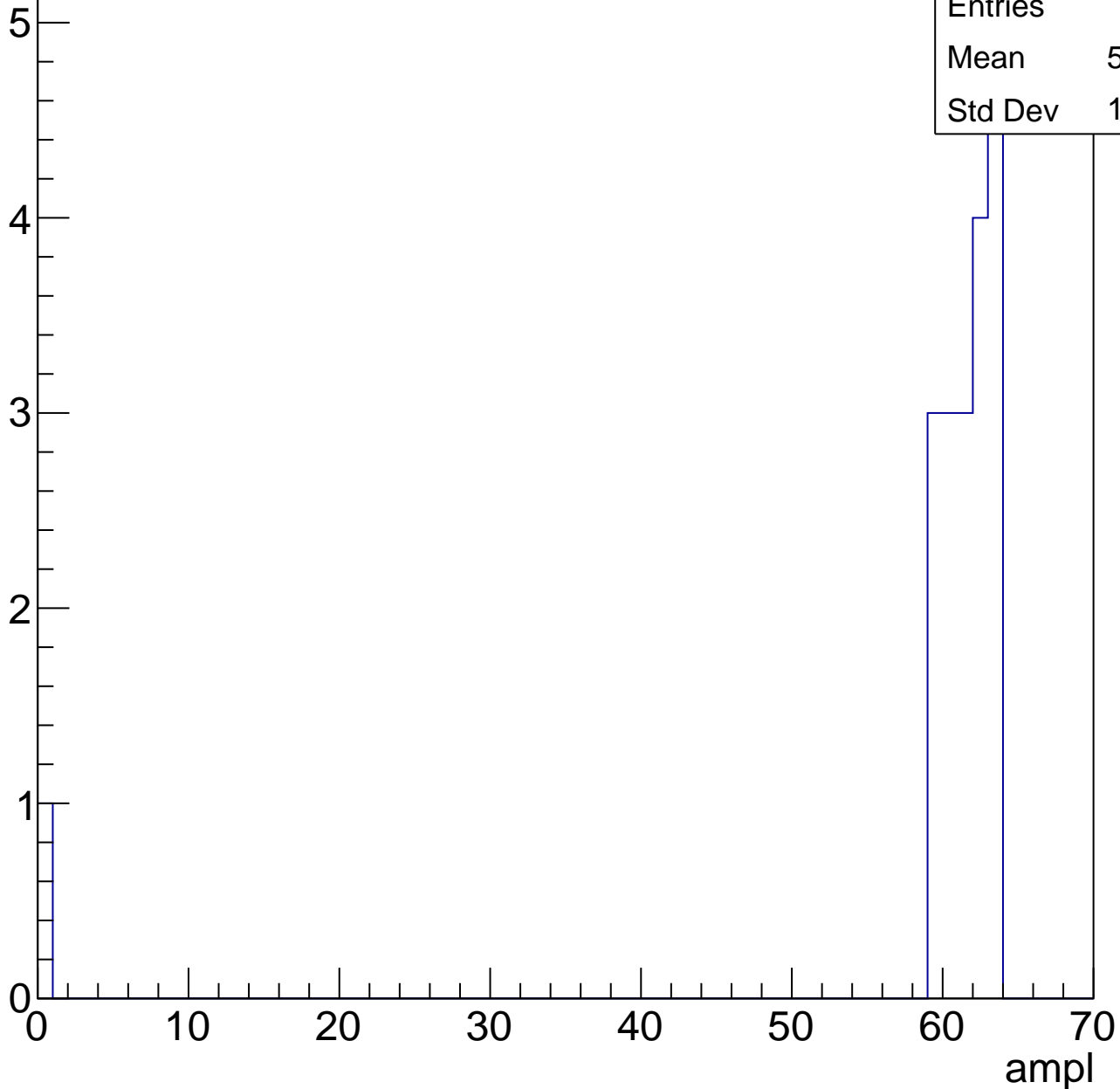
Entries	49
Mean	59.02
Std Dev	2.63

# B1L000S, U23-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.05
Std Dev	13.76





# B1L000S, U23-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch90, adc0

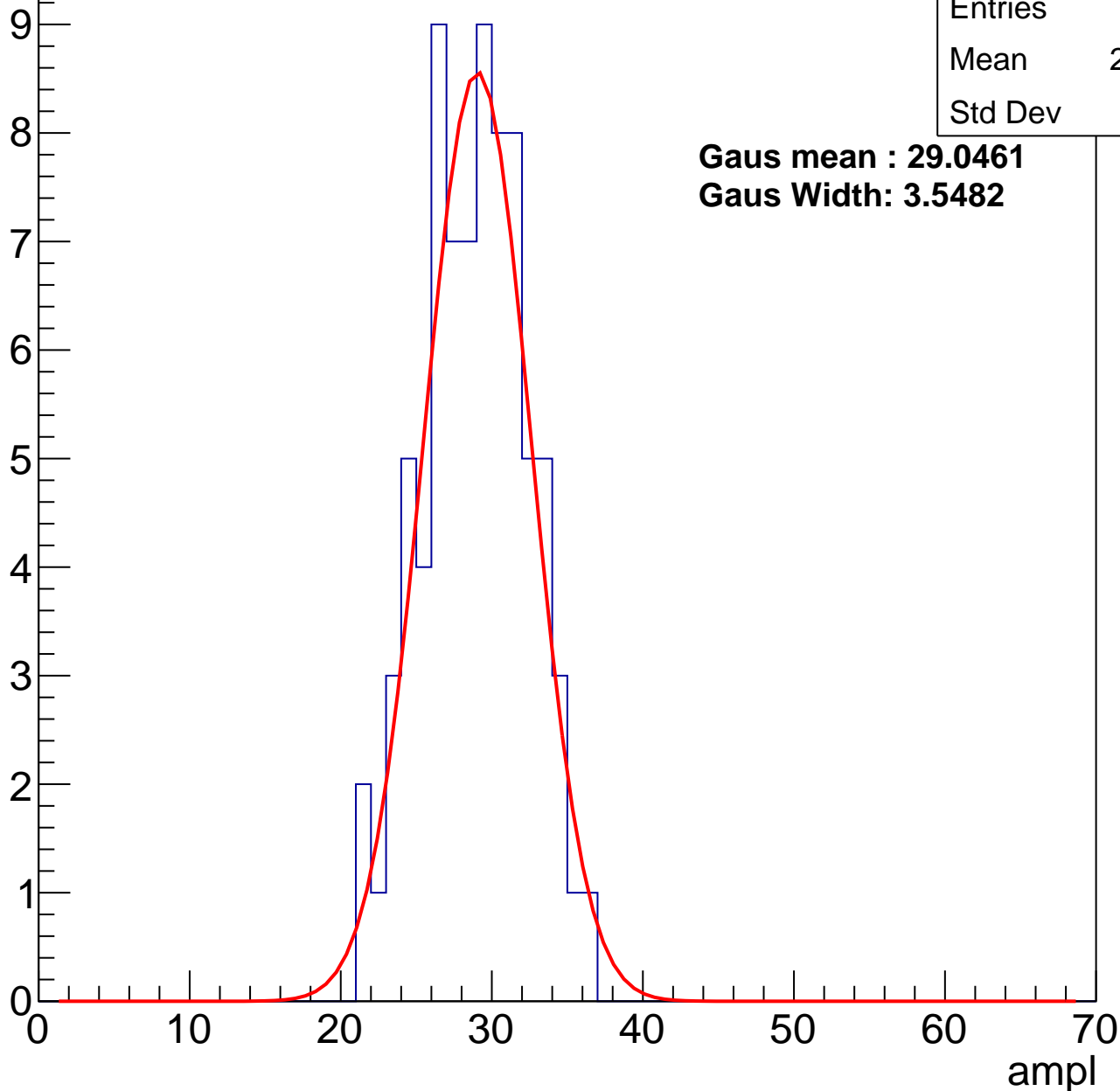
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	28.45
Std Dev	3.38

**Gaus mean : 29.0461**

**Gaus Width: 3.5482**



# B1L000S, U23-ch90, adc1

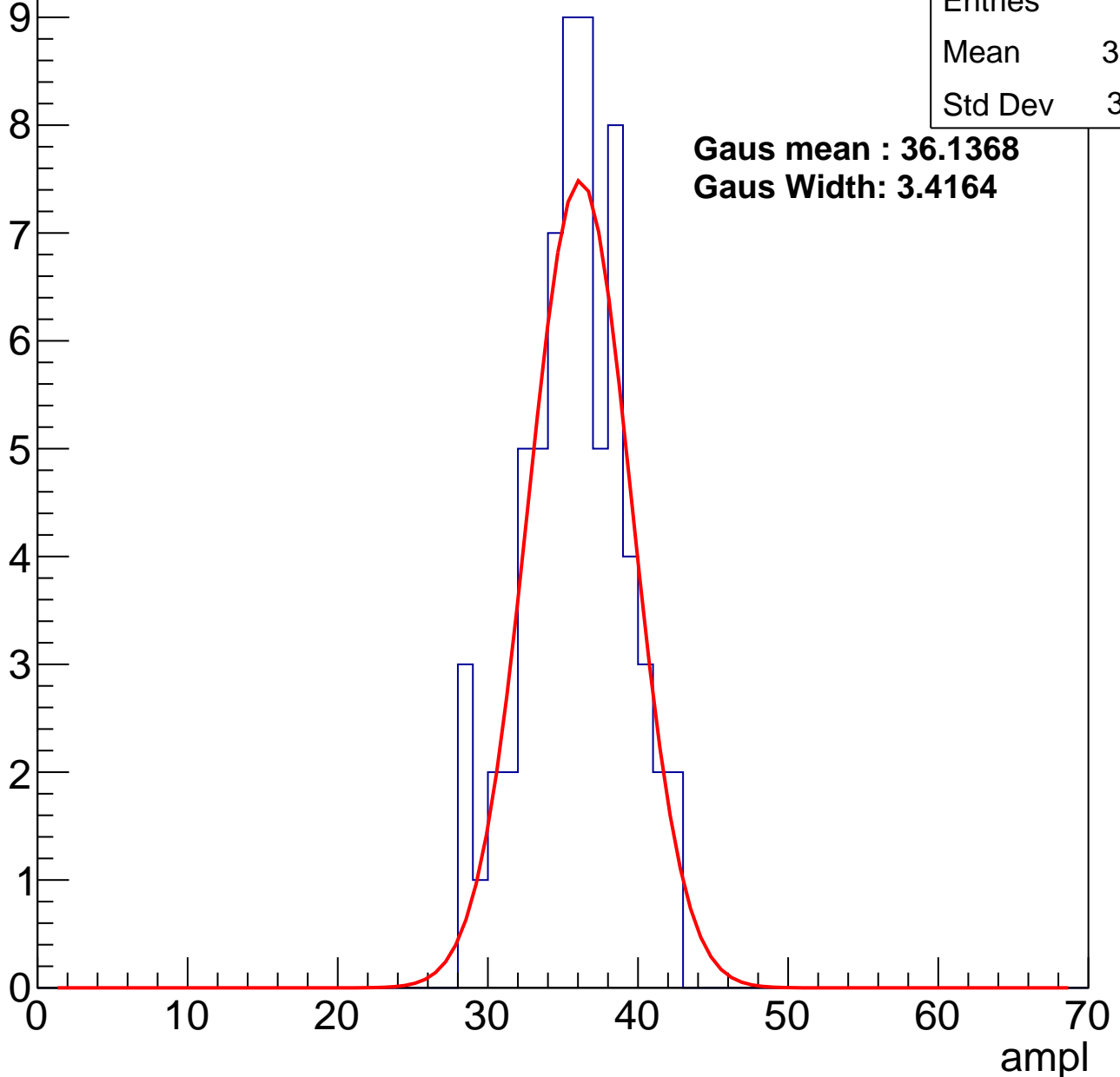
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.34
Std Dev	3.321

**Gaus mean : 36.1368**

**Gaus Width: 3.4164**



# B1L000S, U23-ch90, adc2

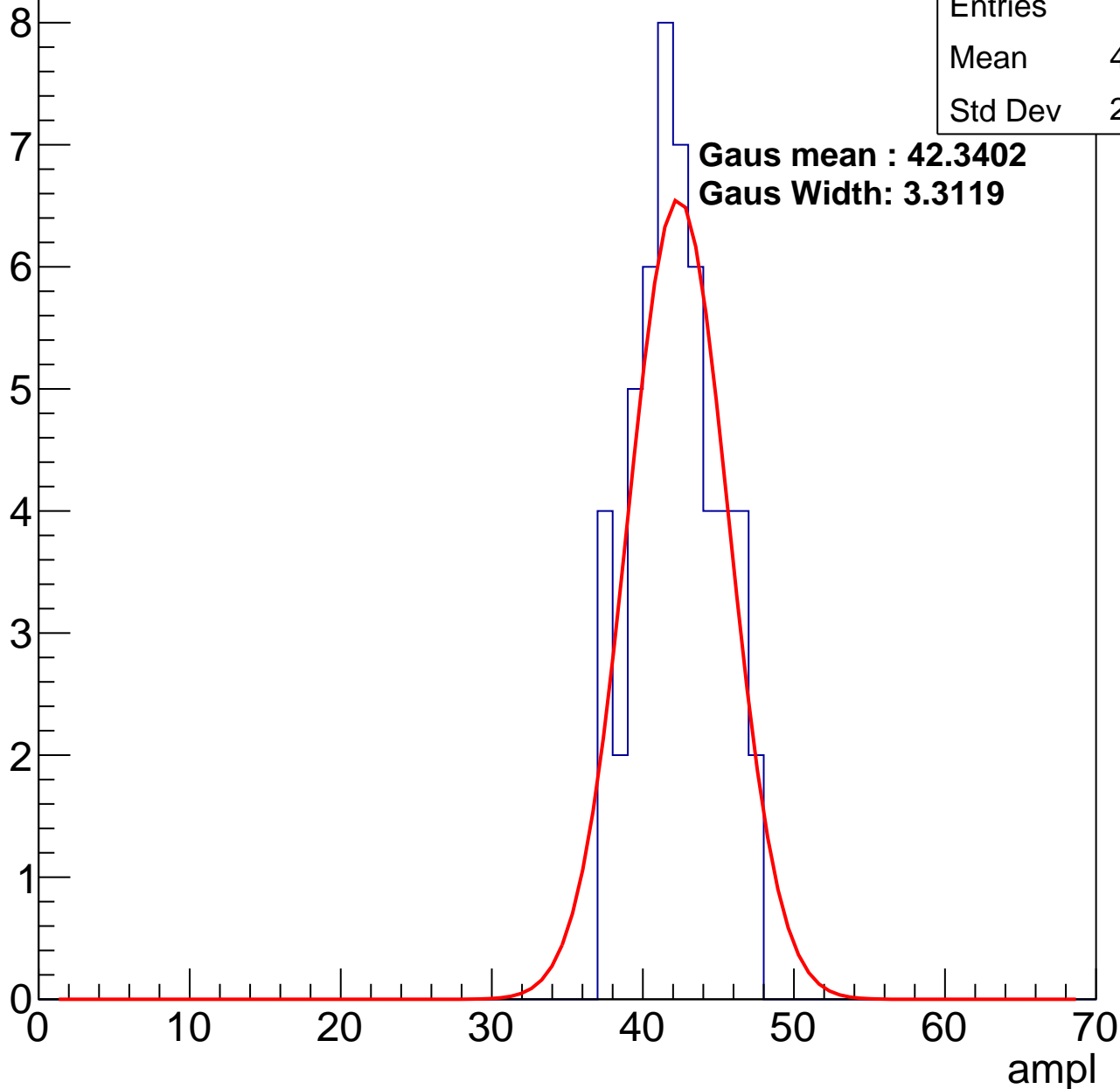
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.79
Std Dev	2.699

**Gaus mean : 42.3402**

**Gaus Width: 3.3119**

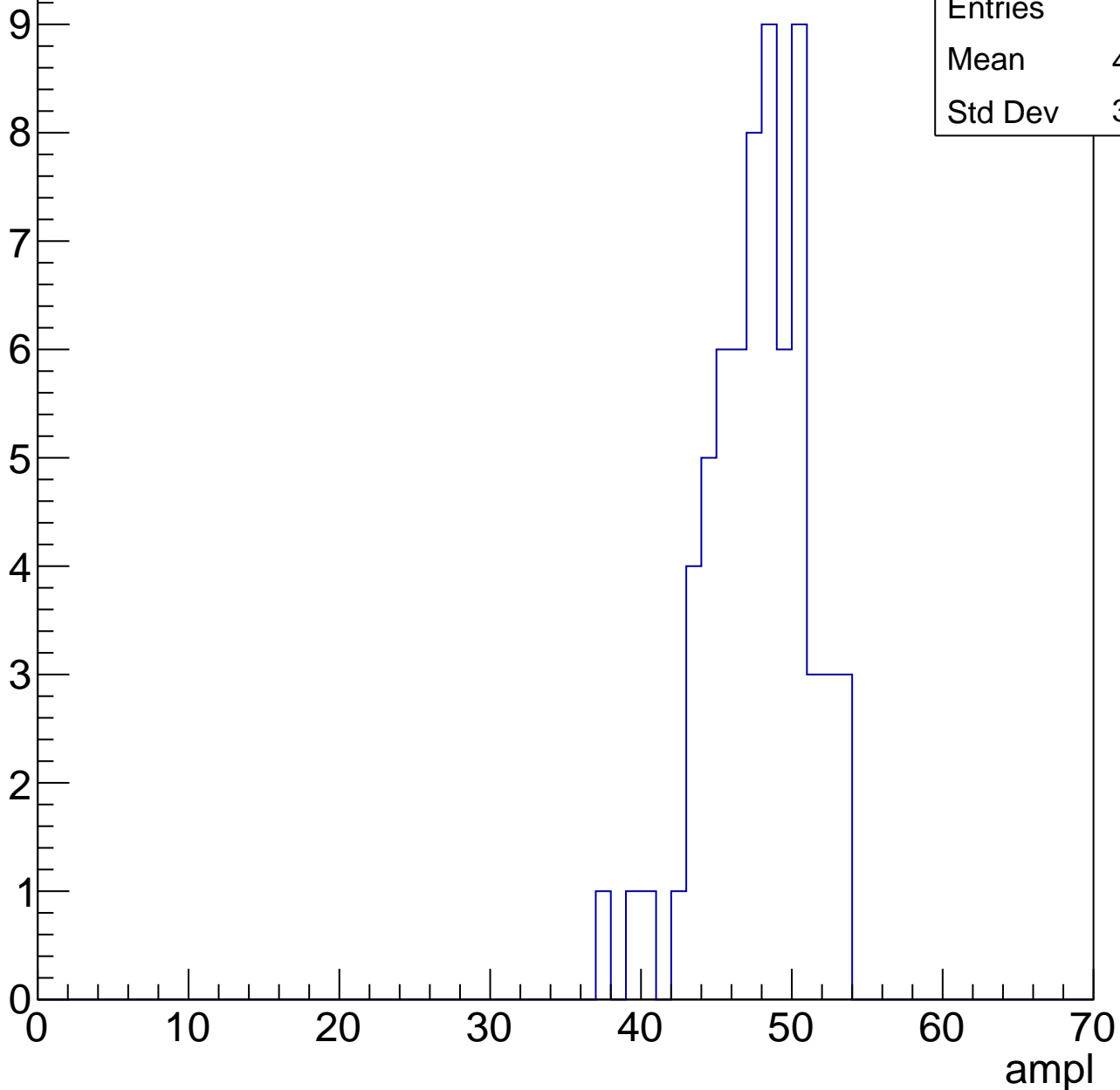


# B1L000S, U23-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	47.21
Std Dev	3.301

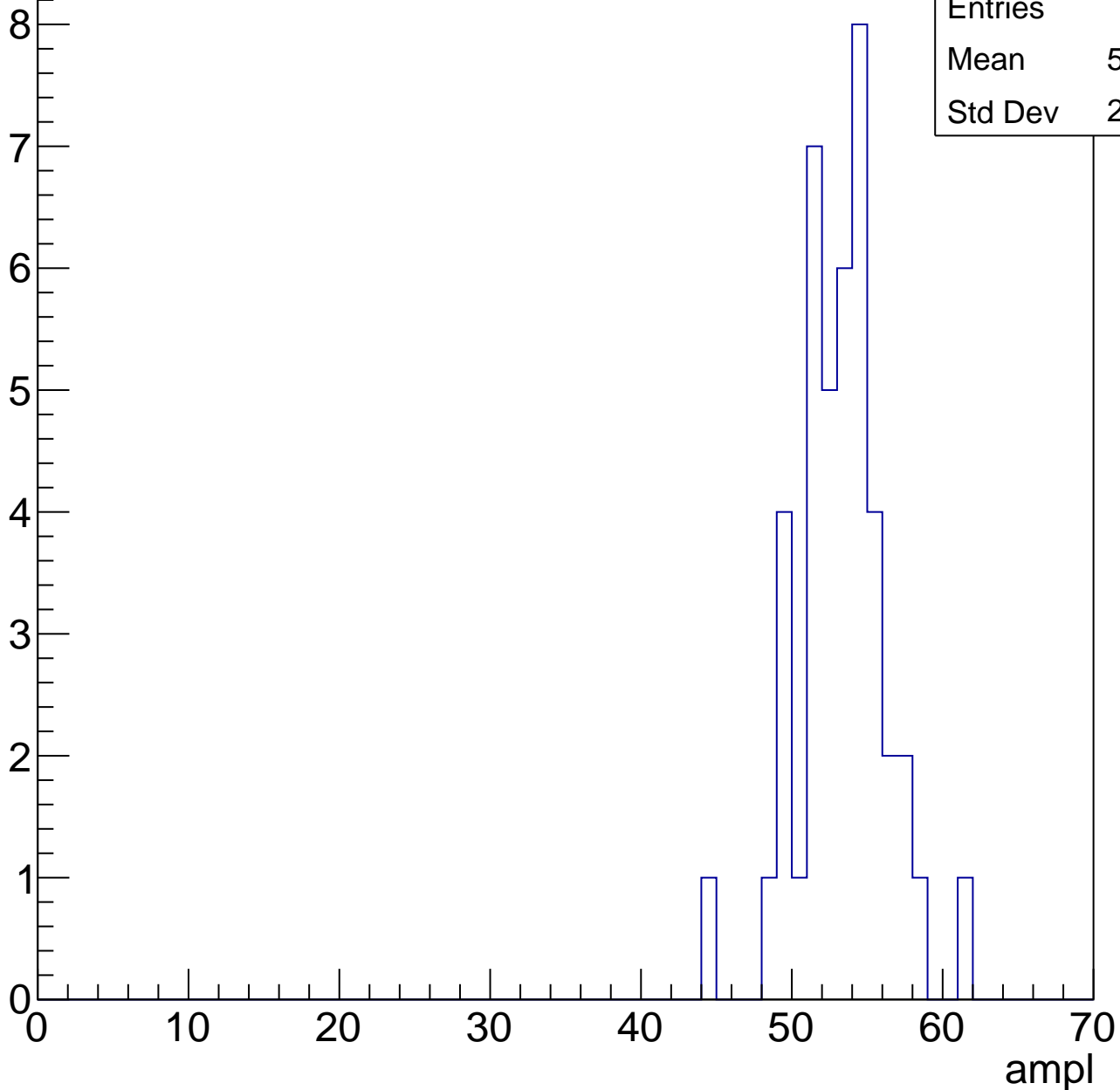


# B1L000S, U23-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	52.79
Std Dev	2.946

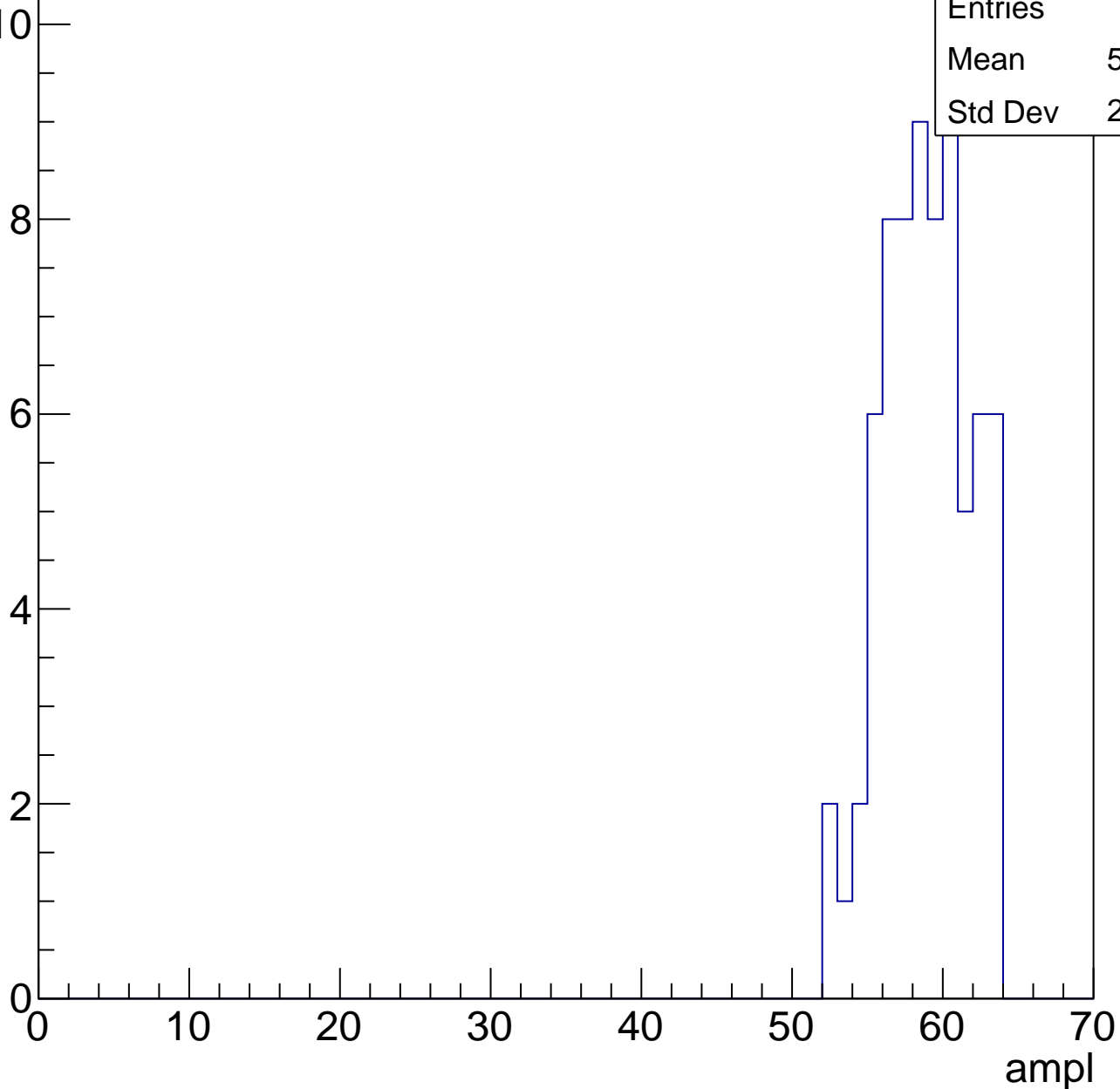


# B1L000S, U23-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

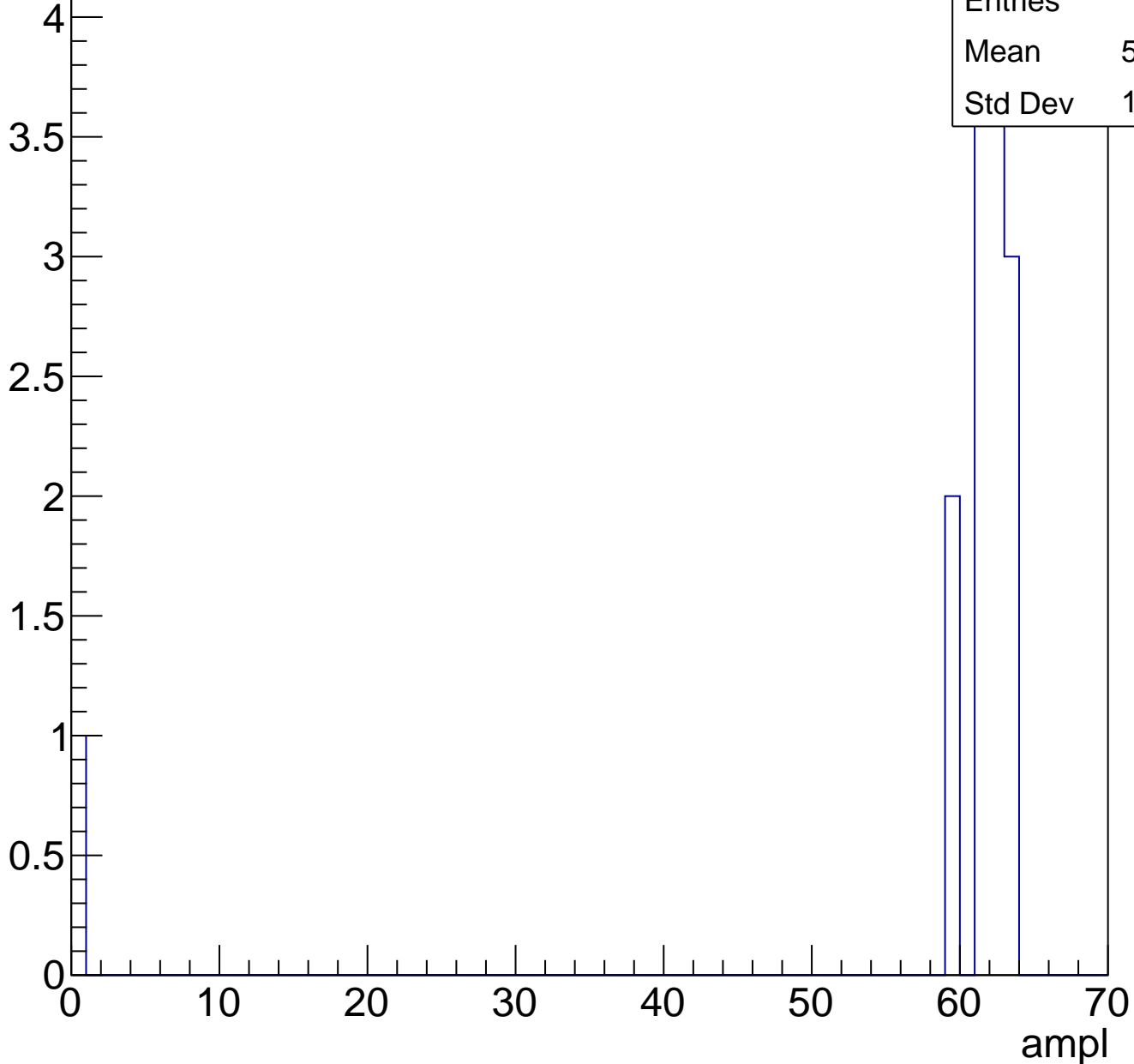
Entries	71
Mean	58.42
Std Dev	2.782



# B1L000S, U23-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch91, adc0

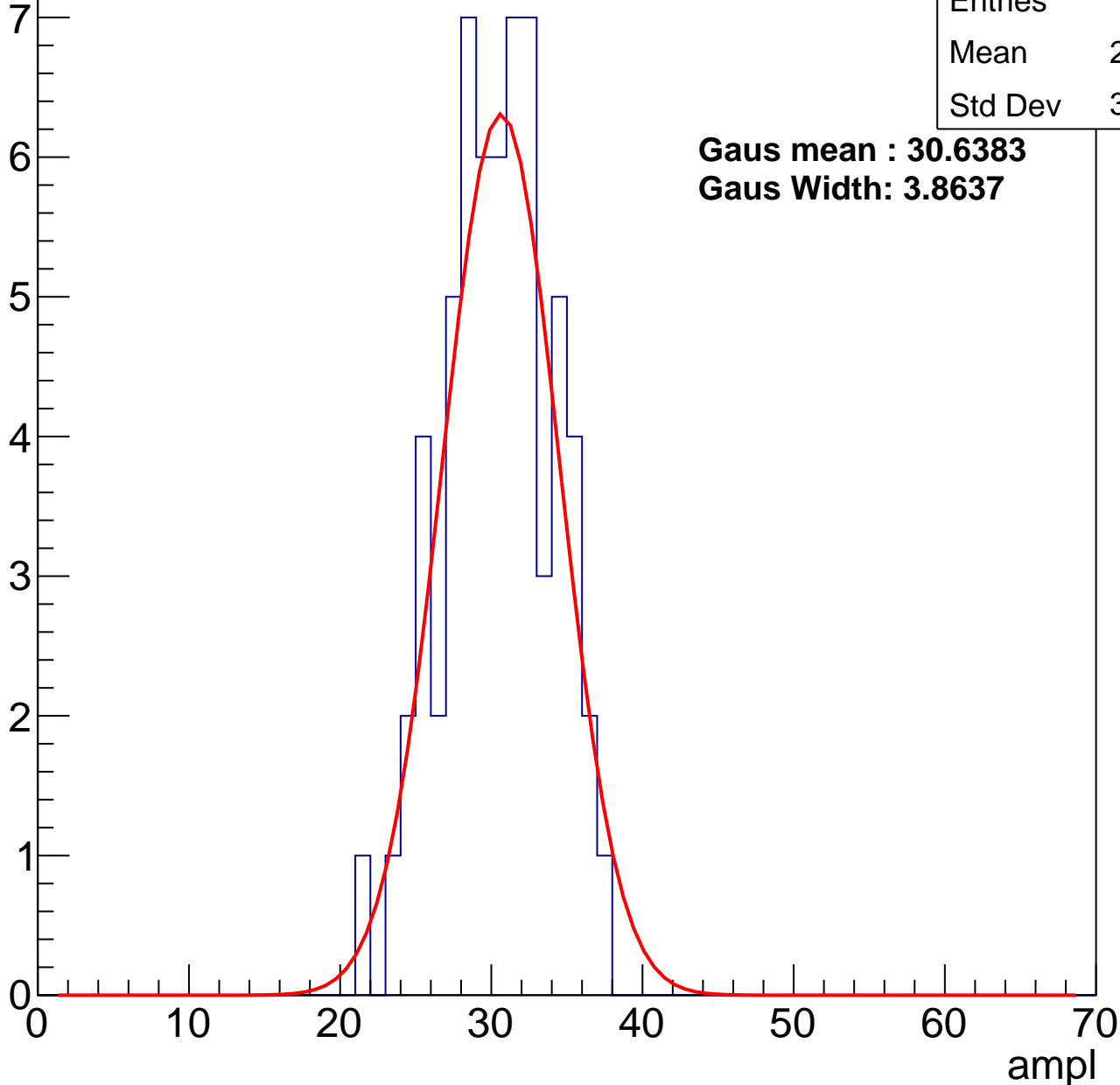
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.97
Std Dev	3.509

**Gaus mean : 30.6383**

**Gaus Width: 3.8637**



# B1L000S, U23-ch91, adc1

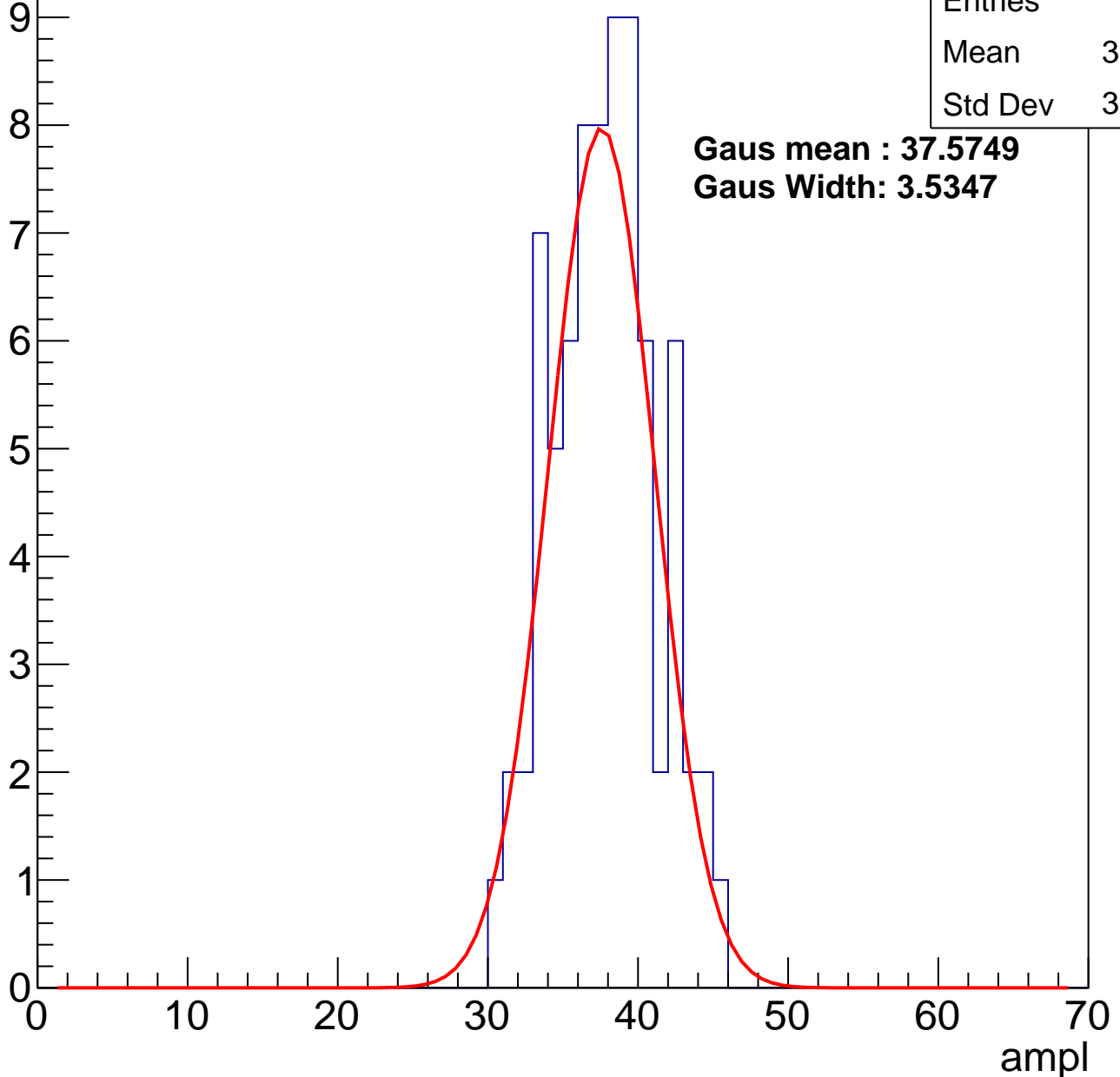
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	37.33
Std Dev	3.377

**Gaus mean : 37.5749**

**Gaus Width: 3.5347**



# B1L000S, U23-ch91, adc2

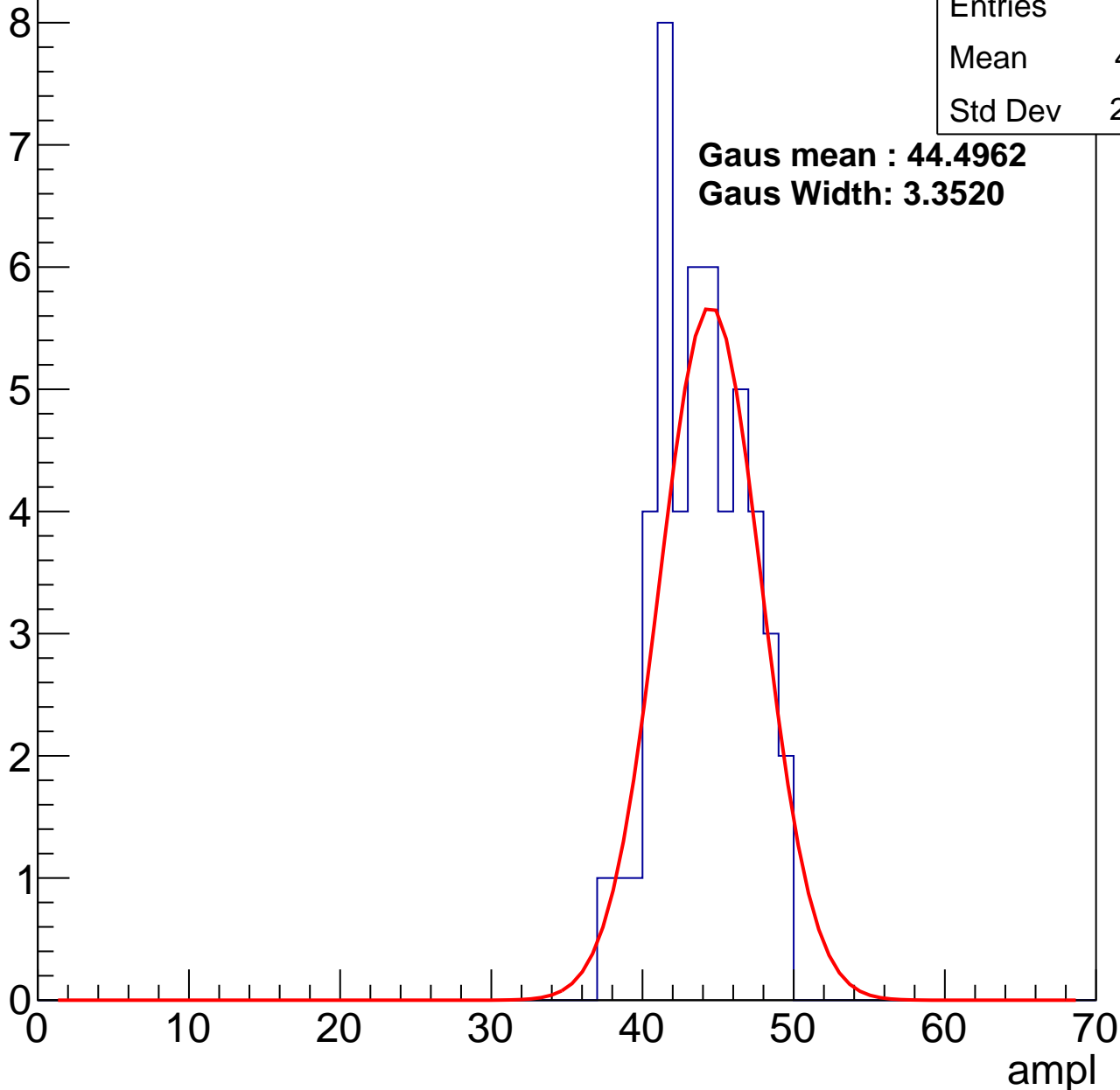
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	43.51
Std Dev	2.908

**Gaus mean : 44.4962**

**Gaus Width: 3.3520**

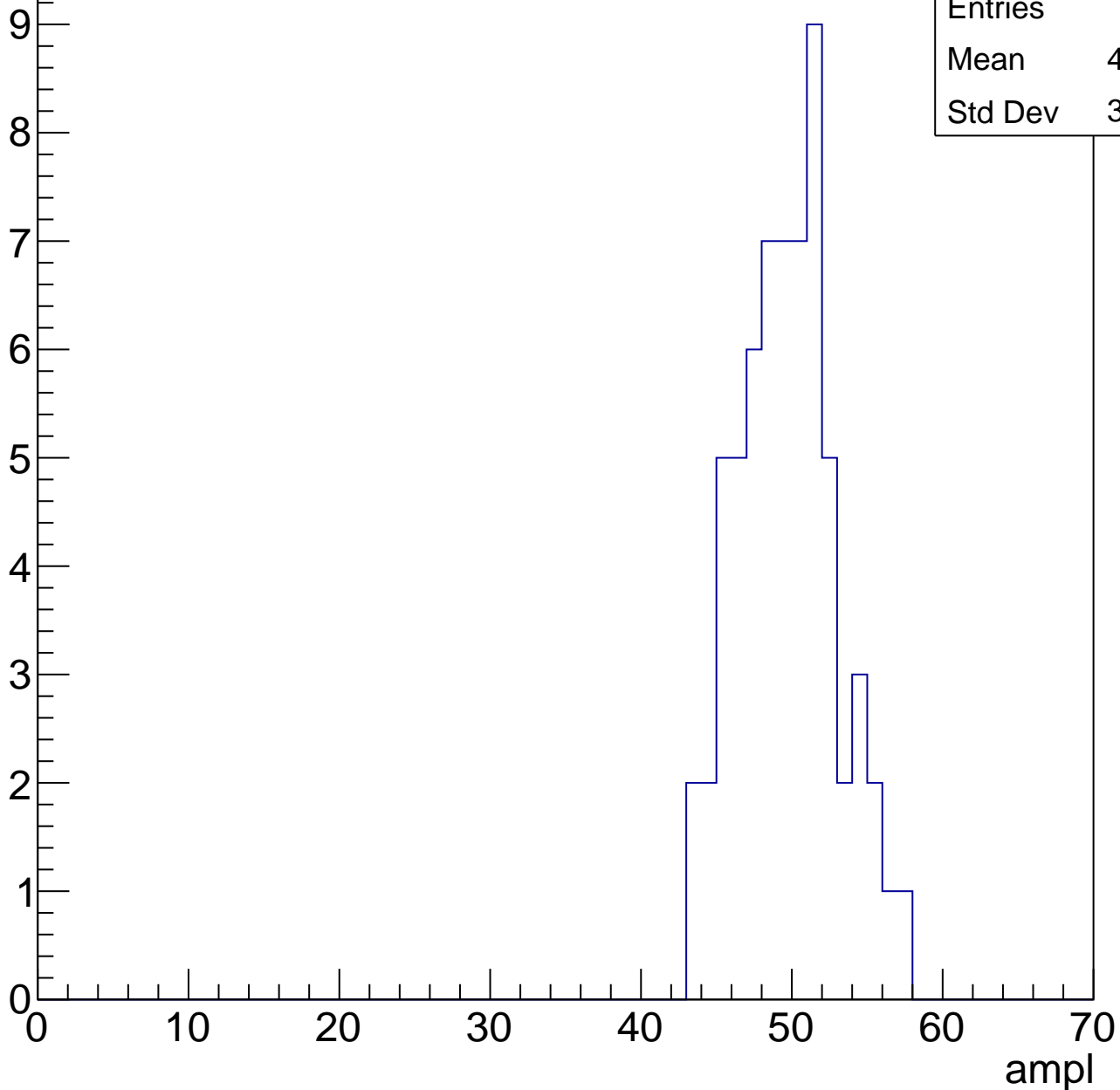


# B1L000S, U23-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

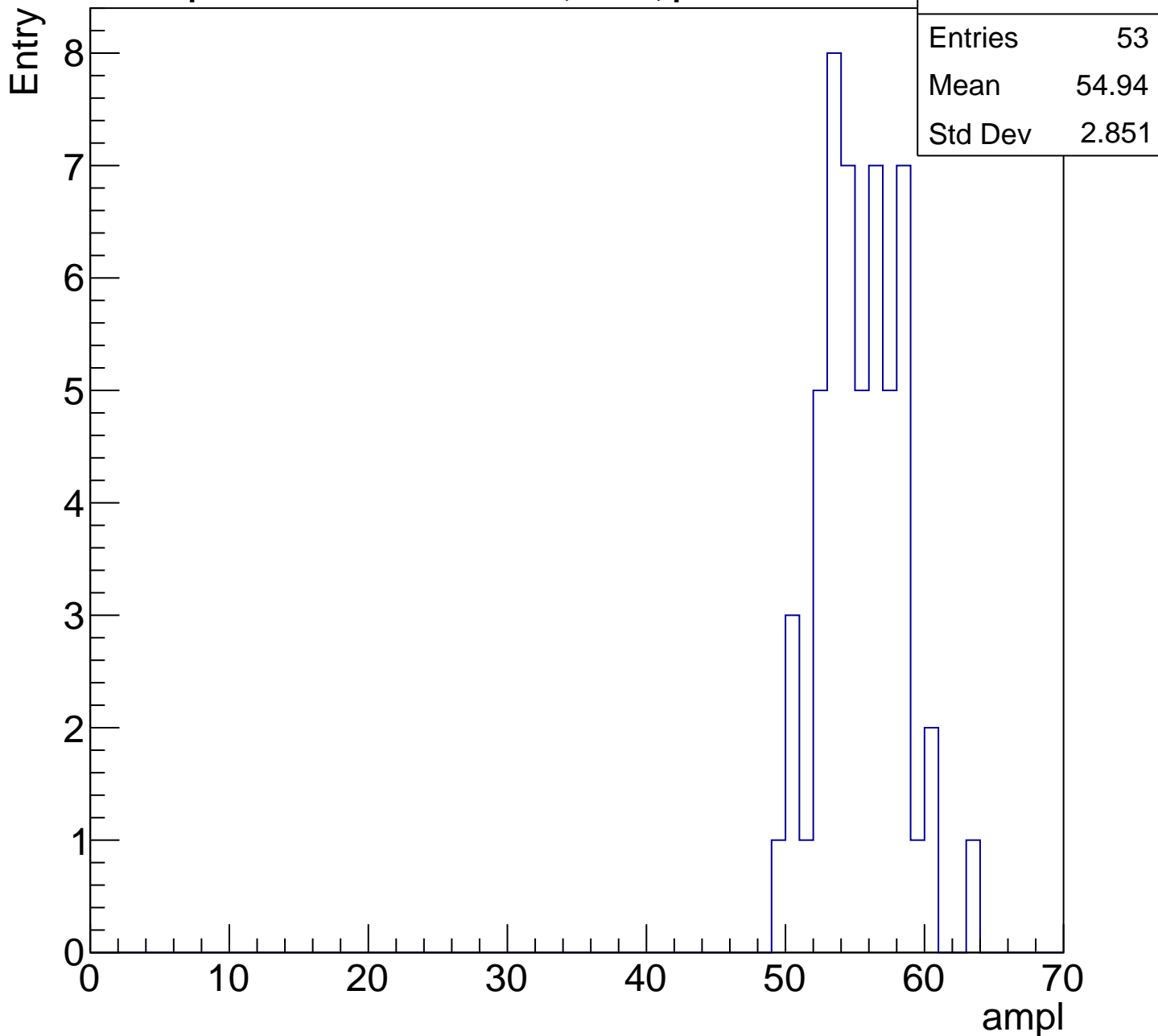
Entry

Entries	64
Mean	49.22
Std Dev	3.199



# B1L000S, U23-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 53

Mean 60.02

Std Dev 2.261

8

6

4

2

0

0

10

20

30

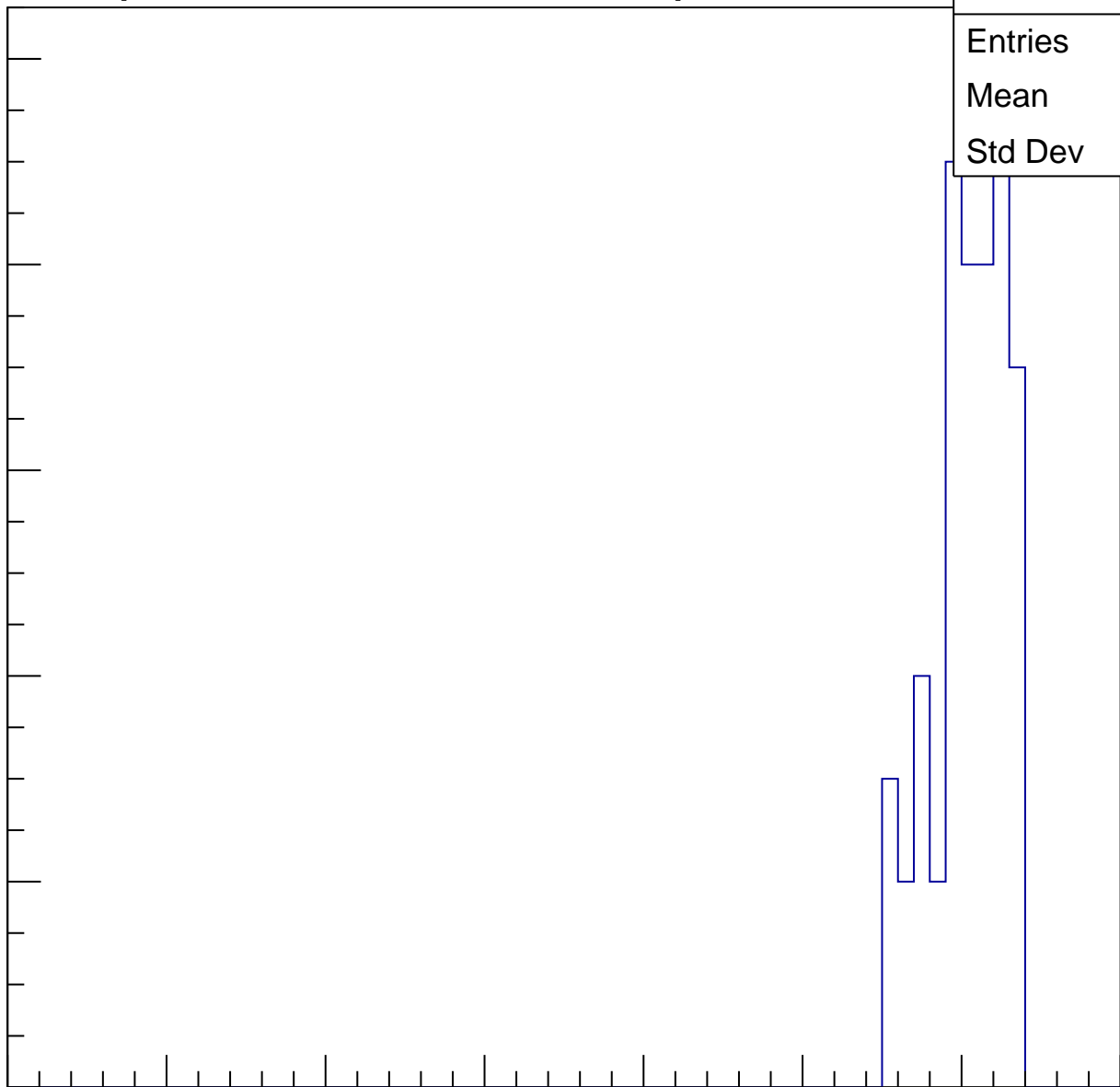
40

50

60

70

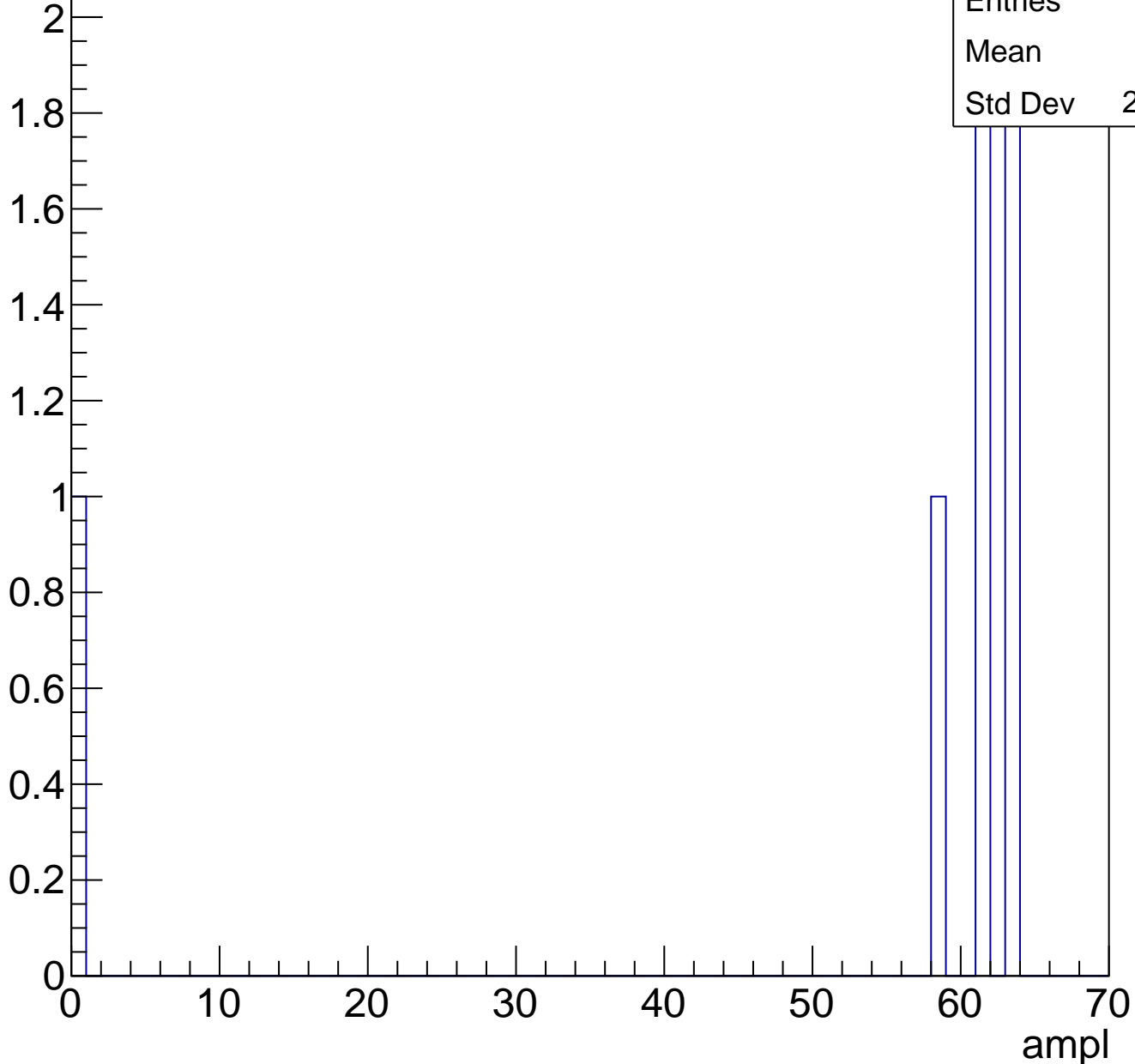
ampl



# B1L000S, U23-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	51
Std Dev	22.87



# B1L000S, U23-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch92, adc0

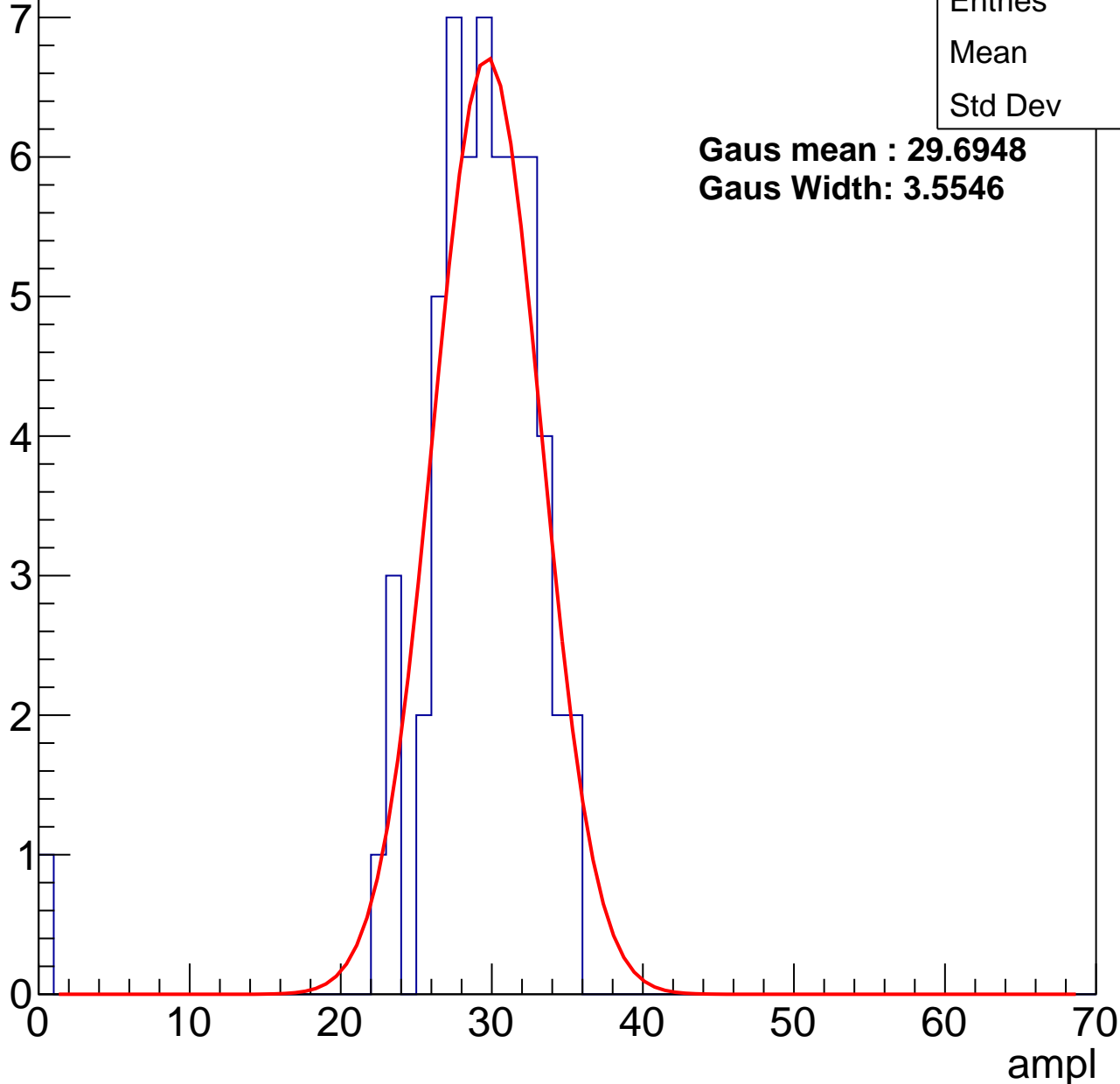
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	28.6
Std Dev	4.86

**Gaus mean : 29.6948**

**Gaus Width: 3.5546**



# B1L000S, U23-ch92, adc1

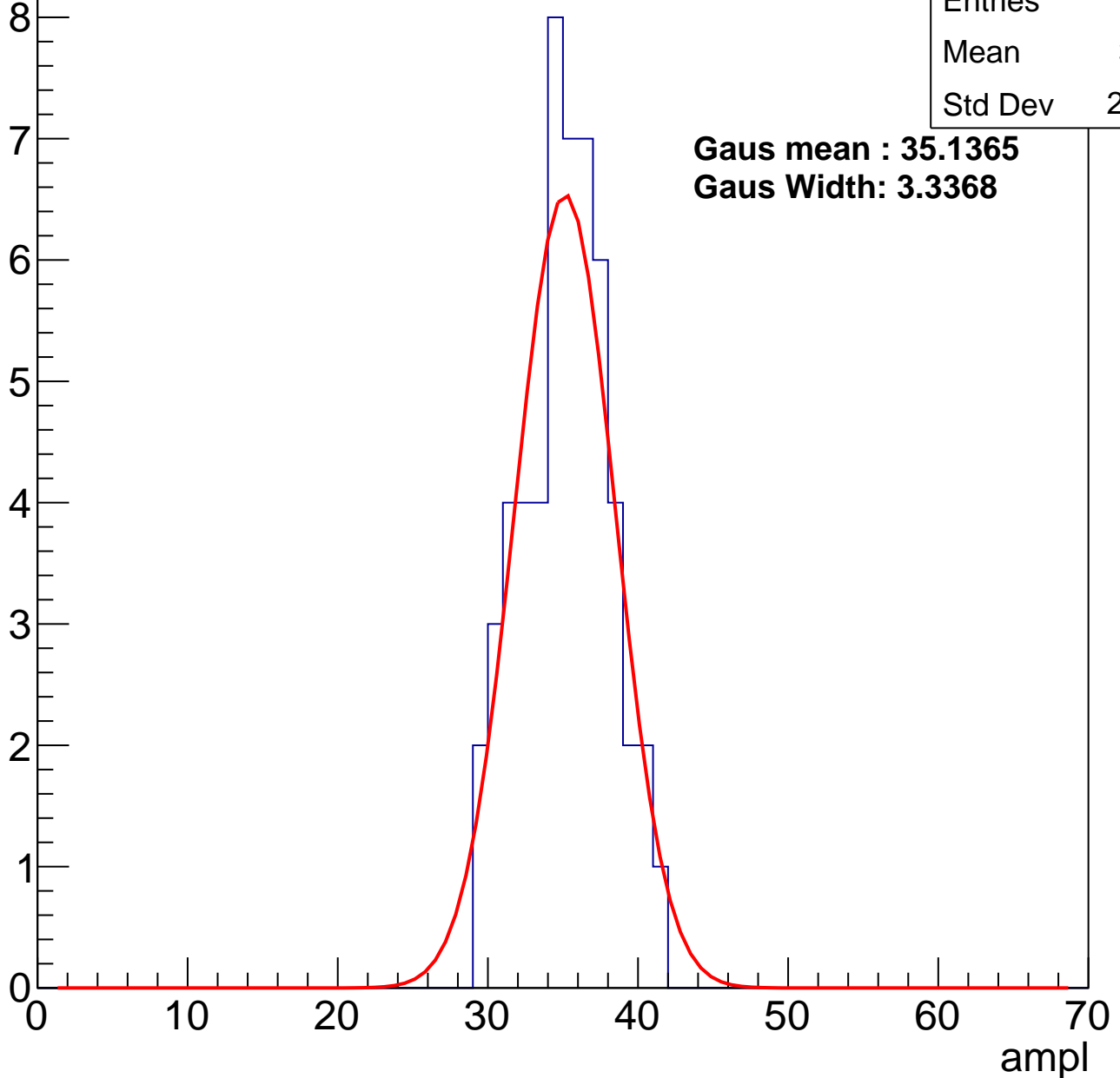
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	34.7
Std Dev	2.891

**Gaus mean : 35.1365**

**Gaus Width: 3.3368**



# B1L000S, U23-ch92, adc2

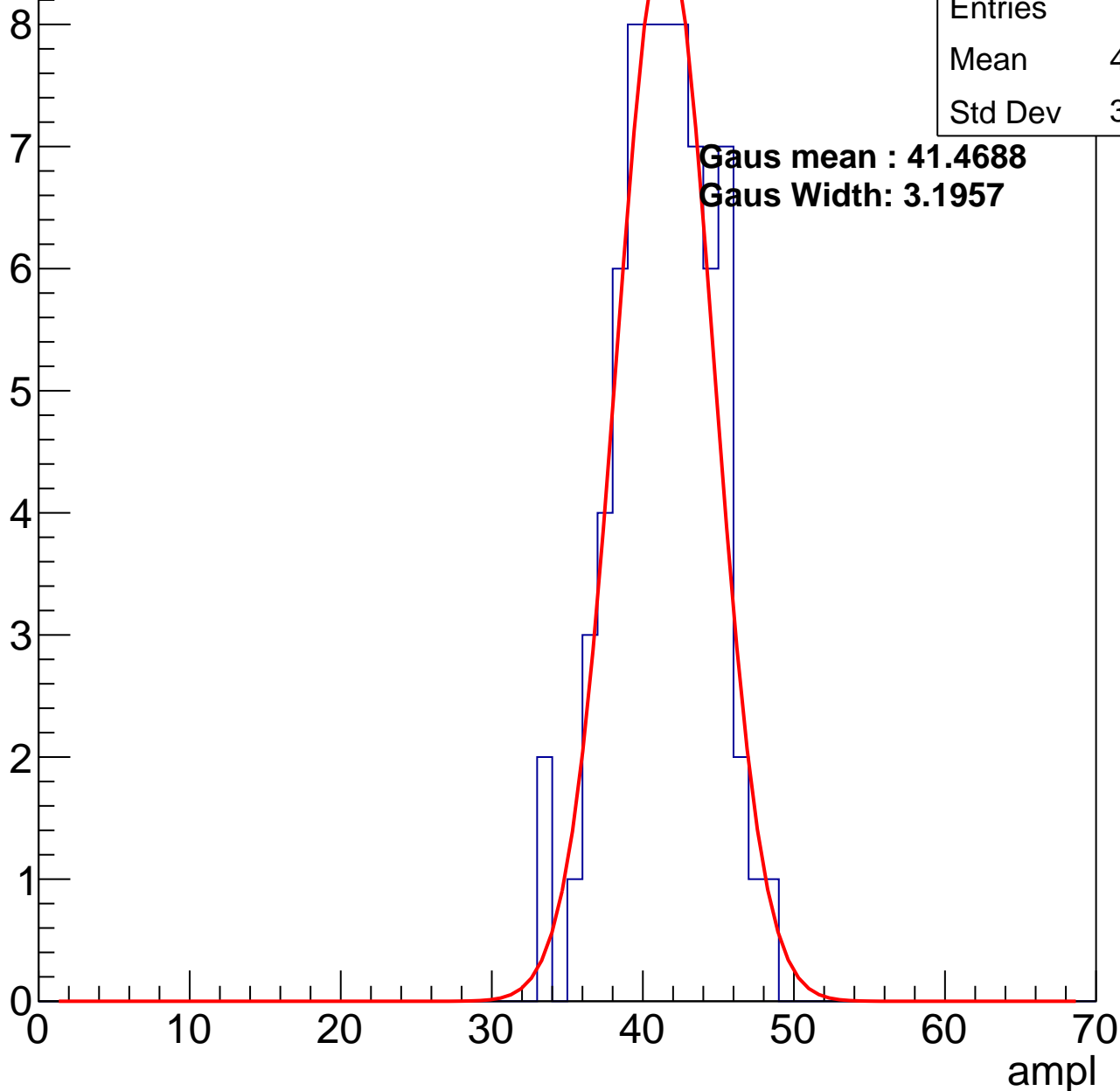
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	40.94
Std Dev	3.192

**Gaus mean : 41.4688**

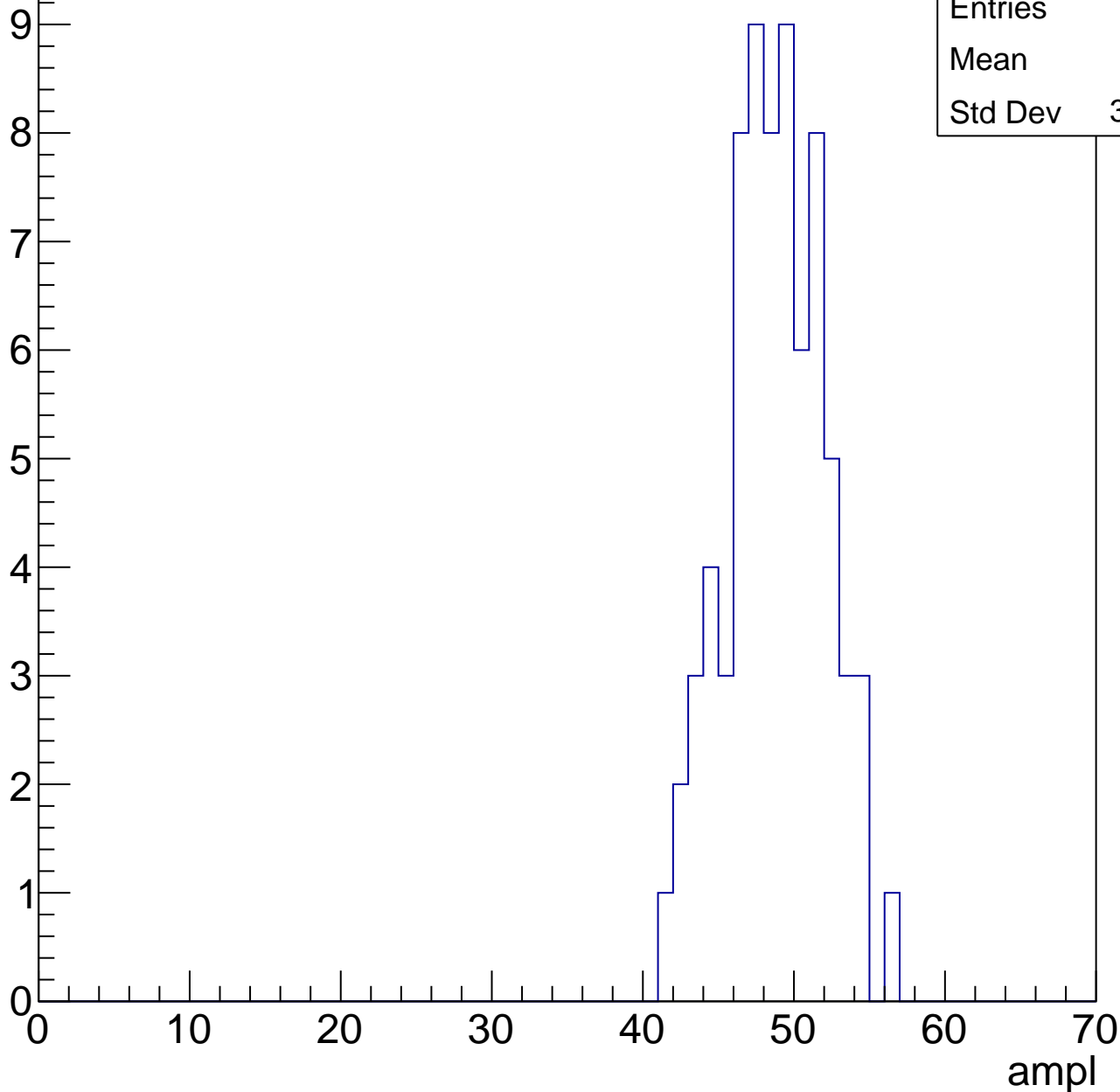
**Gaus Width: 3.1957**



# B1L000S, U23-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

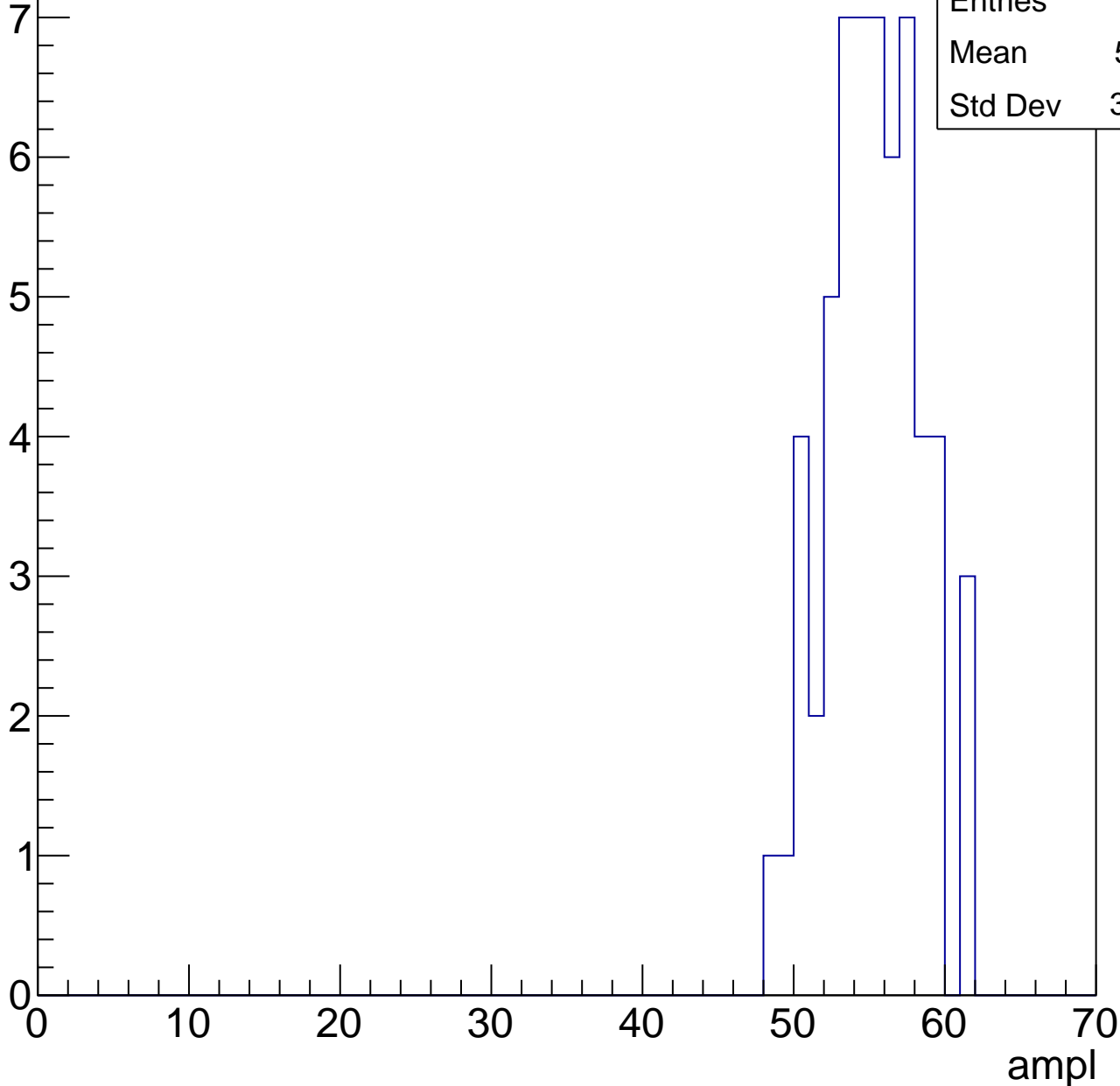


# B1L000S, U23-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	54.81
Std Dev	3.043

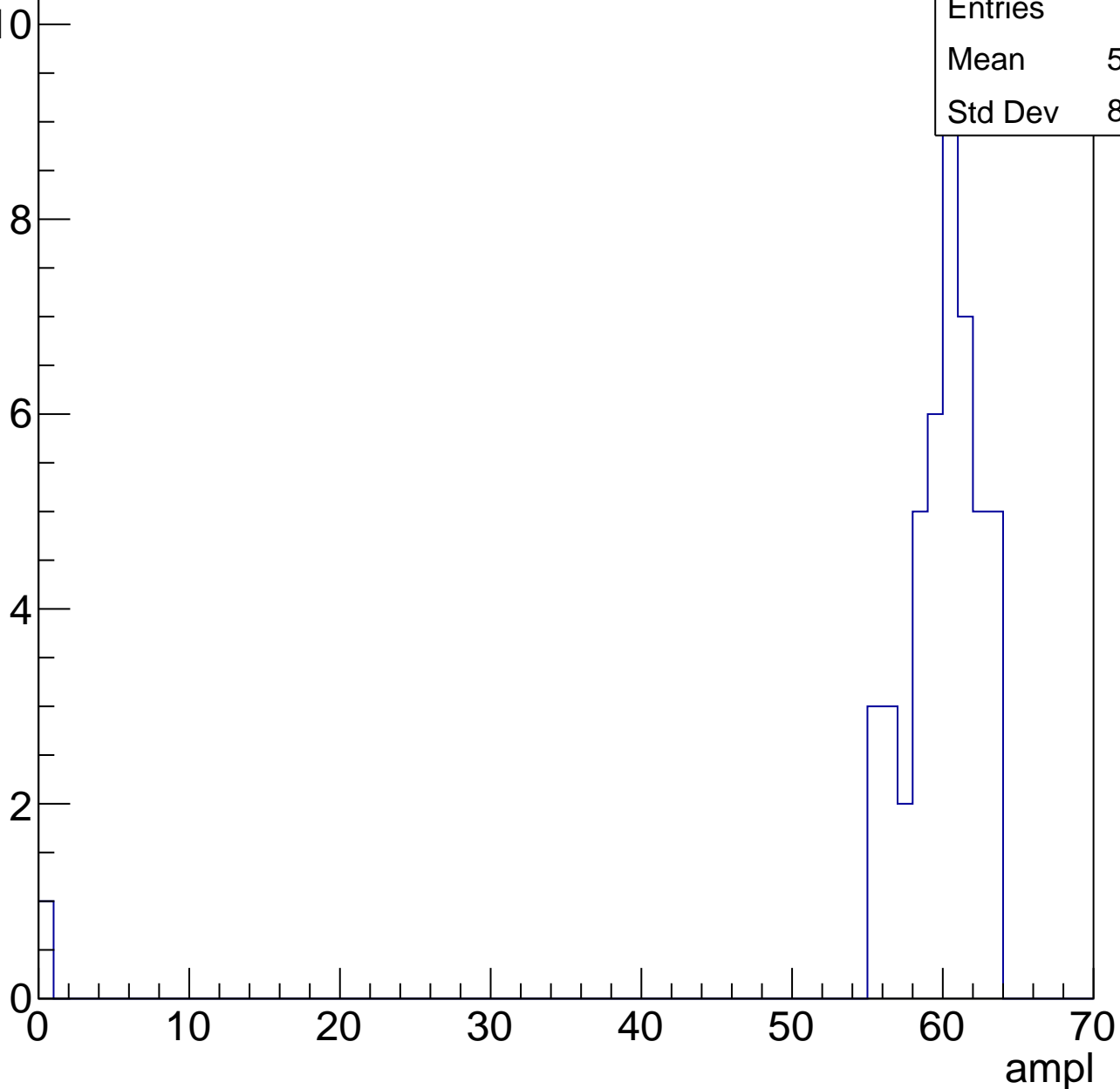


# B1L000S, U23-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	58.36
Std Dev	8.888

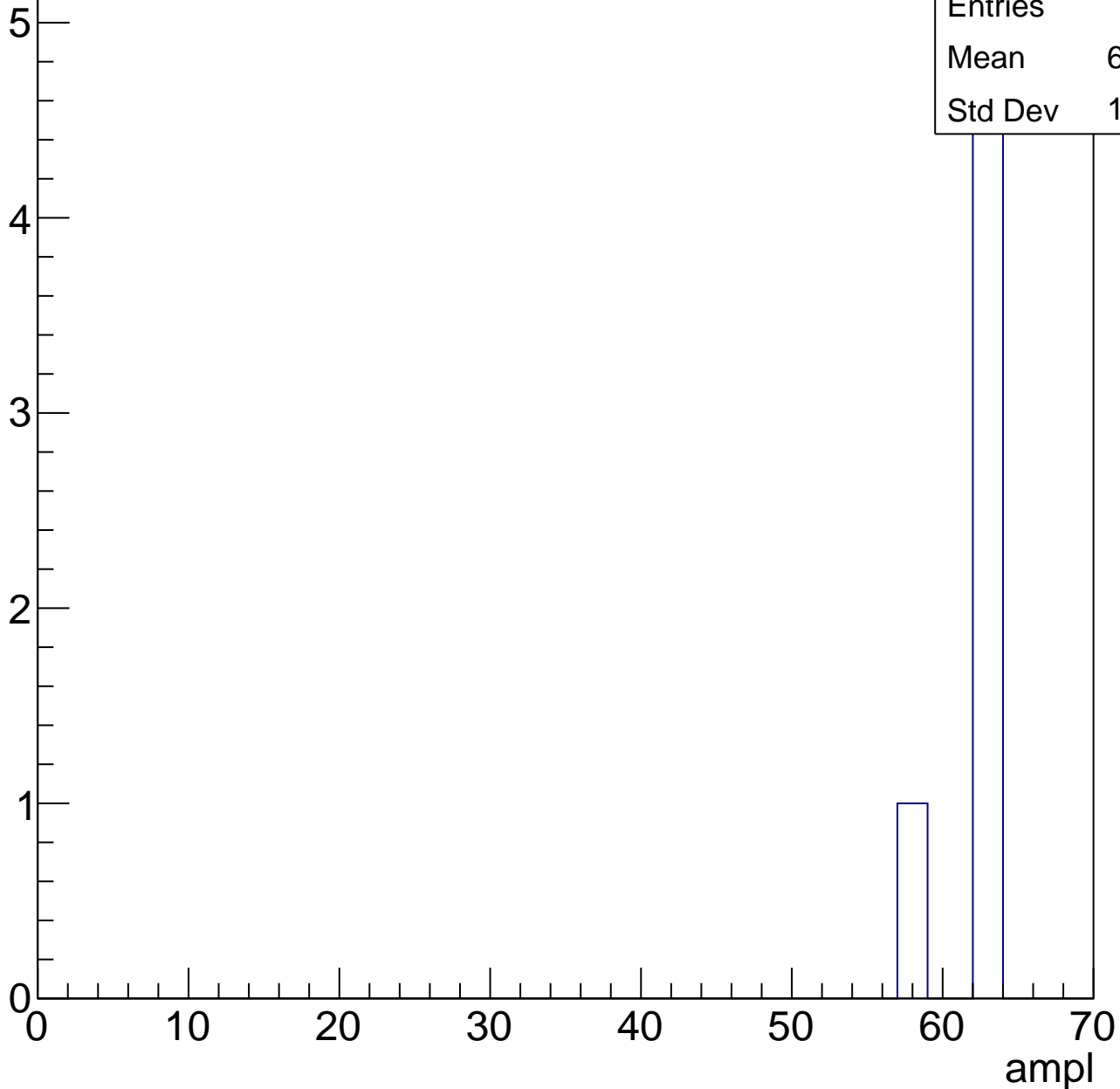


# B1L000S, U23-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.67
Std Dev	1.929





# B1L000S, U23-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch93, adc0

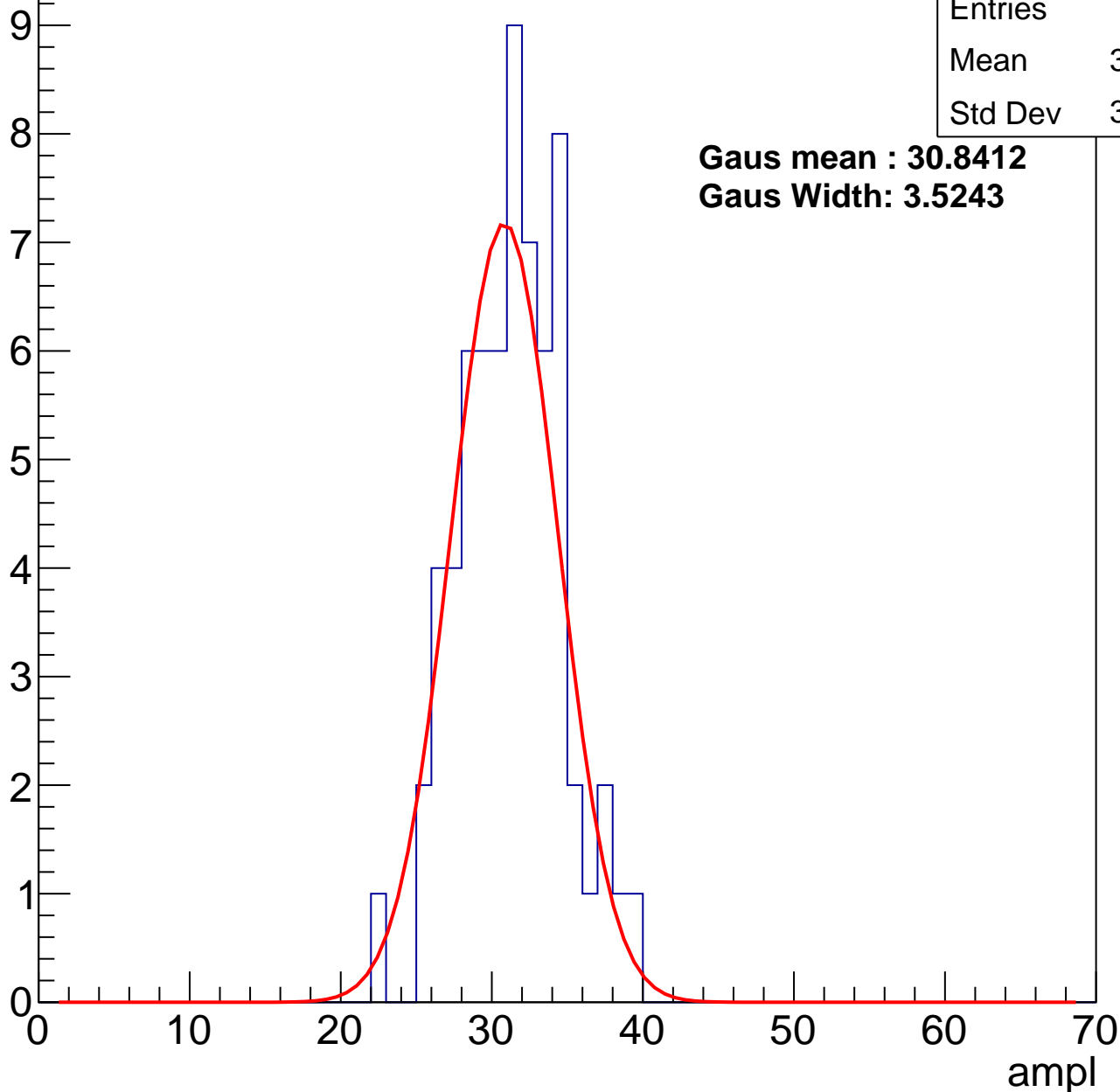
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	30.85
Std Dev	3.359

**Gaus mean : 30.8412**

**Gaus Width: 3.5243**



# B1L000S, U23-ch93, adc1

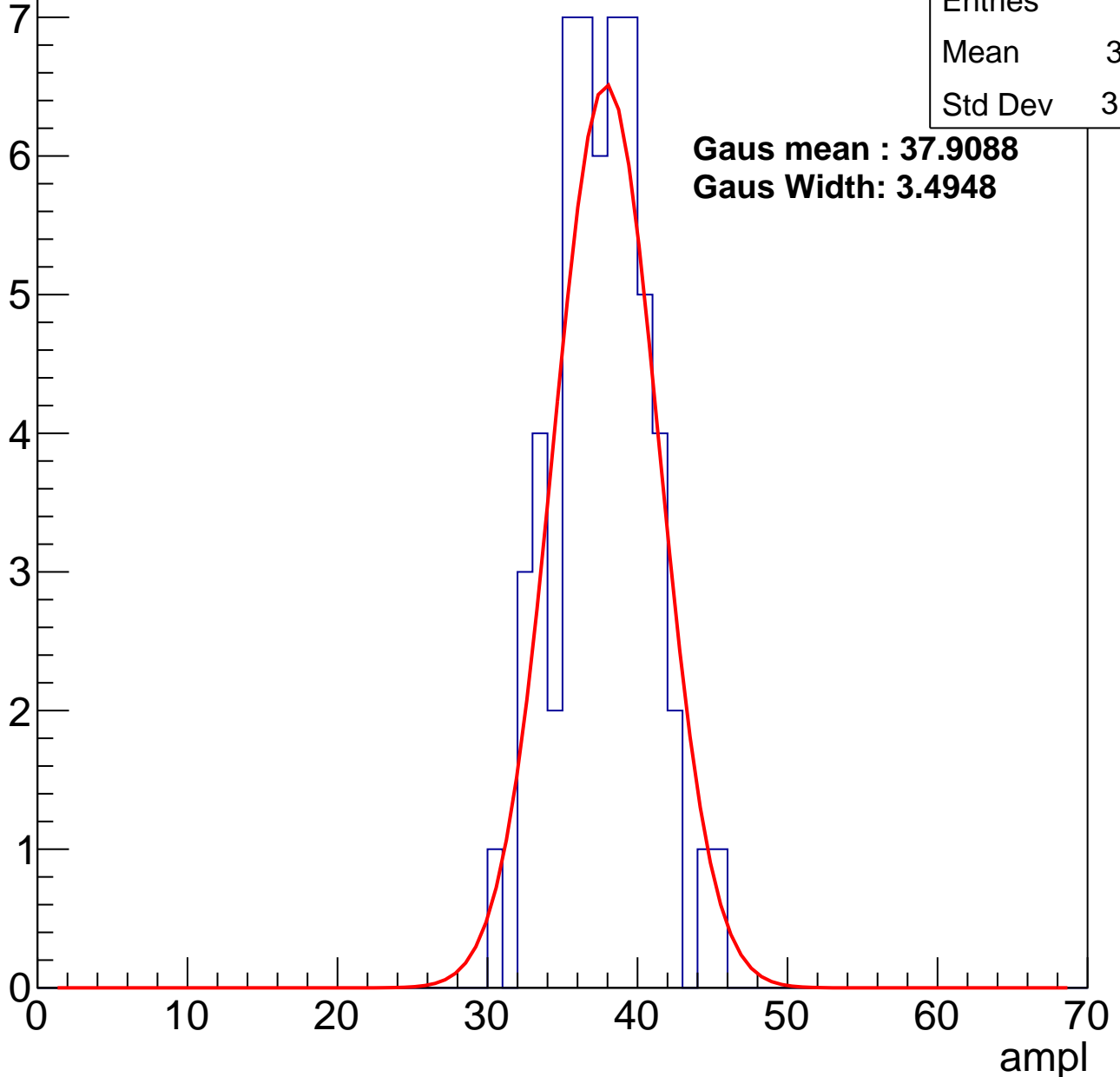
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	37.21
Std Dev	3.093

**Gaus mean : 37.9088**

**Gaus Width: 3.4948**



# B1L000S, U23-ch93, adc2

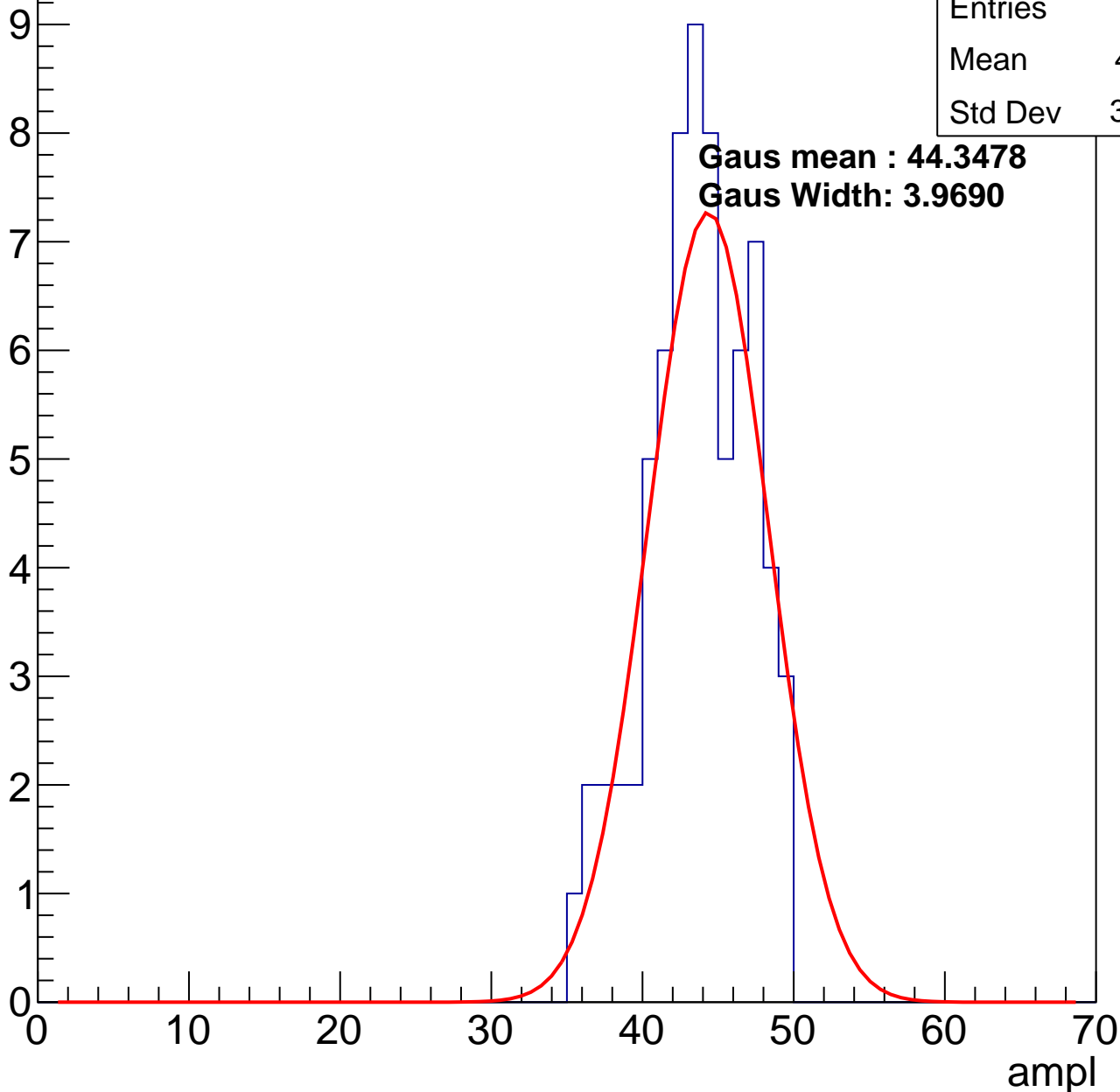
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	43.21
Std Dev	3.363

**Gaus mean : 44.3478**

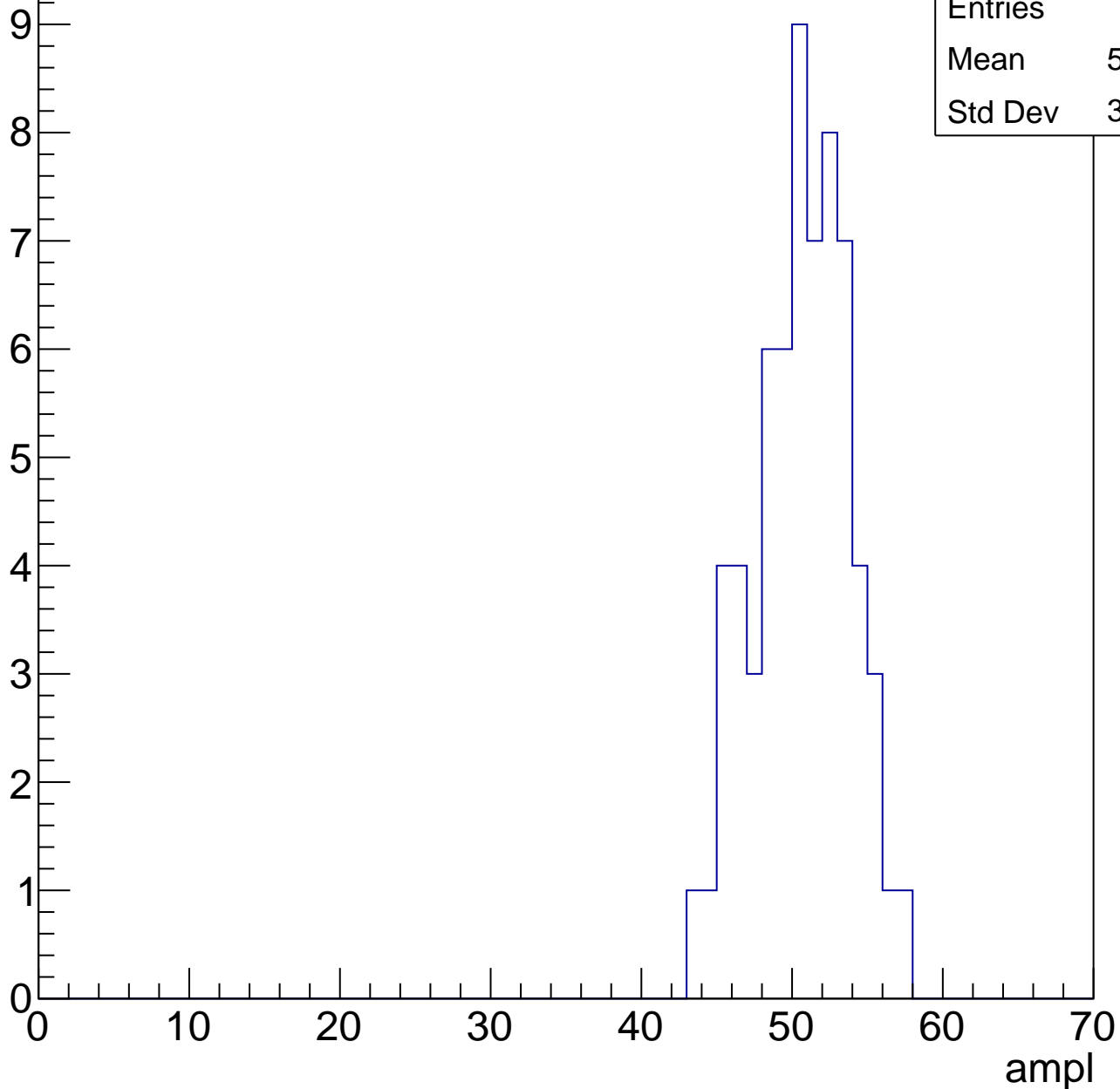
**Gaus Width: 3.9690**



# B1L000S, U23-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



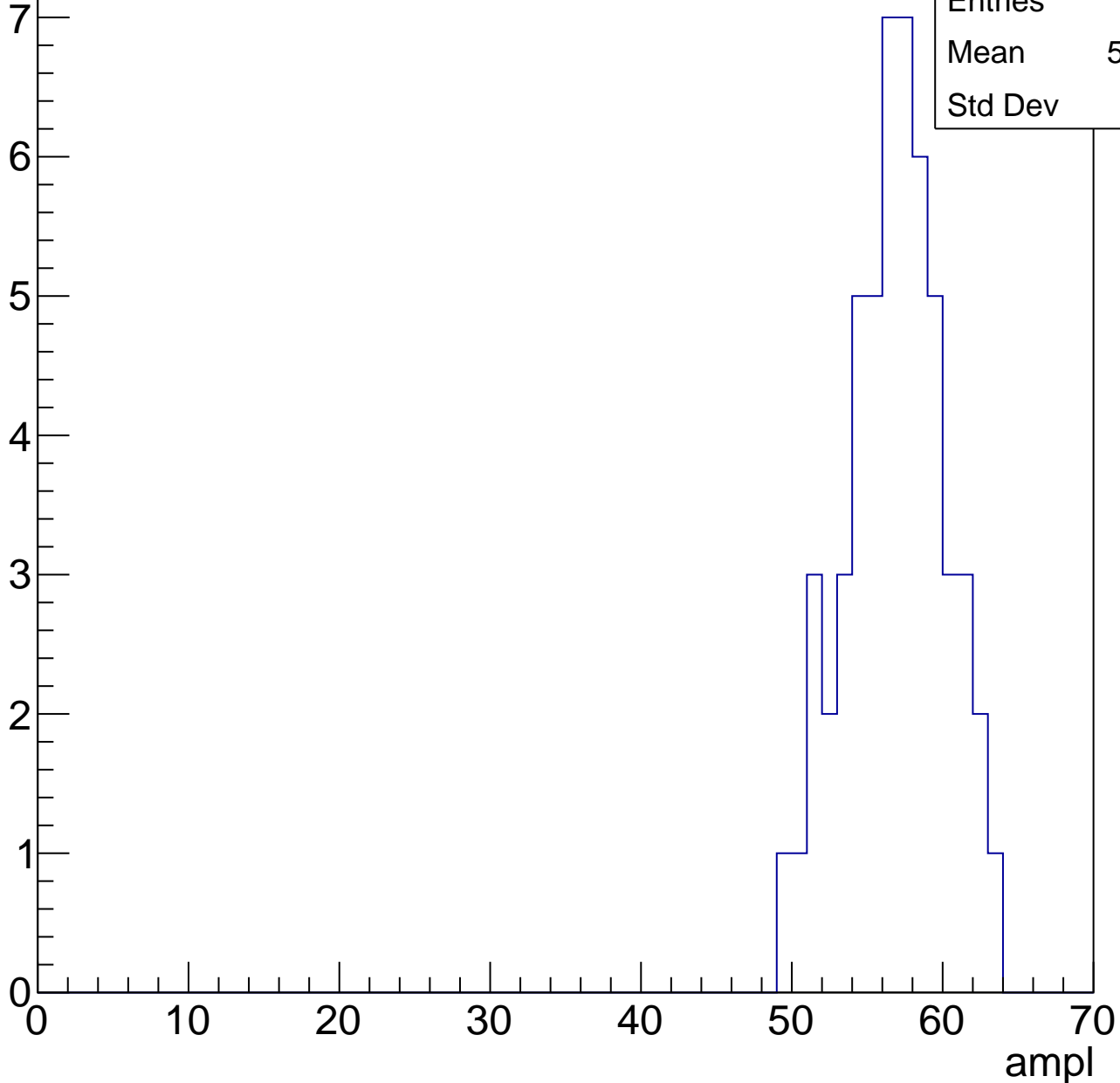
Entries	65
Mean	50.18
Std Dev	3.113

# B1L000S, U23-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	56.37
Std Dev	3.21

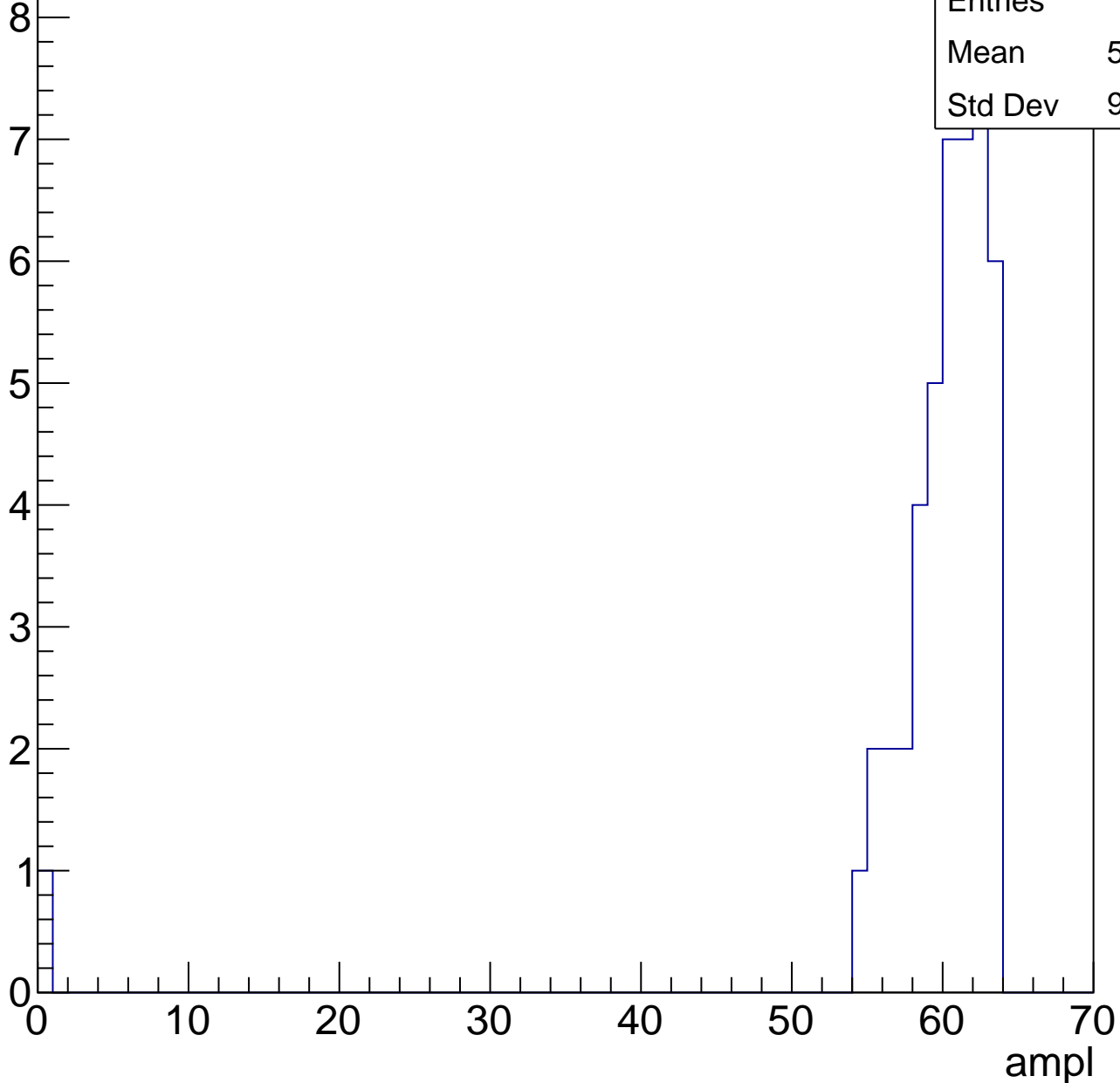


# B1L000S, U23-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	58.62
Std Dev	9.146



# B1L000S, U23-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch94, adc0

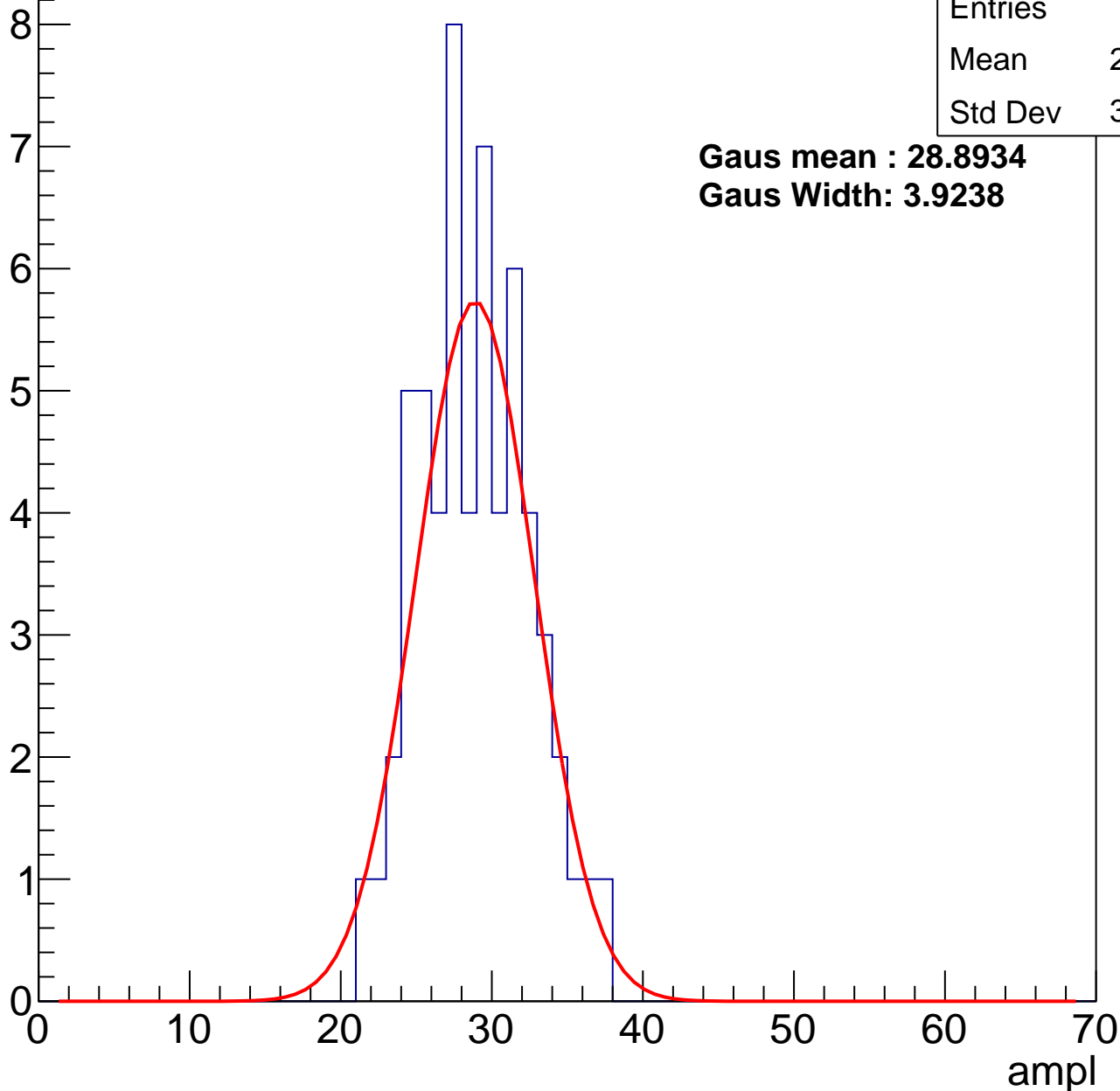
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.44
Std Dev	3.576

**Gaus mean : 28.8934**

**Gaus Width: 3.9238**



# B1L000S, U23-ch94, adc1

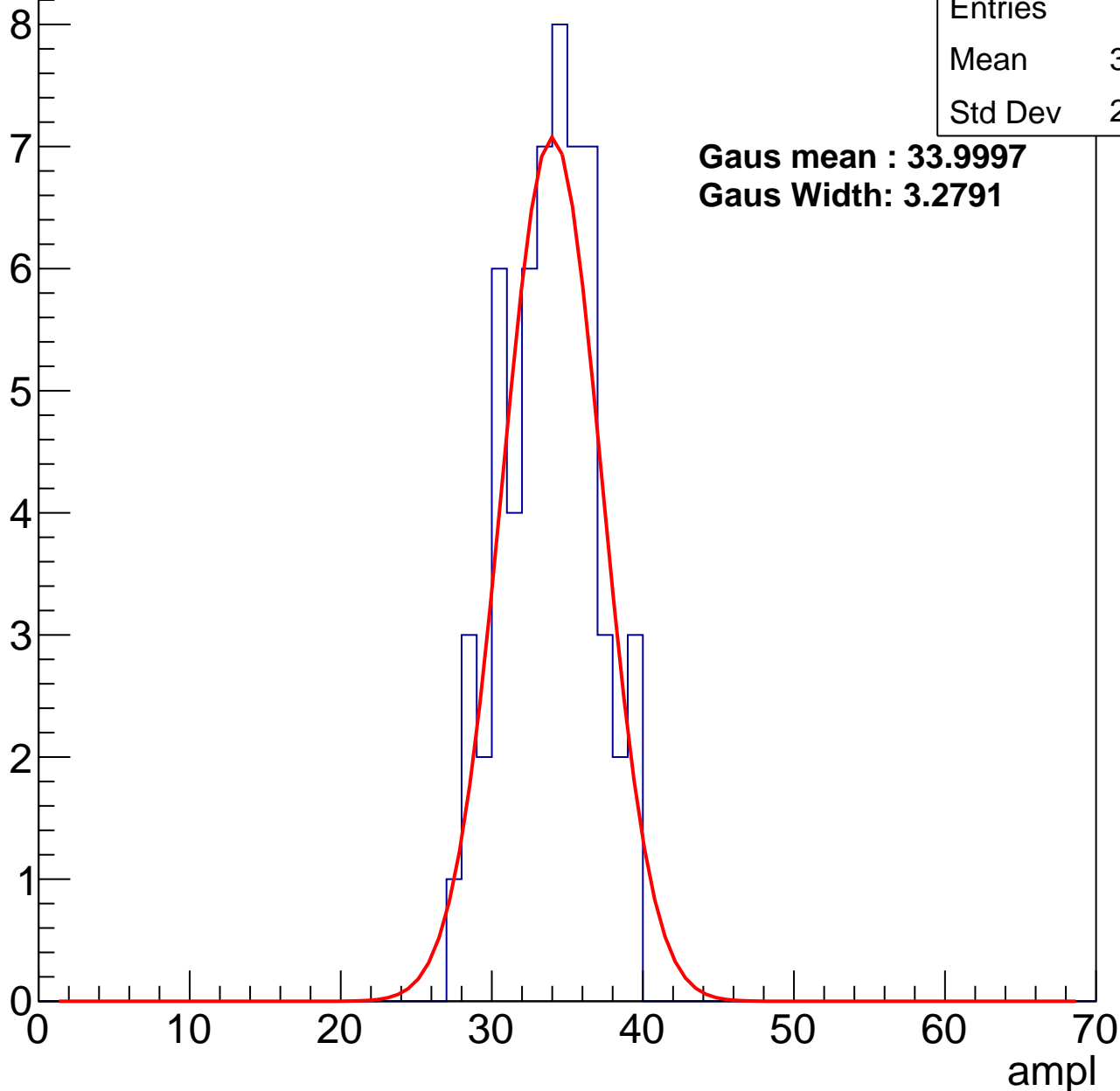
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	33.37
Std Dev	2.957

**Gaus mean : 33.9997**

**Gaus Width: 3.2791**



# B1L000S, U23-ch94, adc2

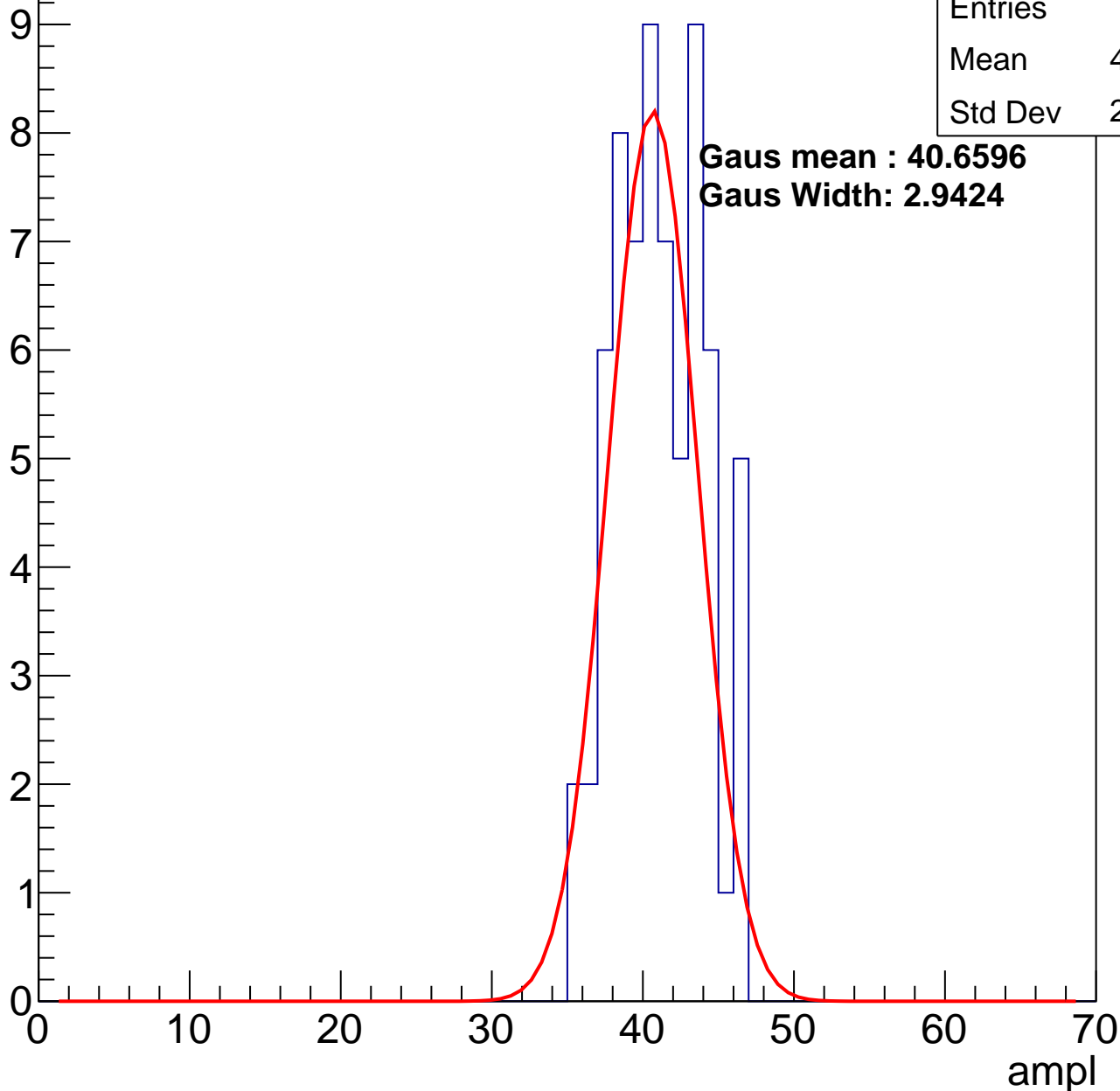
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.66
Std Dev	2.878

**Gaus mean : 40.6596**

**Gaus Width: 2.9424**

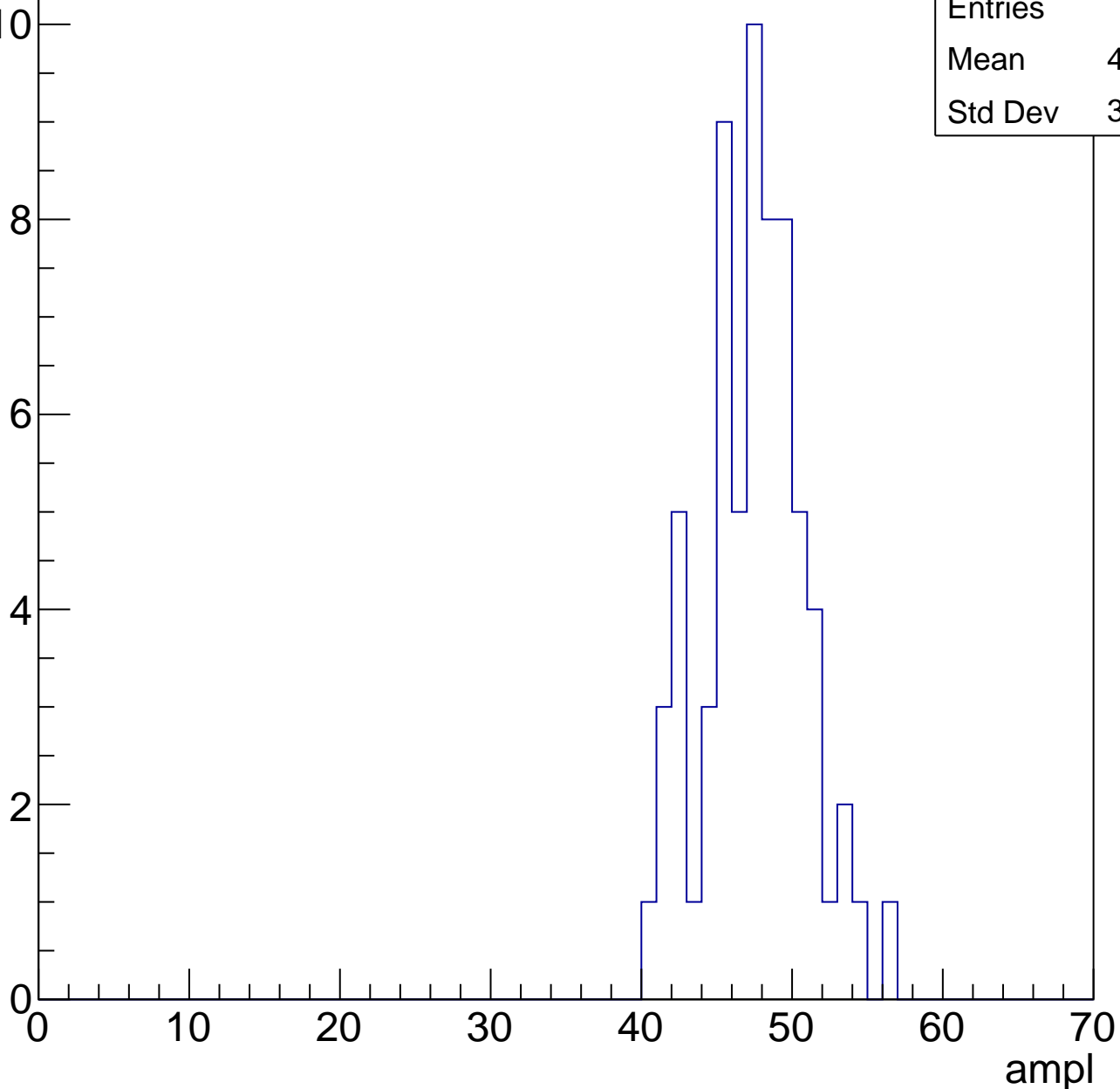


# B1L000S, U23-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	47.03
Std Dev	3.328

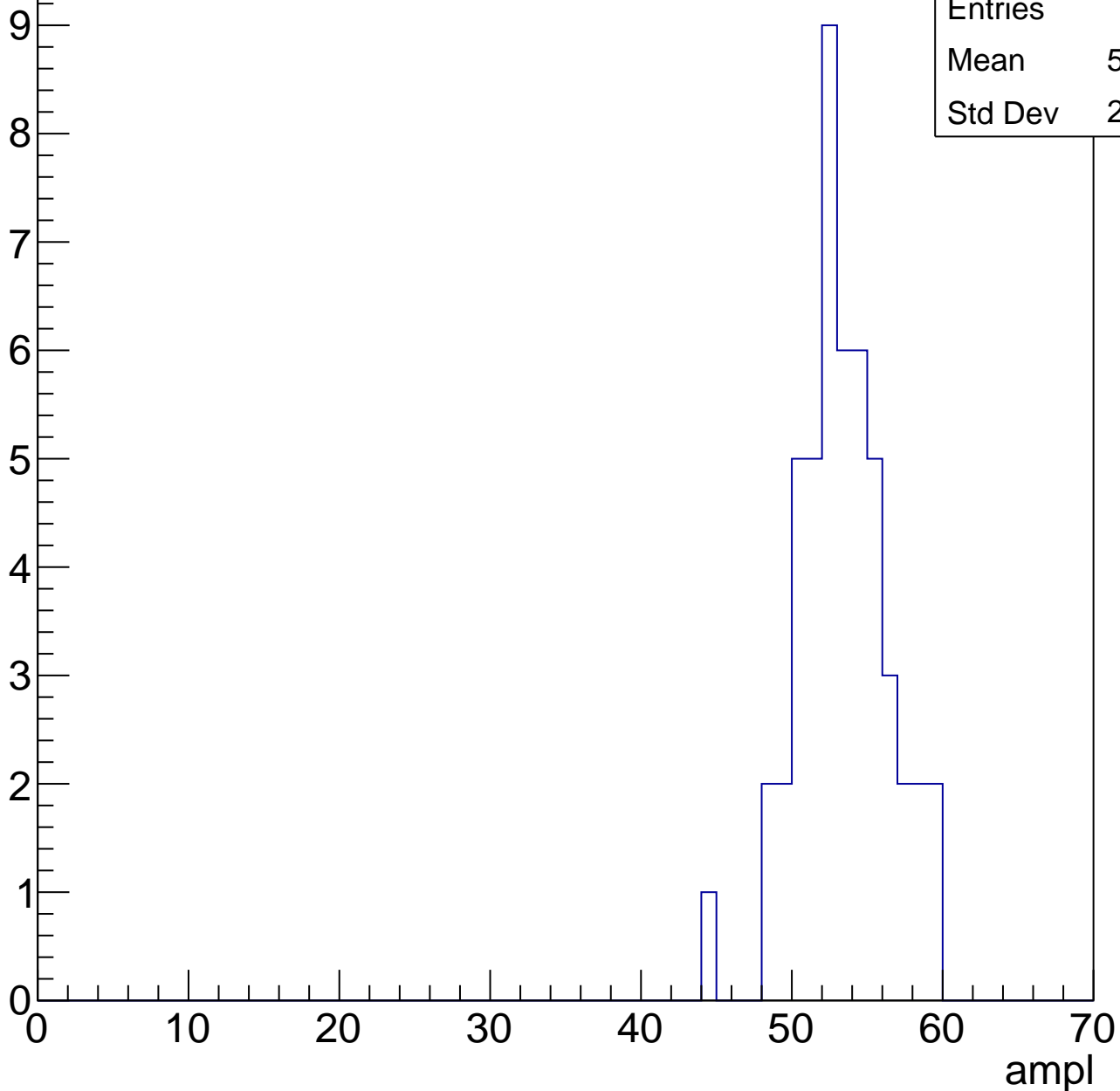


# B1L000S, U23-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	52.88
Std Dev	2.978

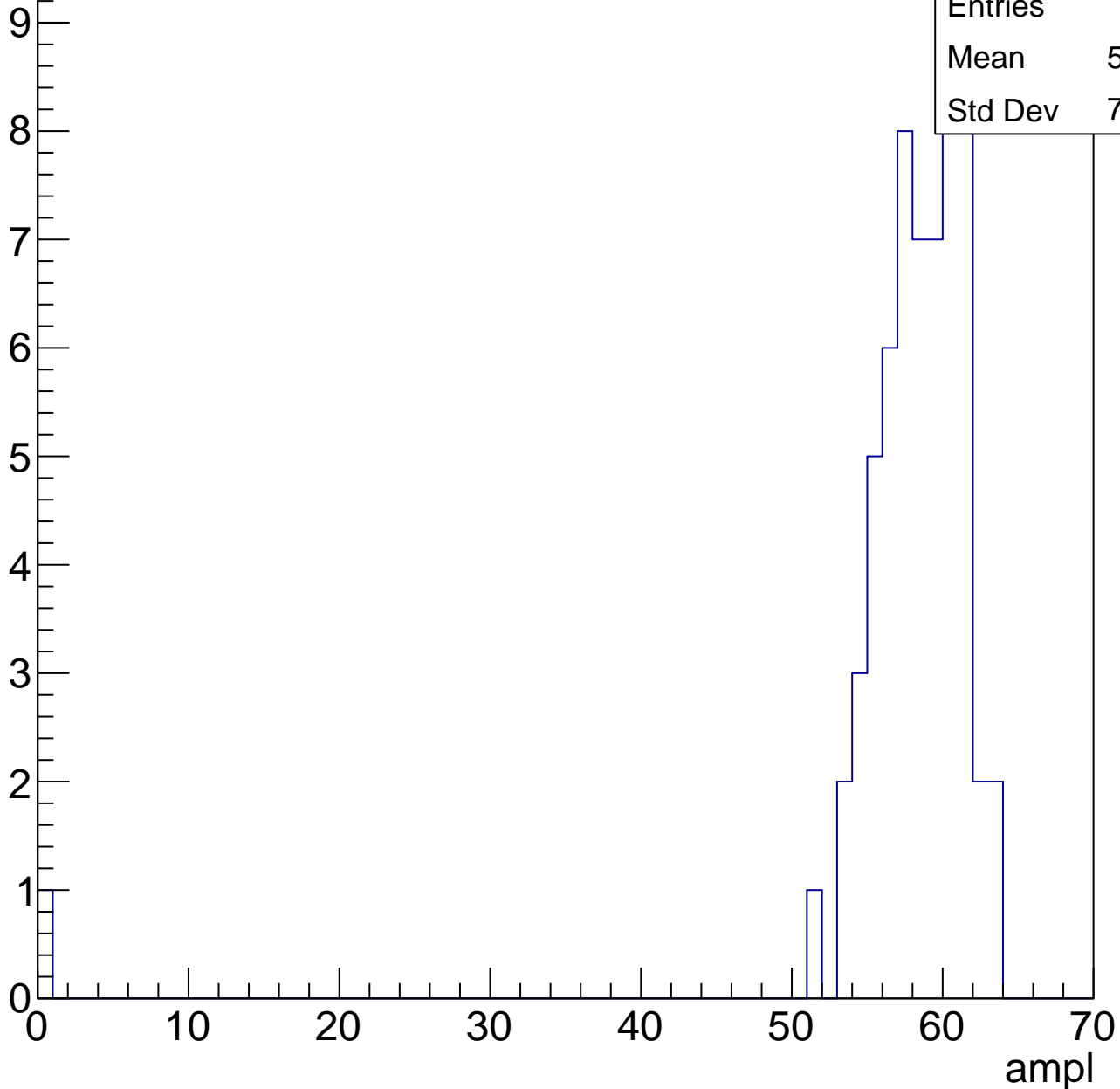


# B1L000S, U23-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.16
Std Dev	7.778

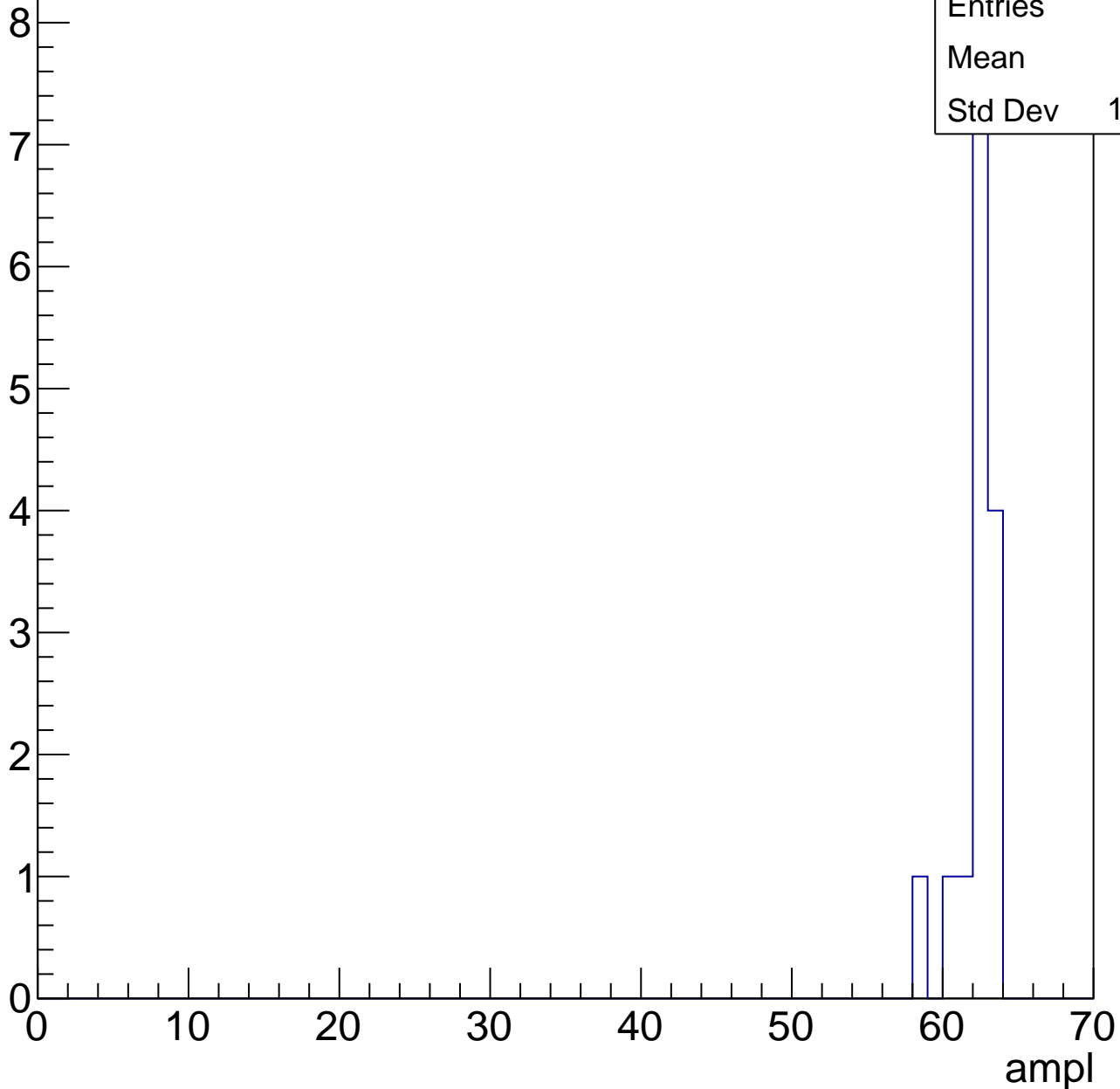


# B1L000S, U23-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.8
Std Dev	1.275

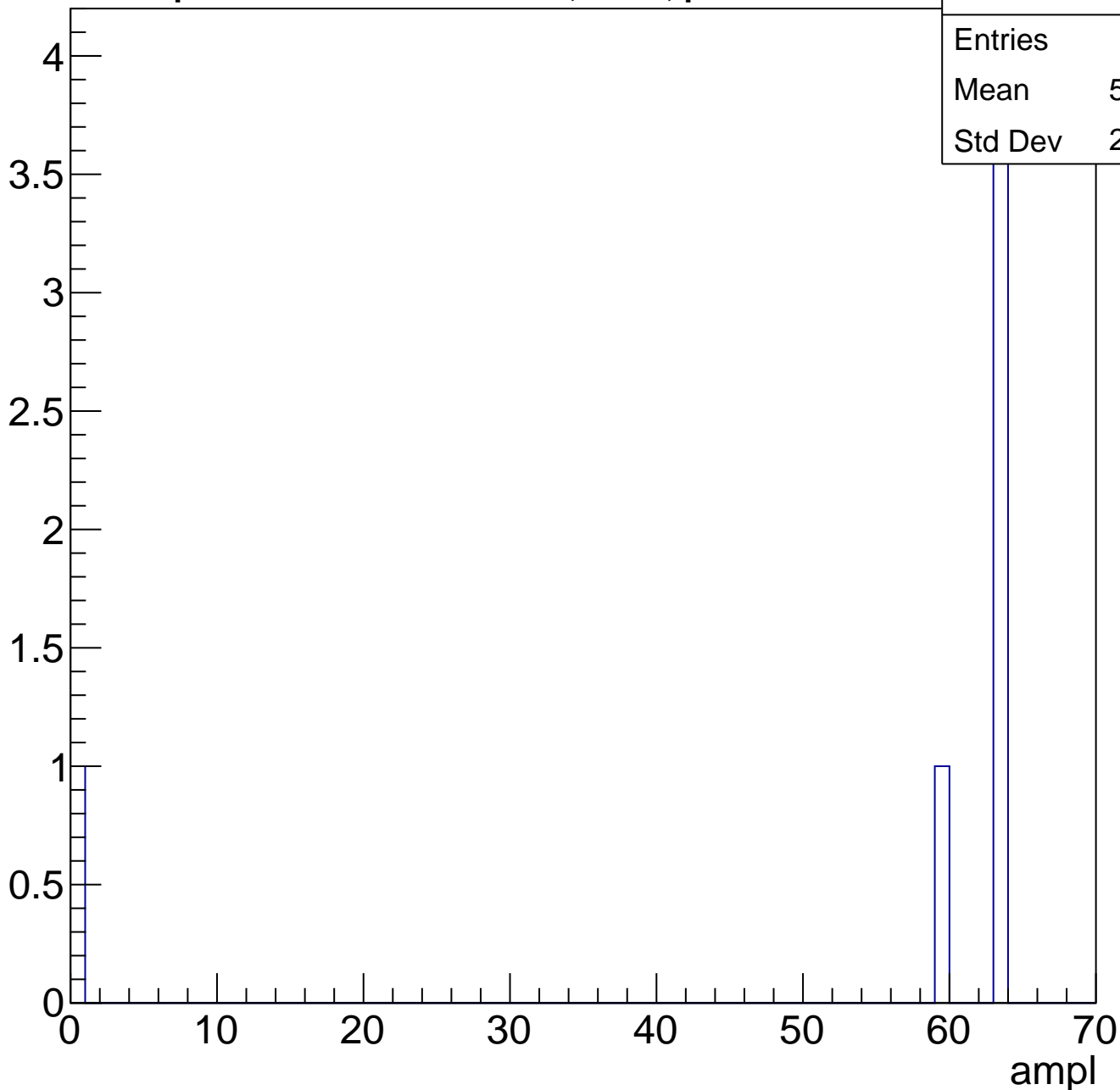




# B1L000S, U23-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch95, adc0

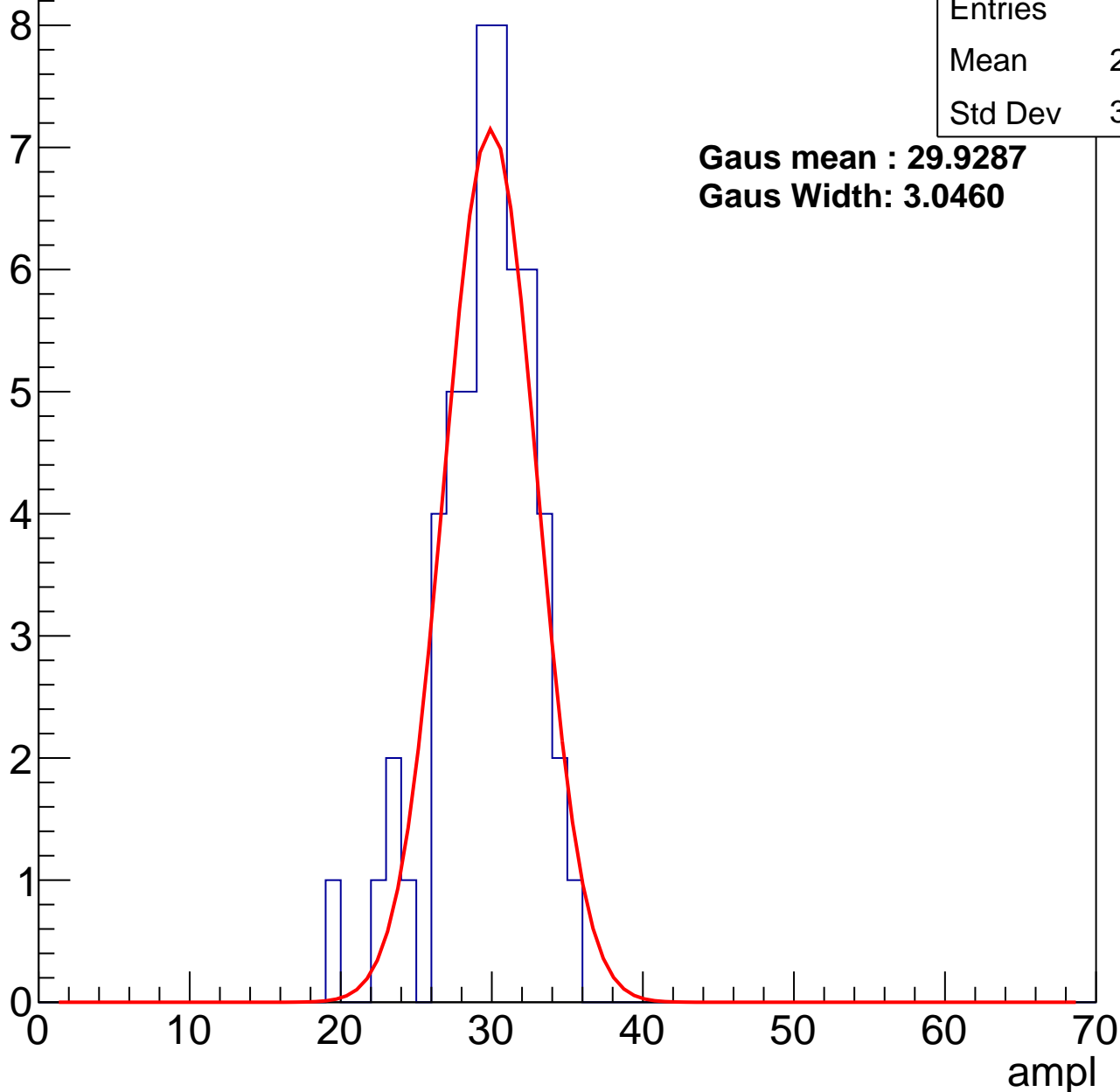
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	29.17
Std Dev	3.167

**Gaus mean : 29.9287**

**Gaus Width: 3.0460**



# B1L000S, U23-ch95, adc1

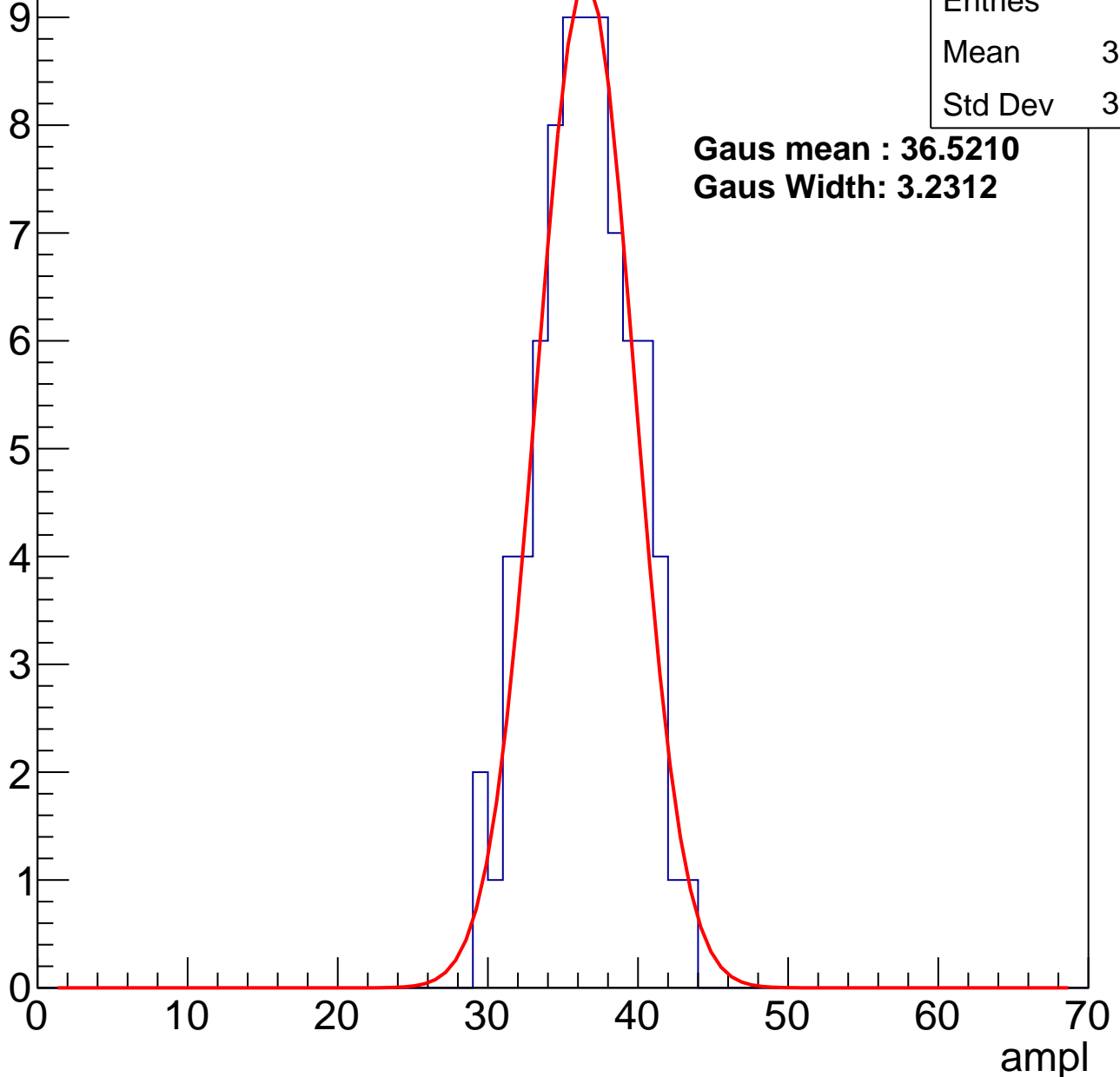
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	35.99
Std Dev	3.152

**Gaus mean : 36.5210**

**Gaus Width: 3.2312**



# B1L000S, U23-ch95, adc2

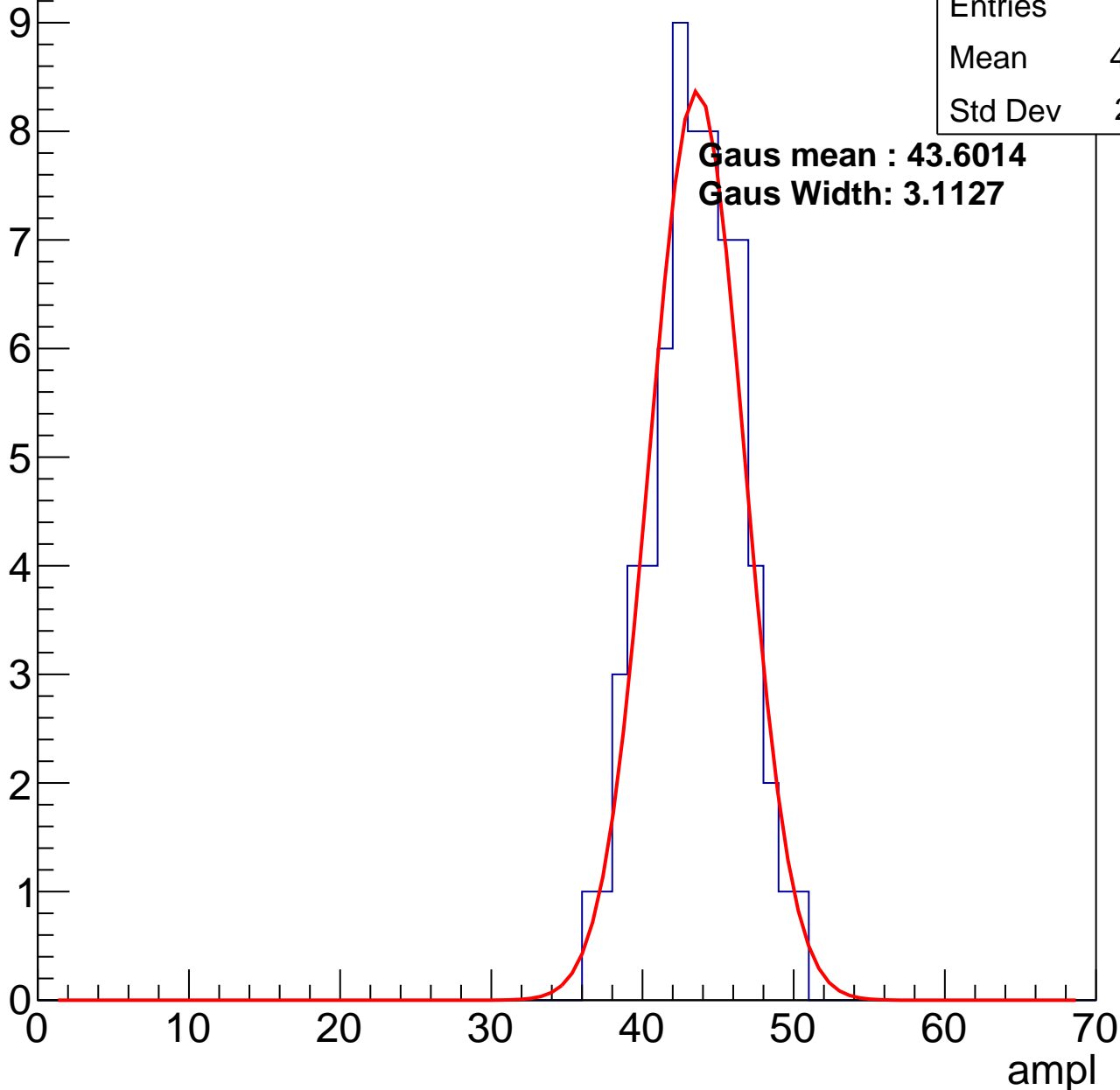
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.08
Std Dev	2.991

**Gaus mean : 43.6014**

**Gaus Width: 3.1127**

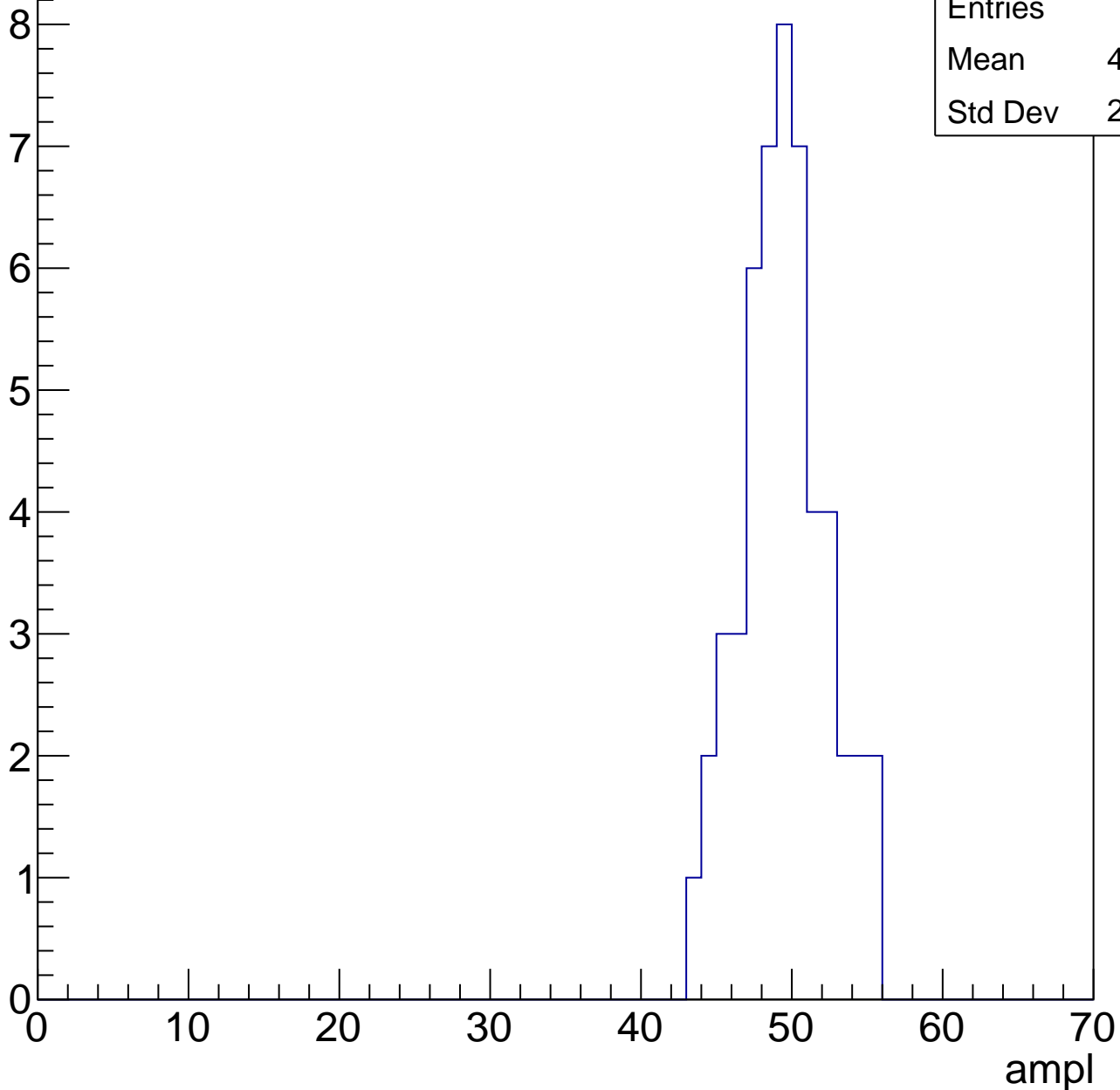


# B1L000S, U23-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

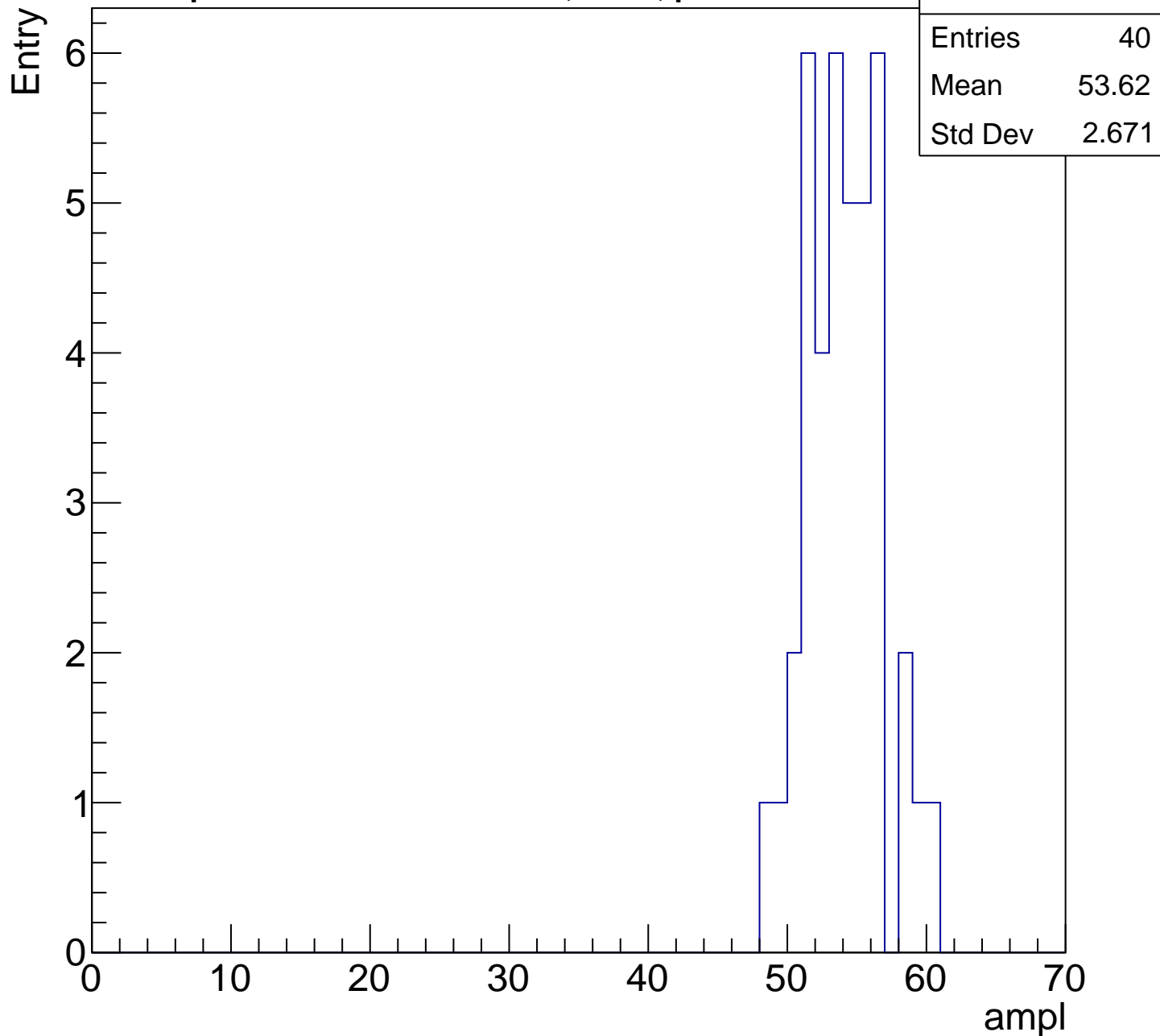
Entry

Entries	51
Mean	49.02
Std Dev	2.818



# B1L000S, U23-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

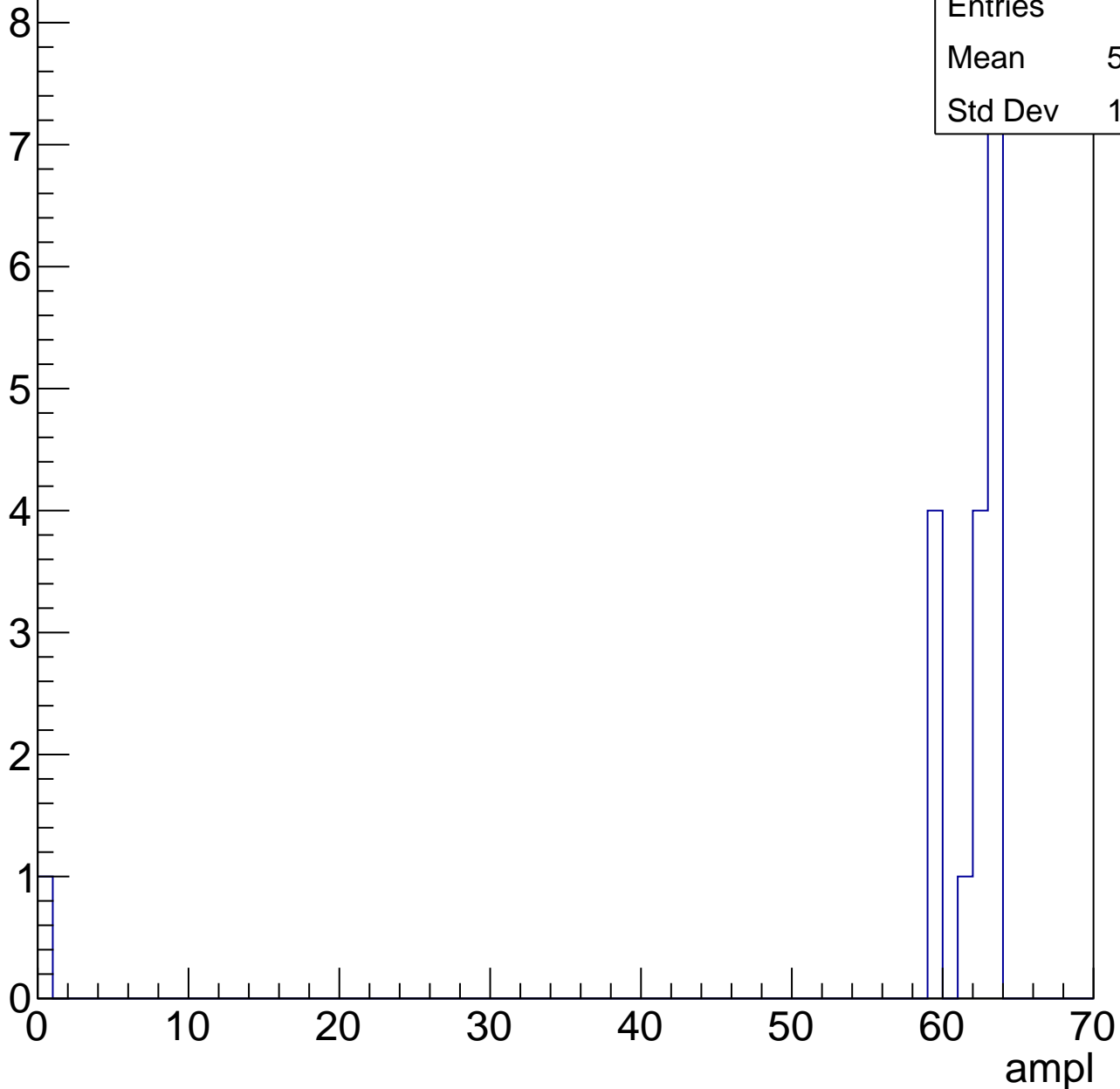
Entries	63
Mean	58.37
Std Dev	2.768

# B1L000S, U23-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.28
Std Dev	14.22





# B1L000S, U23-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch96, adc0

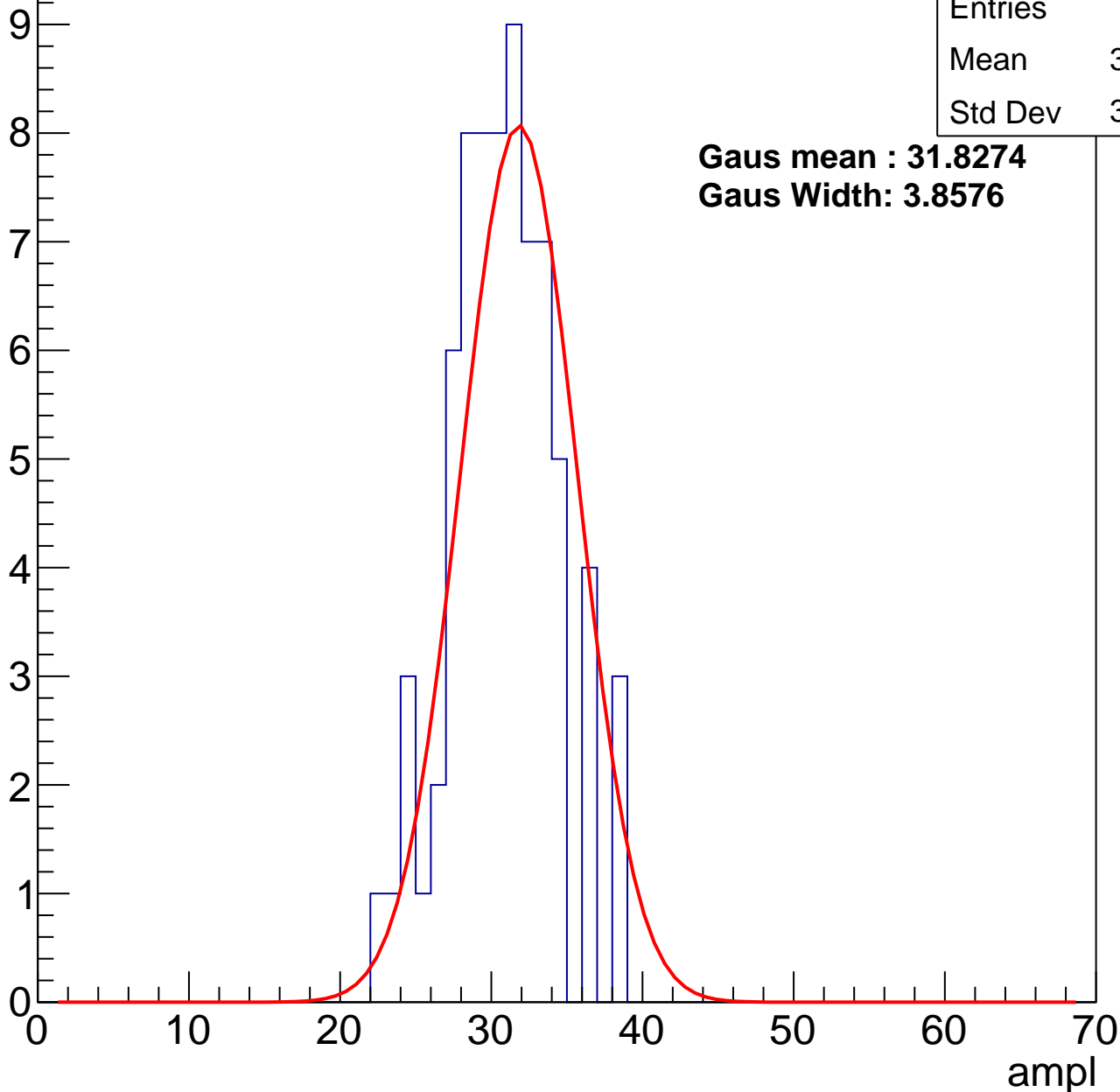
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	30.33
Std Dev	3.472

**Gaus mean : 31.8274**

**Gaus Width: 3.8576**



# B1L000S, U23-ch96, adc1

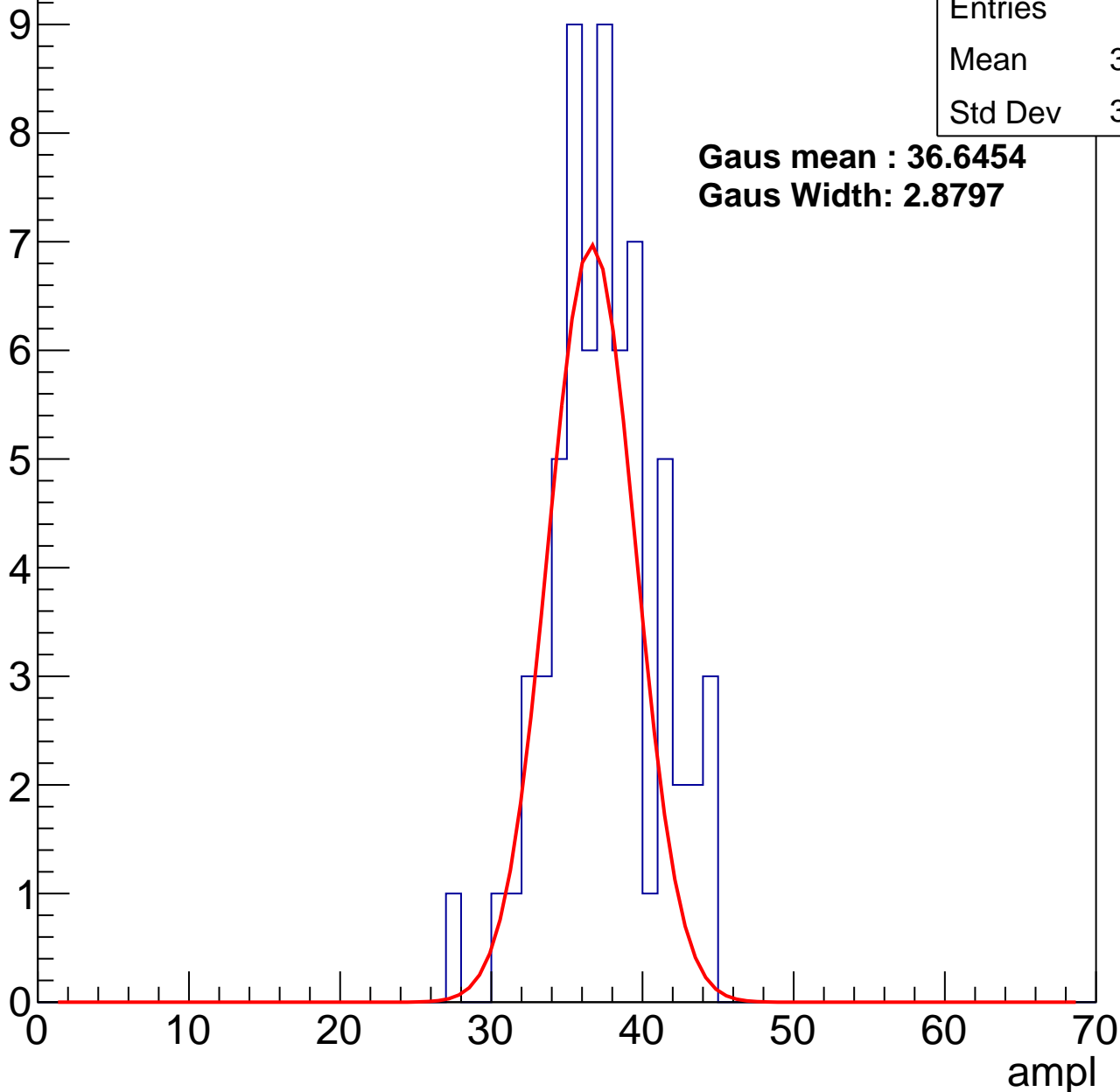
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.95
Std Dev	3.506

**Gaus mean : 36.6454**

**Gaus Width: 2.8797**



# B1L000S, U23-ch96, adc2

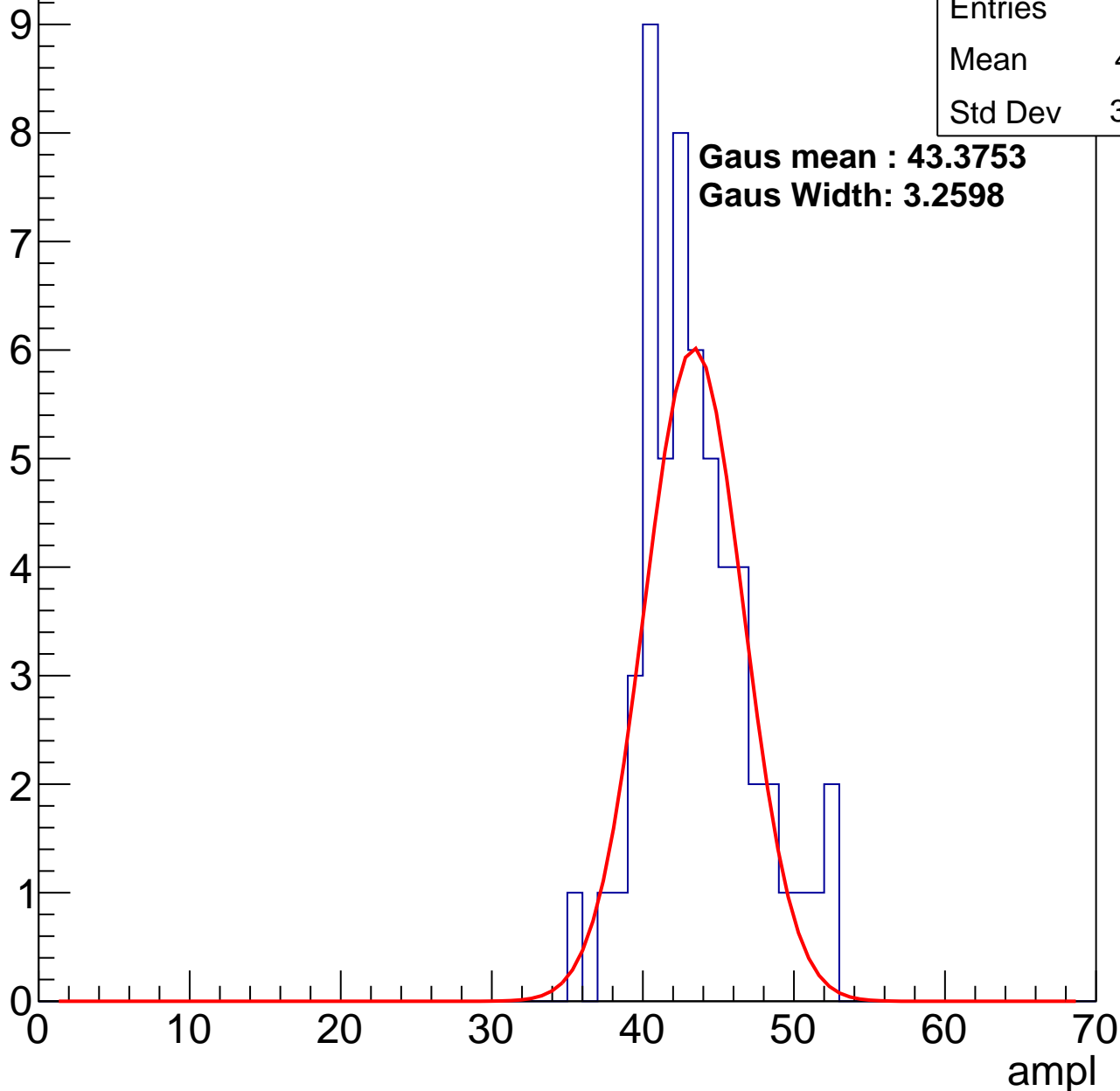
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	43.11
Std Dev	3.614

**Gaus mean : 43.3753**

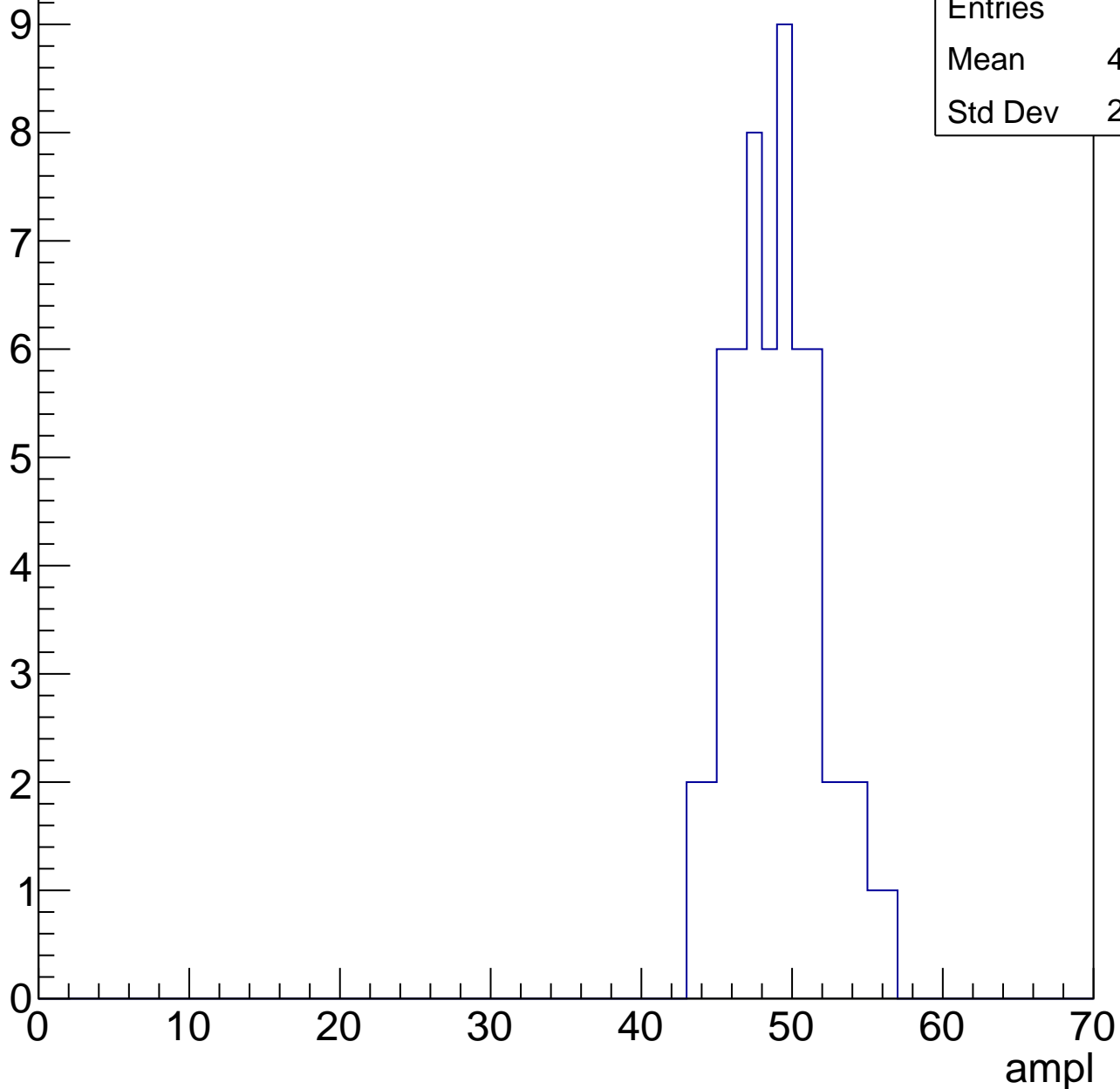
**Gaus Width: 3.2598**



# B1L000S, U23-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

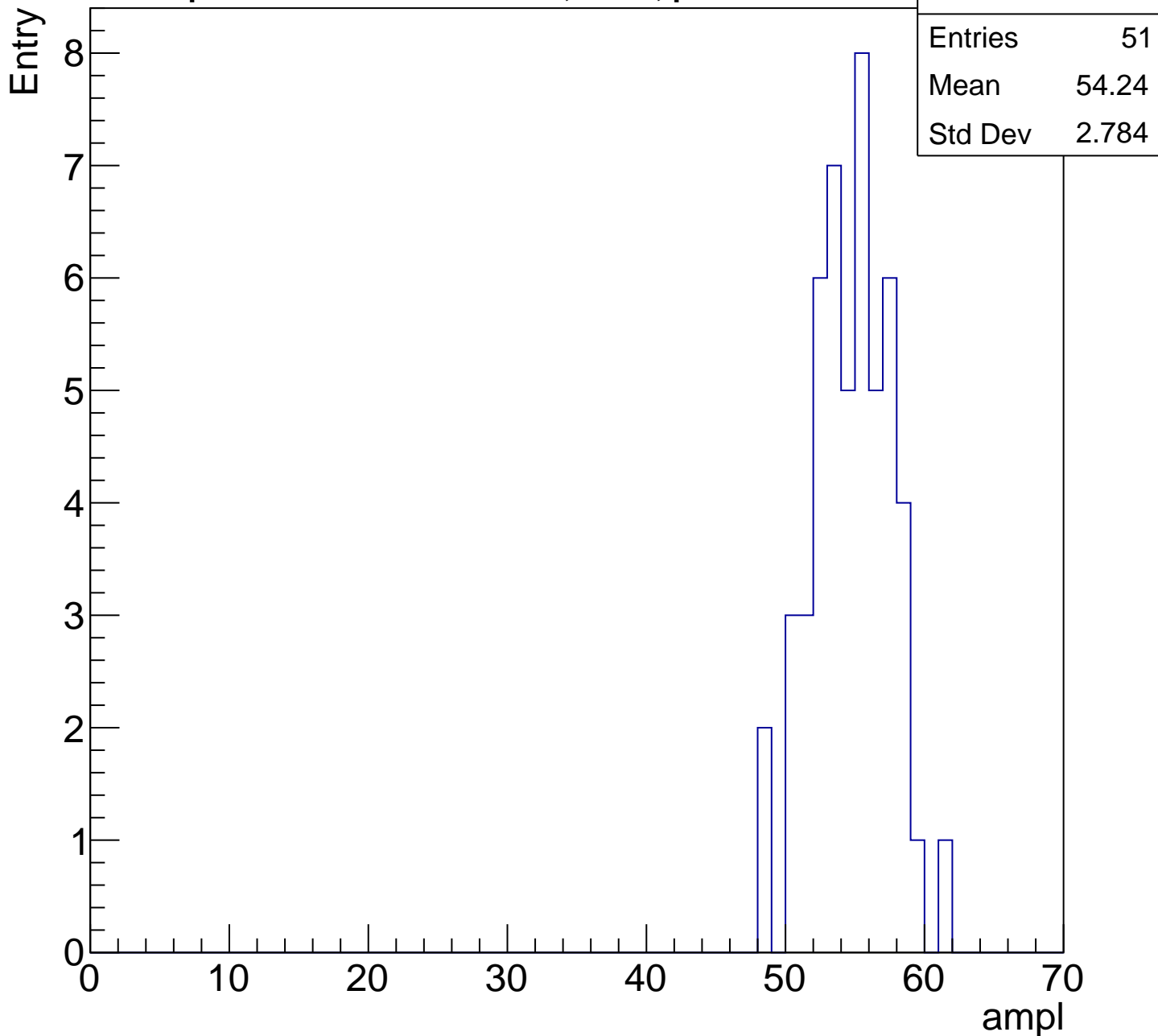
Entry



Entries	59
Mean	48.47
Std Dev	2.936

# B1L000S, U23-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U23-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 57

Mean 58.3

Std Dev 8.199

8

6

4

2

0

0

10

20

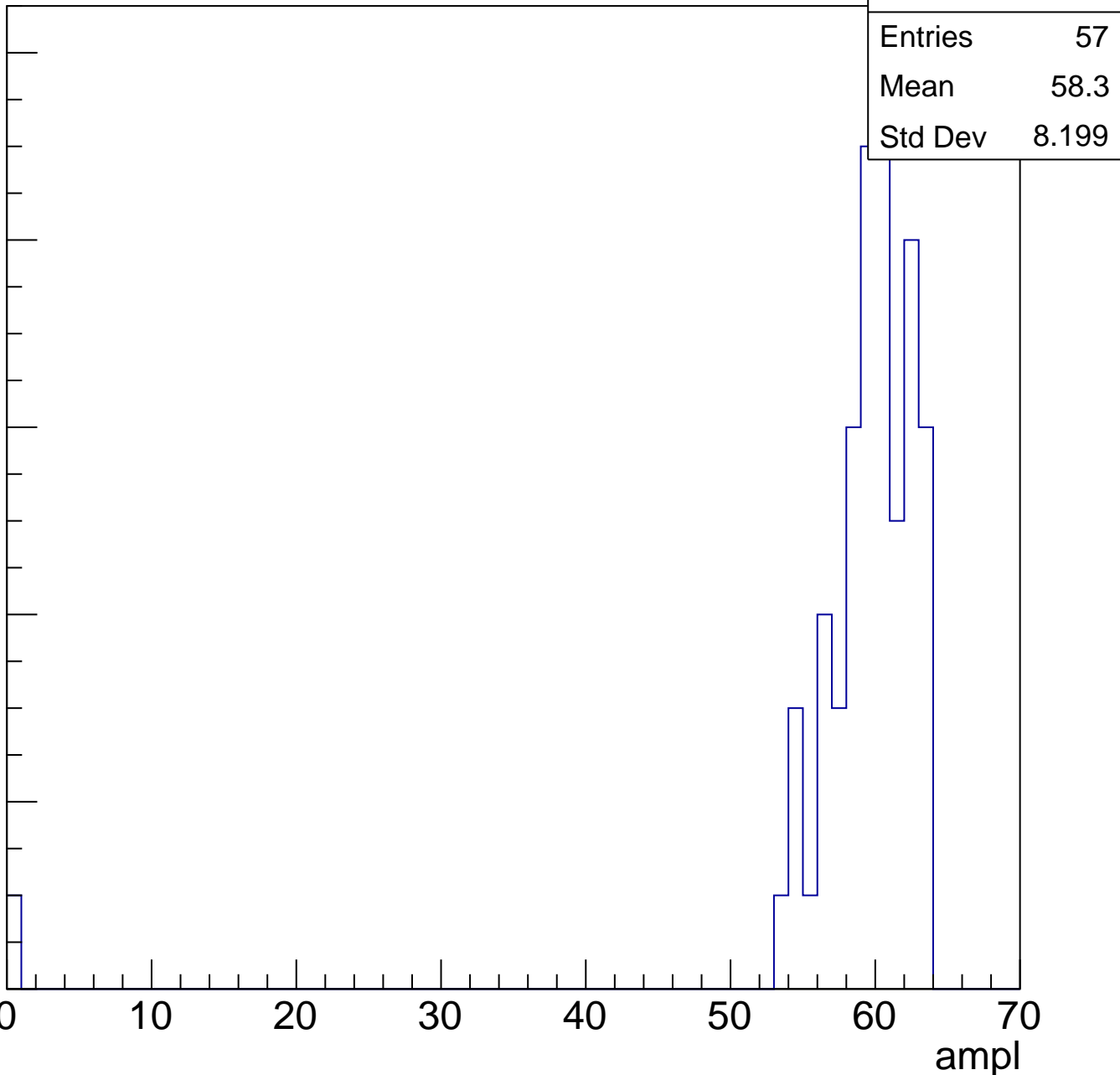
30

40

50

60

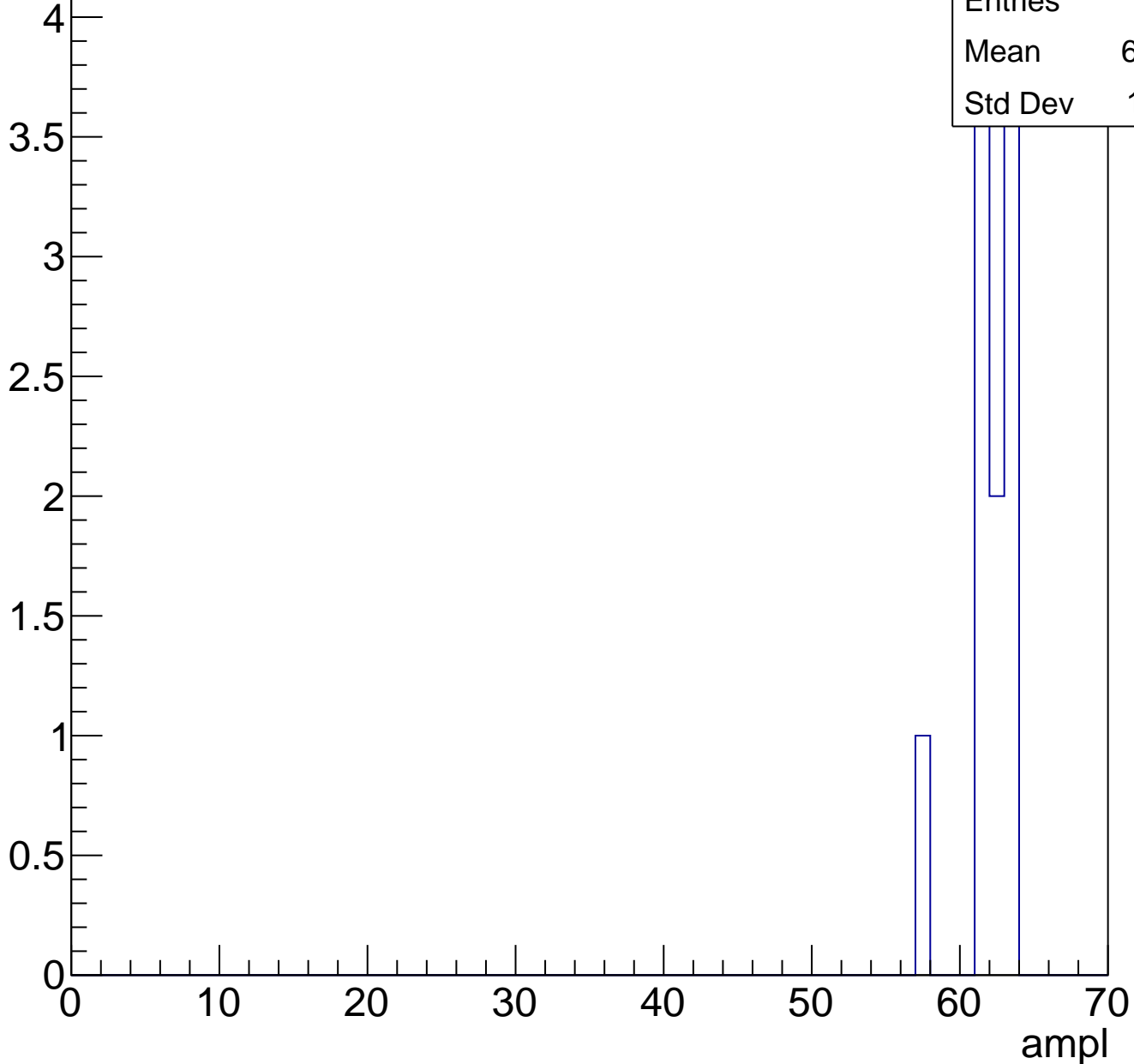
ampl



# B1L000S, U23-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch97, adc0

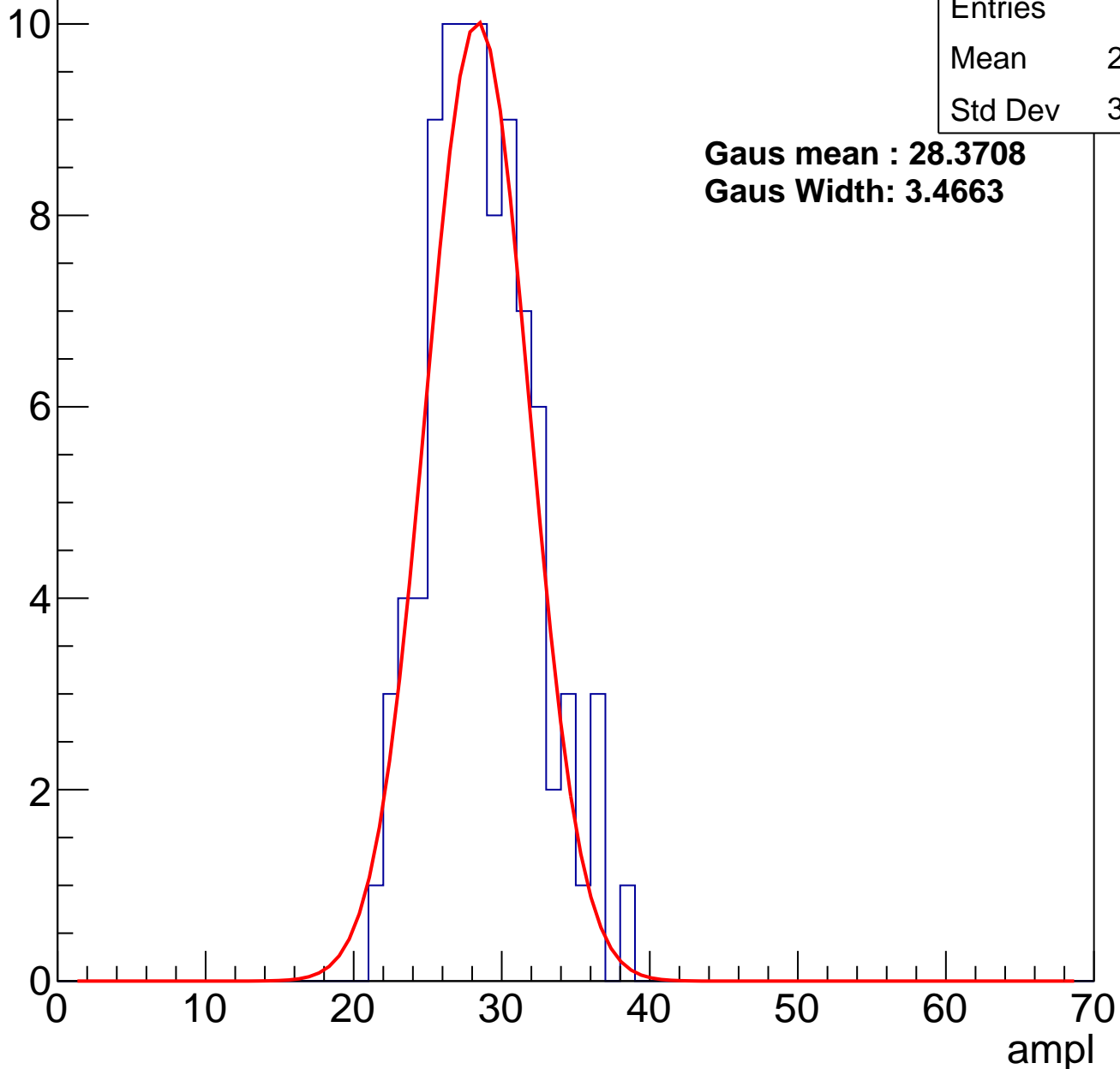
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	91
Mean	28.24
Std Dev	3.553

**Gaus mean : 28.3708**

**Gaus Width: 3.4663**

Entry



# B1L000S, U23-ch97, adc1

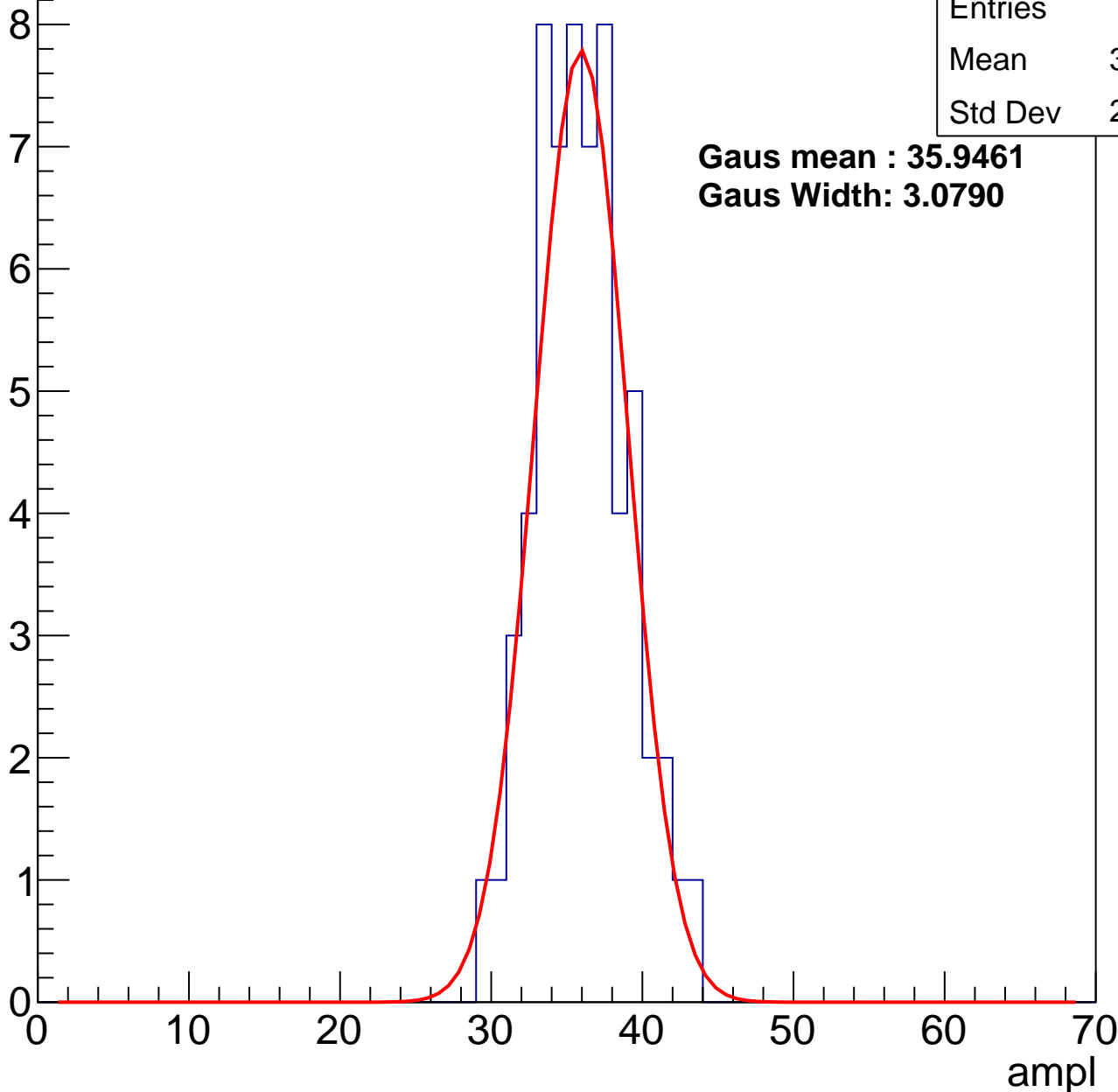
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.55
Std Dev	2.993

**Gaus mean : 35.9461**

**Gaus Width: 3.0790**



# B1L000S, U23-ch97, adc2

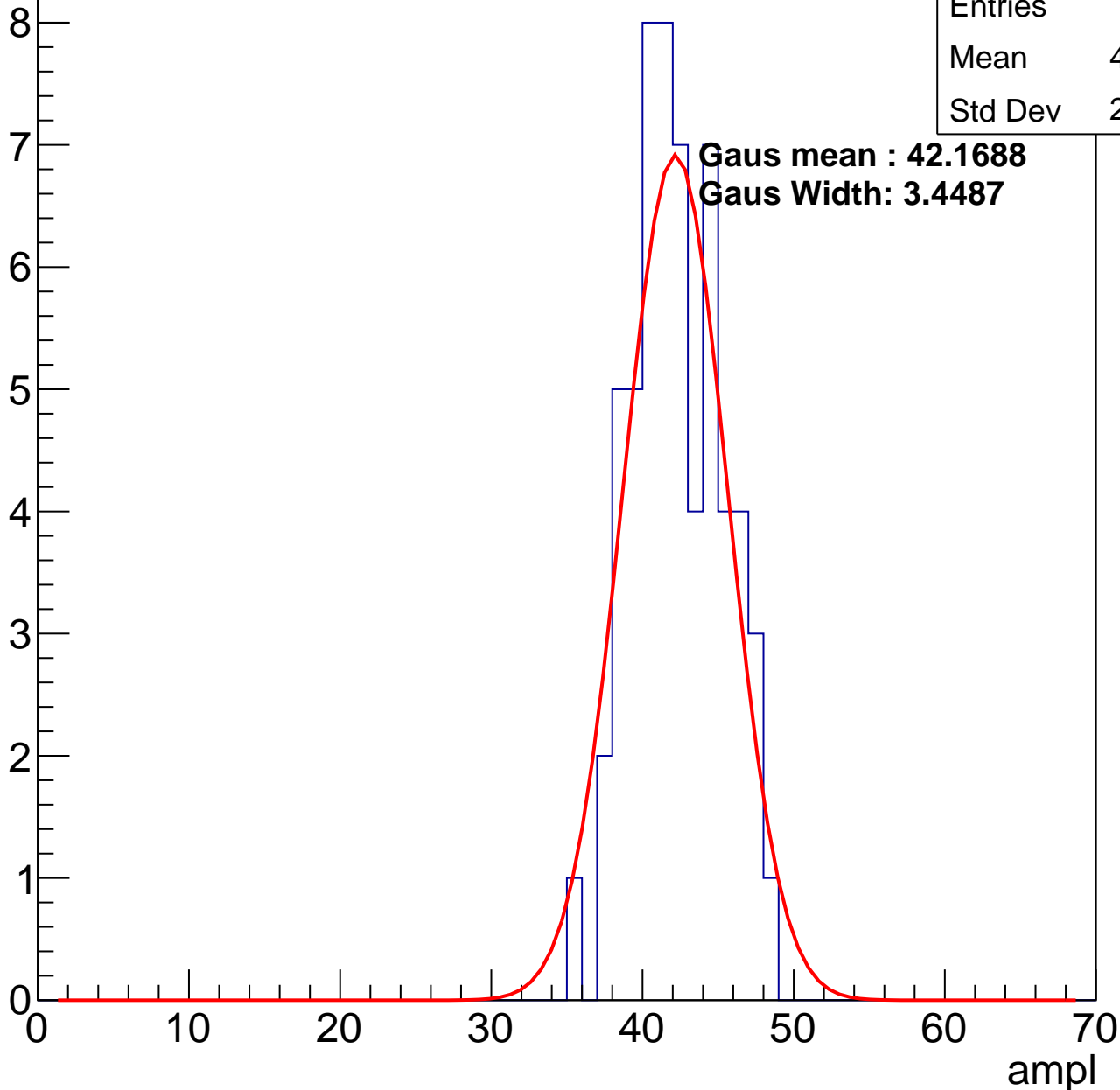
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.85
Std Dev	2.927

**Gaus mean : 42.1688**

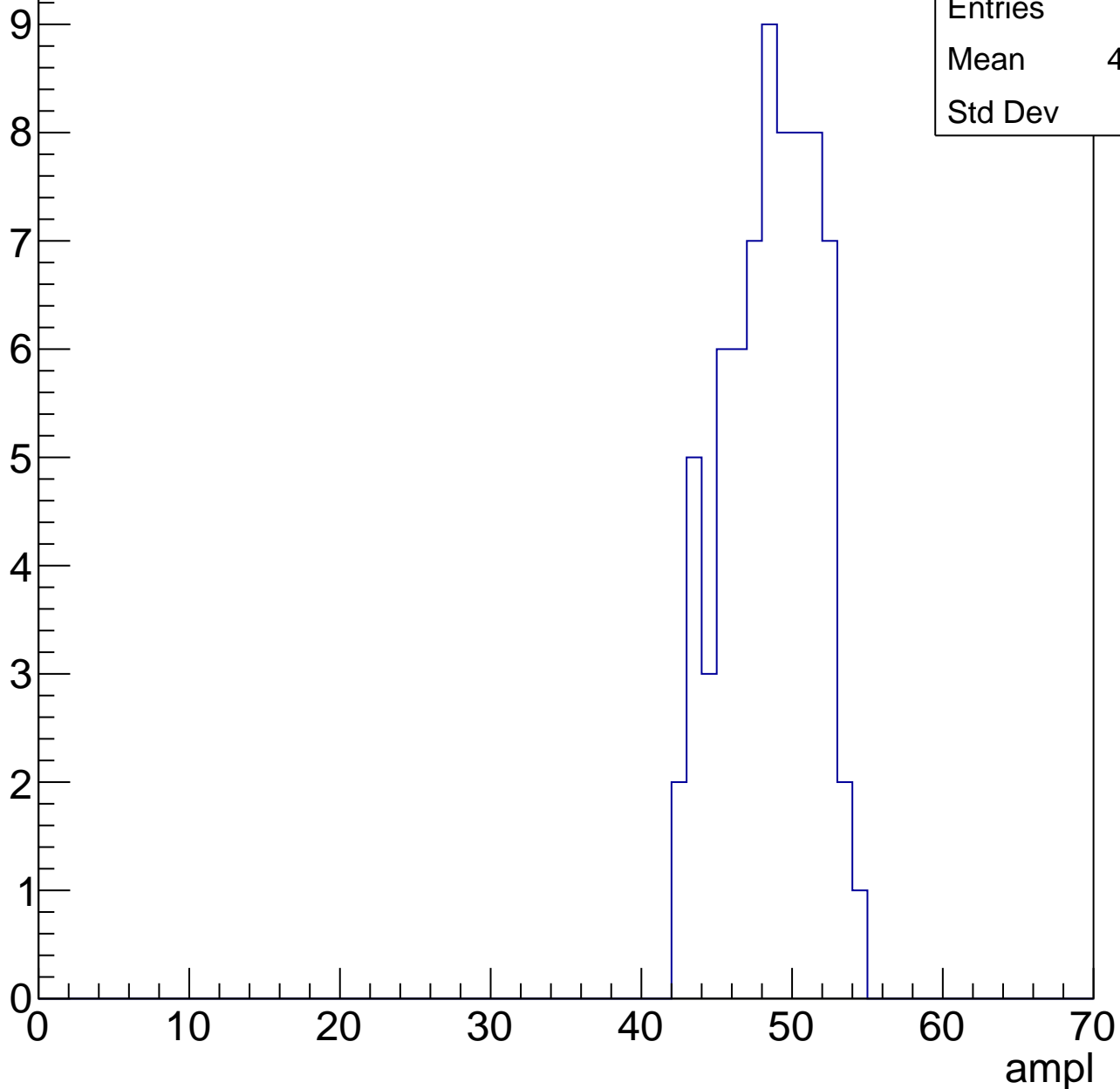
**Gaus Width: 3.4487**



# B1L000S, U23-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

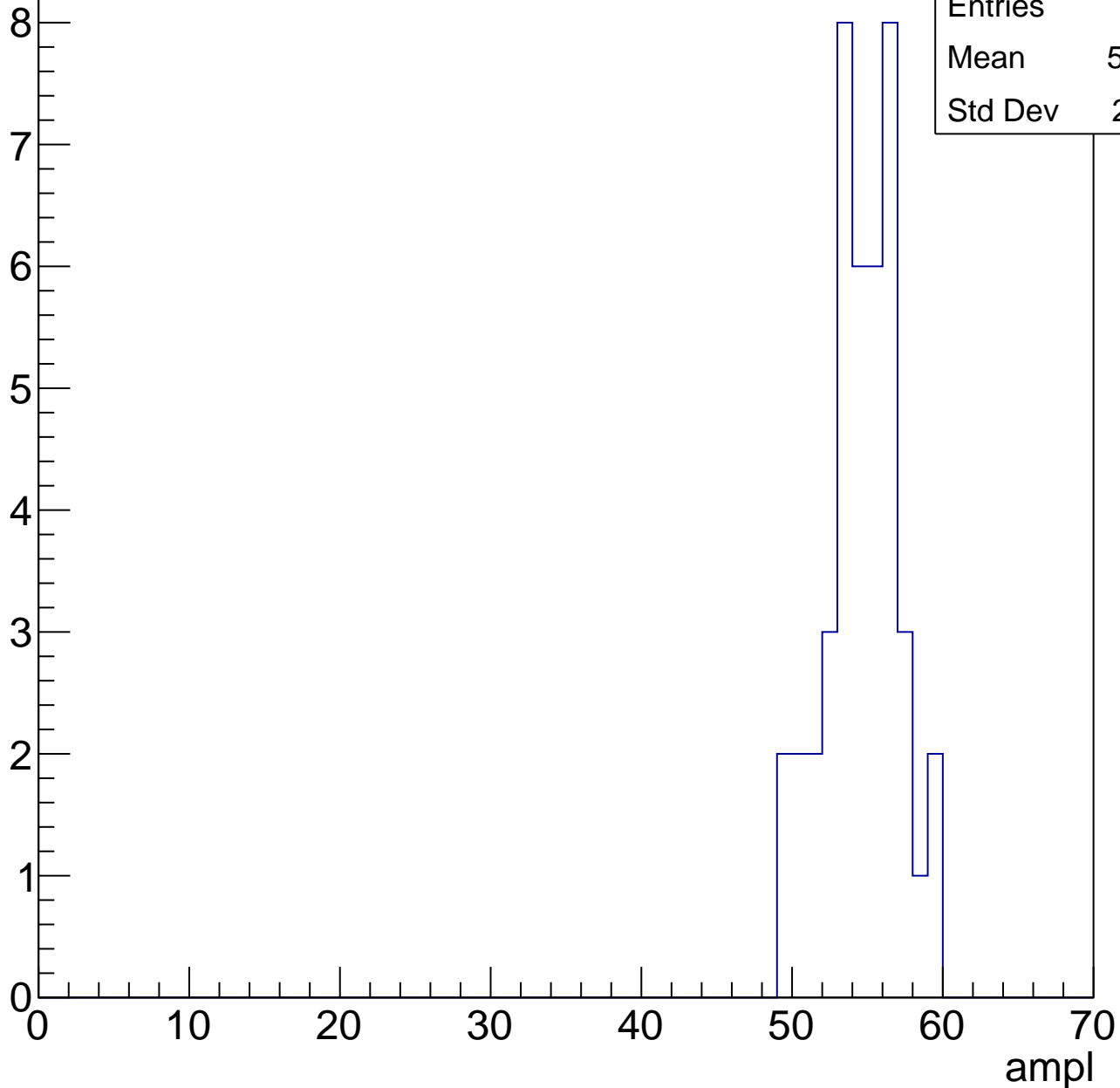
Entry



# B1L000S, U23-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	43
Mean	54.16
Std Dev	2.411

# B1L000S, U23-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 66

Mean 59.33

Std Dev 2.501

8

6

4

2

0

0

10

20

30

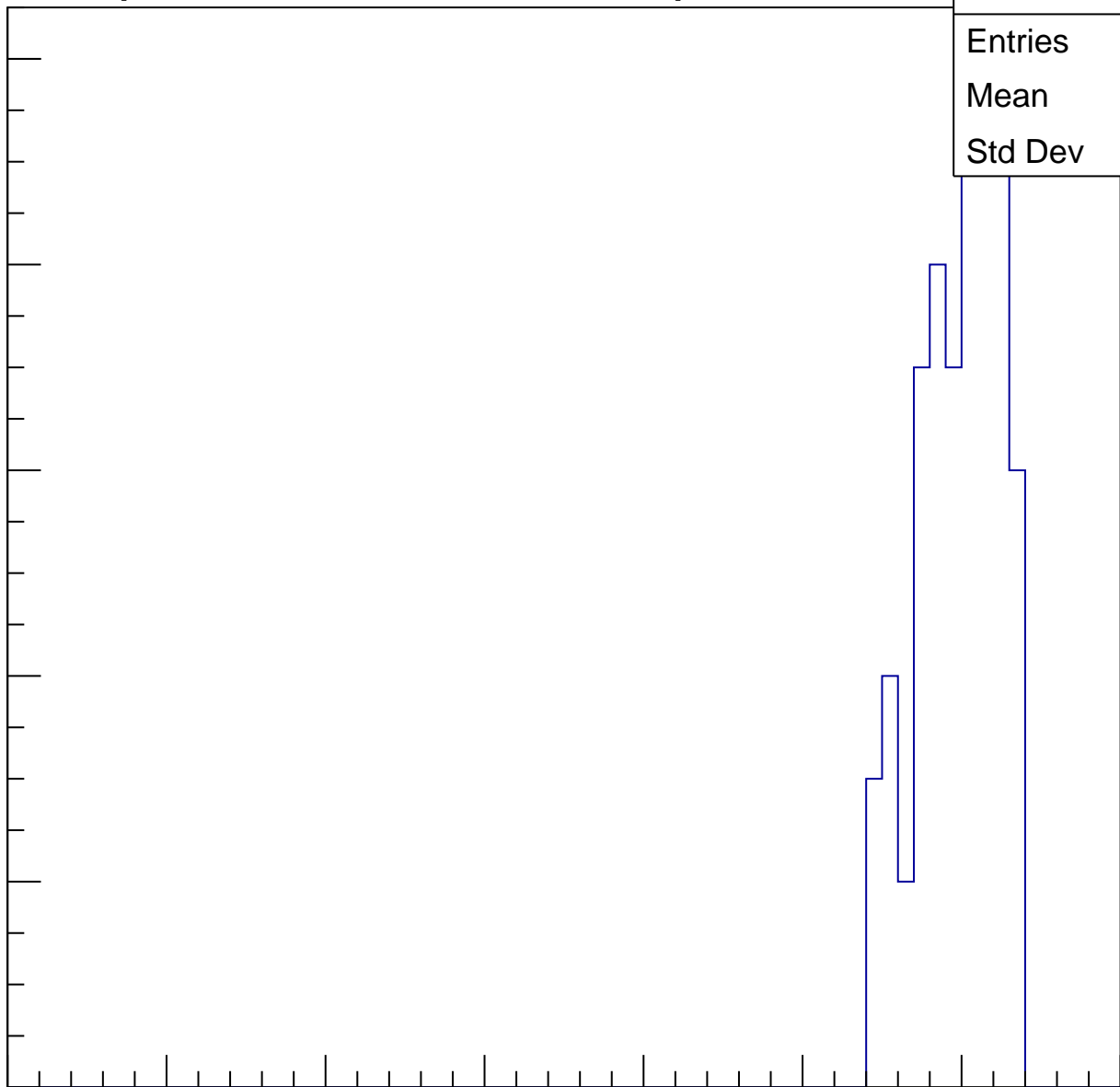
40

50

60

70

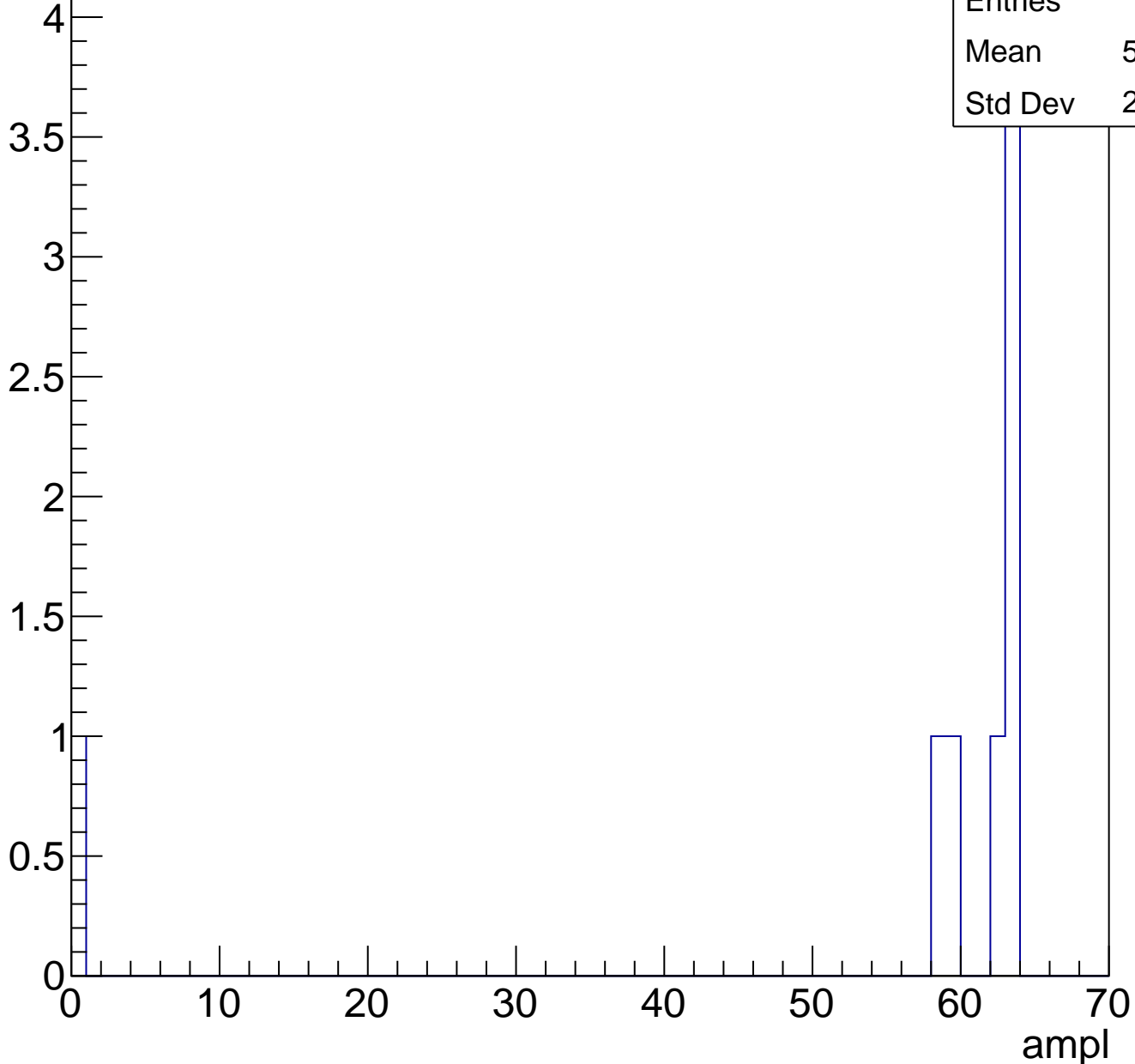
ampl



# B1L000S, U23-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U23-ch98, adc0

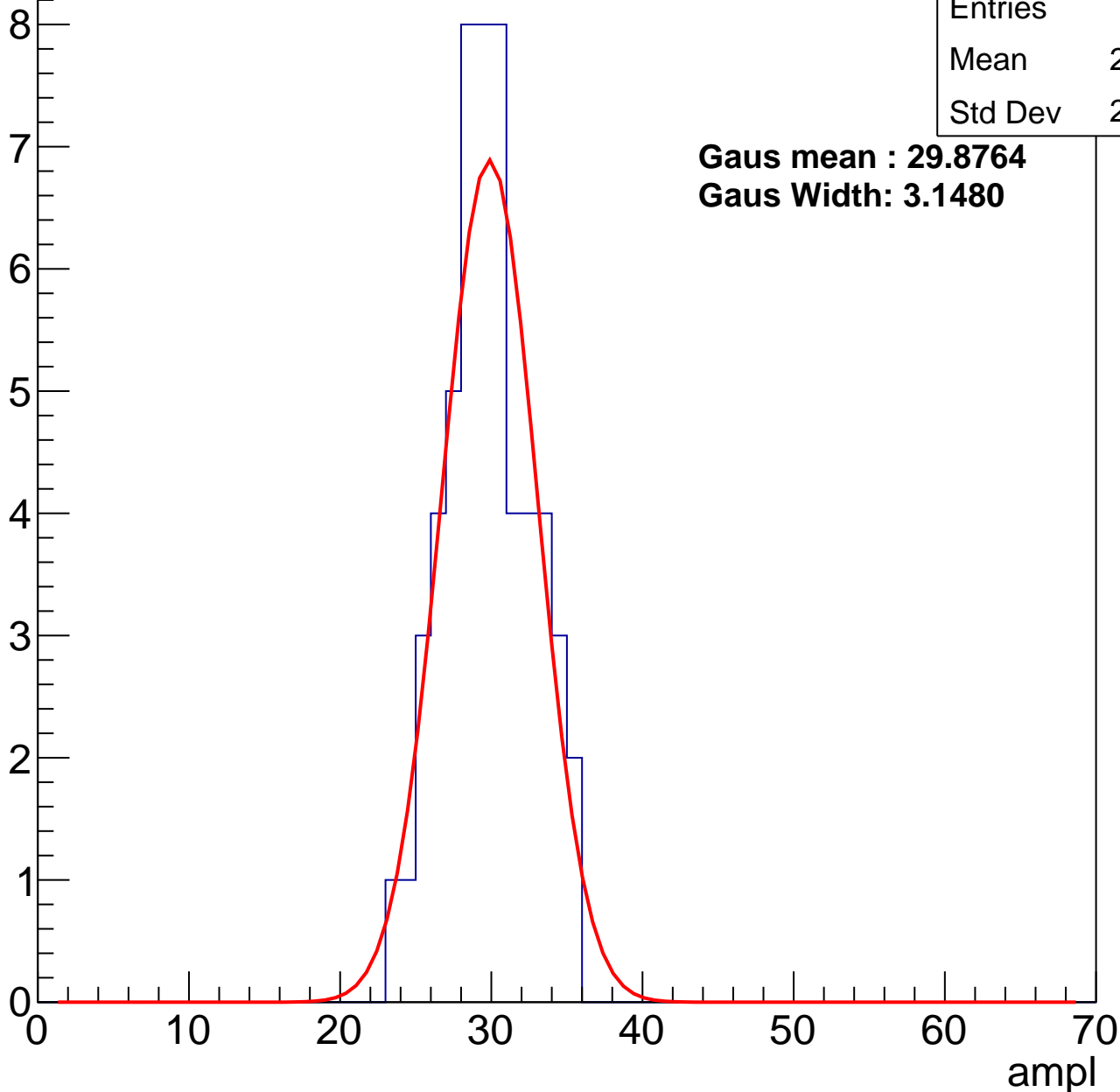
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	29.33
Std Dev	2.822

**Gaus mean : 29.8764**

**Gaus Width: 3.1480**



# B1L000S, U23-ch98, adc1

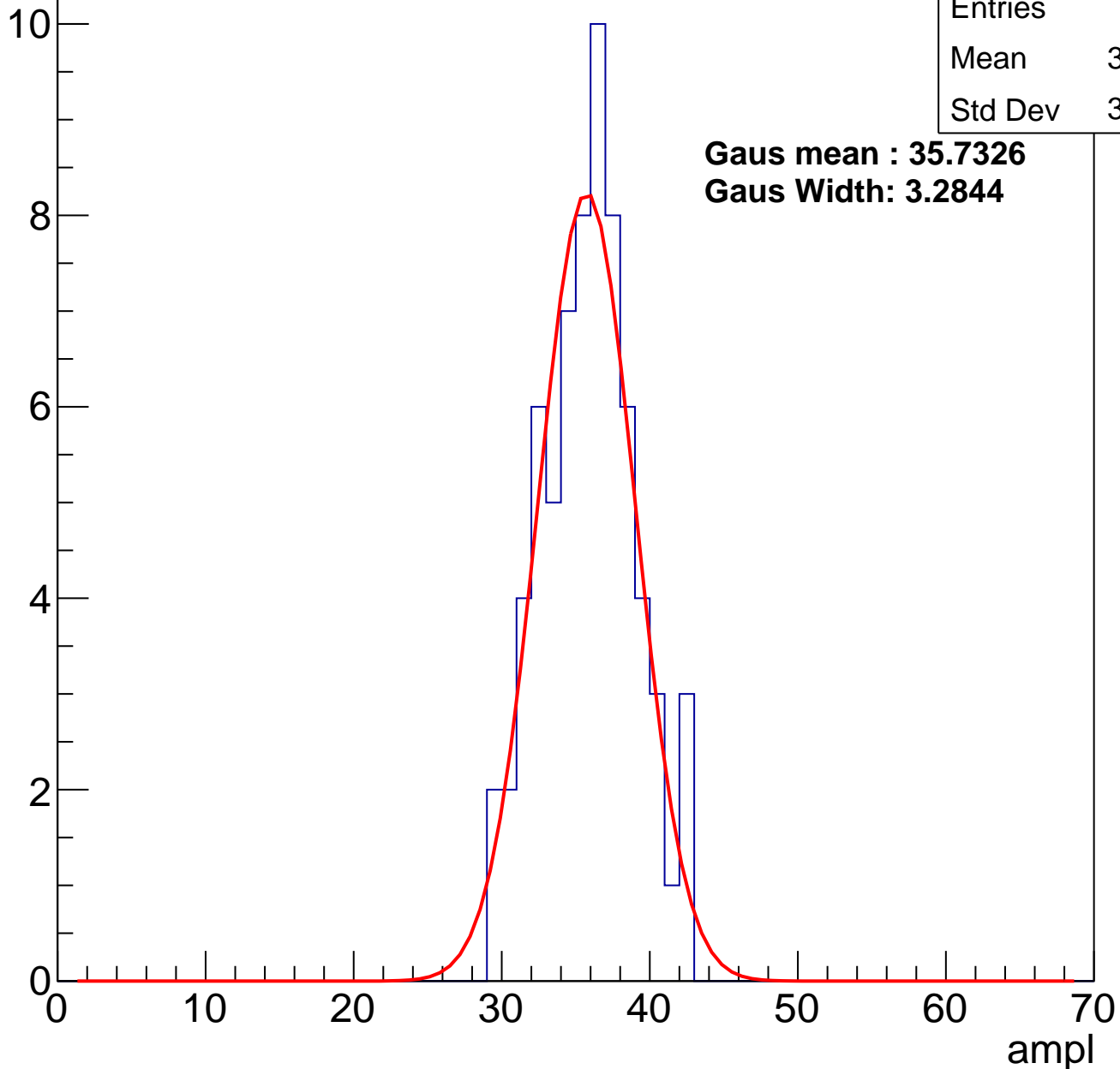
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	35.42
Std Dev	3.123

**Gaus mean : 35.7326**

**Gaus Width: 3.2844**

Entry



# B1L000S, U23-ch98, adc2

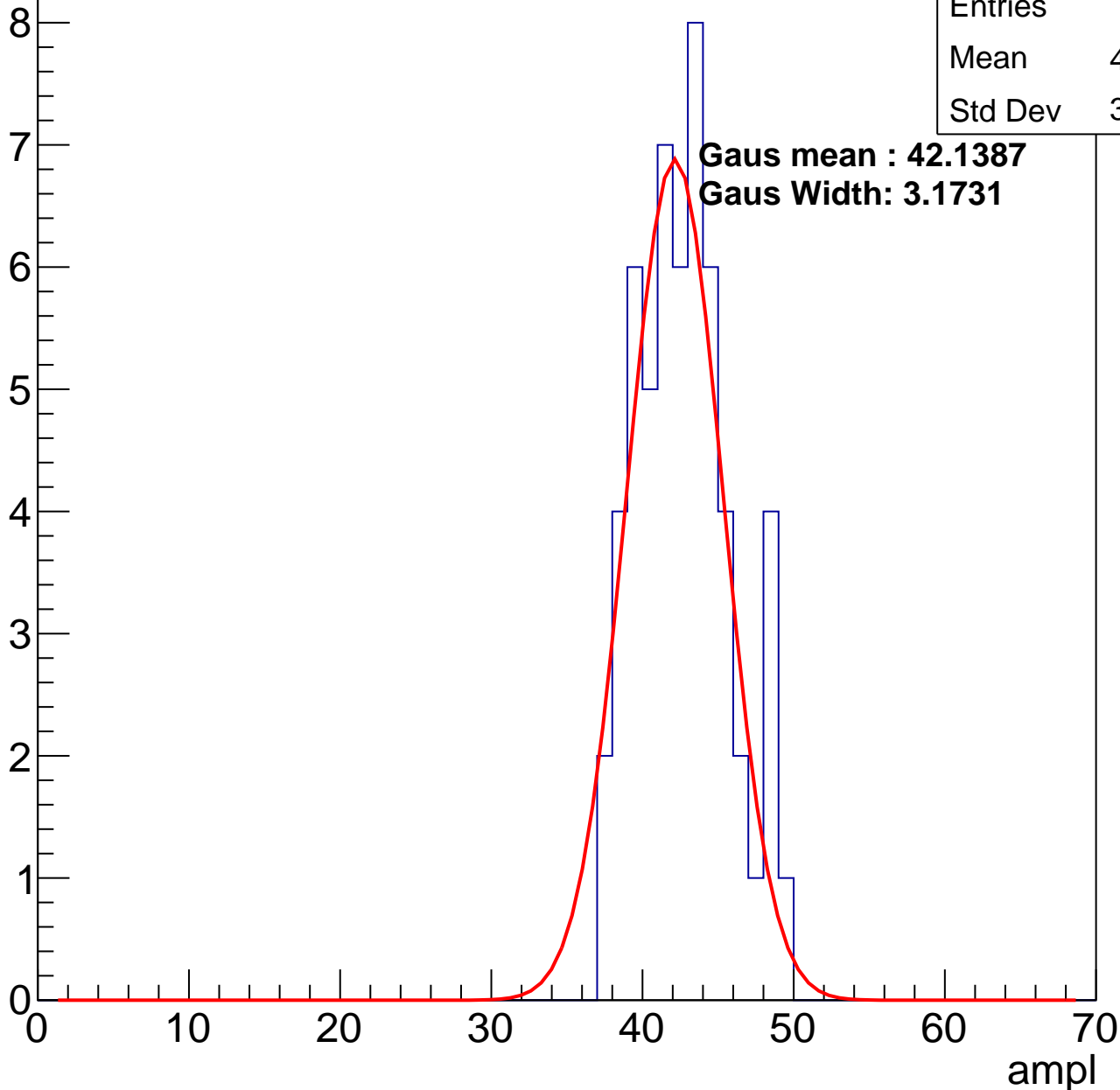
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.27
Std Dev	3.015

**Gaus mean : 42.1387**

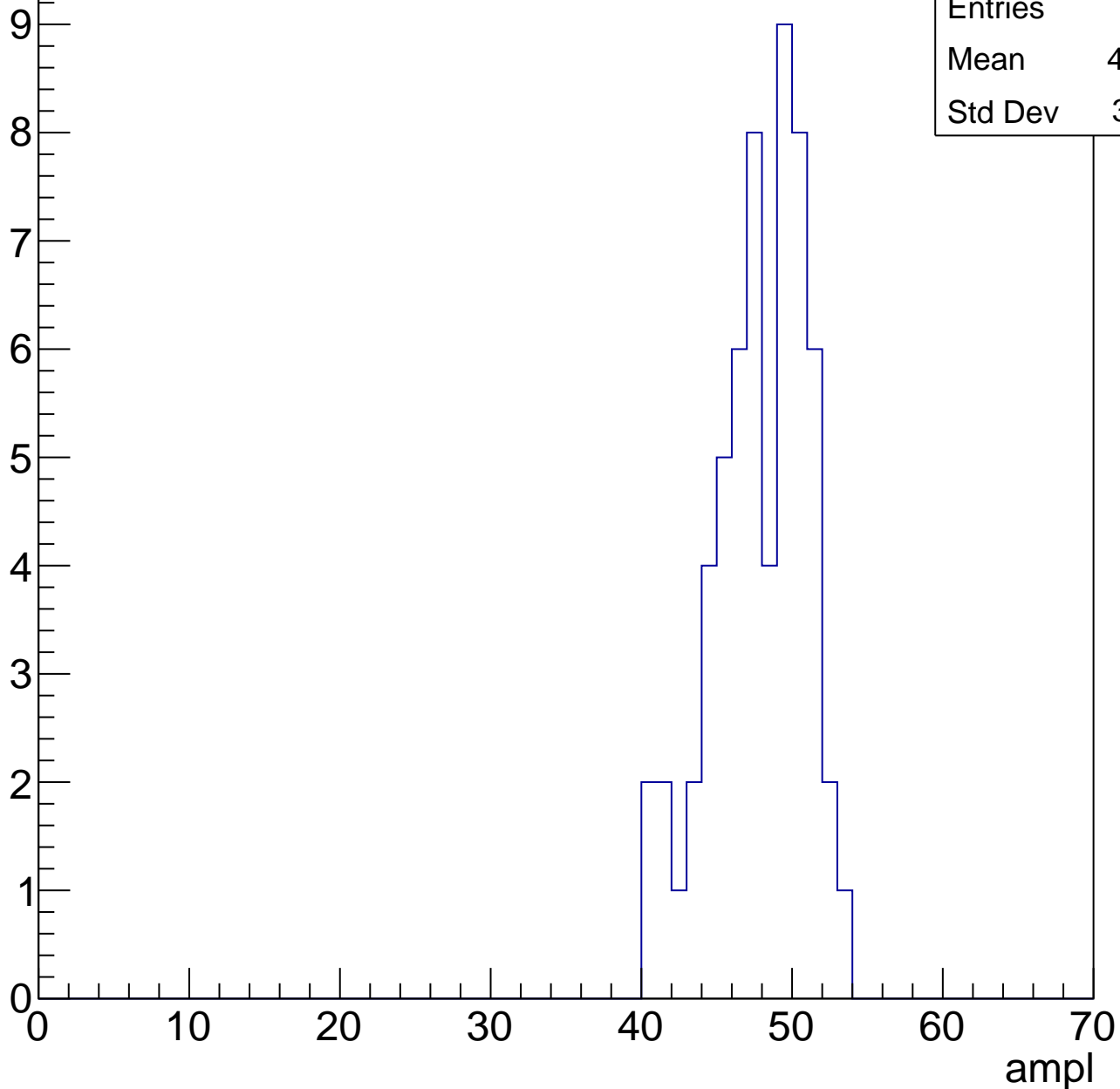
**Gaus Width: 3.1731**



# B1L000S, U23-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

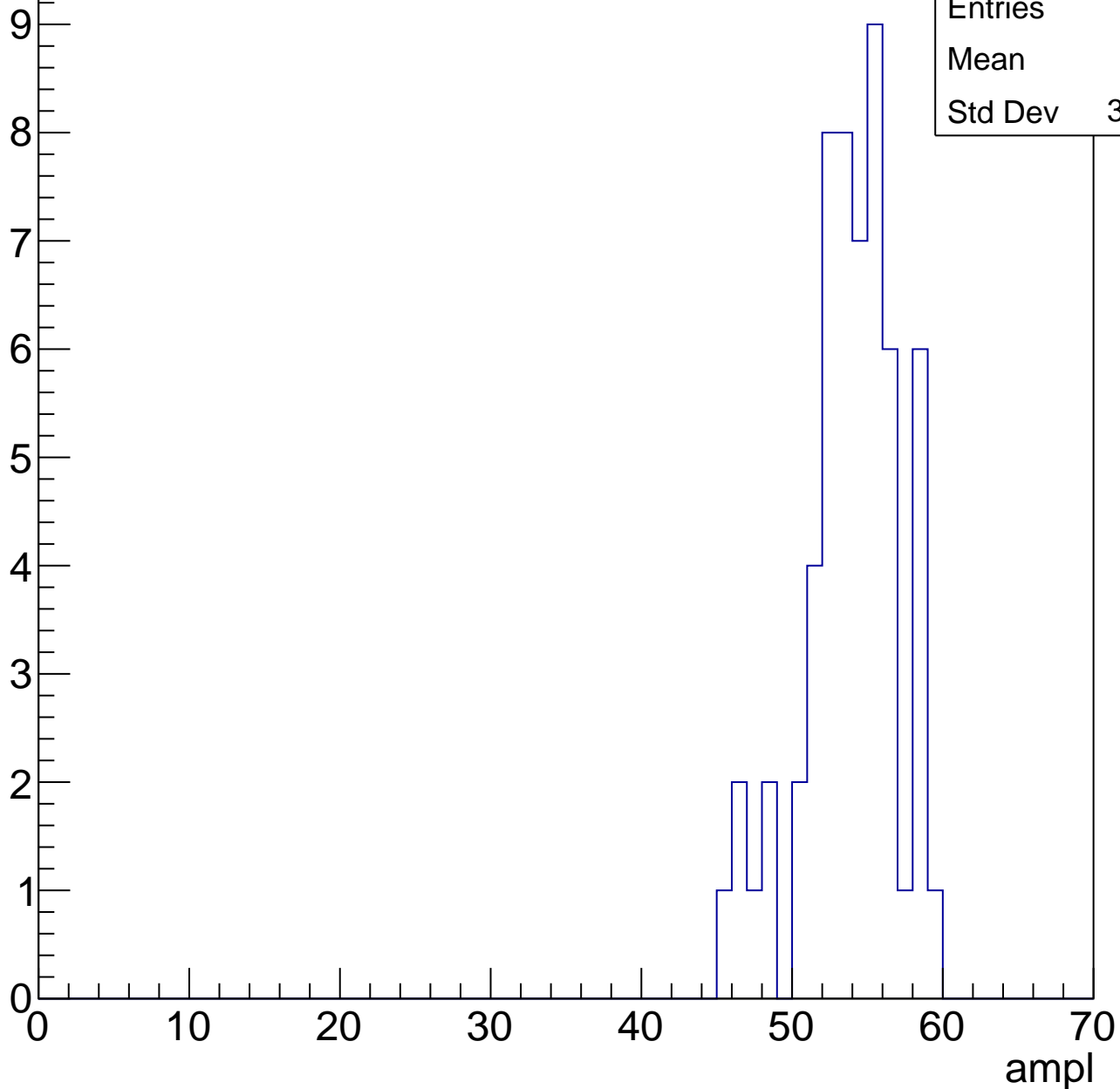


Entries	60
Mean	47.32
Std Dev	3.101

# B1L000S, U23-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

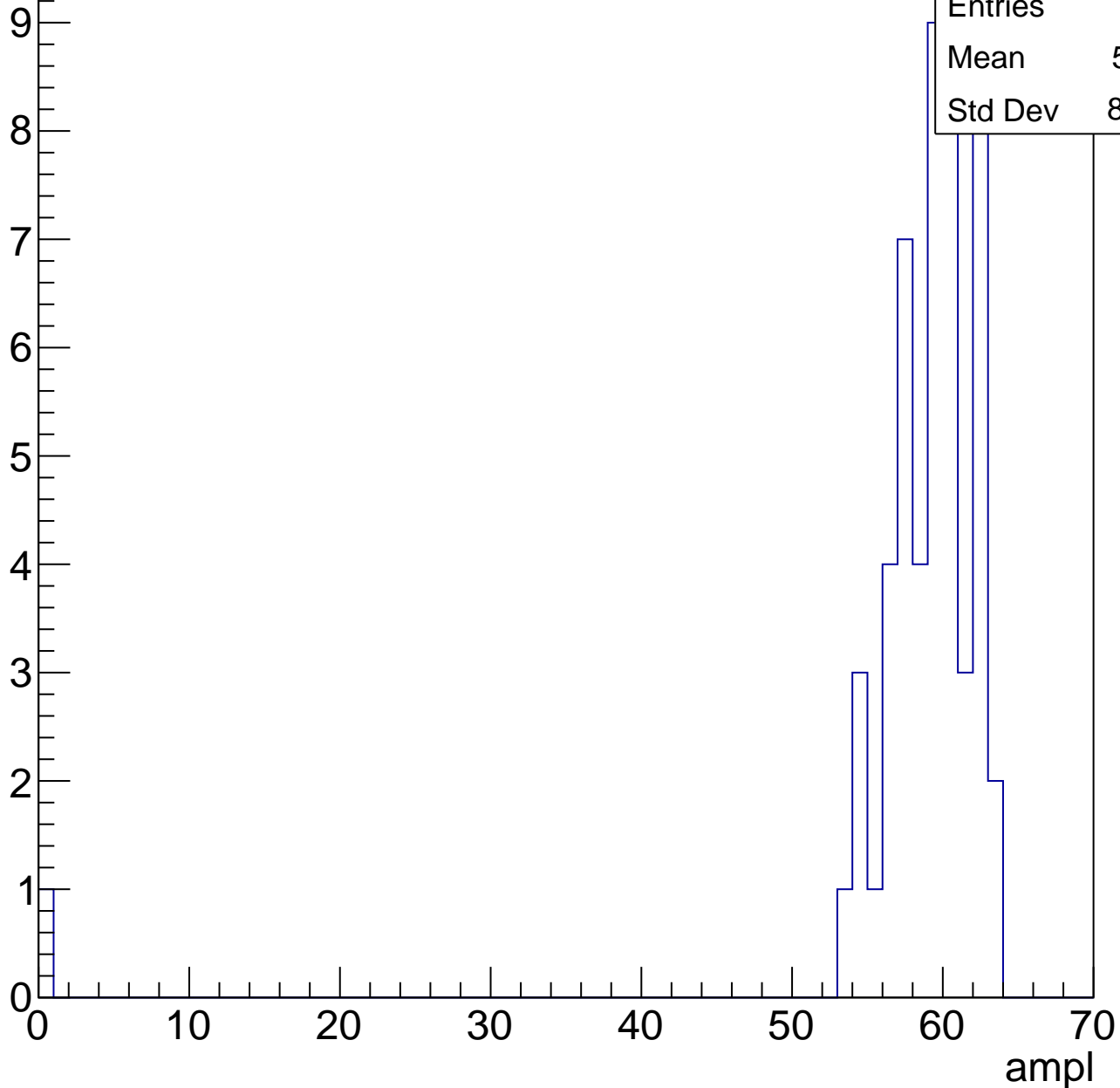


Entries	58
Mean	53.4
Std Dev	3.162

# B1L000S, U23-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

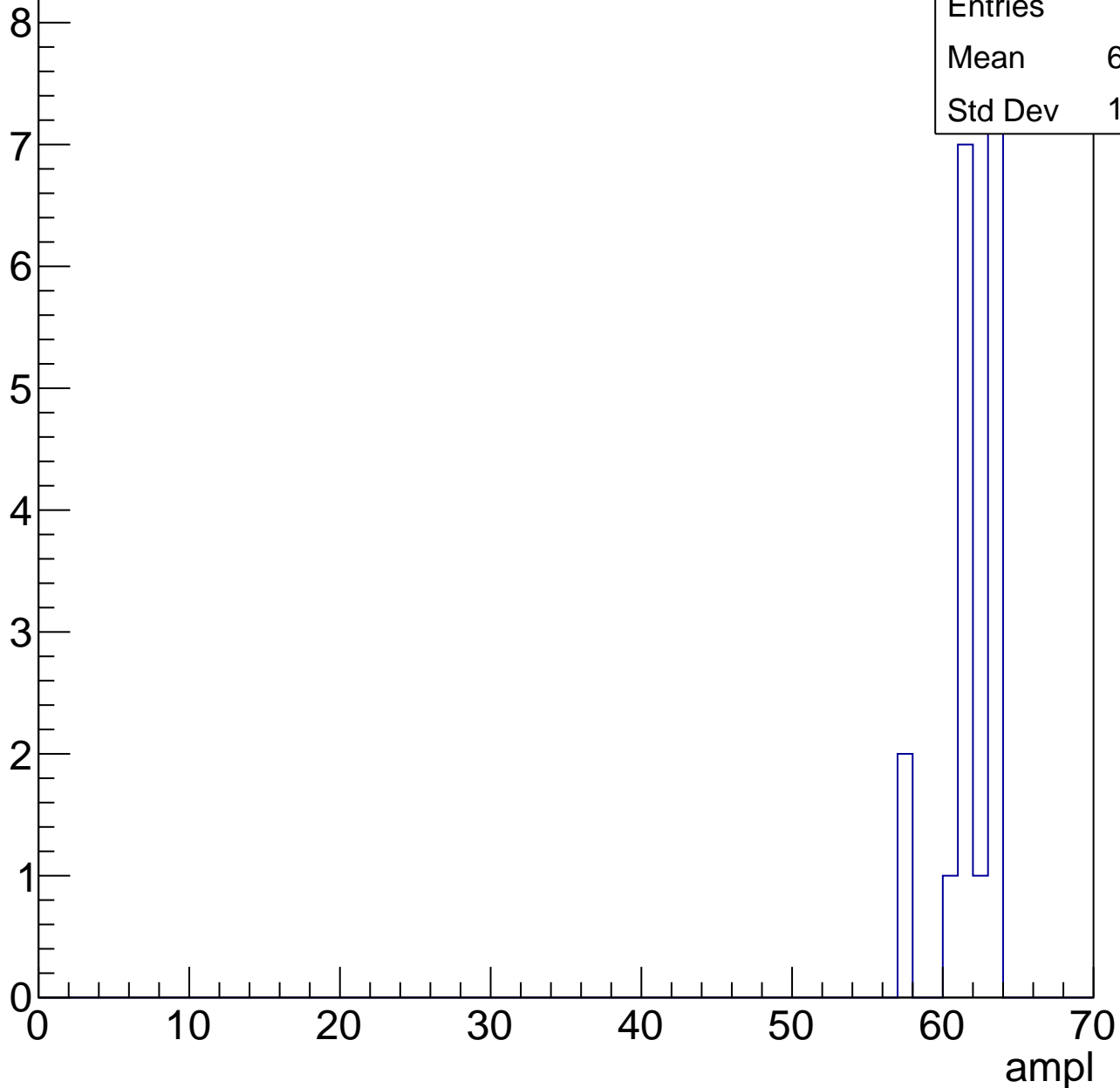


# B1L000S, U23-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.42
Std Dev	1.816





# B1L000S, U23-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.76
Std Dev	4.737

**Gaus mean : 29.2541**

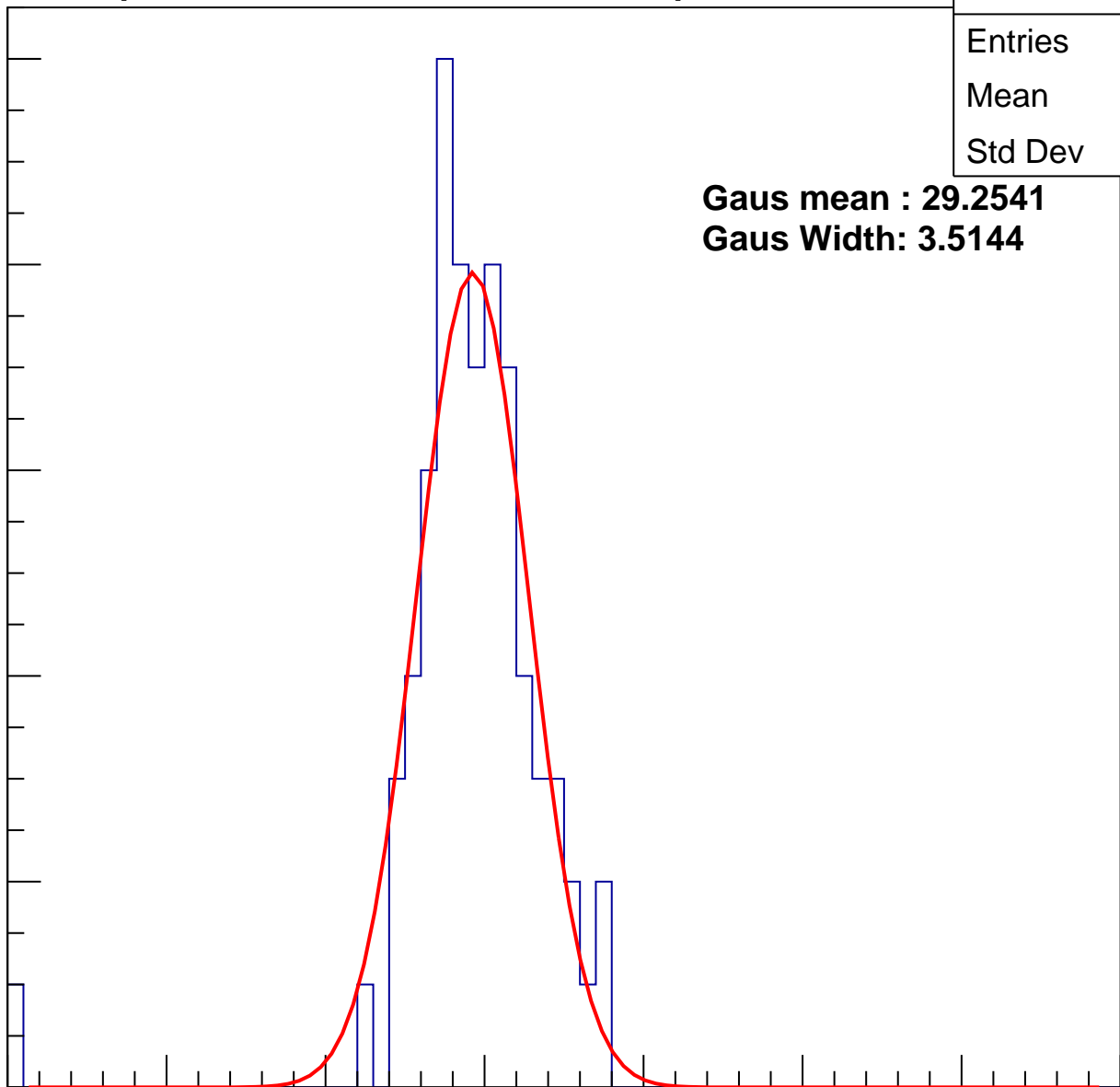
**Gaus Width: 3.5144**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch99, adc1

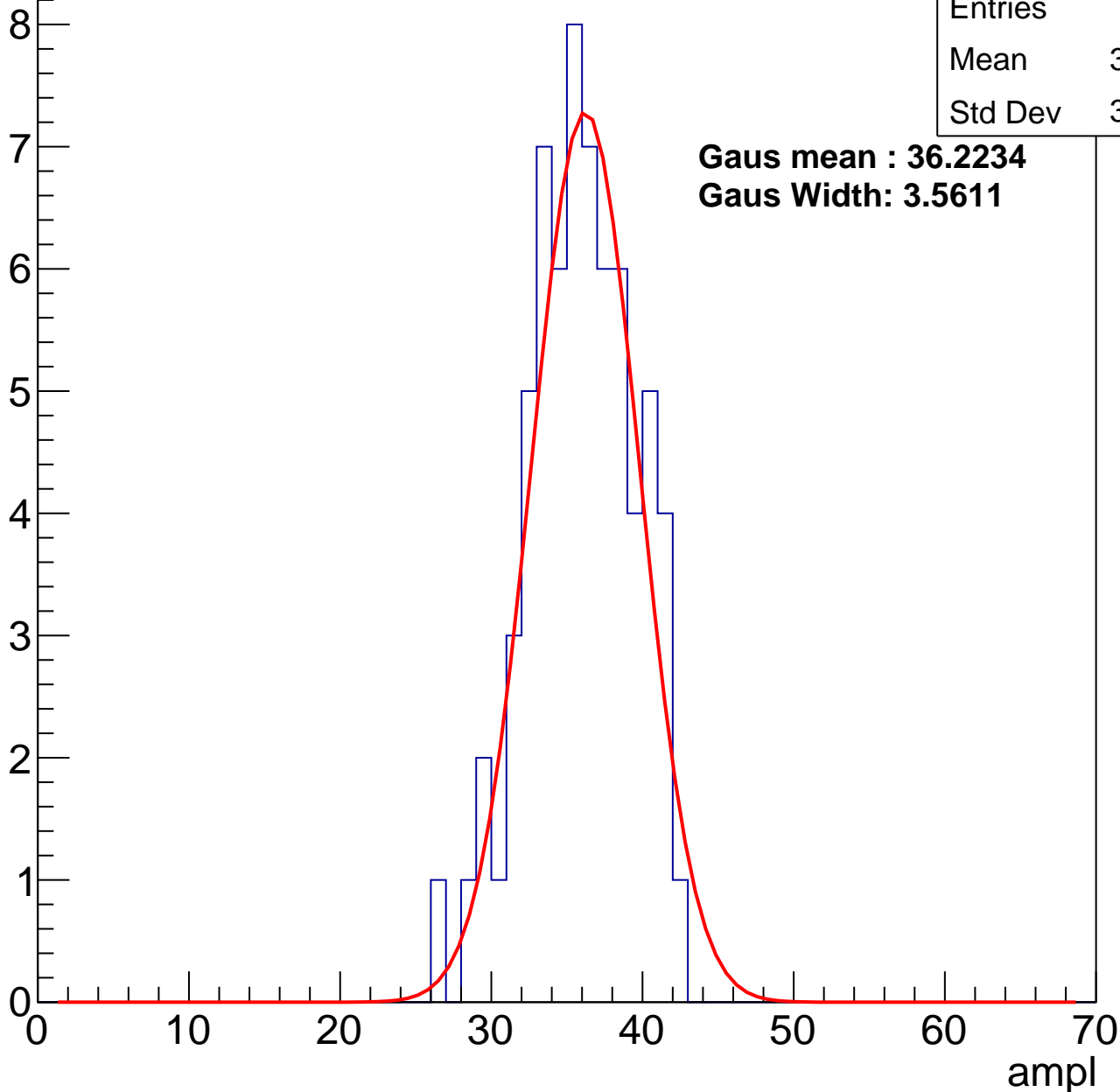
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.43
Std Dev	3.474

**Gaus mean : 36.2234**

**Gaus Width: 3.5611**



# B1L000S, U23-ch99, adc2

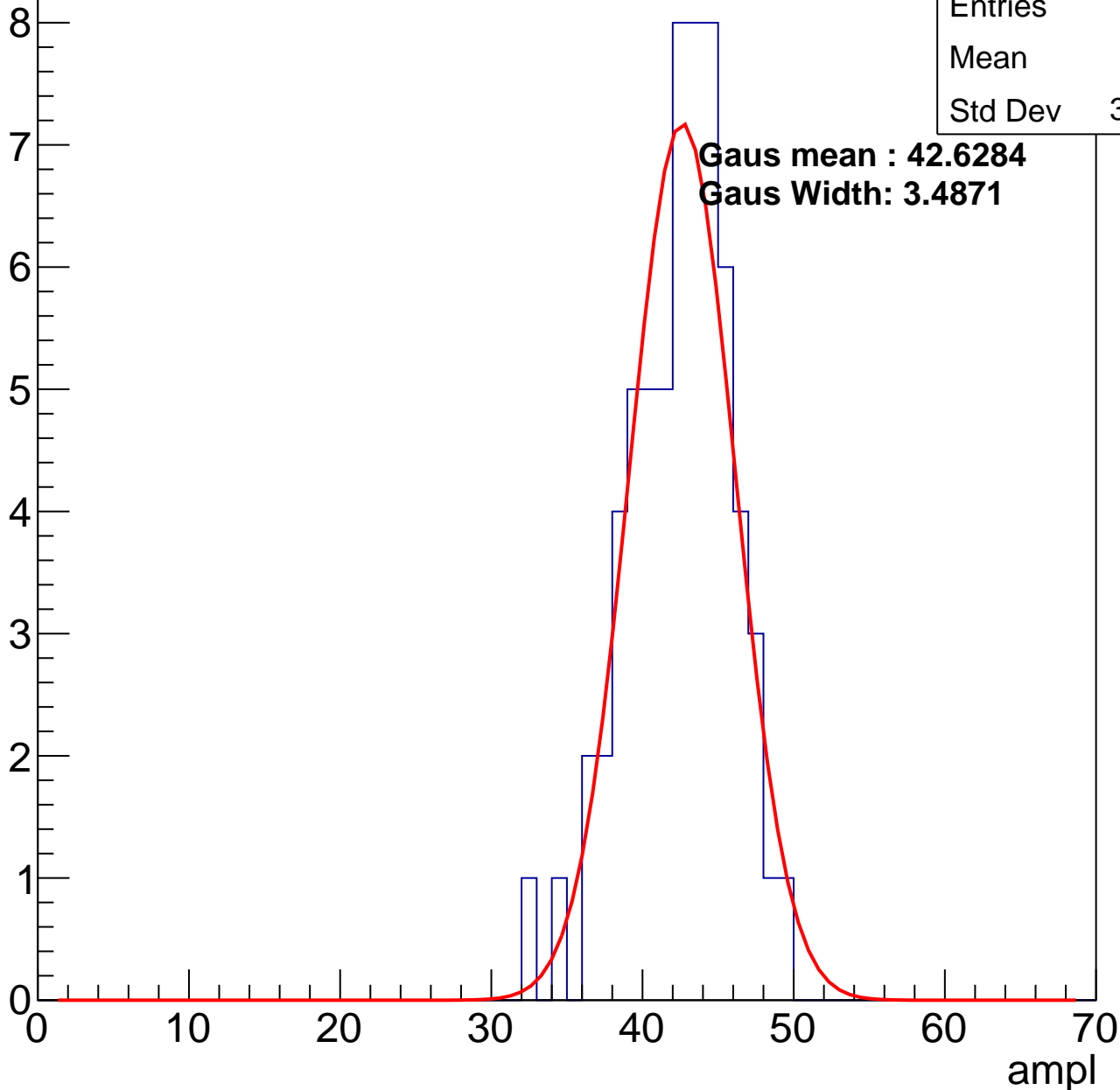
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	42
Std Dev	3.396

**Gaus mean : 42.6284**

**Gaus Width: 3.4871**

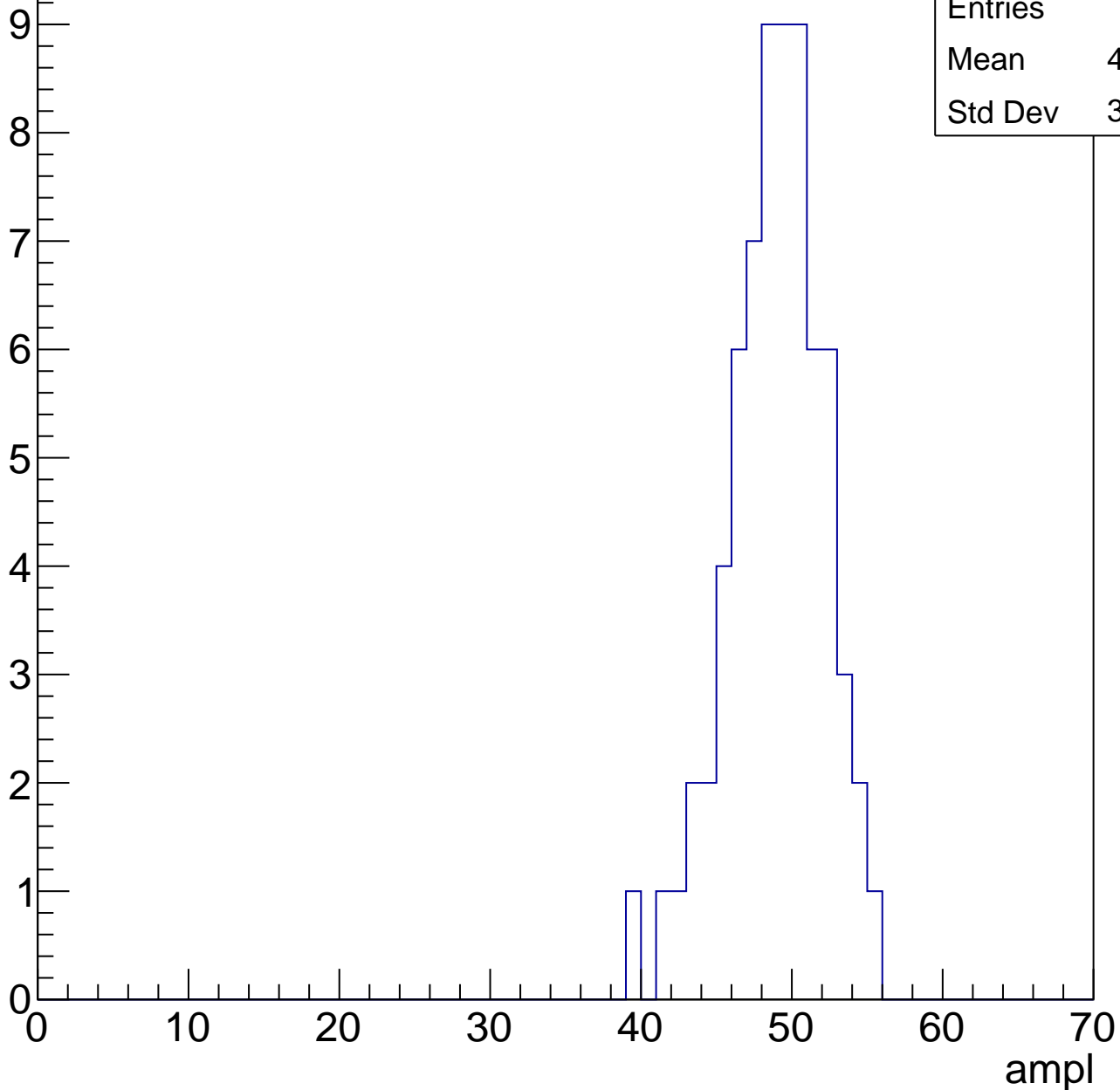


# B1L000S, U23-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.46
Std Dev	3.165

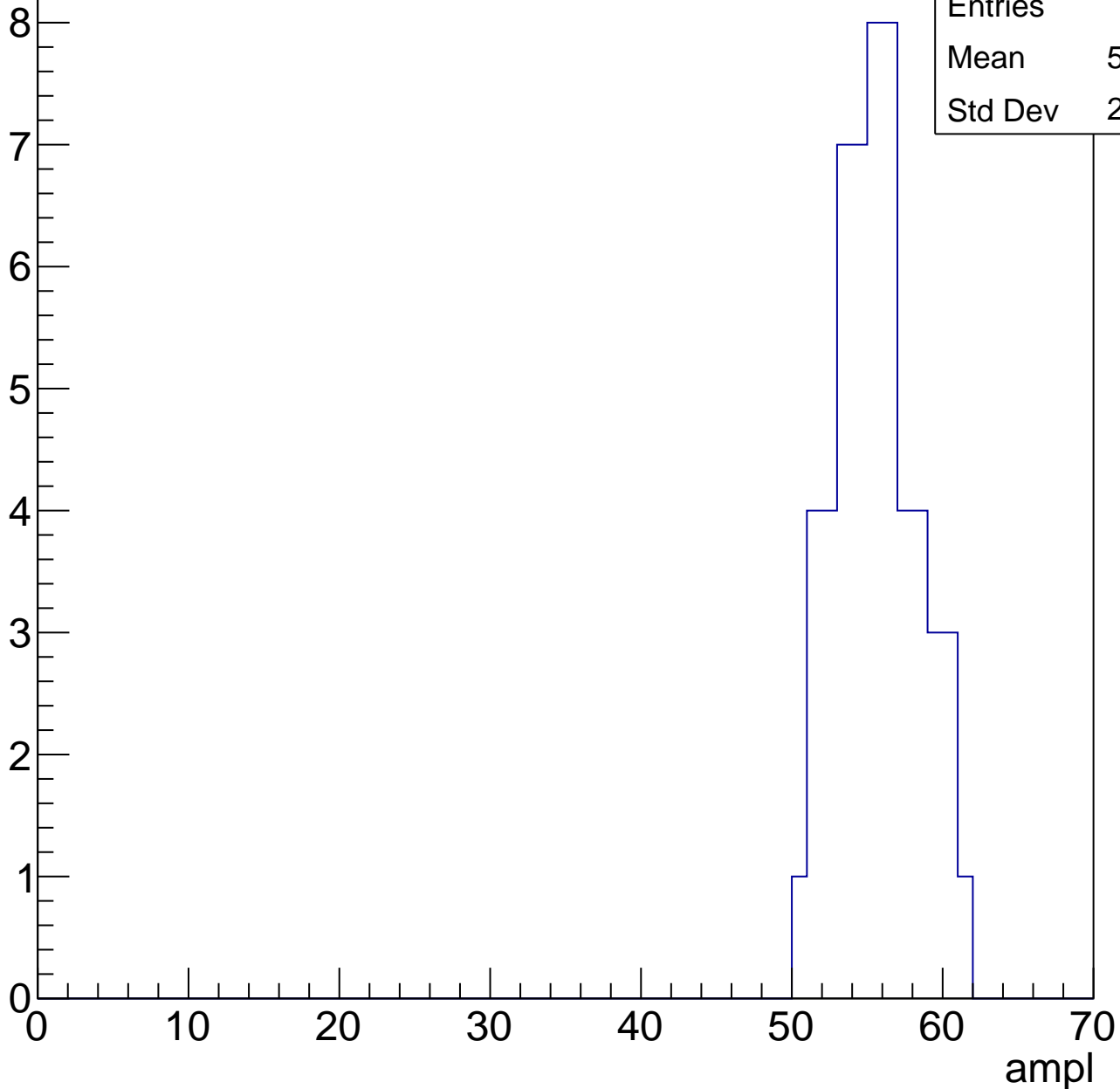


# B1L000S, U23-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	55.13
Std Dev	2.646

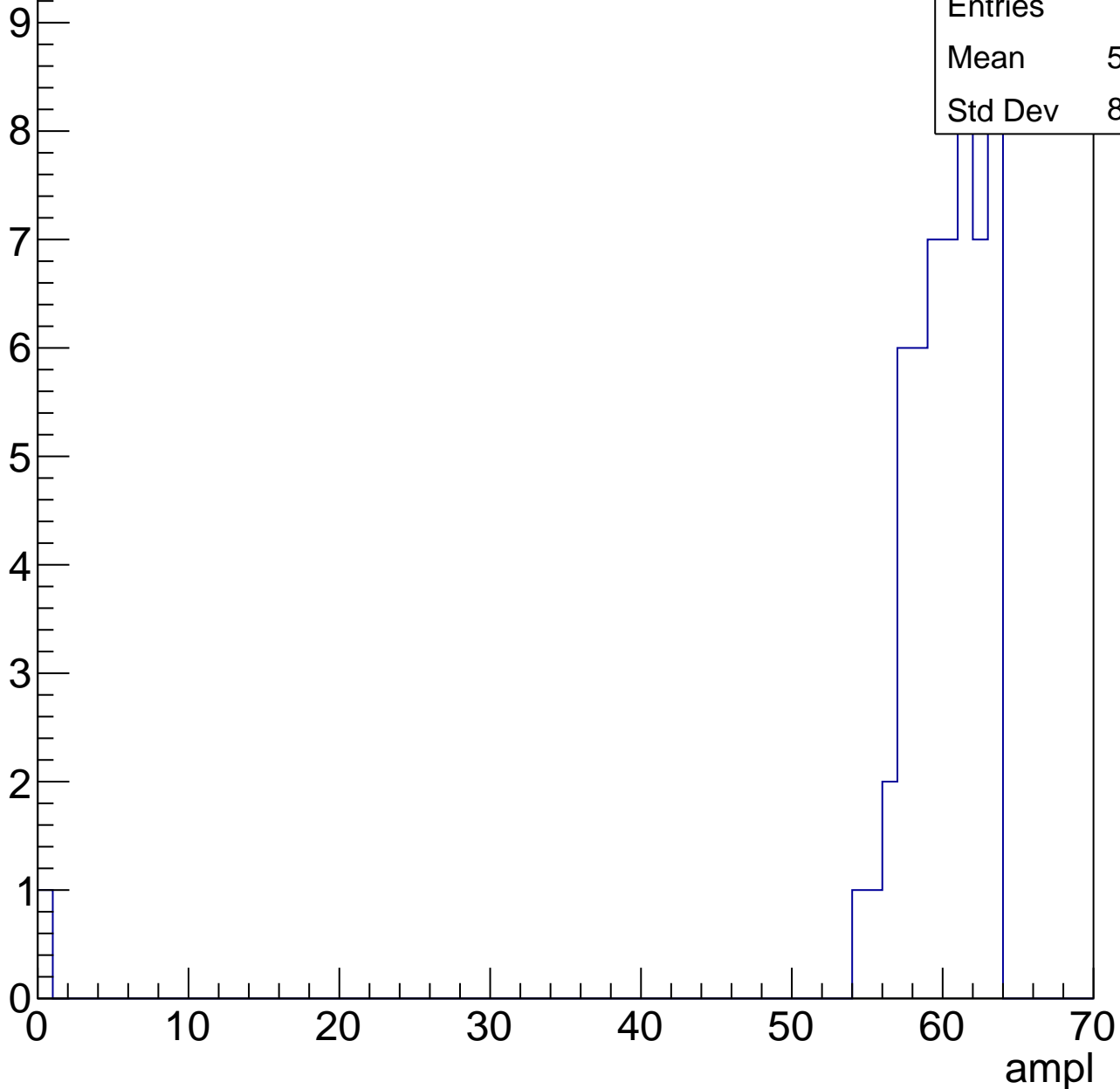


# B1L000S, U23-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.78
Std Dev	8.327



# B1L000S, U23-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch100, adc0

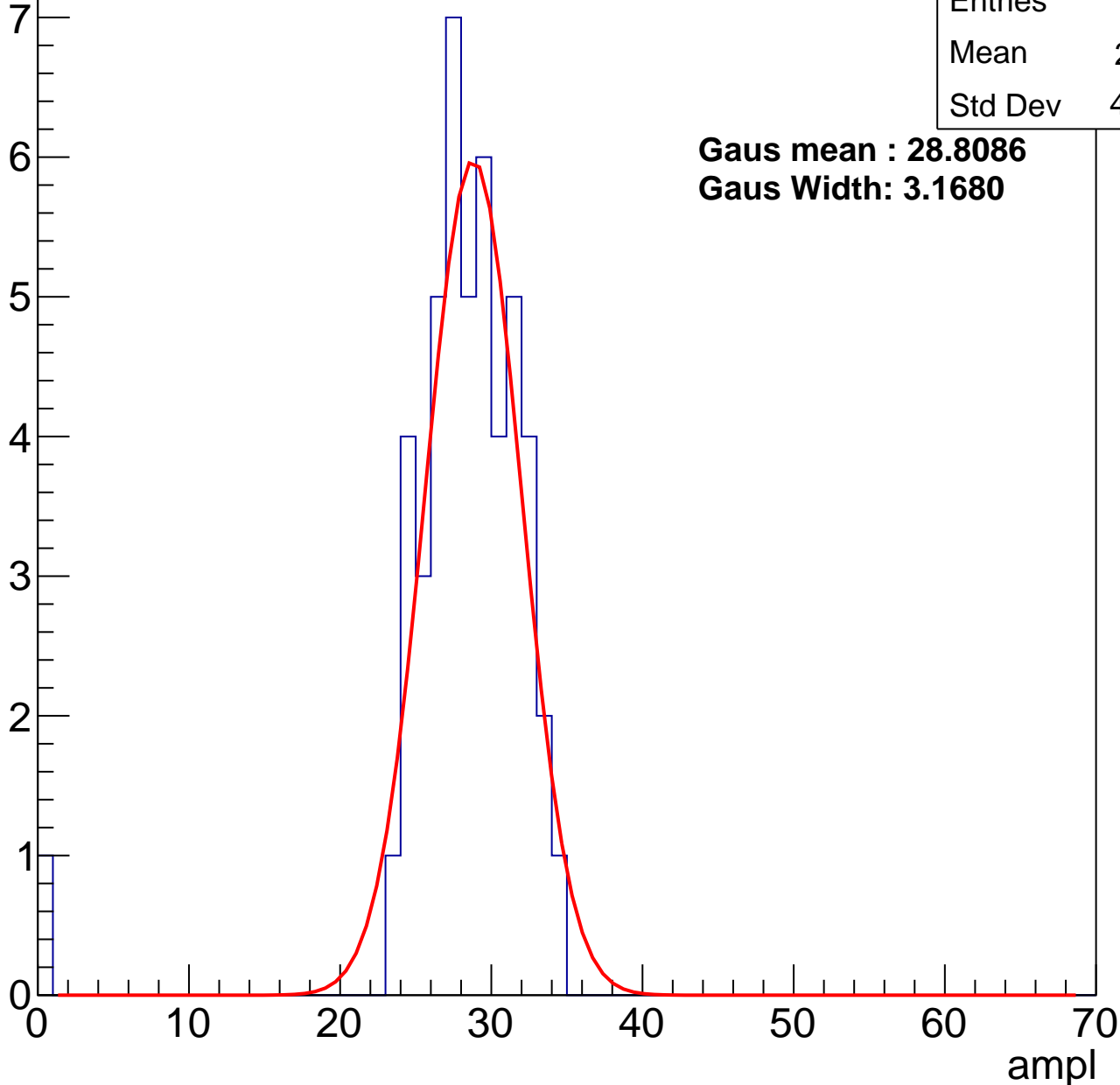
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	27.71
Std Dev	4.873

**Gaus mean : 28.8086**

**Gaus Width: 3.1680**



# B1L000S, U23-ch100, adc1

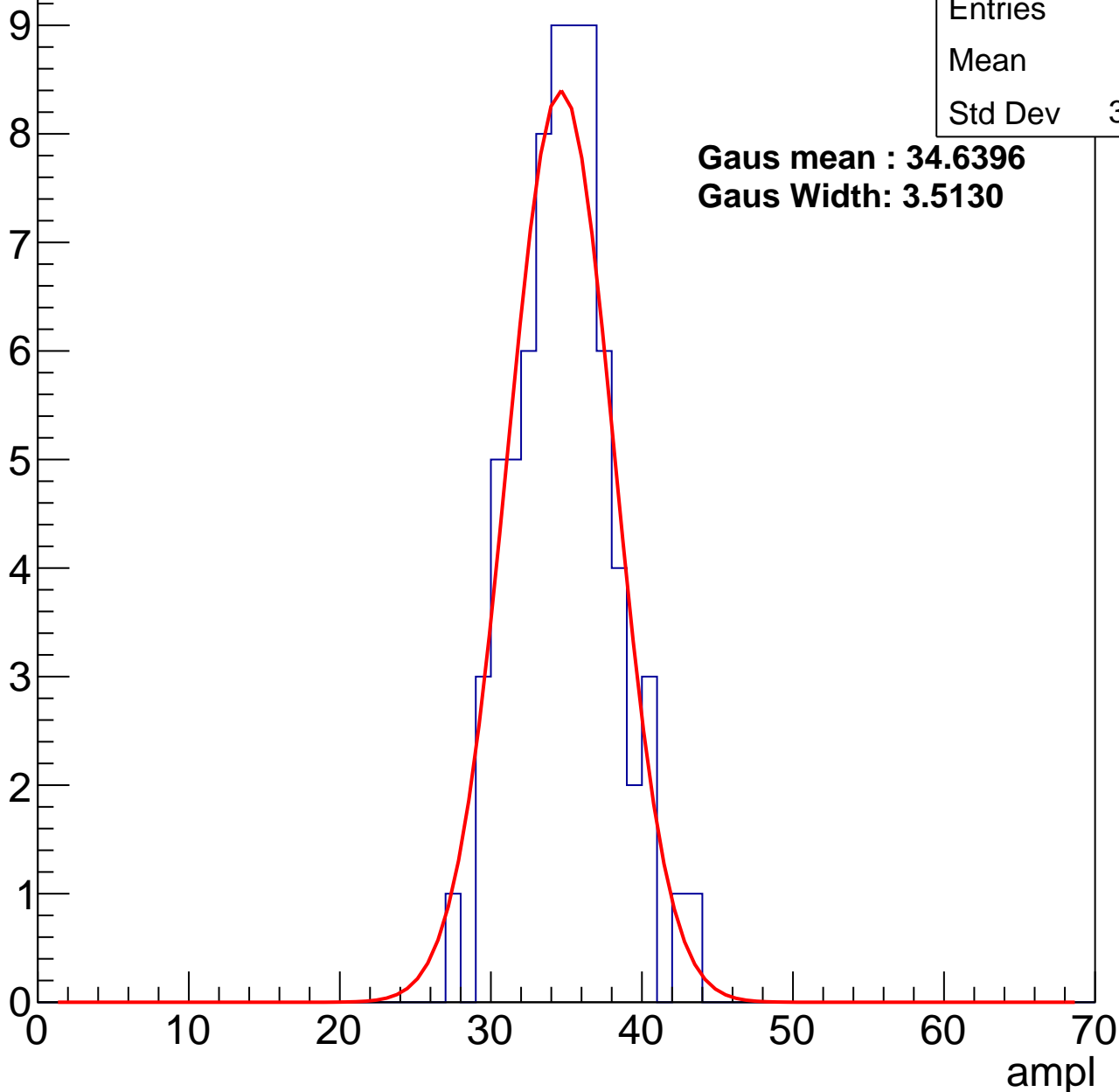
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.4
Std Dev	3.196

**Gaus mean : 34.6396**

**Gaus Width: 3.5130**



# B1L000S, U23-ch100, adc2

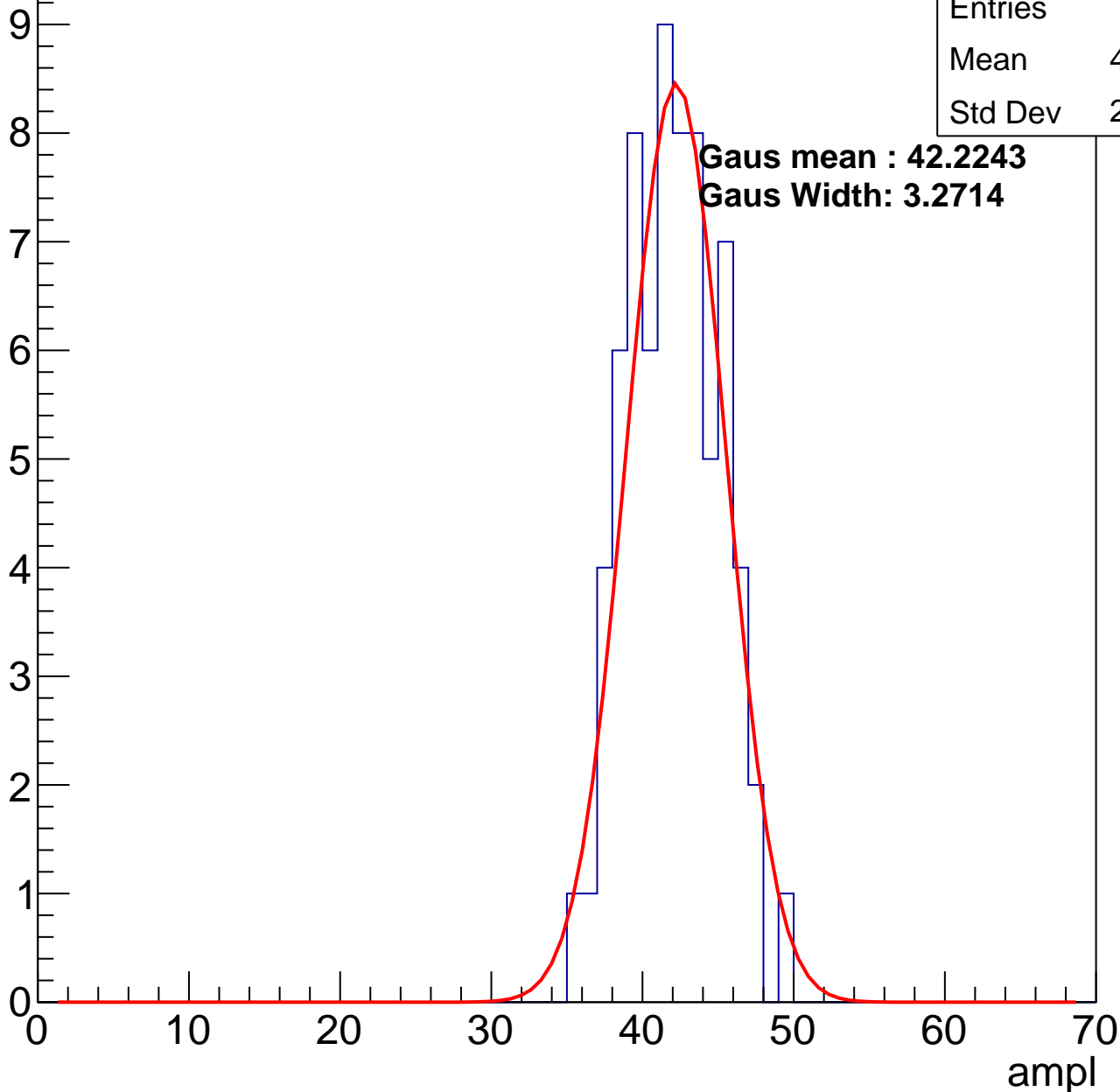
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	41.57
Std Dev	2.993

**Gaus mean : 42.2243**

**Gaus Width: 3.2714**

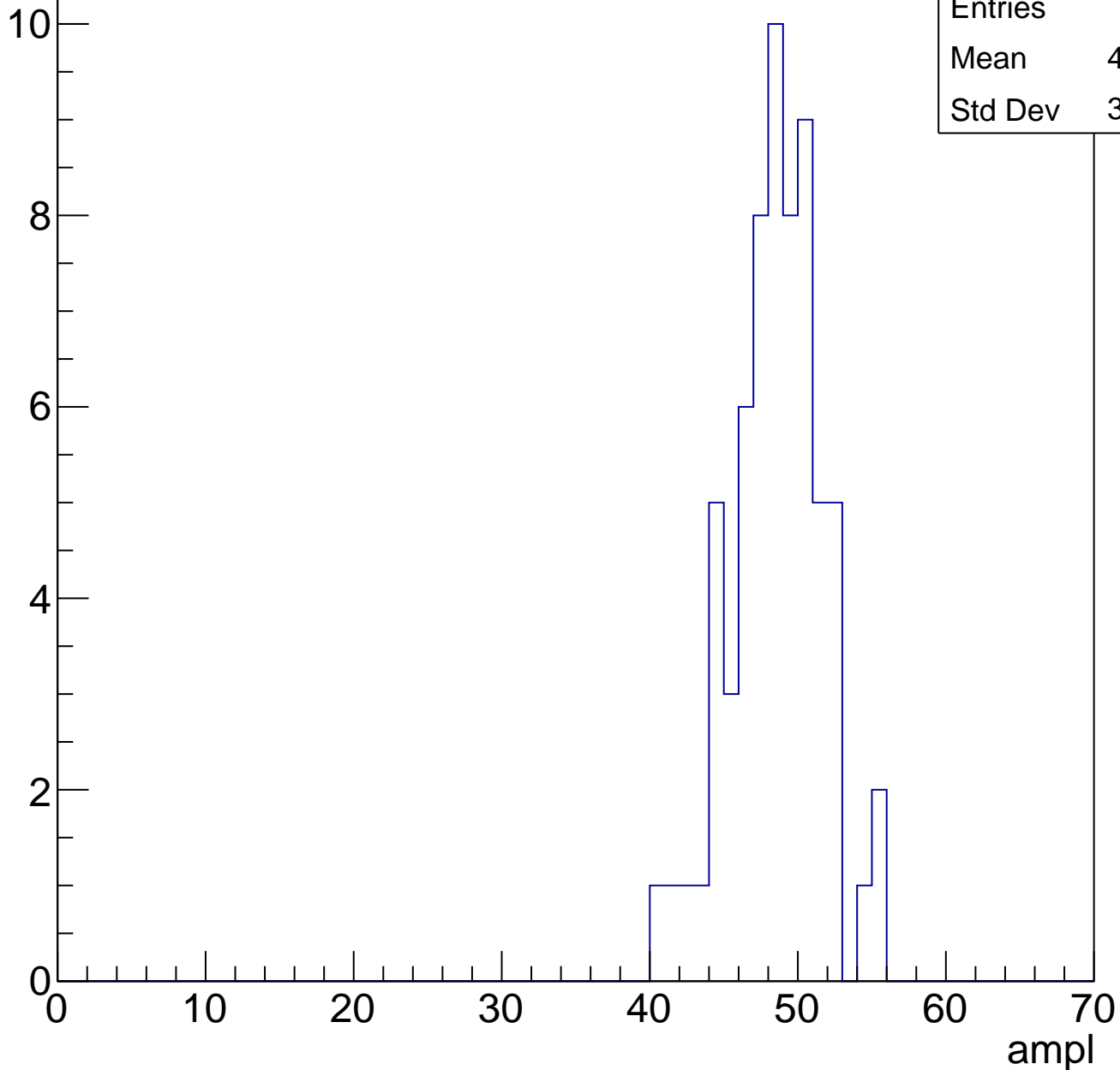


# B1L000S, U23-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	48.09
Std Dev	3.054

Entry

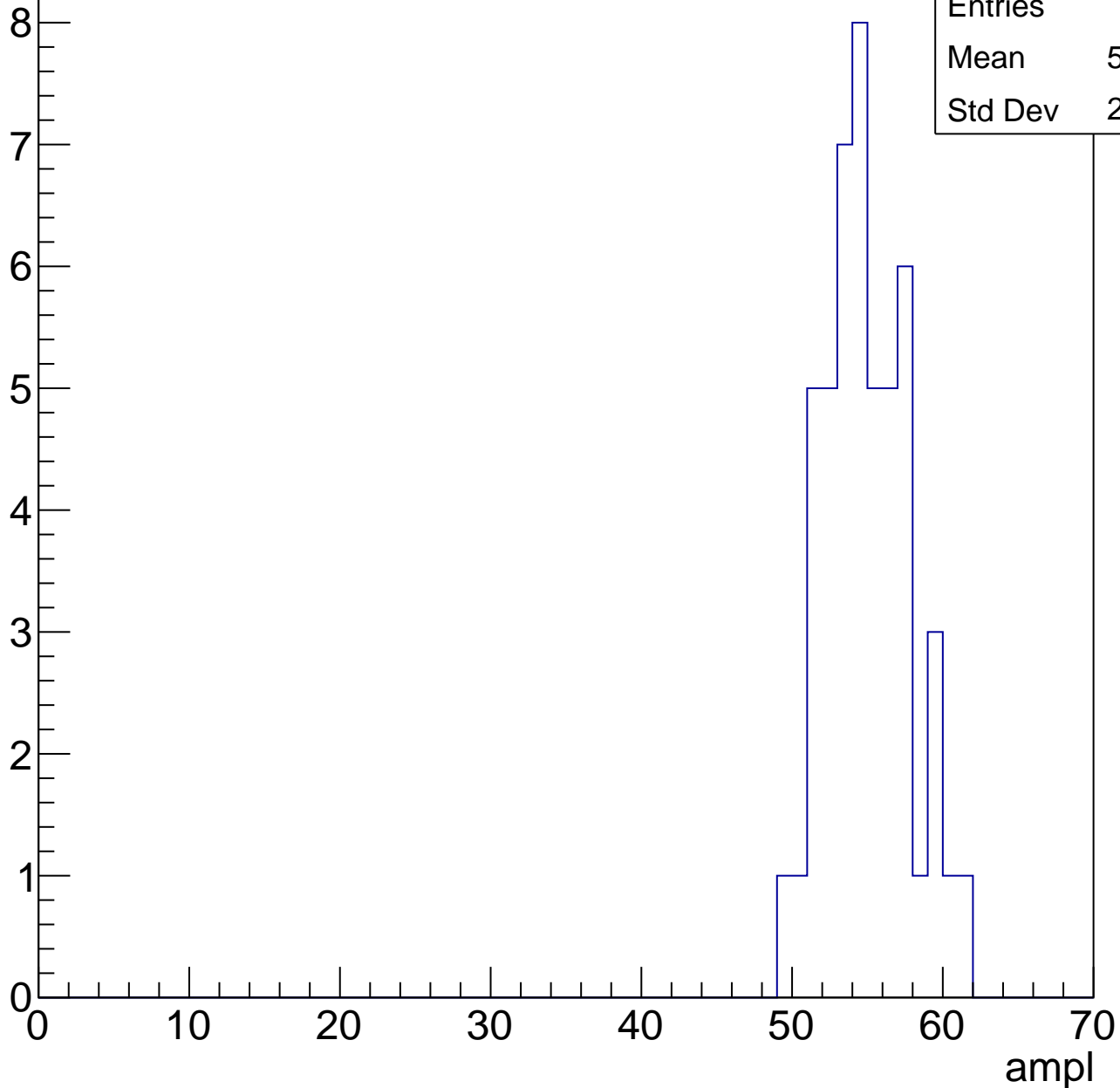


# B1L000S, U23-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

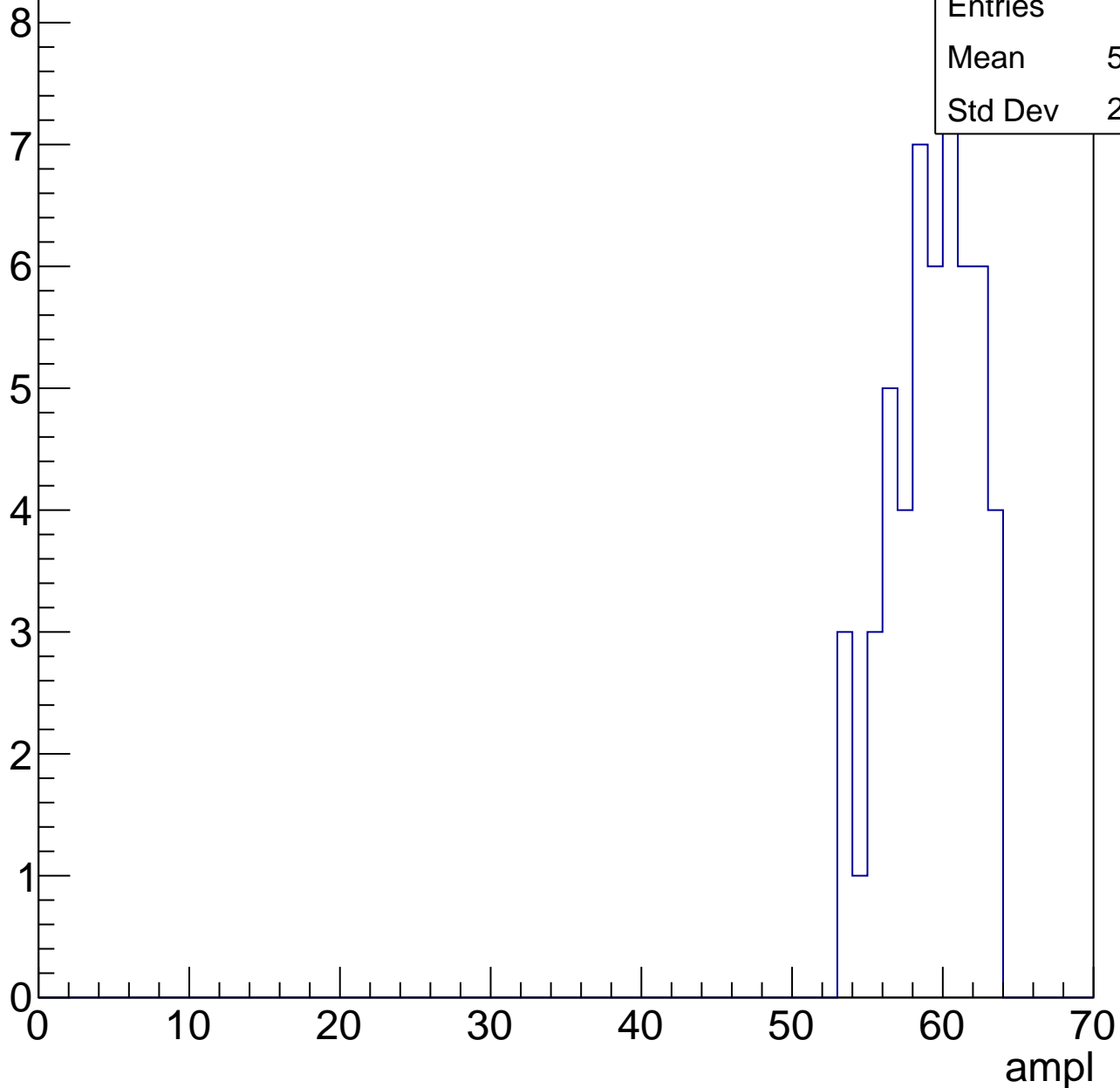
Entries	49
Mean	54.49
Std Dev	2.696



# B1L000S, U23-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

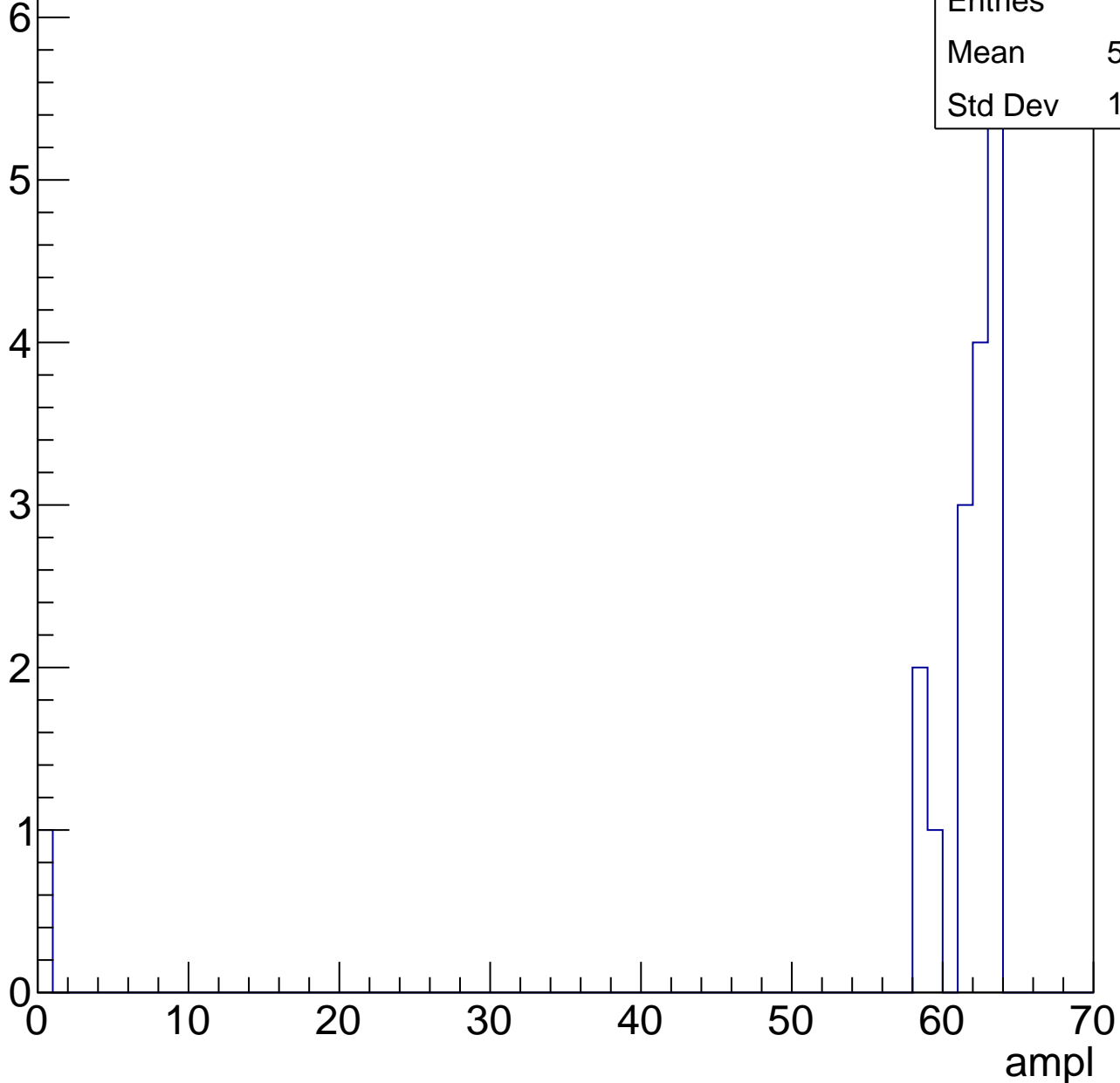


# B1L000S, U23-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.88
Std Dev	14.56

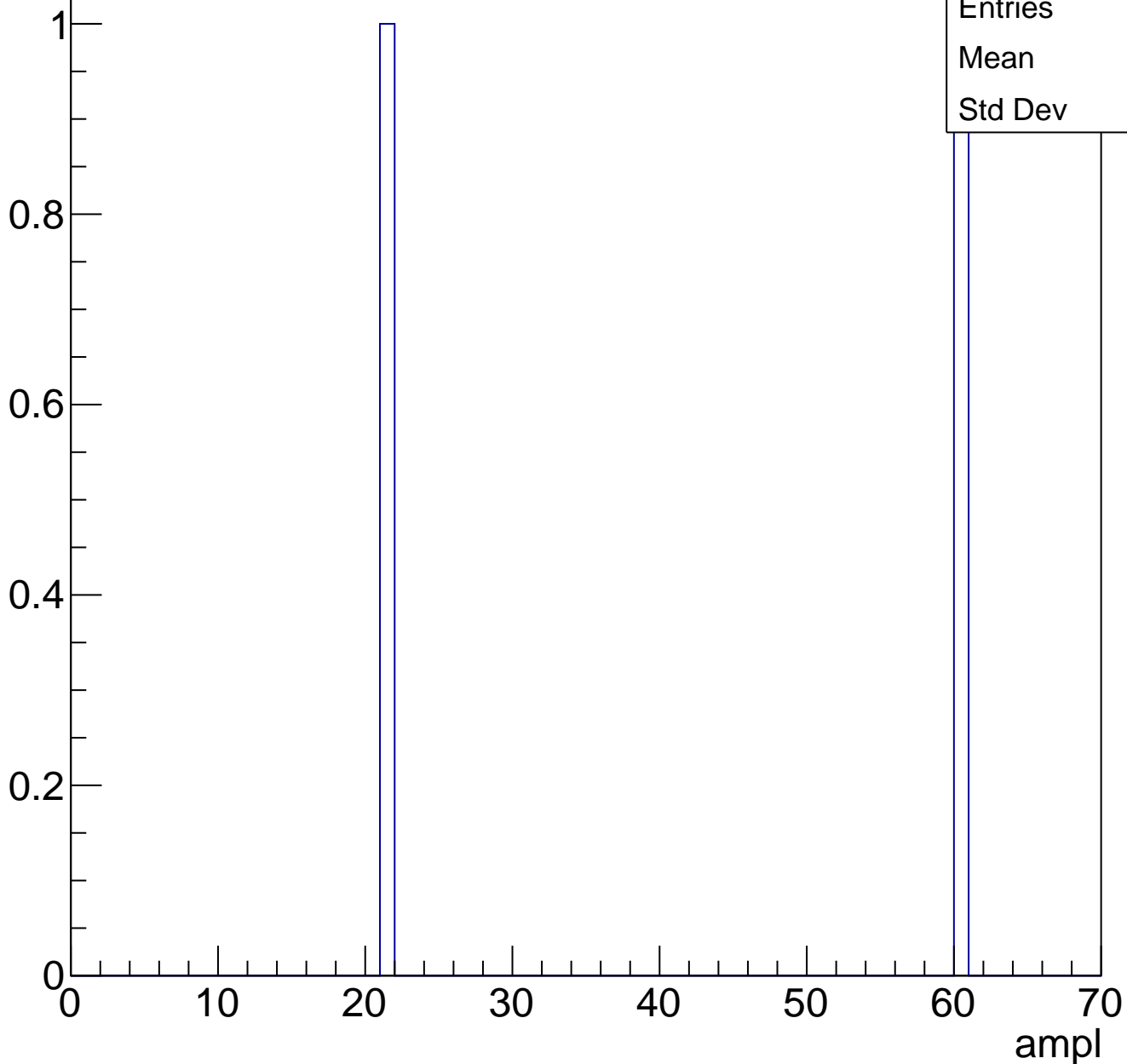




# B1L000S, U23-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch101, adc0

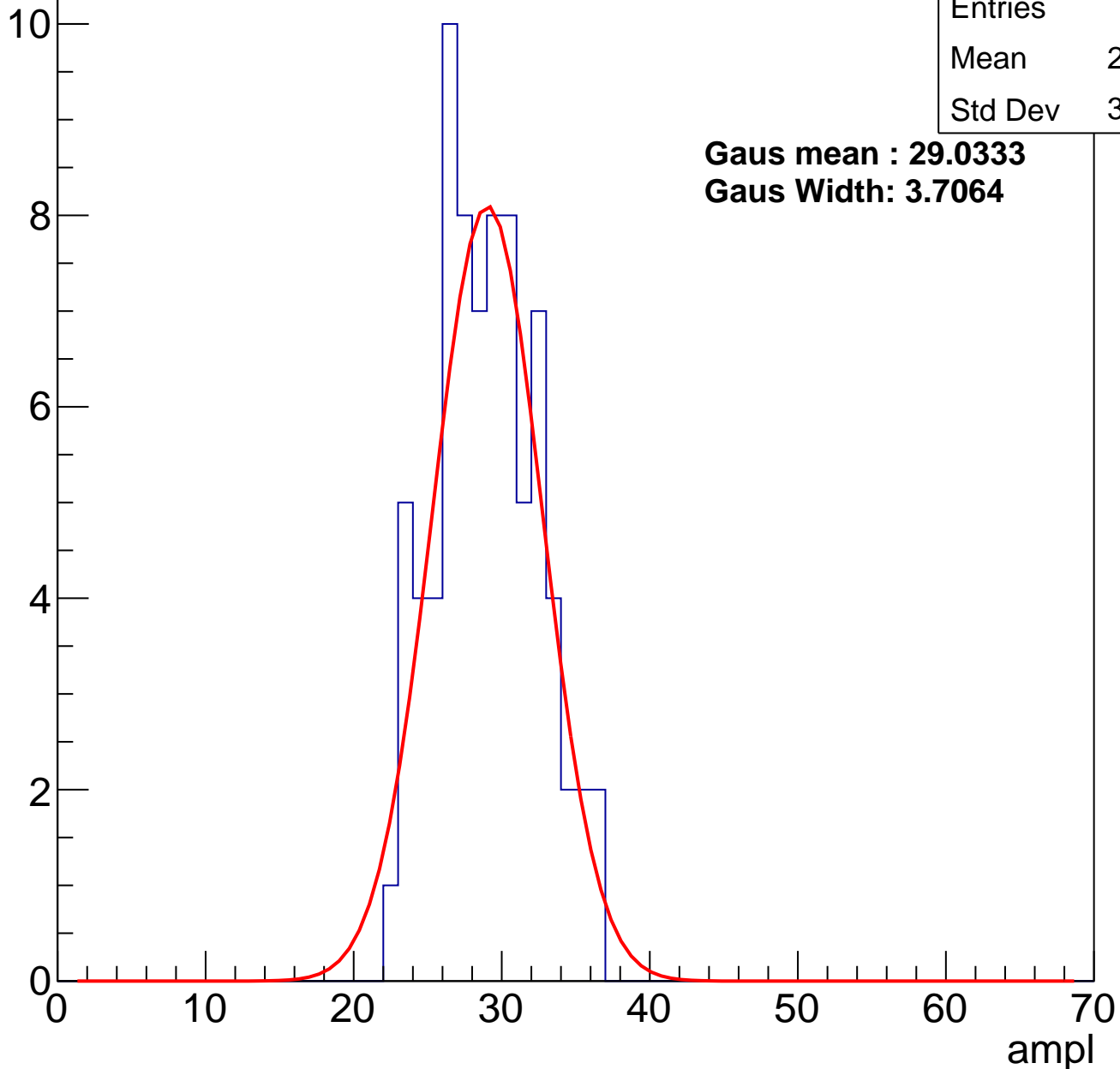
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	28.55
Std Dev	3.383

**Gaus mean : 29.0333**

**Gaus Width: 3.7064**

Entry



# B1L000S, U23-ch101, adc1

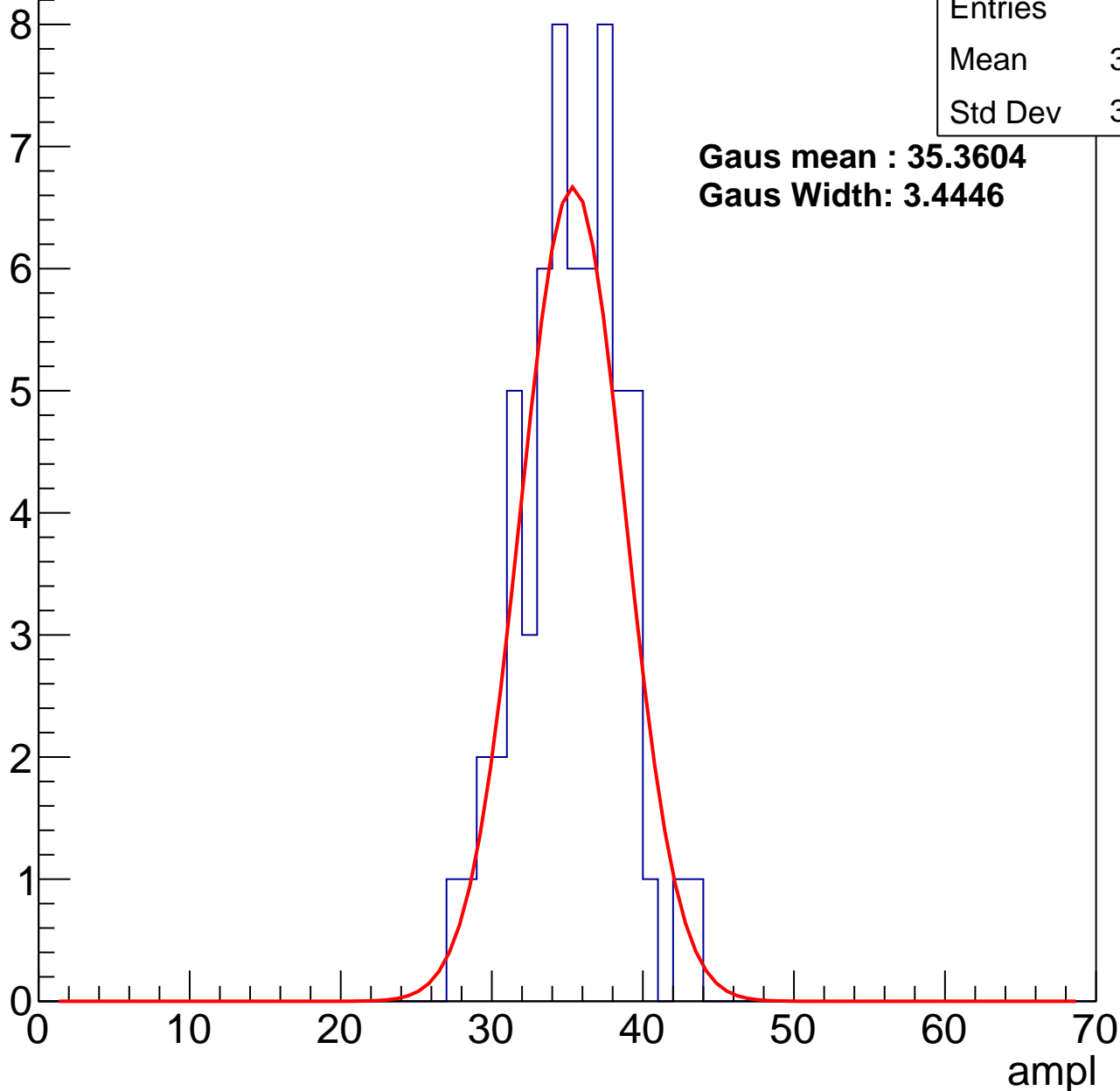
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.85
Std Dev	3.323

**Gaus mean : 35.3604**

**Gaus Width: 3.4446**



# B1L000S, U23-ch101, adc2

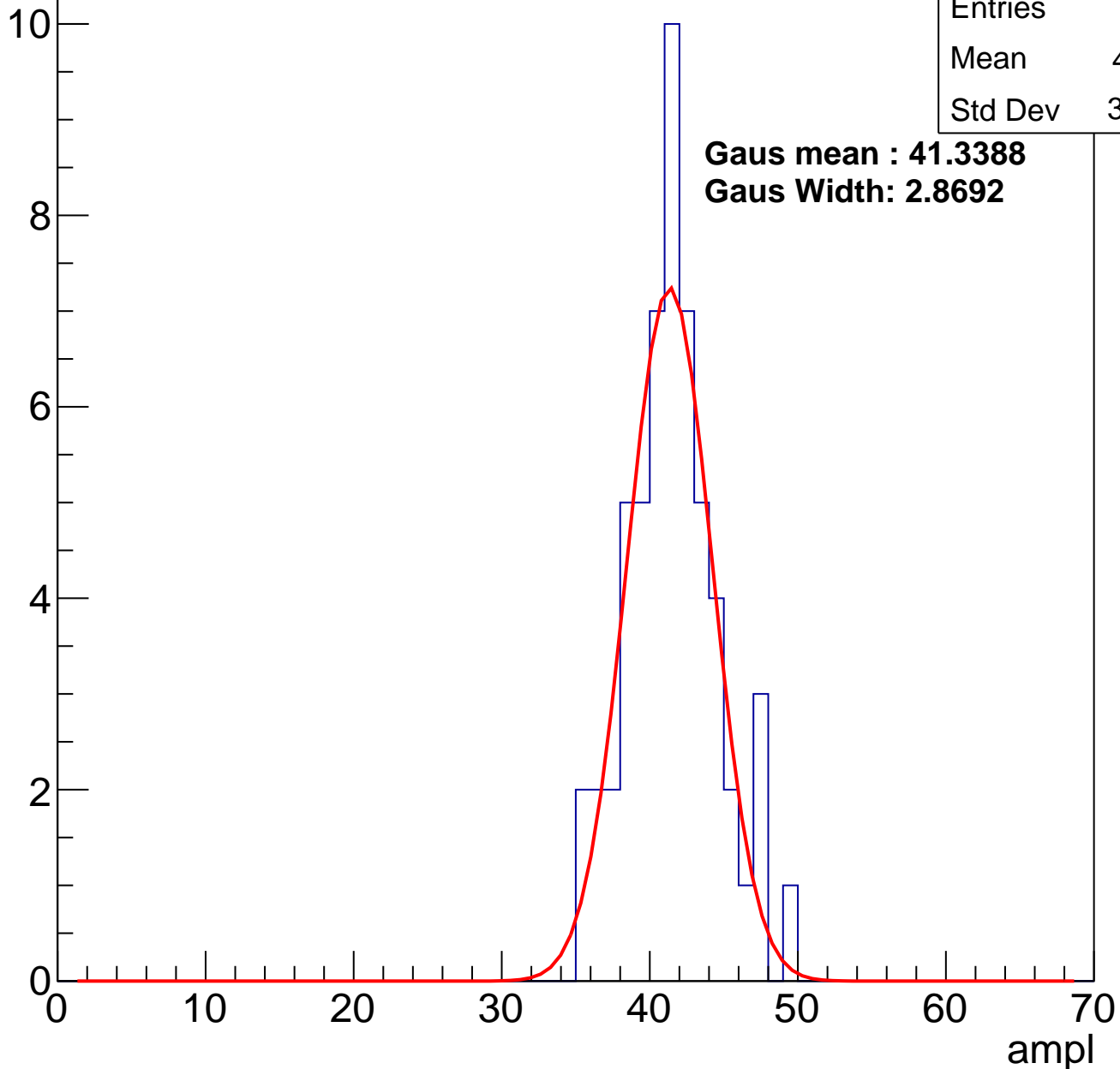
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	41.11
Std Dev	3.039

**Gaus mean : 41.3388**

**Gaus Width: 2.8692**

Entry

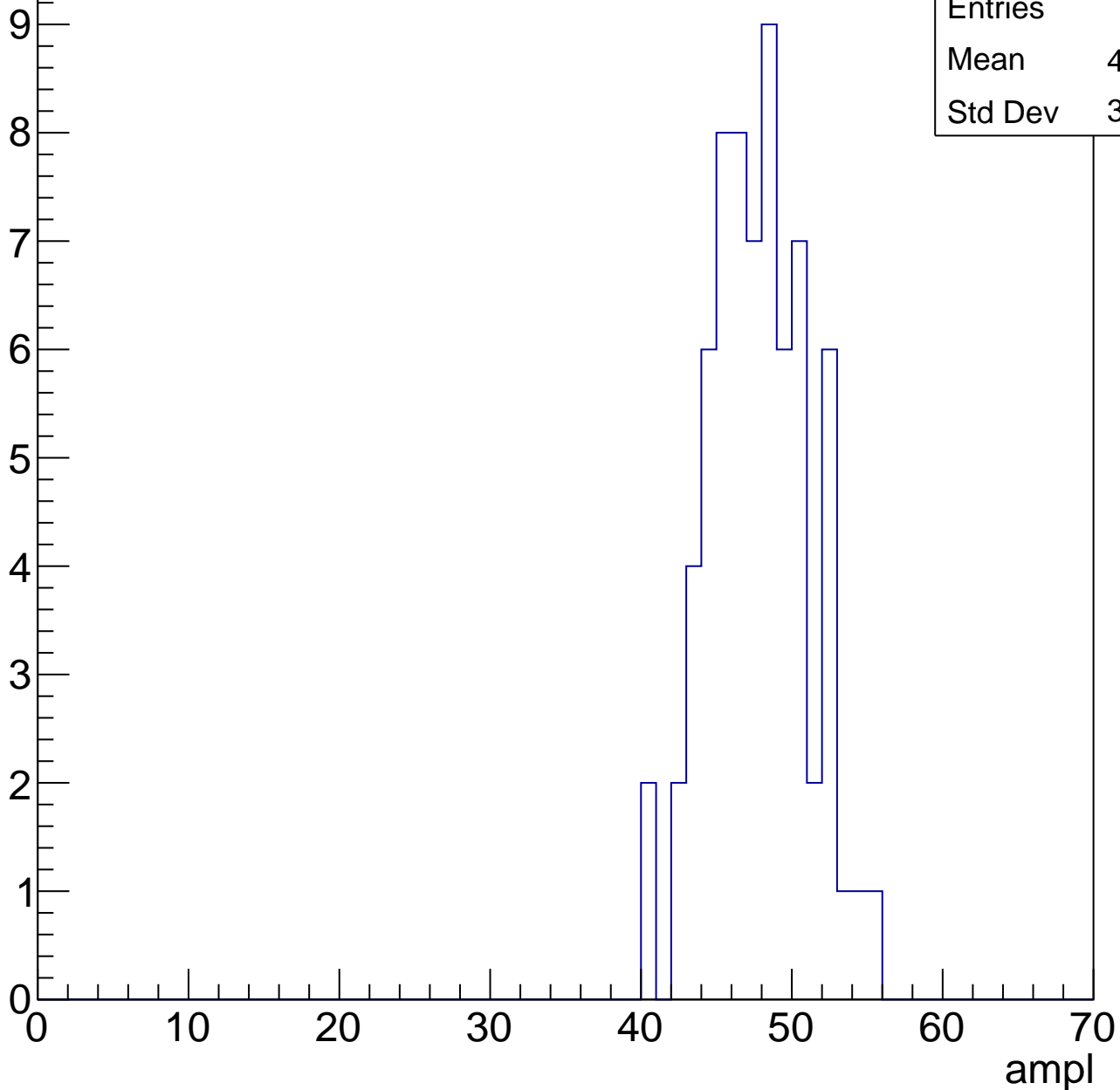


# B1L000S, U23-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.27
Std Dev	3.225

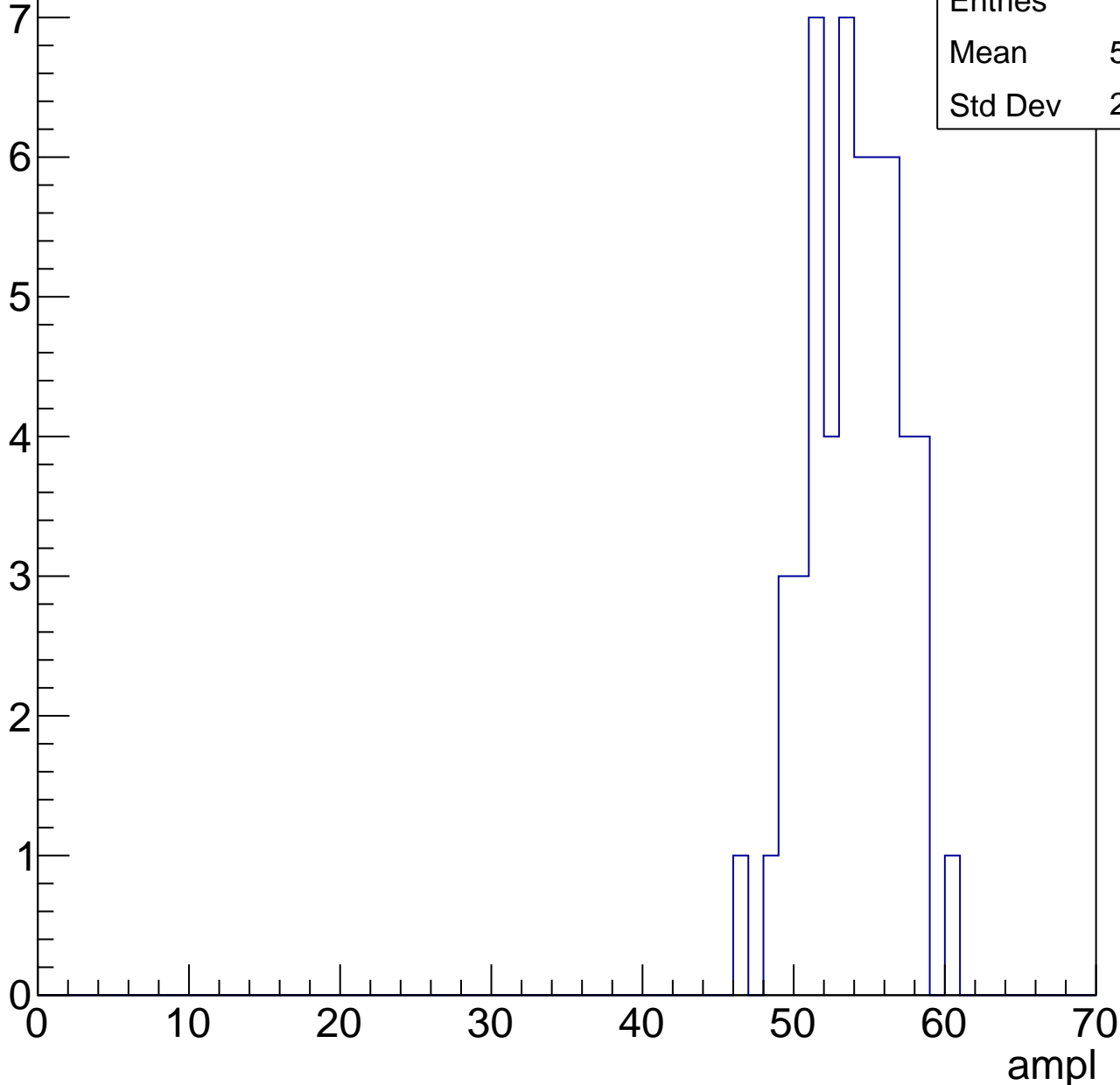


# B1L000S, U23-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	53.53
Std Dev	2.956

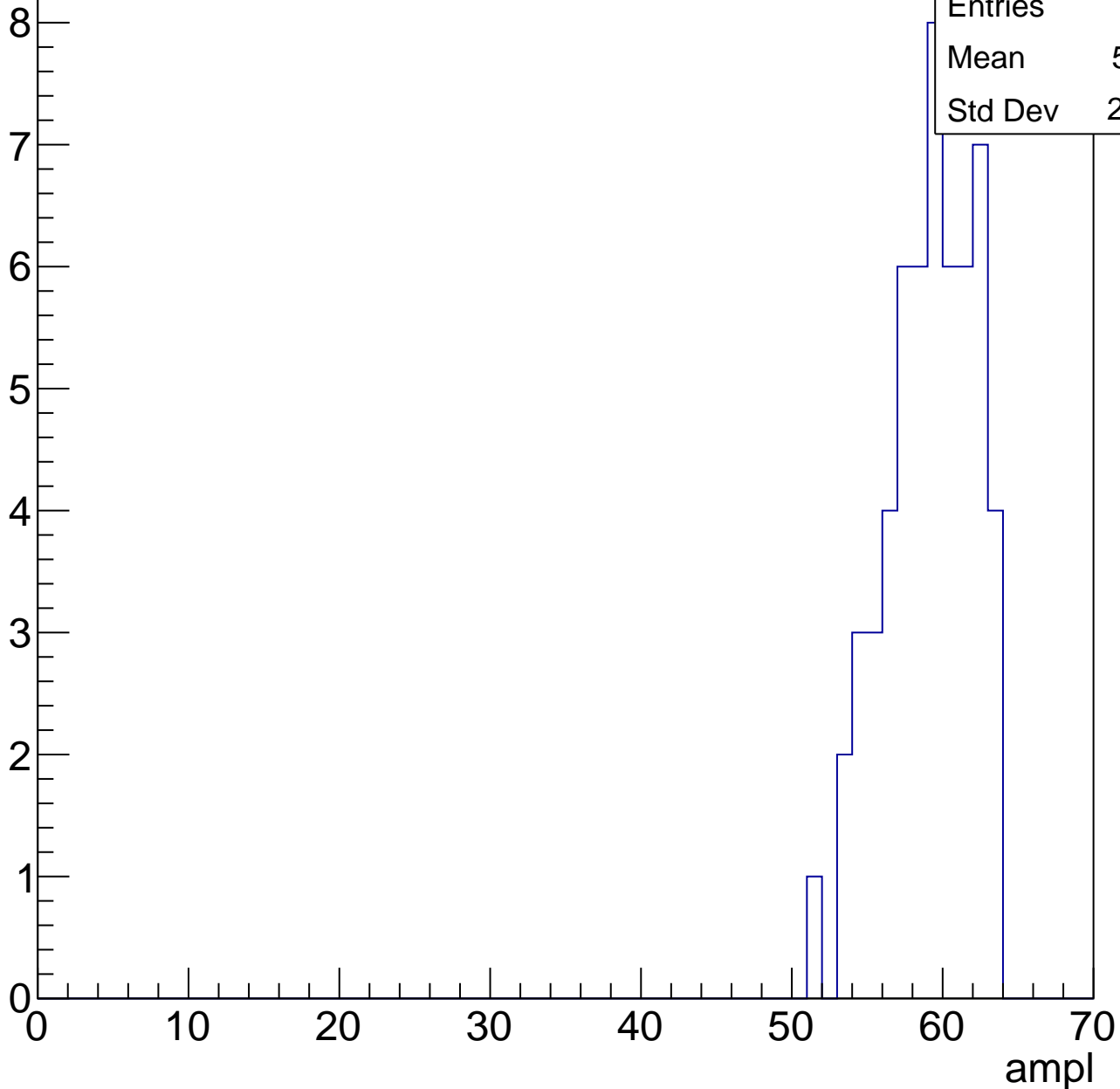


# B1L000S, U23-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	58.61
Std Dev	2.907

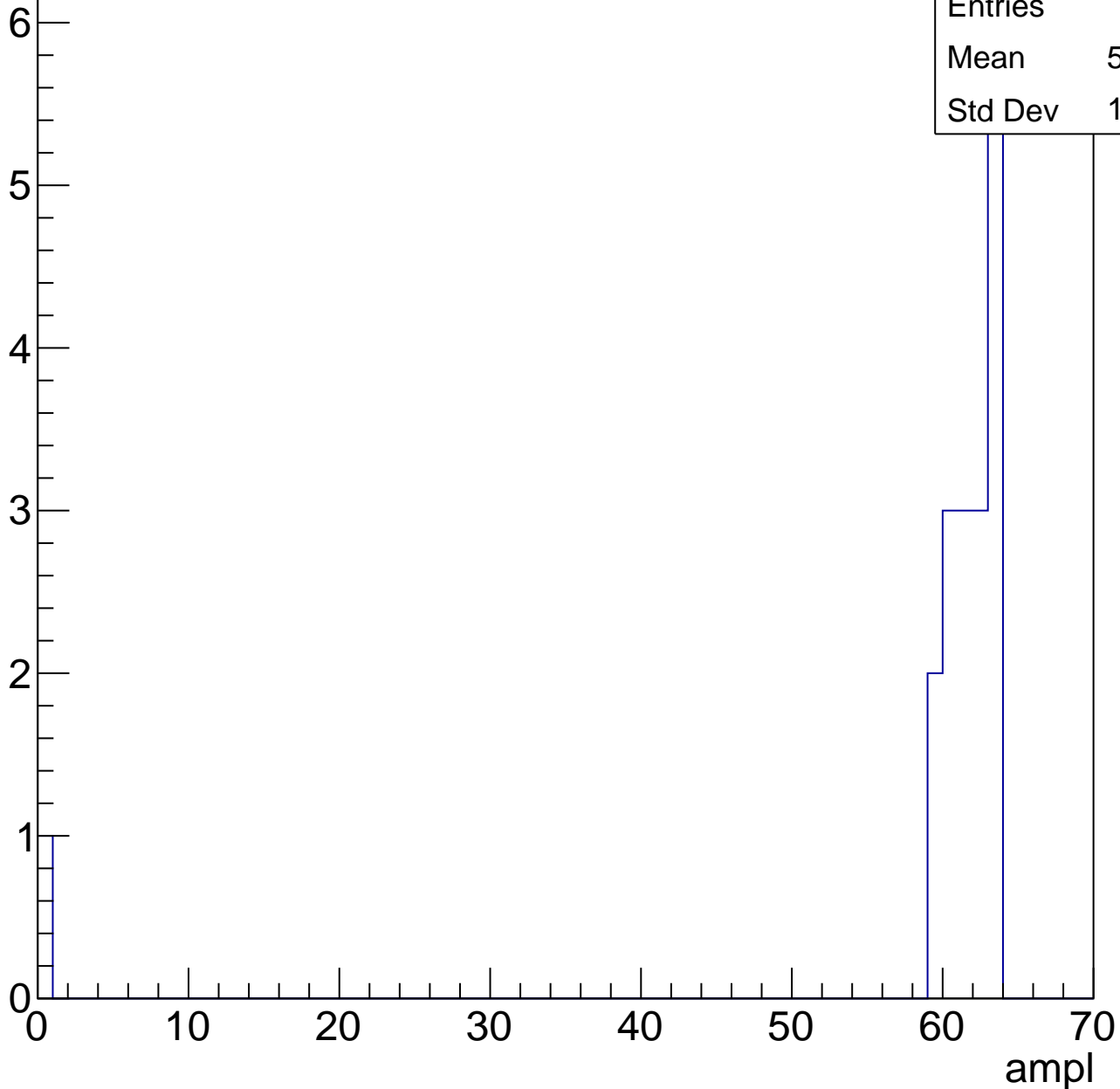


# B1L000S, U23-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.06
Std Dev	14.15





# B1L000S, U23-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch102, adc0

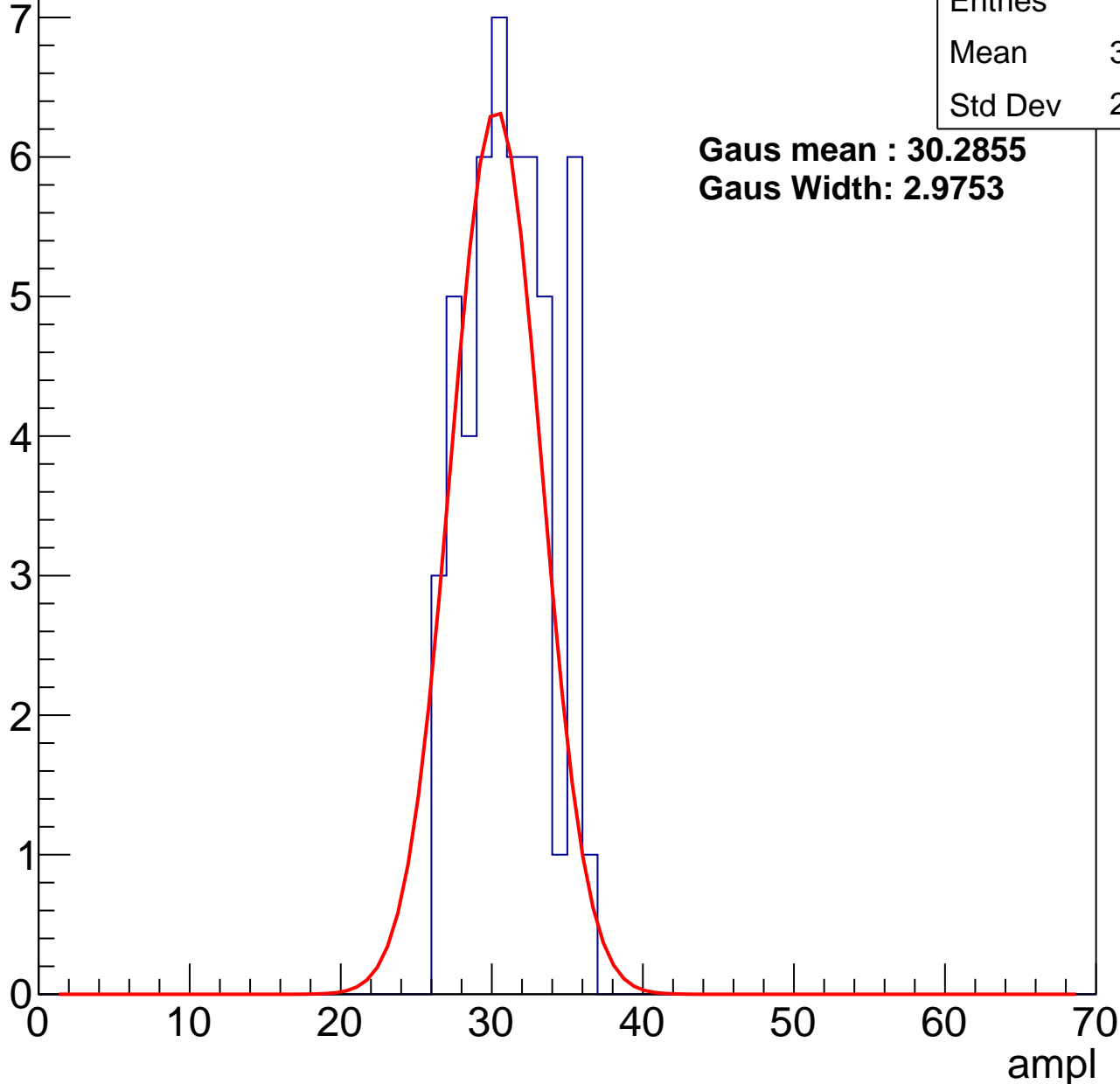
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	30.64
Std Dev	2.726

**Gaus mean : 30.2855**

**Gaus Width: 2.9753**



# B1L000S, U23-ch102, adc1

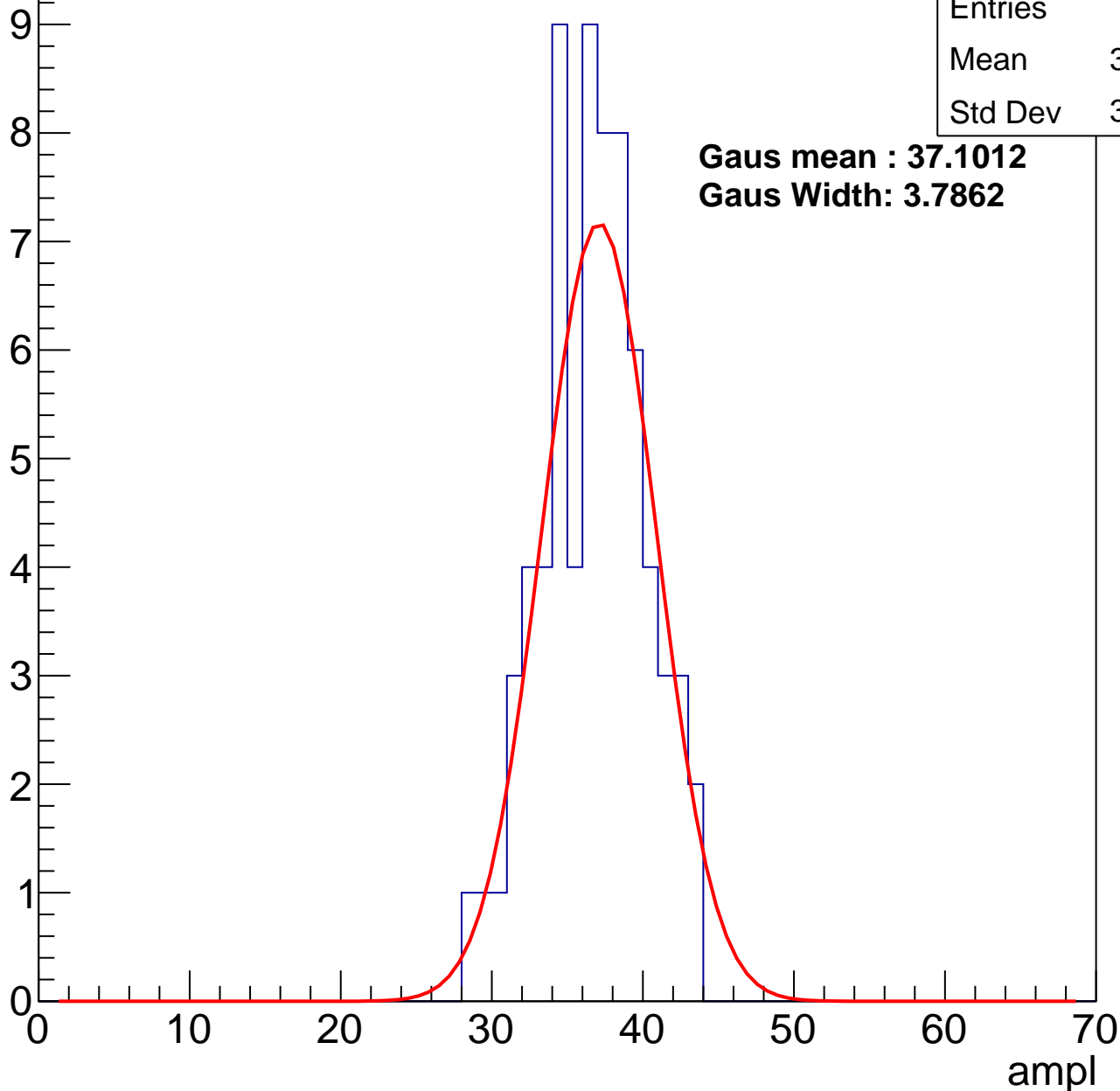
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.27
Std Dev	3.376

**Gaus mean : 37.1012**

**Gaus Width: 3.7862**



# B1L000S, U23-ch102, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 42

Mean 41.88

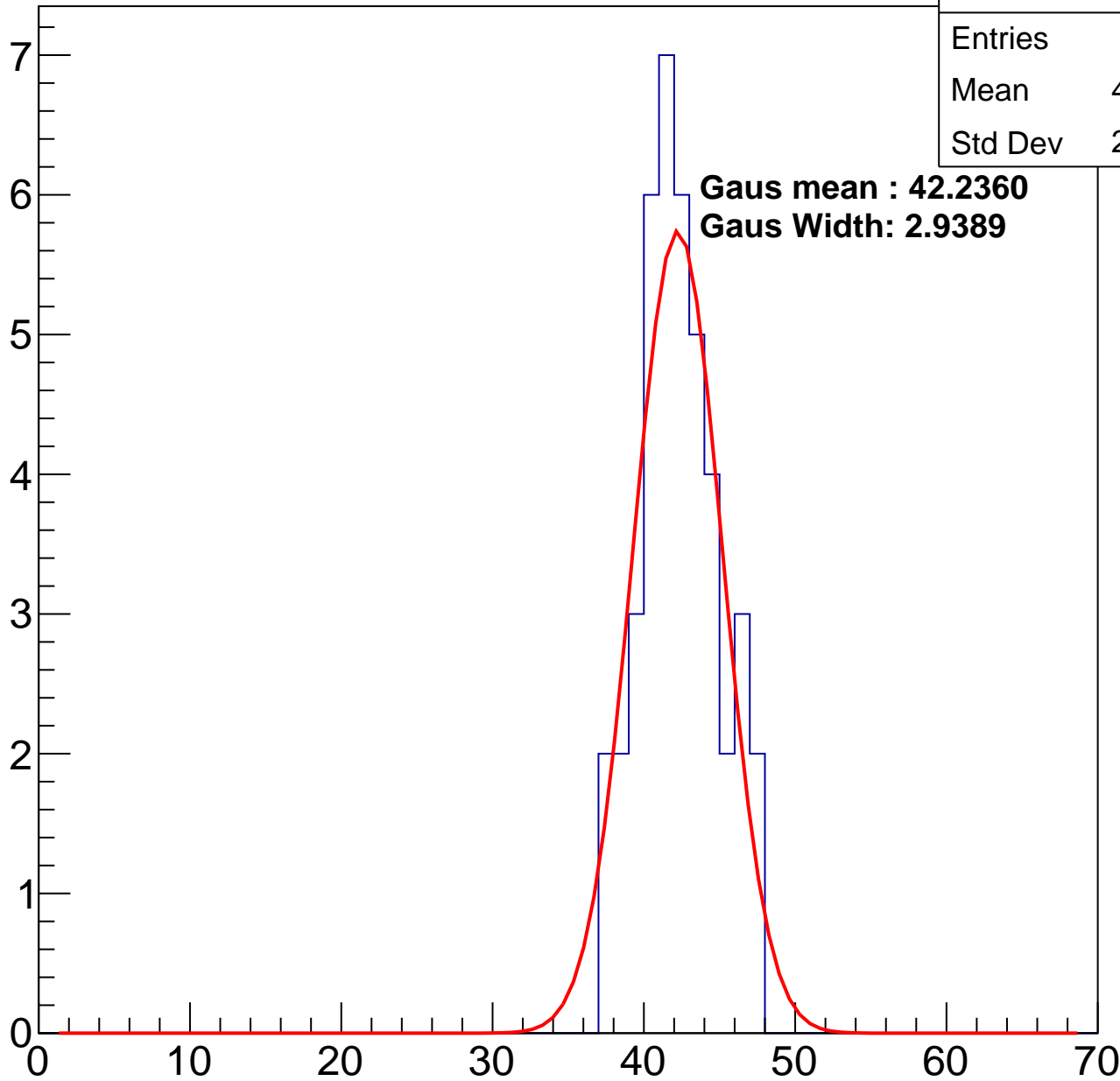
Std Dev 2.565

**Gaus mean : 42.2360**

**Gaus Width: 2.9389**

0 10 20 30 40 50 60 70

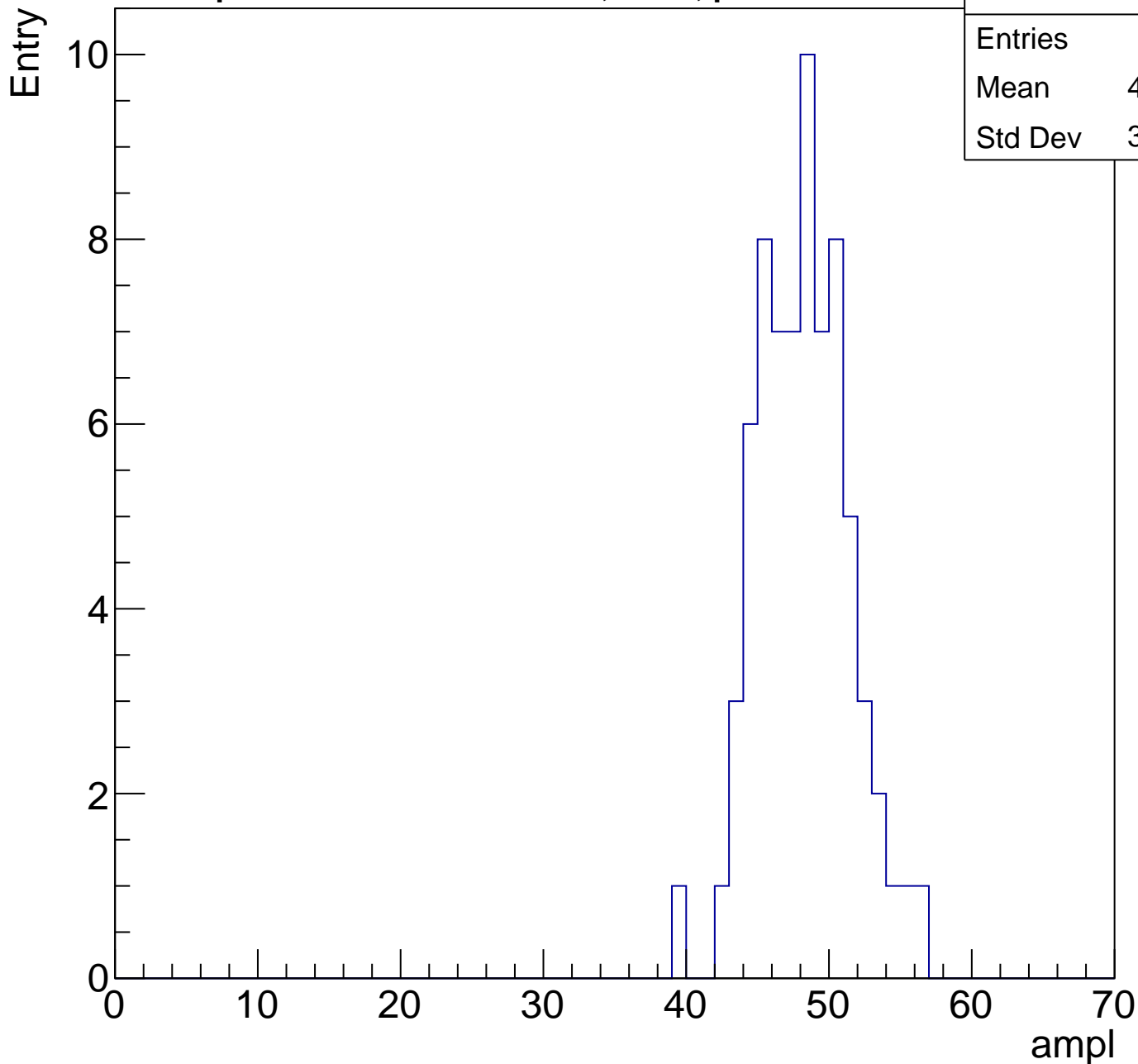
ampl



# B1L000S, U23-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	47.75
Std Dev	3.188

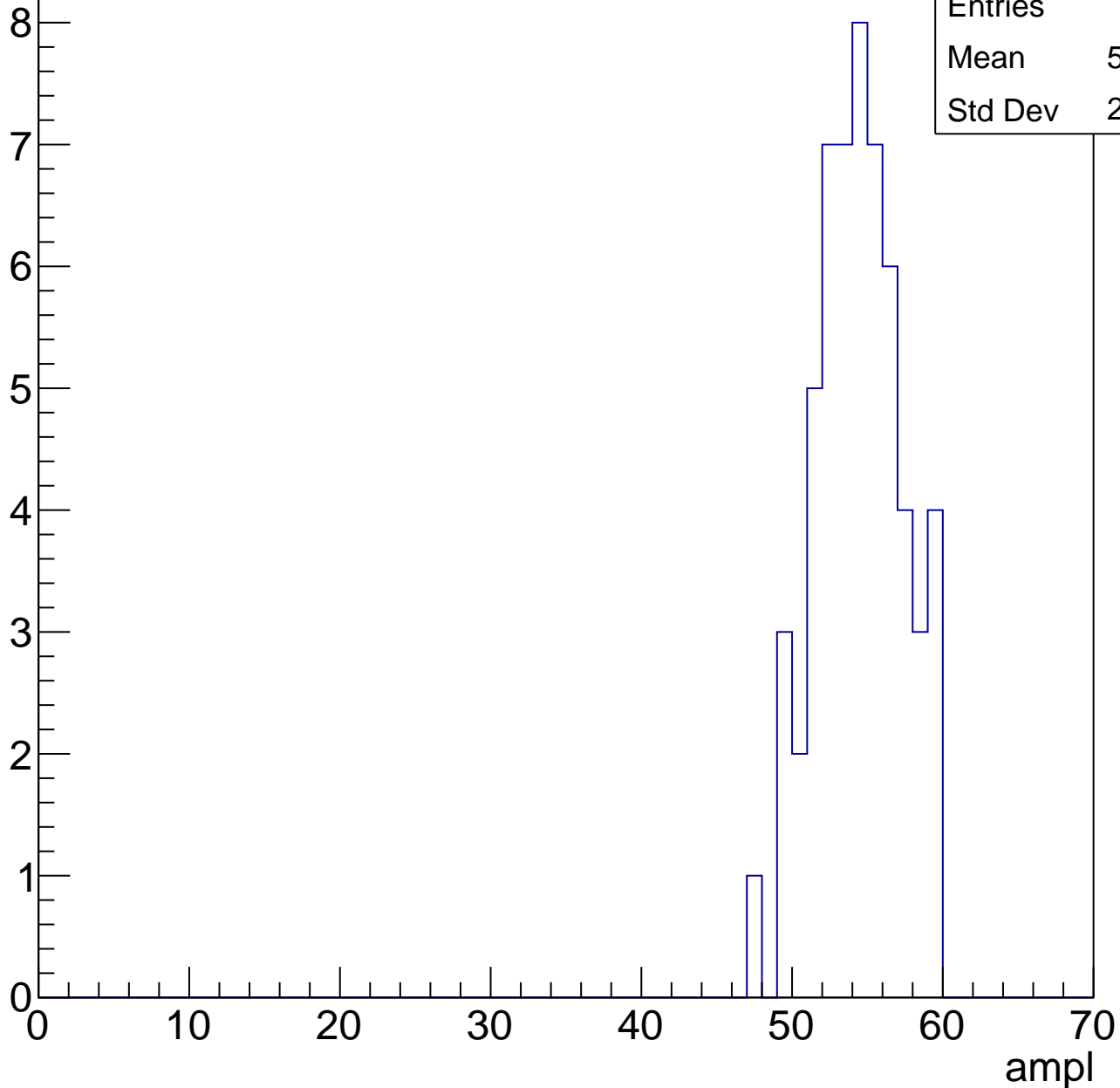


# B1L000S, U23-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.95
Std Dev	2.812

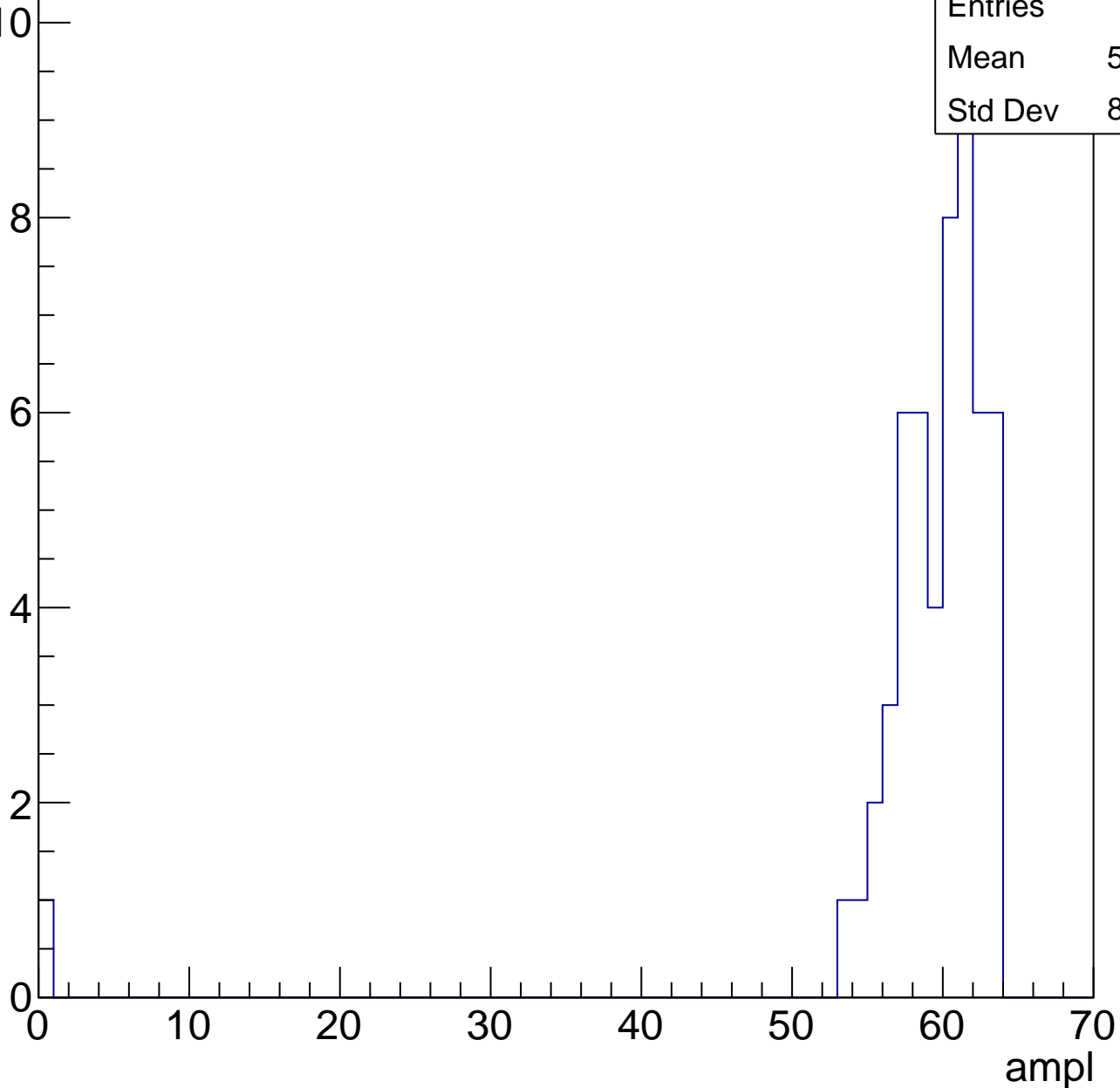


# B1L000S, U23-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

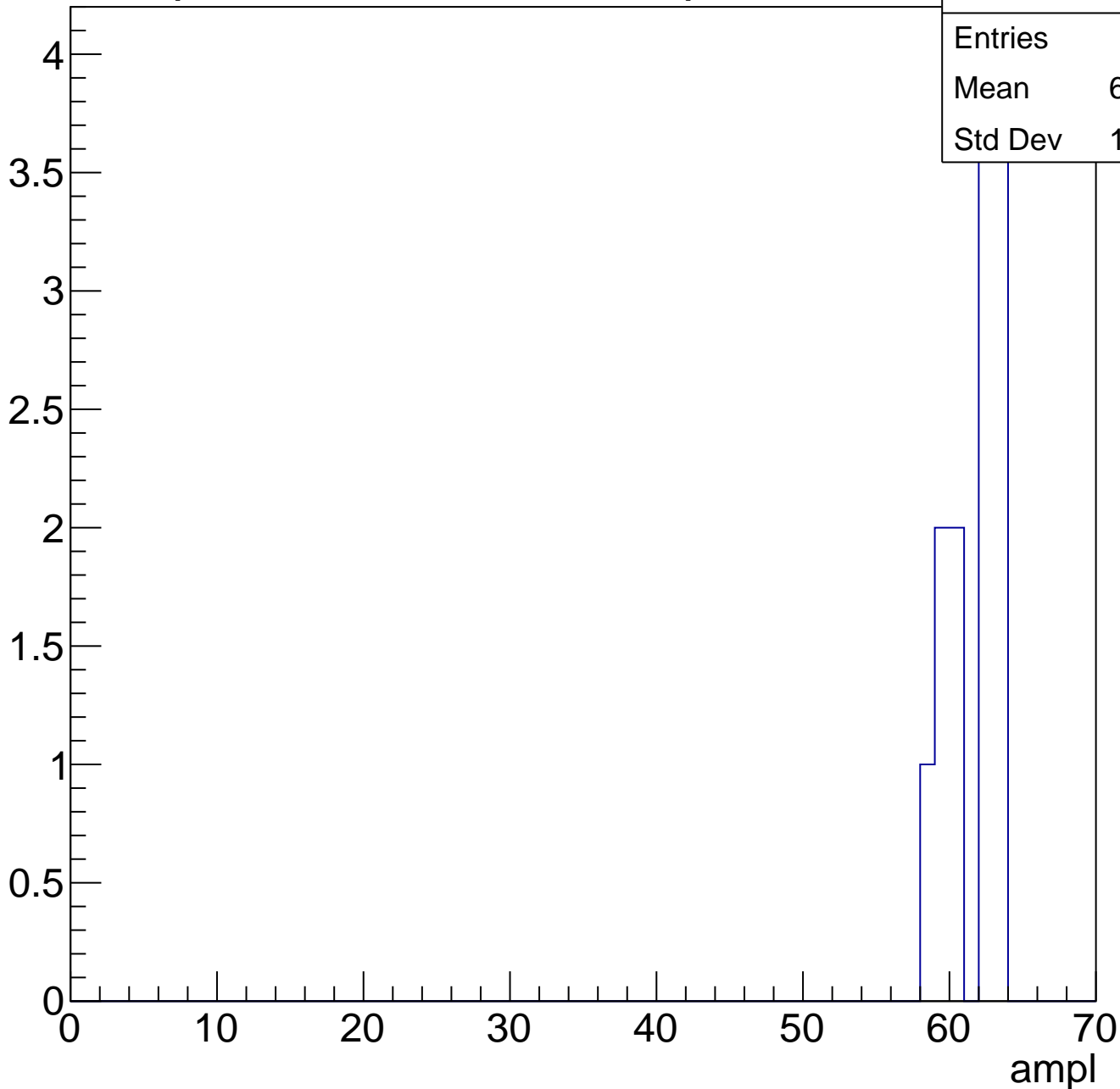
Entries	54
Mean	58.35
Std Dev	8.396



# B1L000S, U23-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

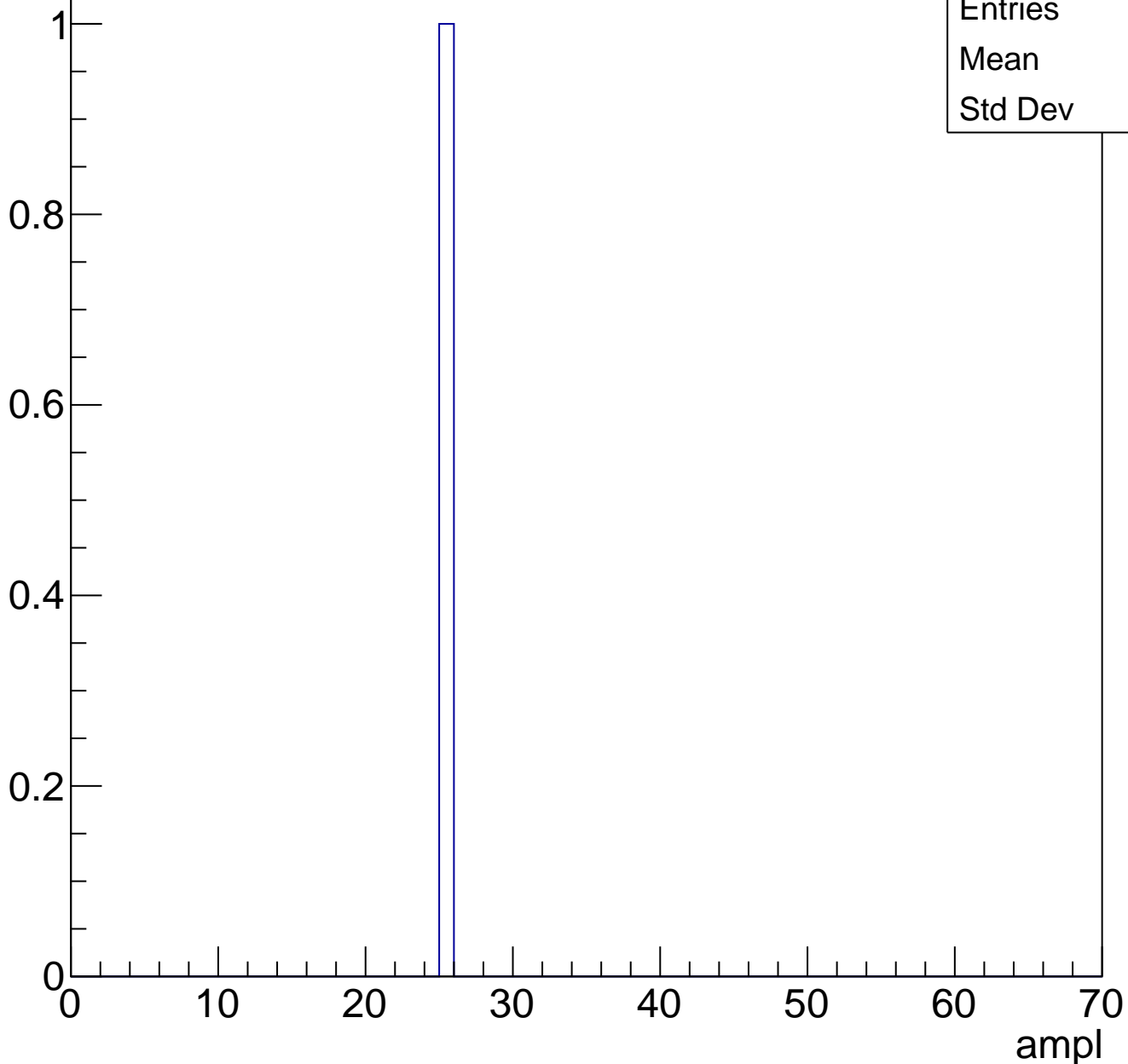




# B1L000S, U23-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	25
Std Dev	0

# B1L000S, U23-ch103, adc0

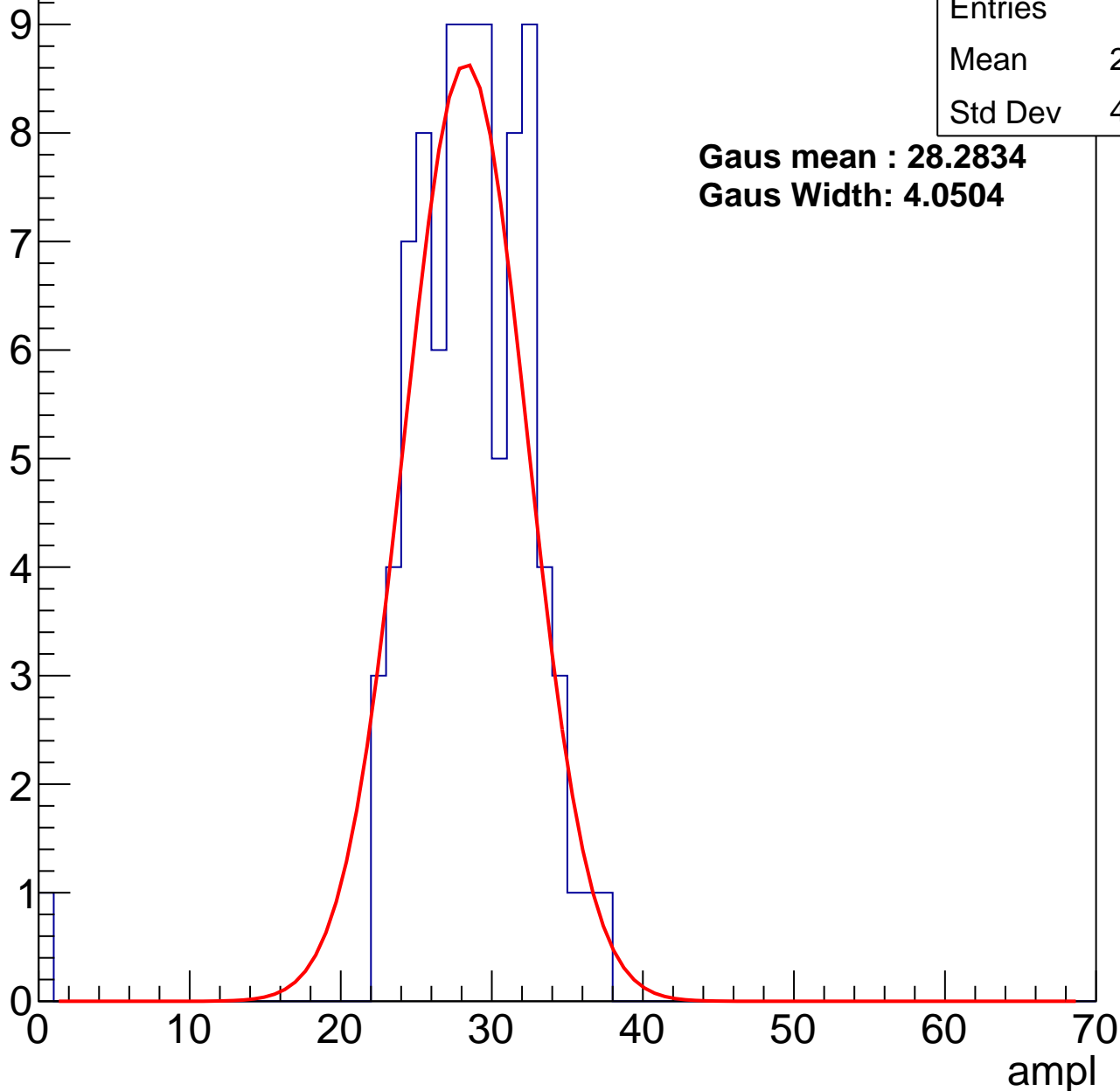
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	88
Mean	28.02
Std Dev	4.592

**Gaus mean : 28.2834**

**Gaus Width: 4.0504**



# B1L000S, U23-ch103, adc1

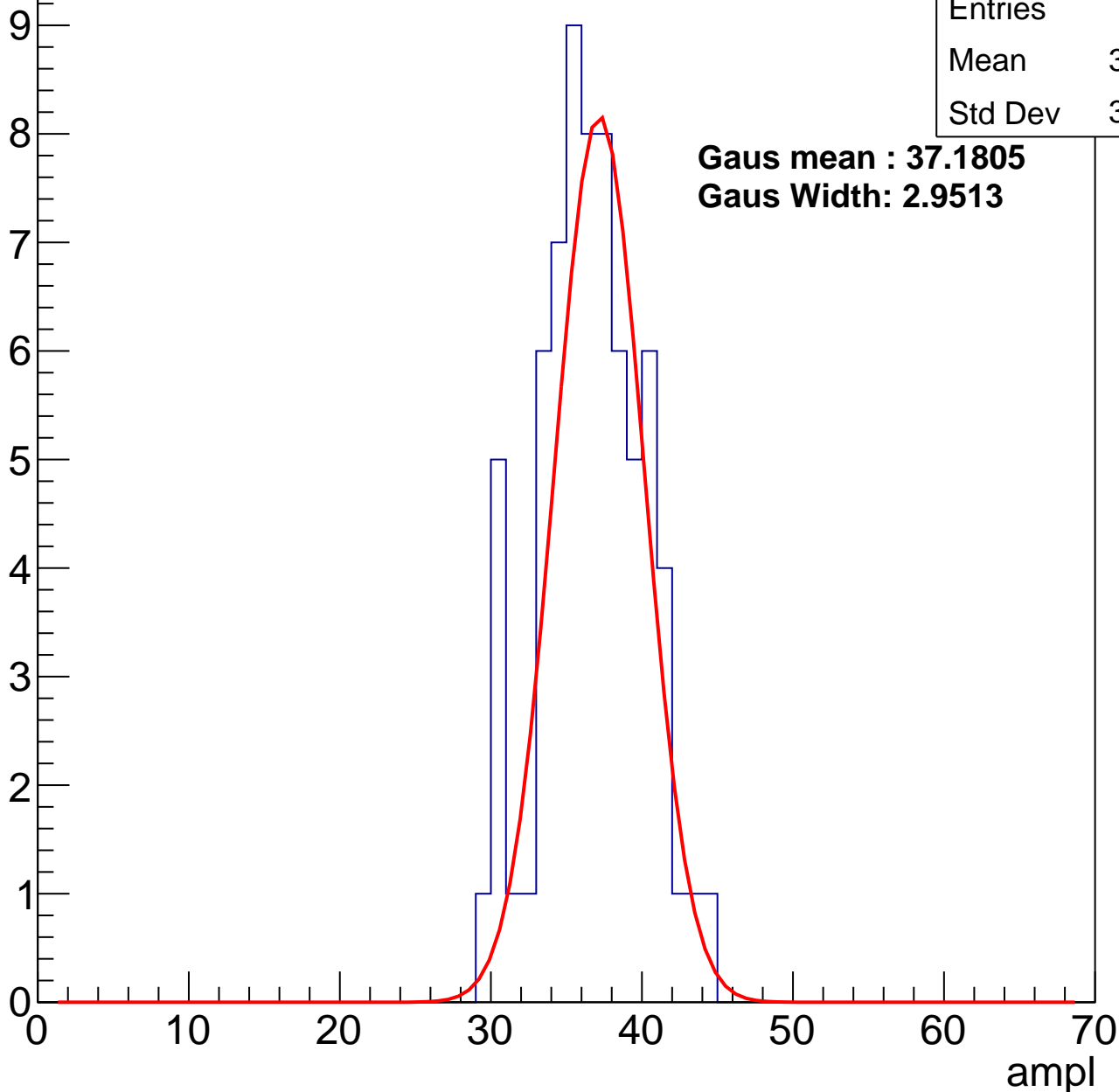
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.19
Std Dev	3.339

**Gaus mean : 37.1805**

**Gaus Width: 2.9513**



# B1L000S, U23-ch103, adc2

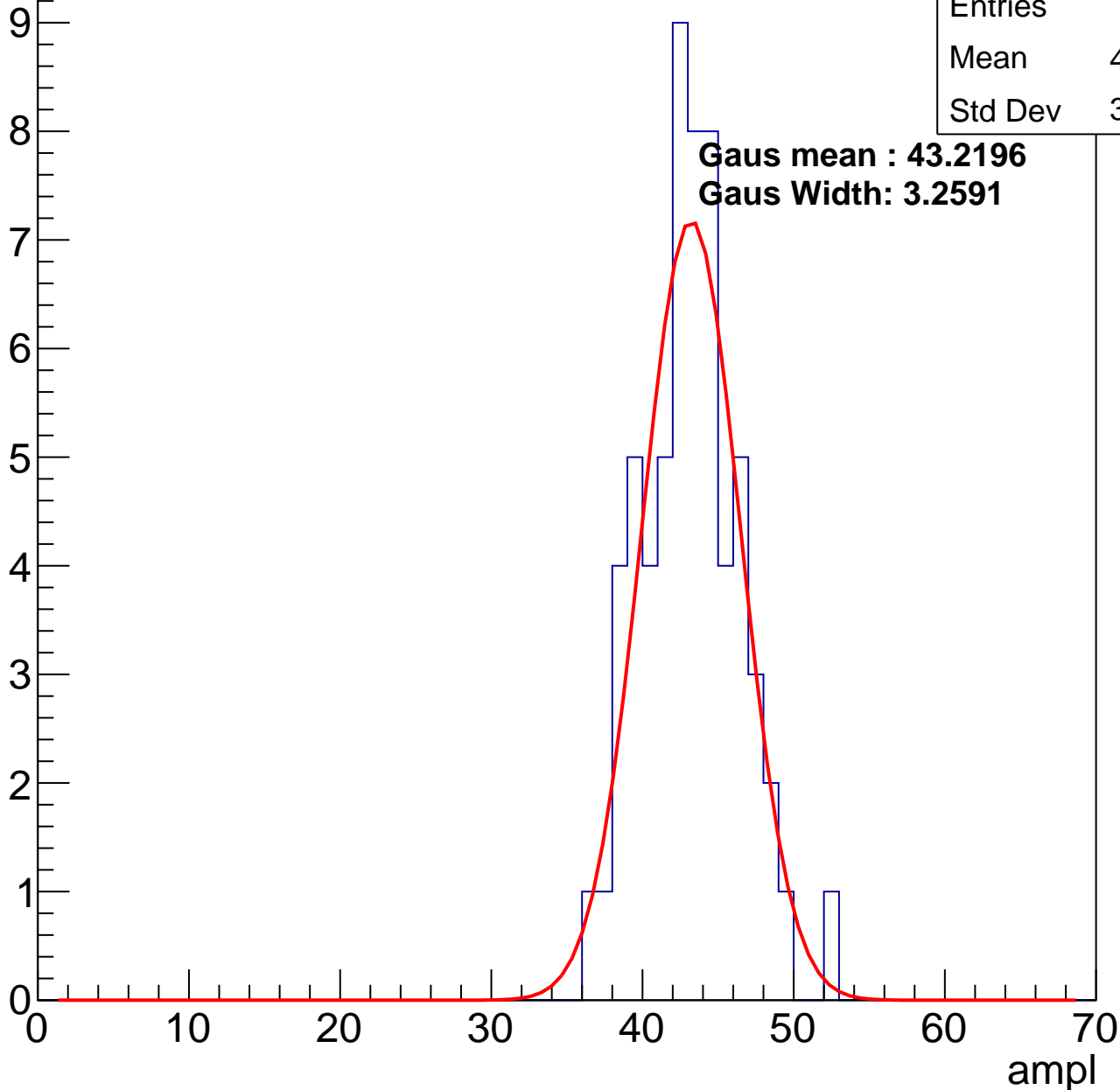
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.74
Std Dev	3.162

**Gaus mean : 43.2196**

**Gaus Width: 3.2591**

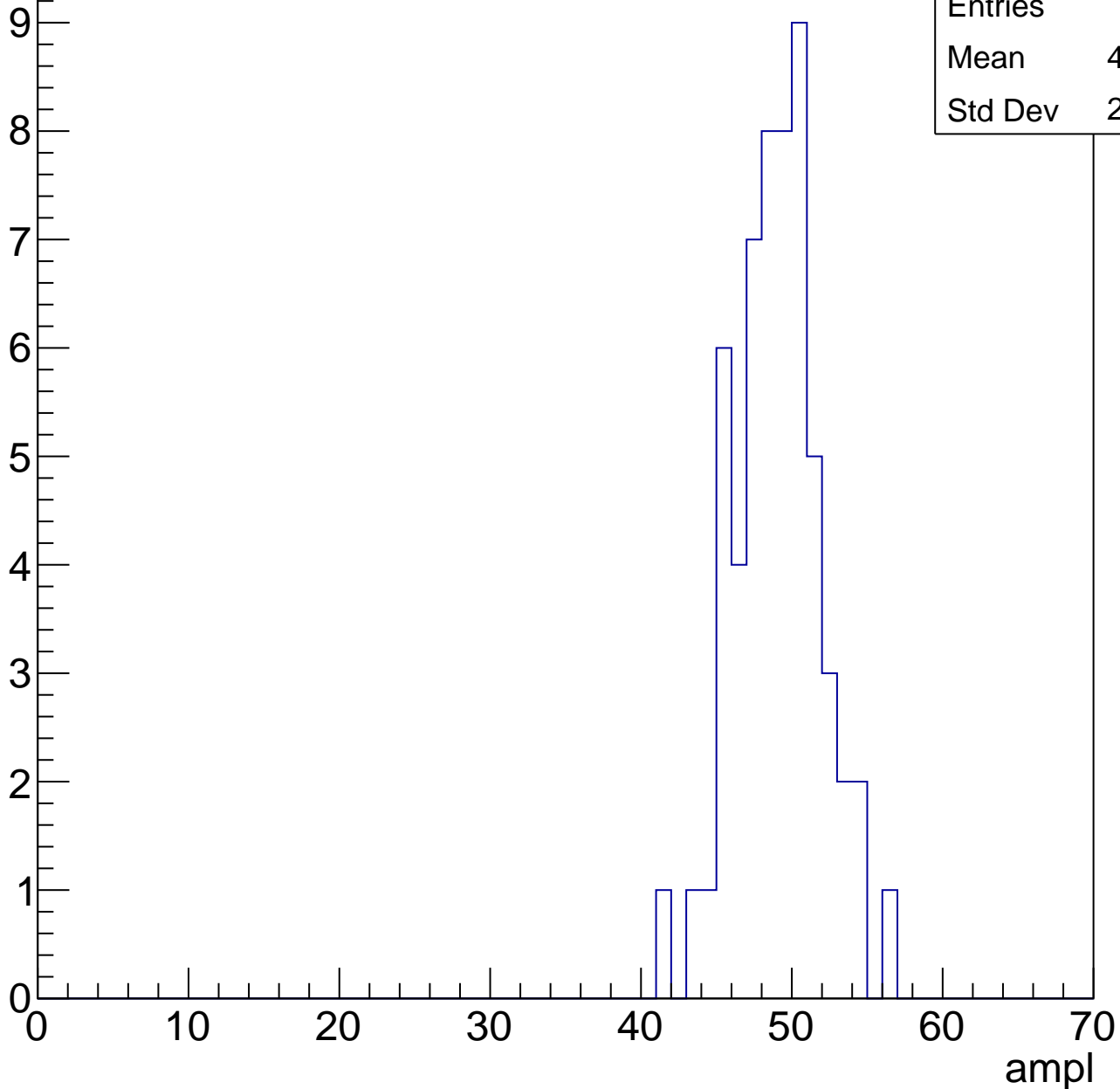


# B1L000S, U23-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.59
Std Dev	2.847

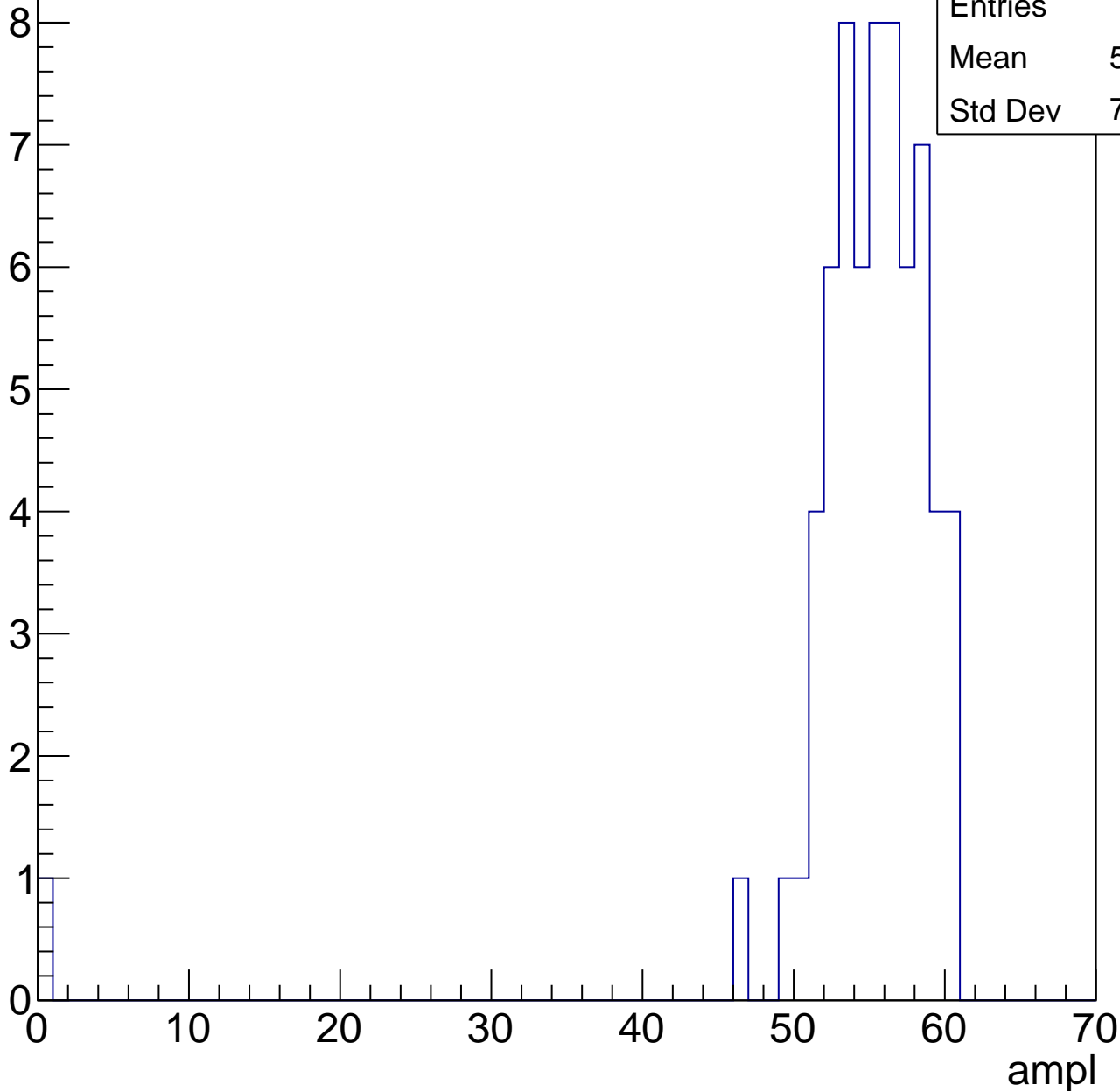


# B1L000S, U23-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.17
Std Dev	7.377

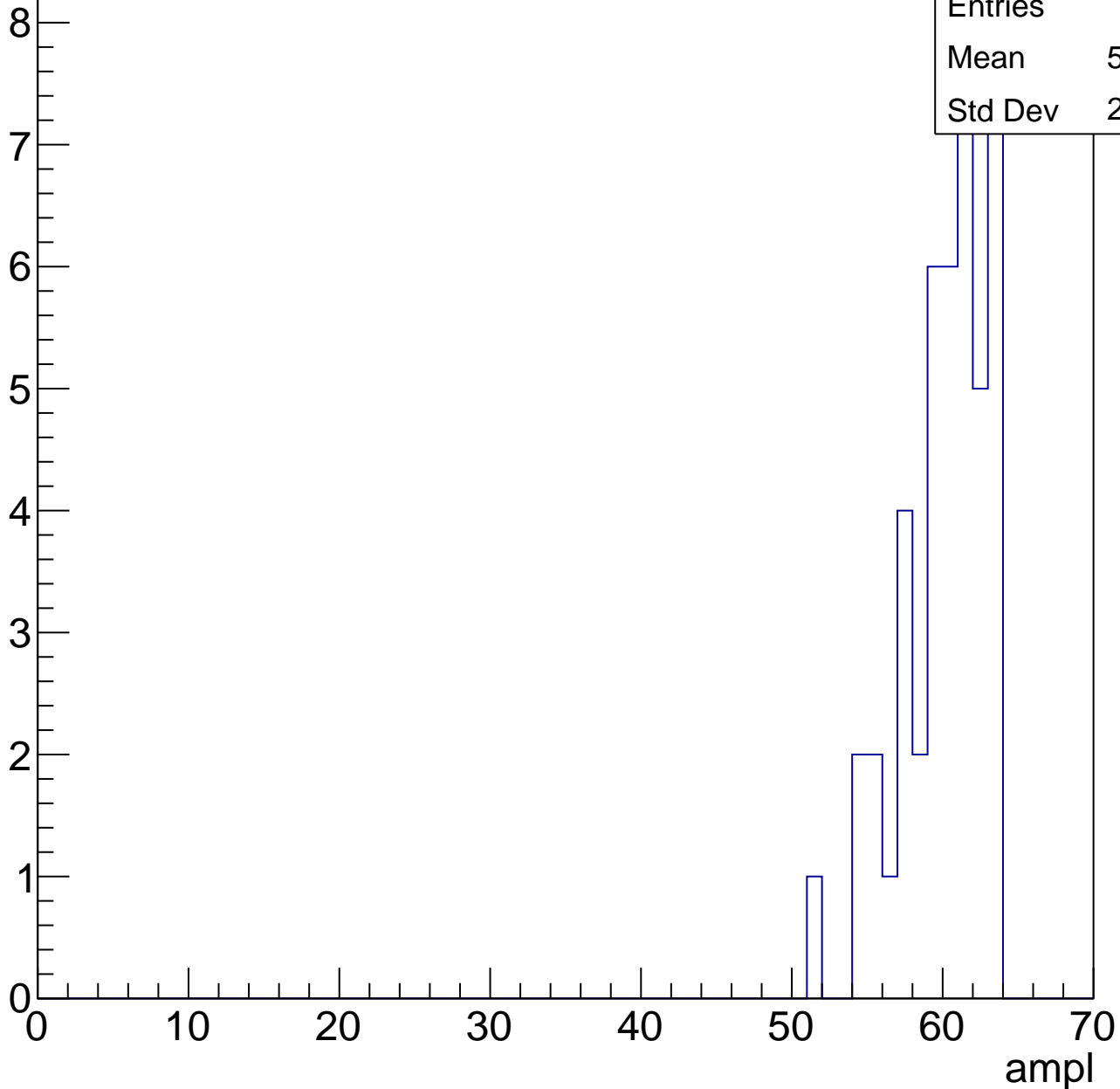


# B1L000S, U23-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	59.67
Std Dev	2.844

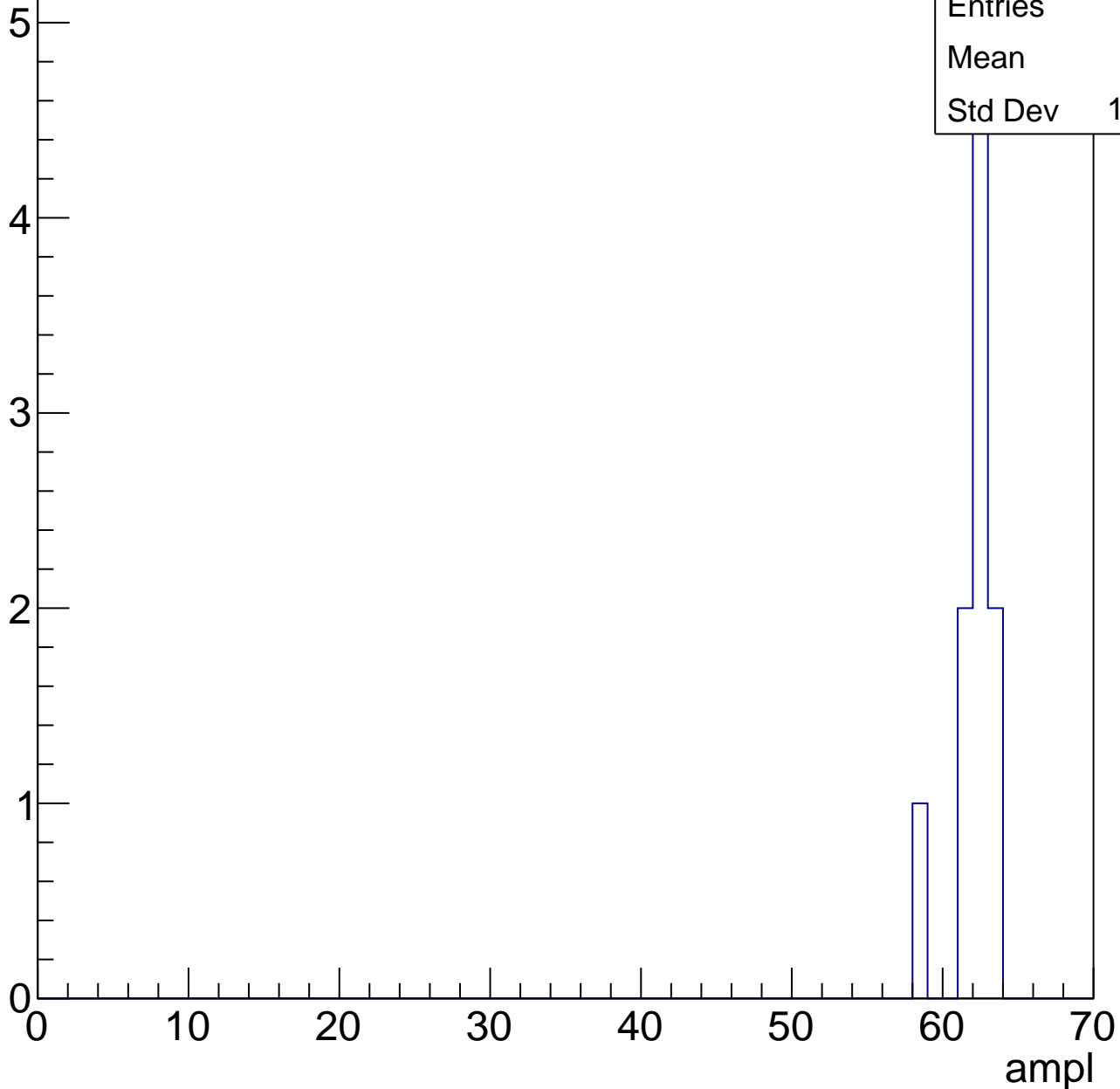


# B1L000S, U23-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	10
Mean	61.6
Std Dev	1.356





# B1L000S, U23-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U23-ch104, adc0

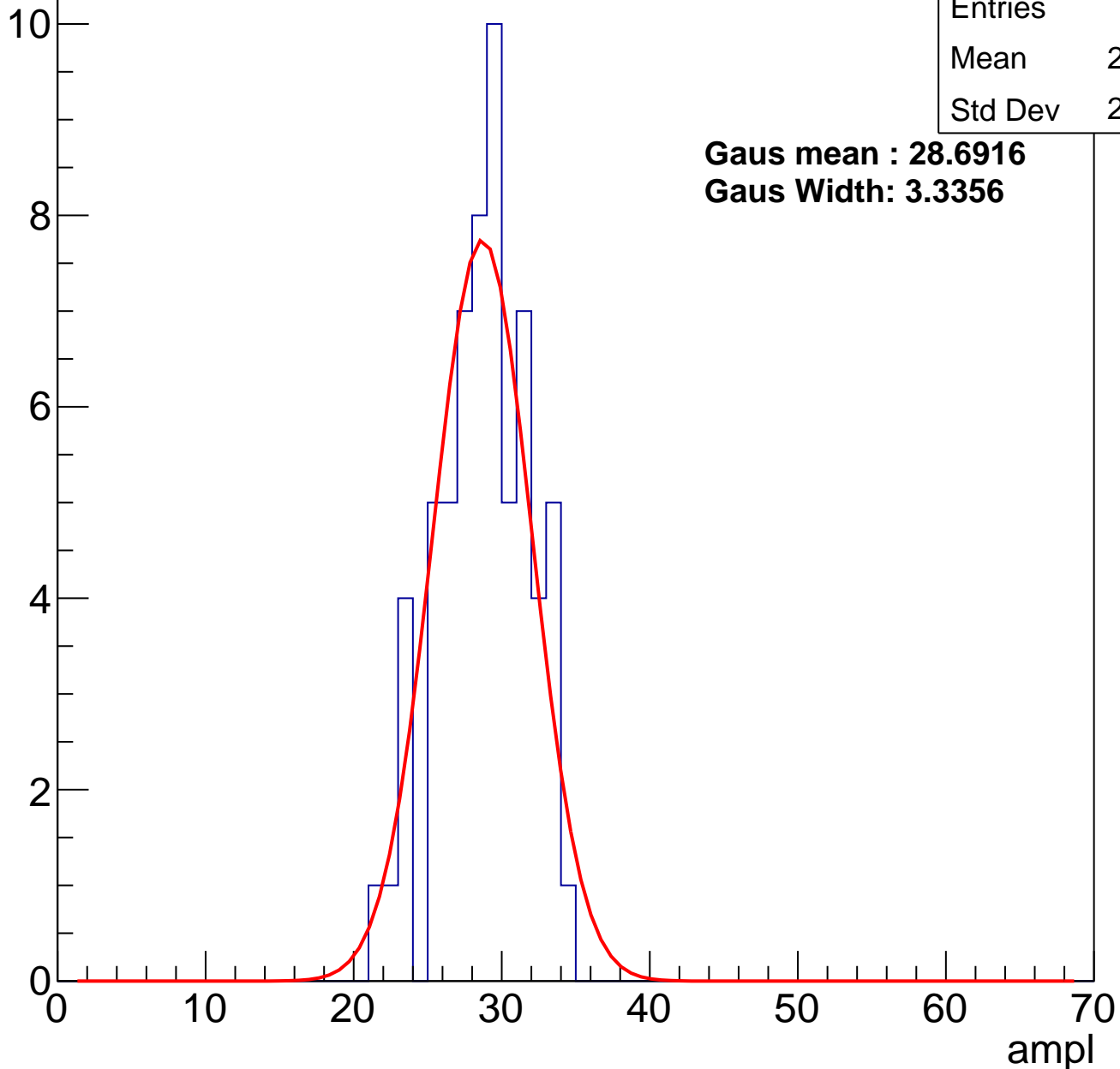
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	28.37
Std Dev	2.999

**Gaus mean : 28.6916**

**Gaus Width: 3.3356**

Entry



# B1L000S, U23-ch104, adc1

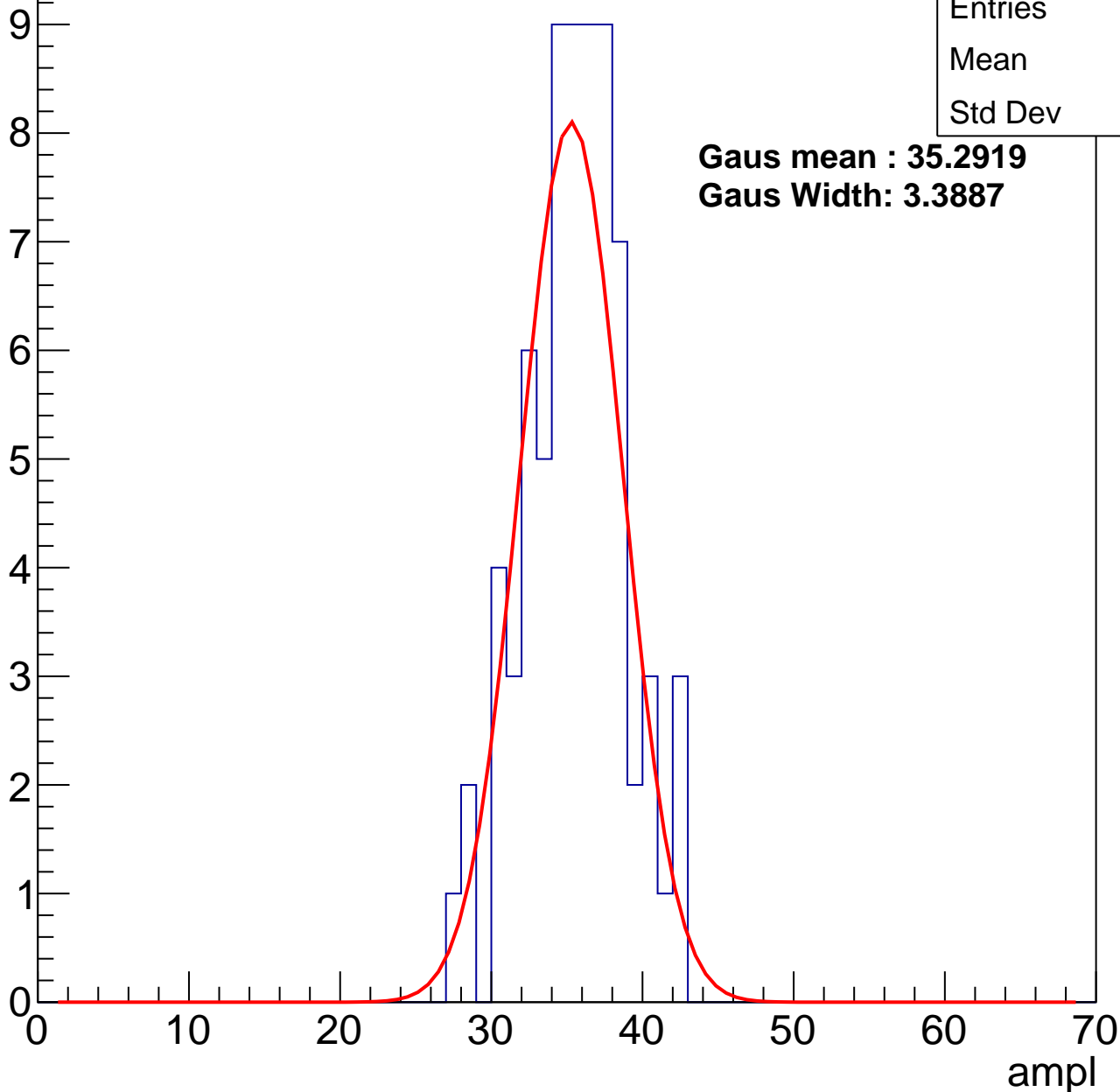
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	35.1
Std Dev	3.29

**Gaus mean : 35.2919**

**Gaus Width: 3.3887**



# B1L000S, U23-ch104, adc2

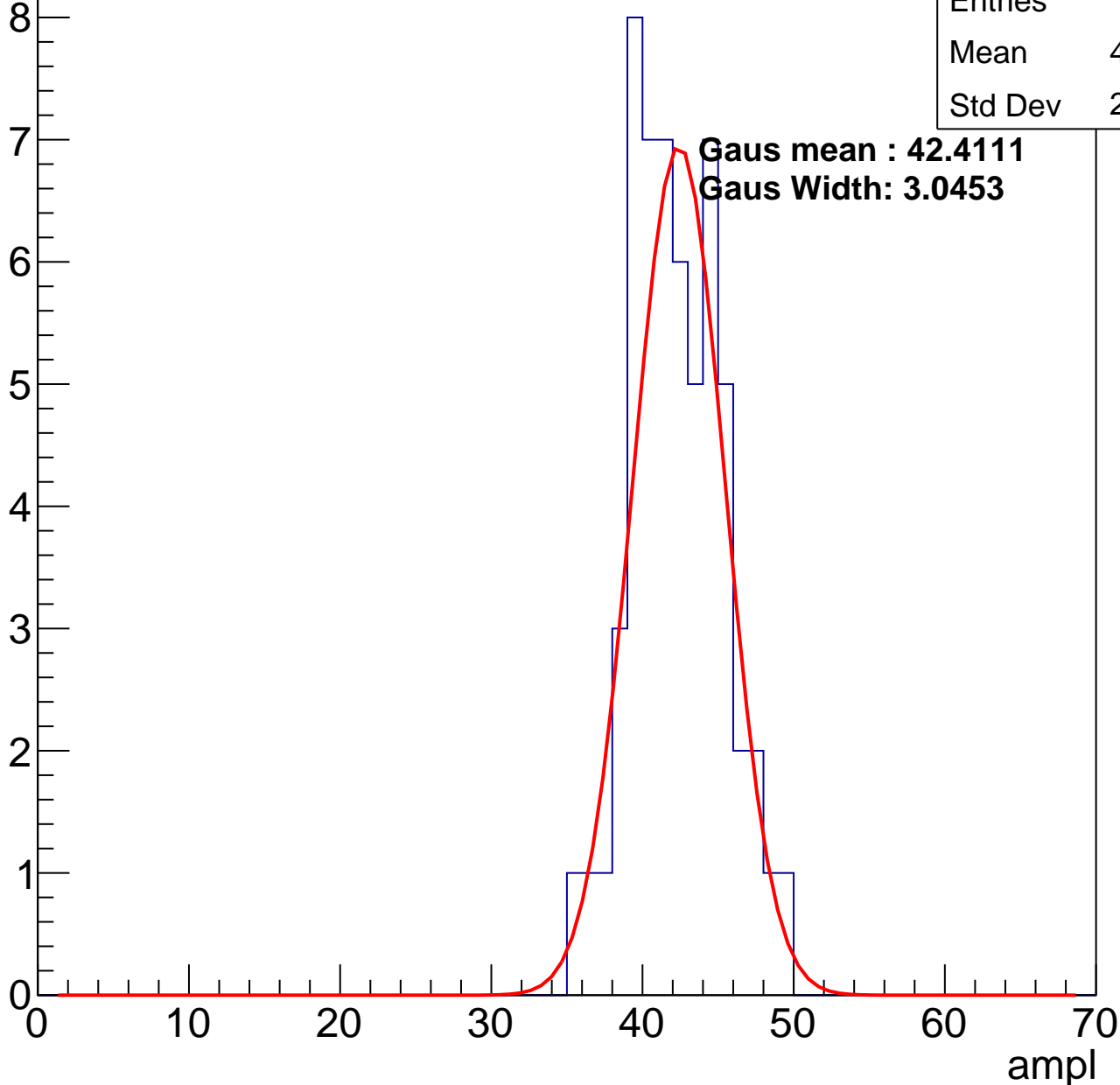
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.82
Std Dev	2.986

**Gaus mean : 42.4111**

**Gaus Width: 3.0453**

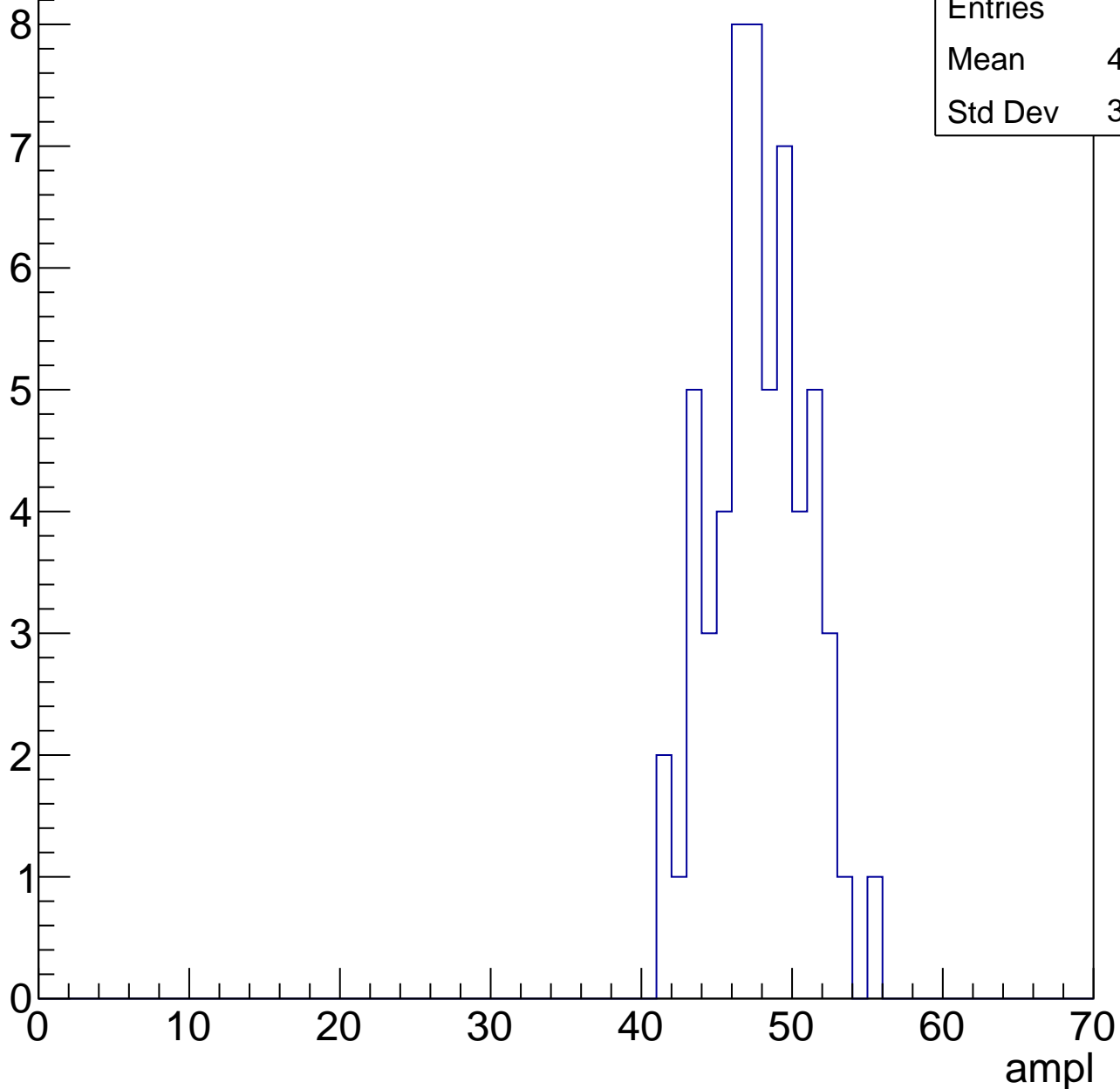


# B1L000S, U23-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.32
Std Dev	3.096

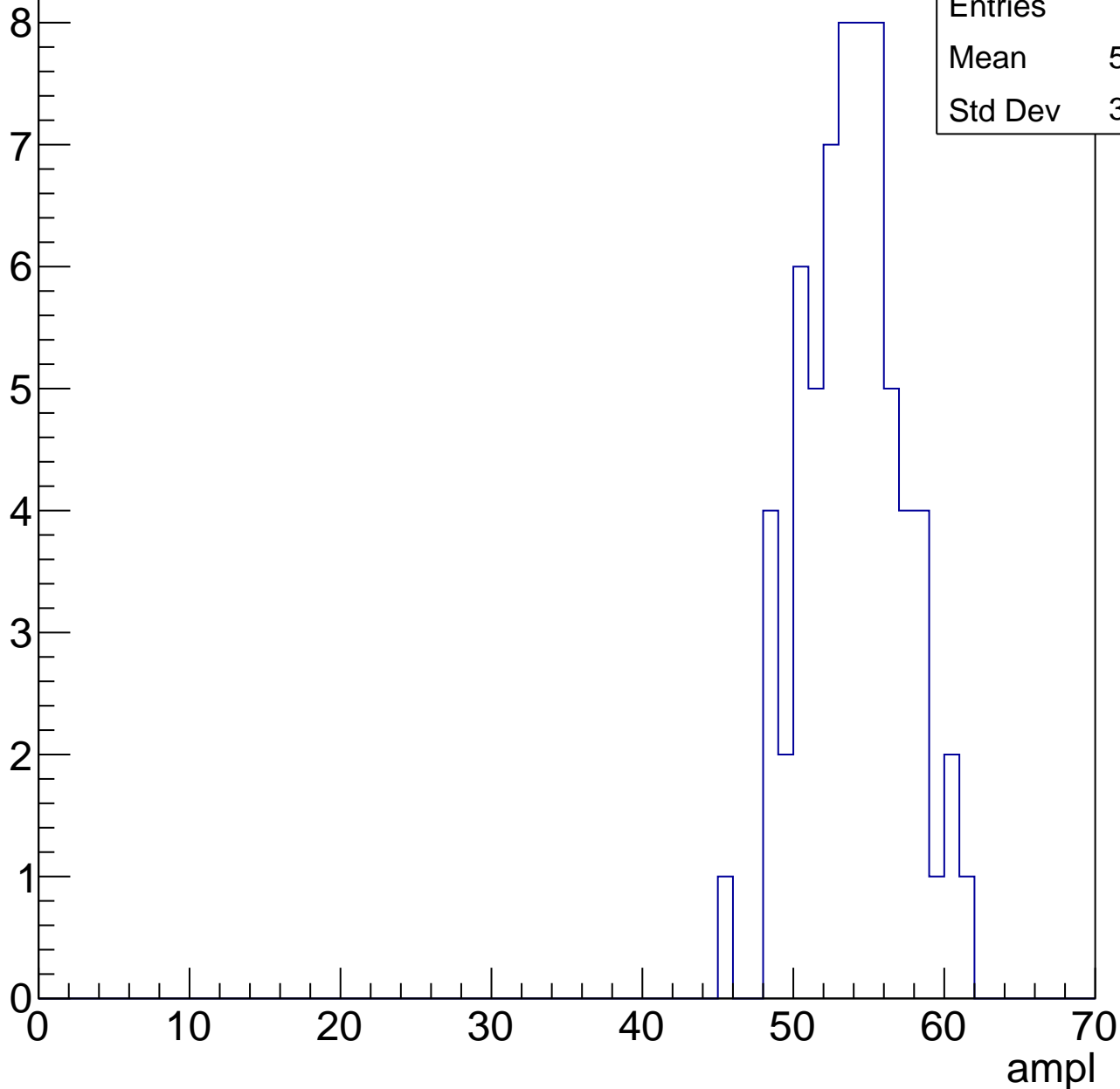


# B1L000S, U23-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	53.48
Std Dev	3.276

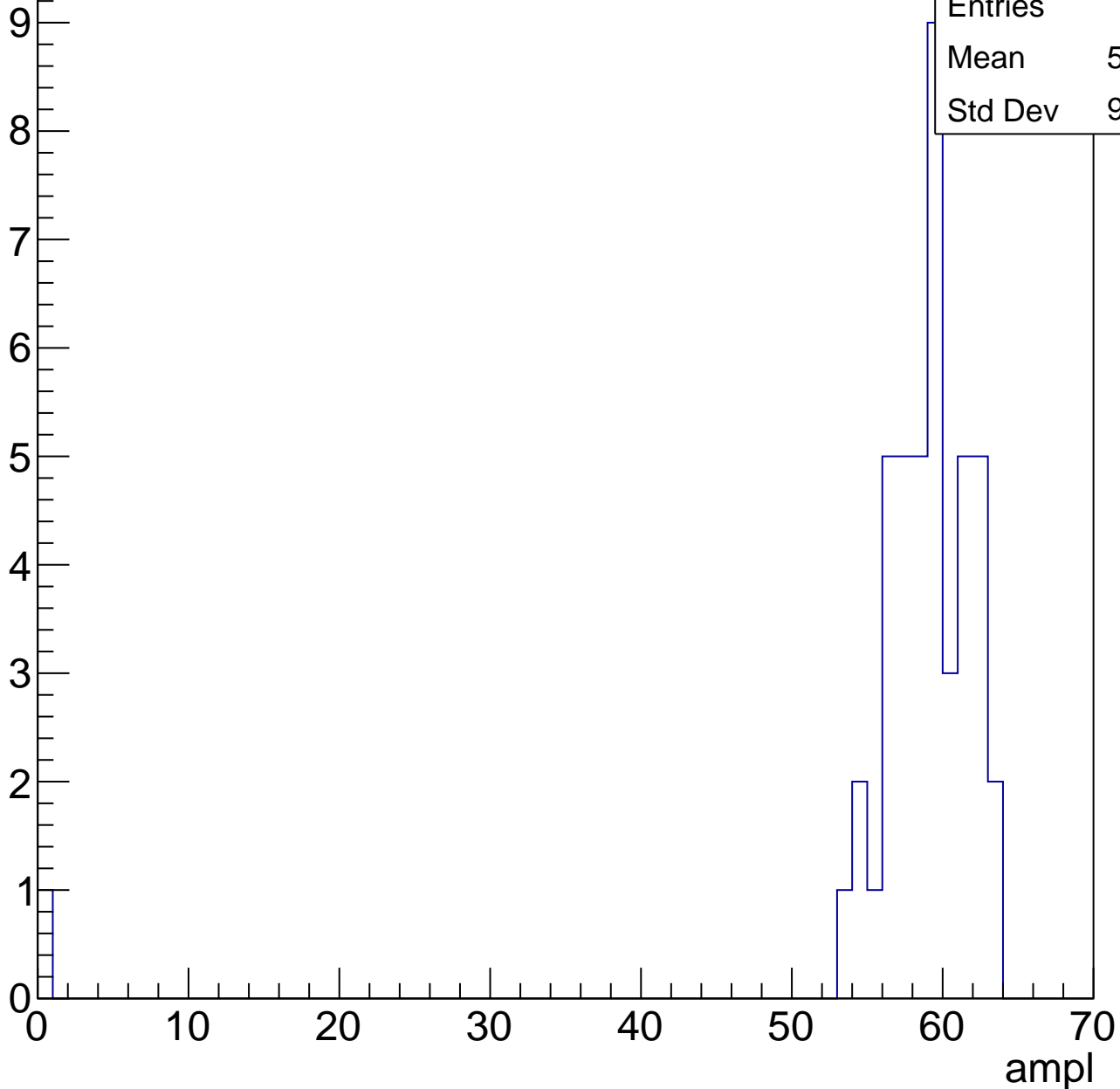


# B1L000S, U23-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	57.34
Std Dev	9.085

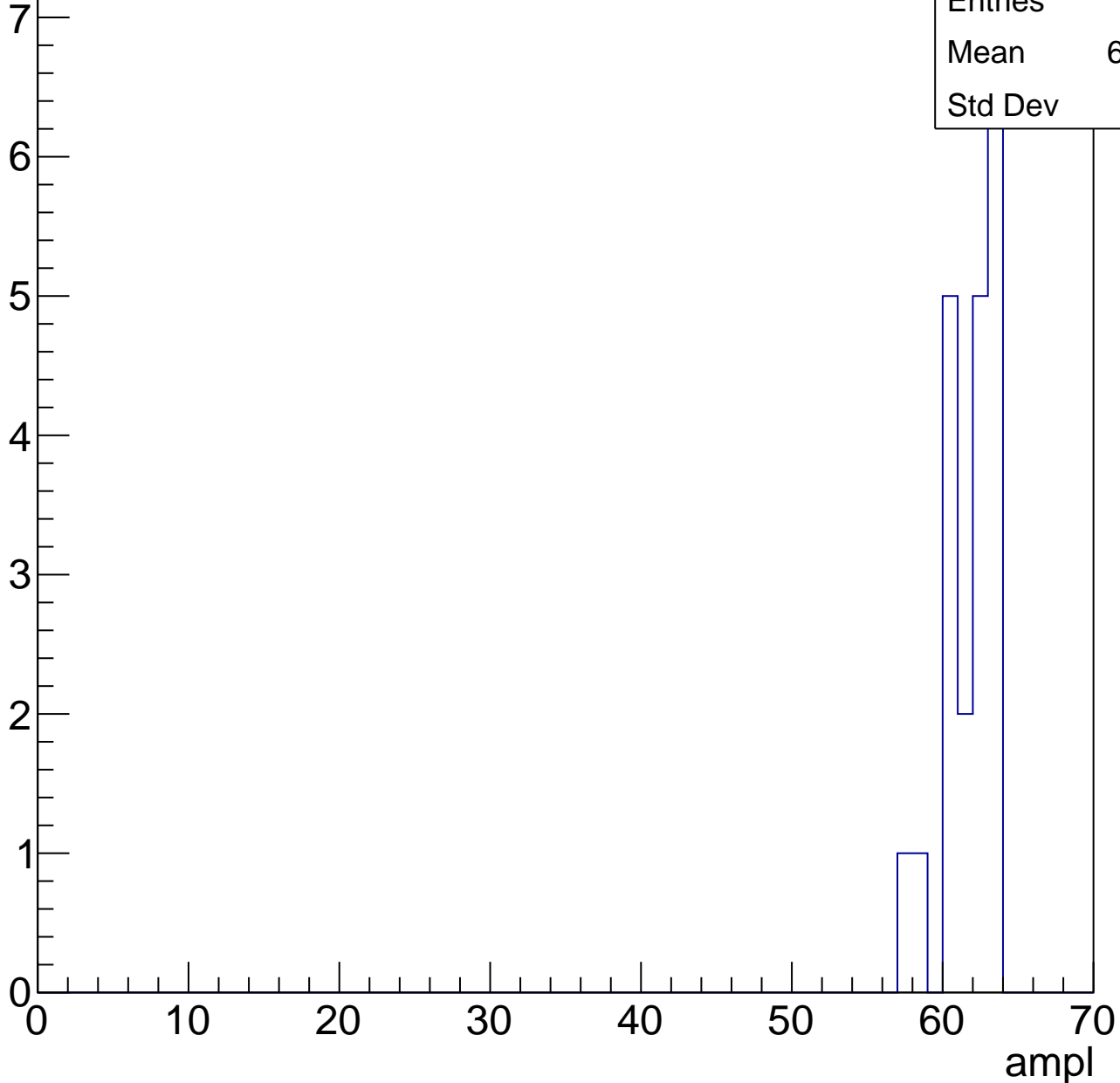


# B1L000S, U23-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	61.33
Std Dev	1.7

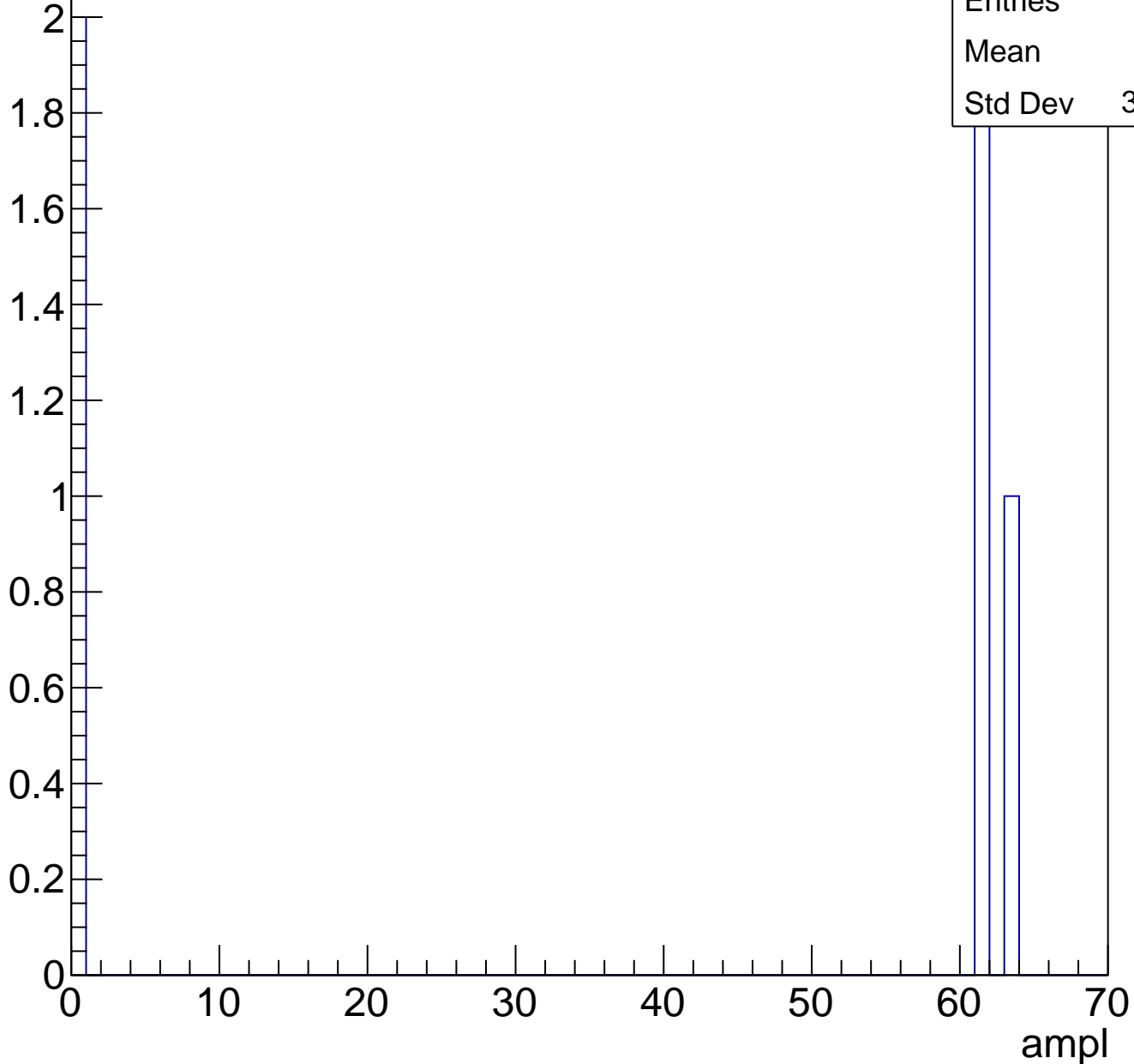




# B1L000S, U23-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch105, adc0

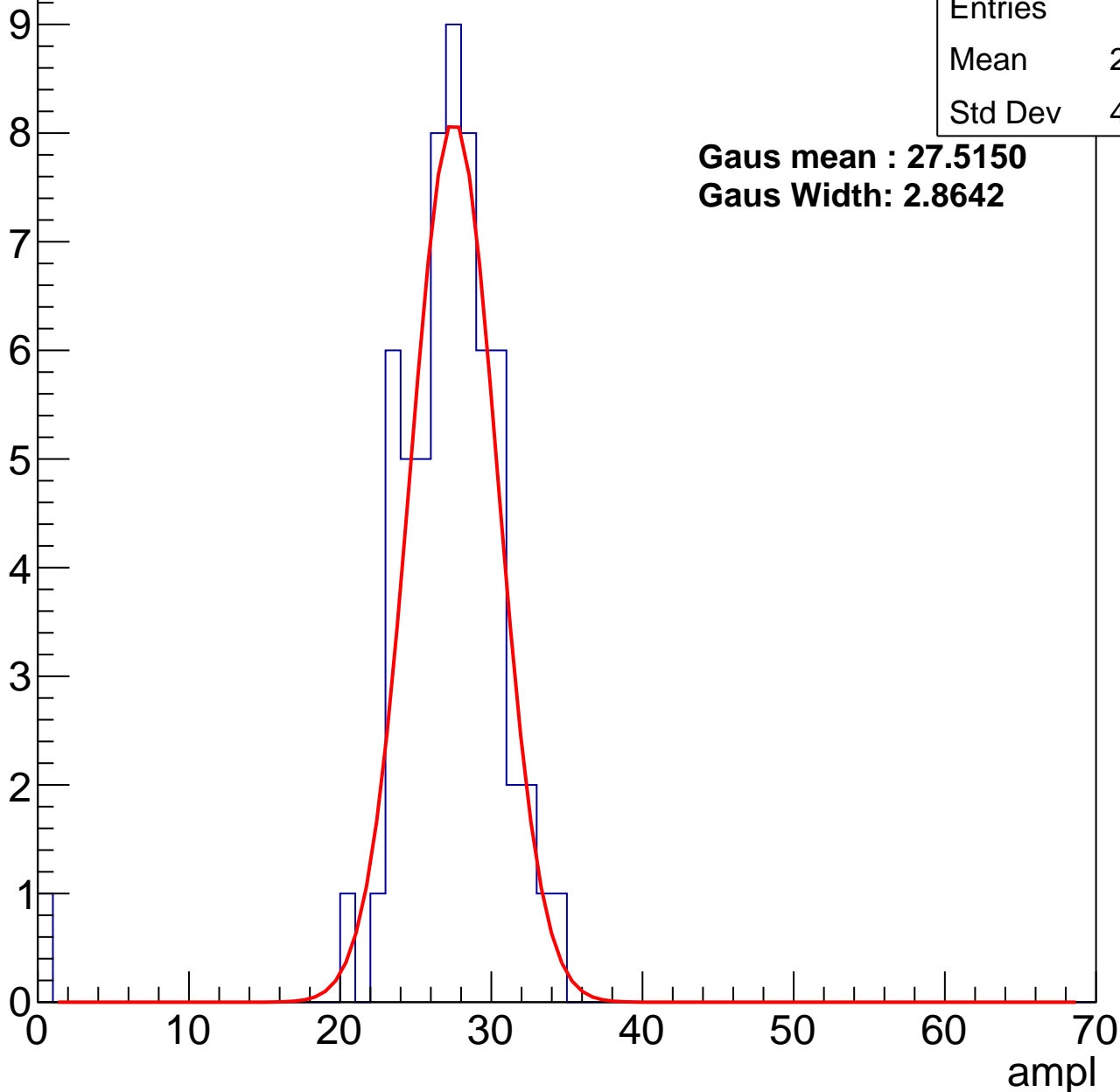
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	26.56
Std Dev	4.424

**Gaus mean : 27.5150**

**Gaus Width: 2.8642**



# B1L000S, U23-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	87
Mean	34.61
Std Dev	3.69

**Gaus mean : 34.7985**

**Gaus Width: 3.8142**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

# B1L000S, U23-ch105, adc2

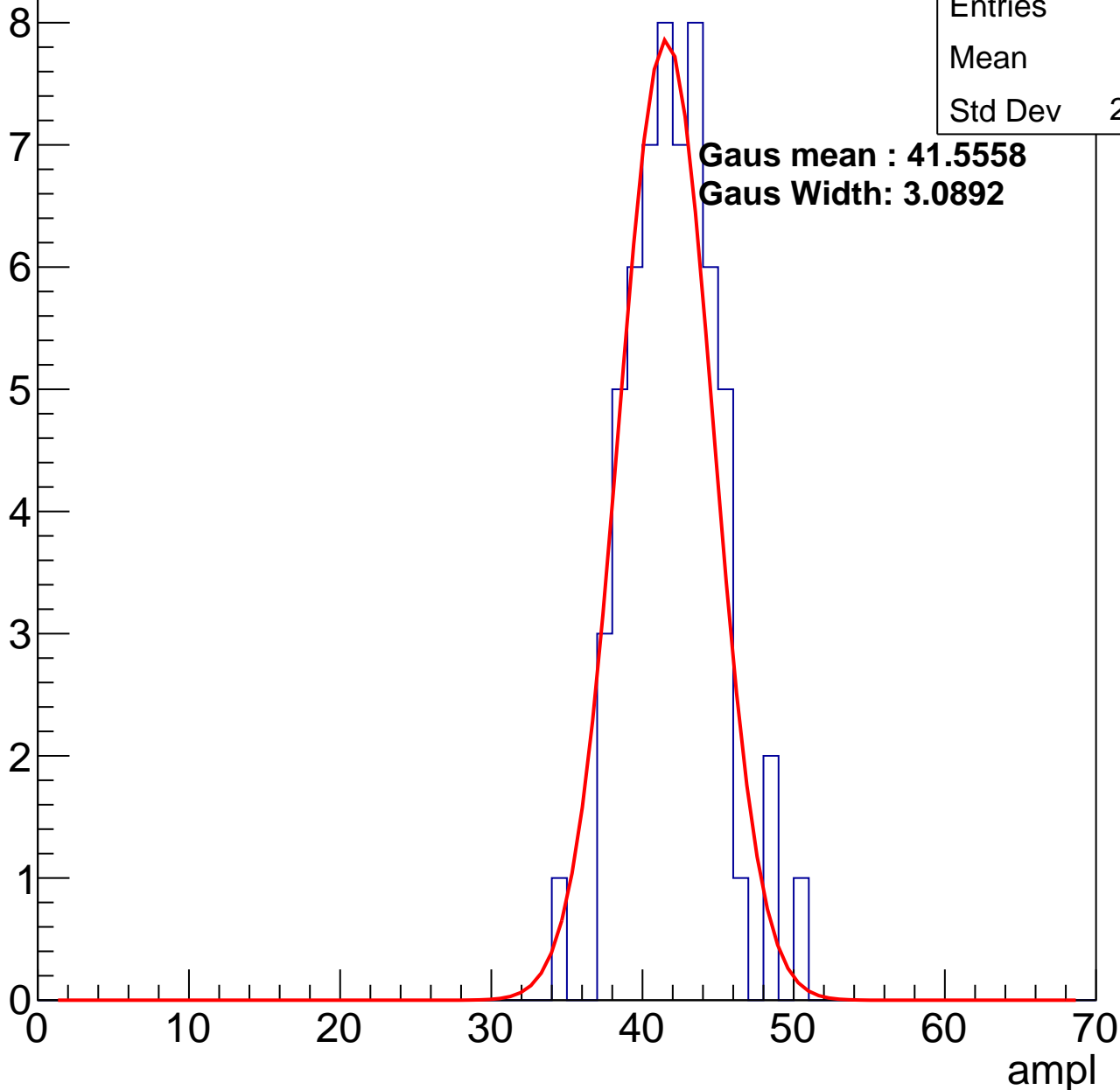
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.6
Std Dev	2.968

**Gaus mean : 41.5558**

**Gaus Width: 3.0892**

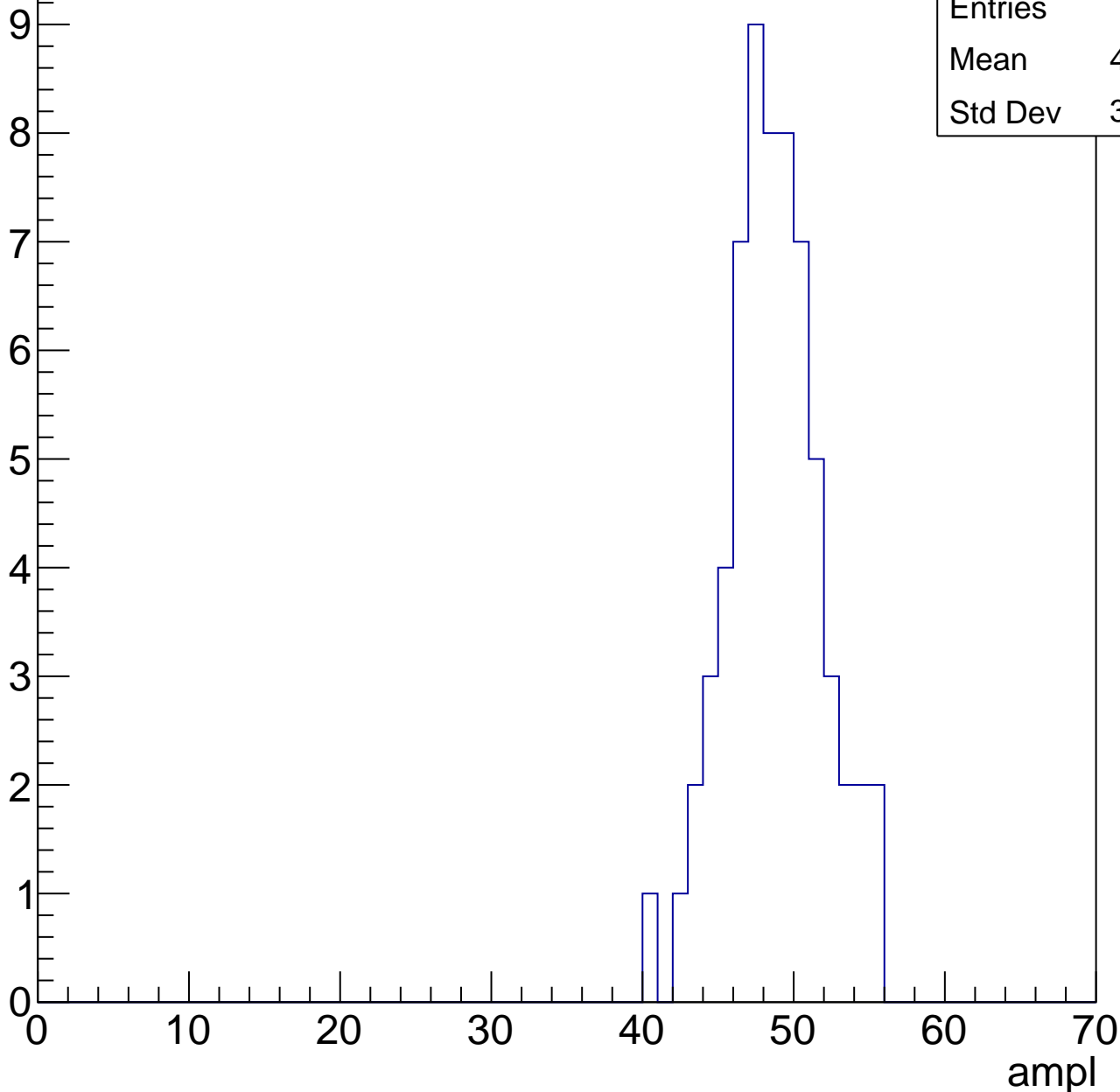


# B1L000S, U23-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.22
Std Dev	3.105

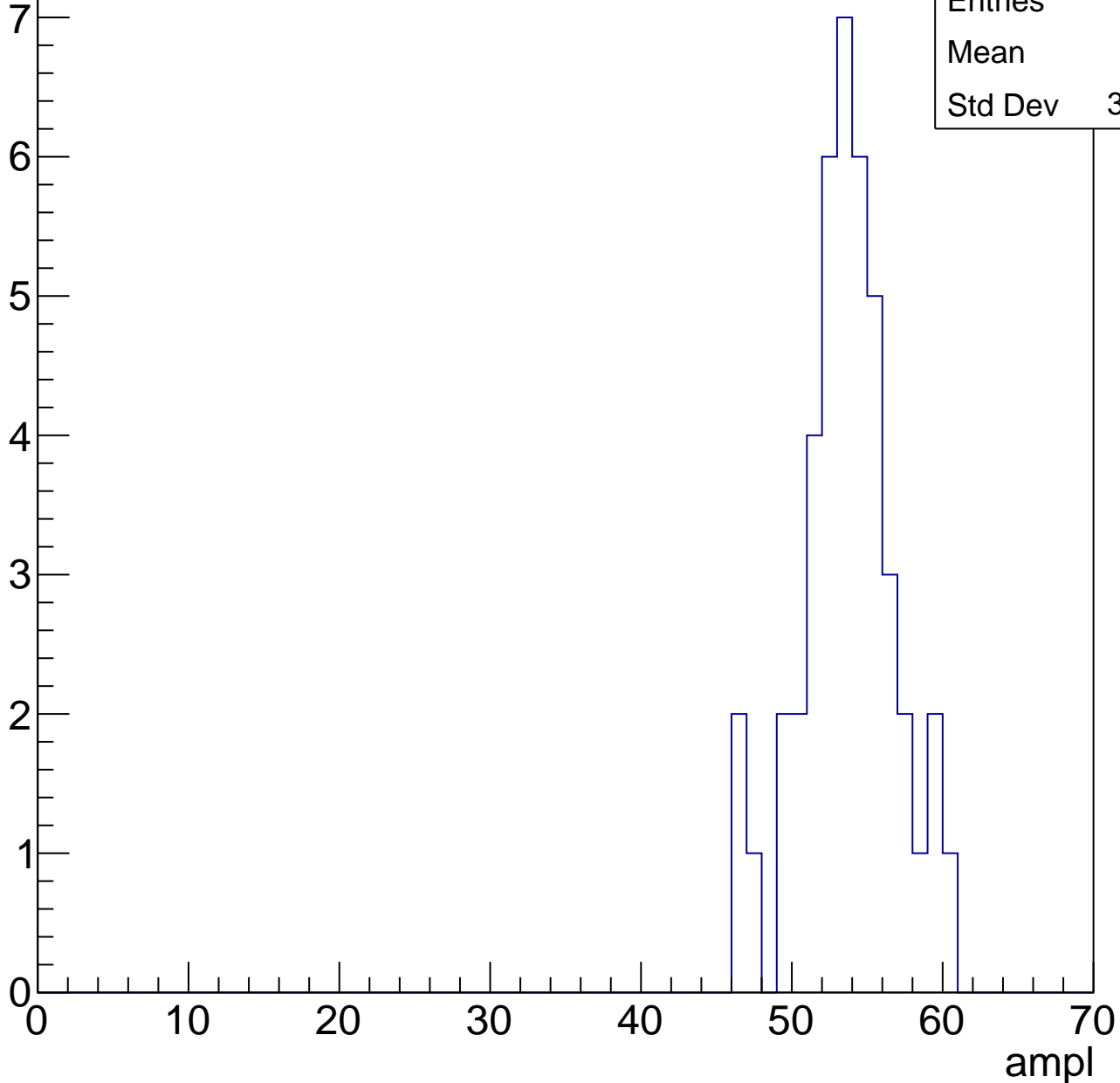


# B1L000S, U23-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	53.2
Std Dev	3.145

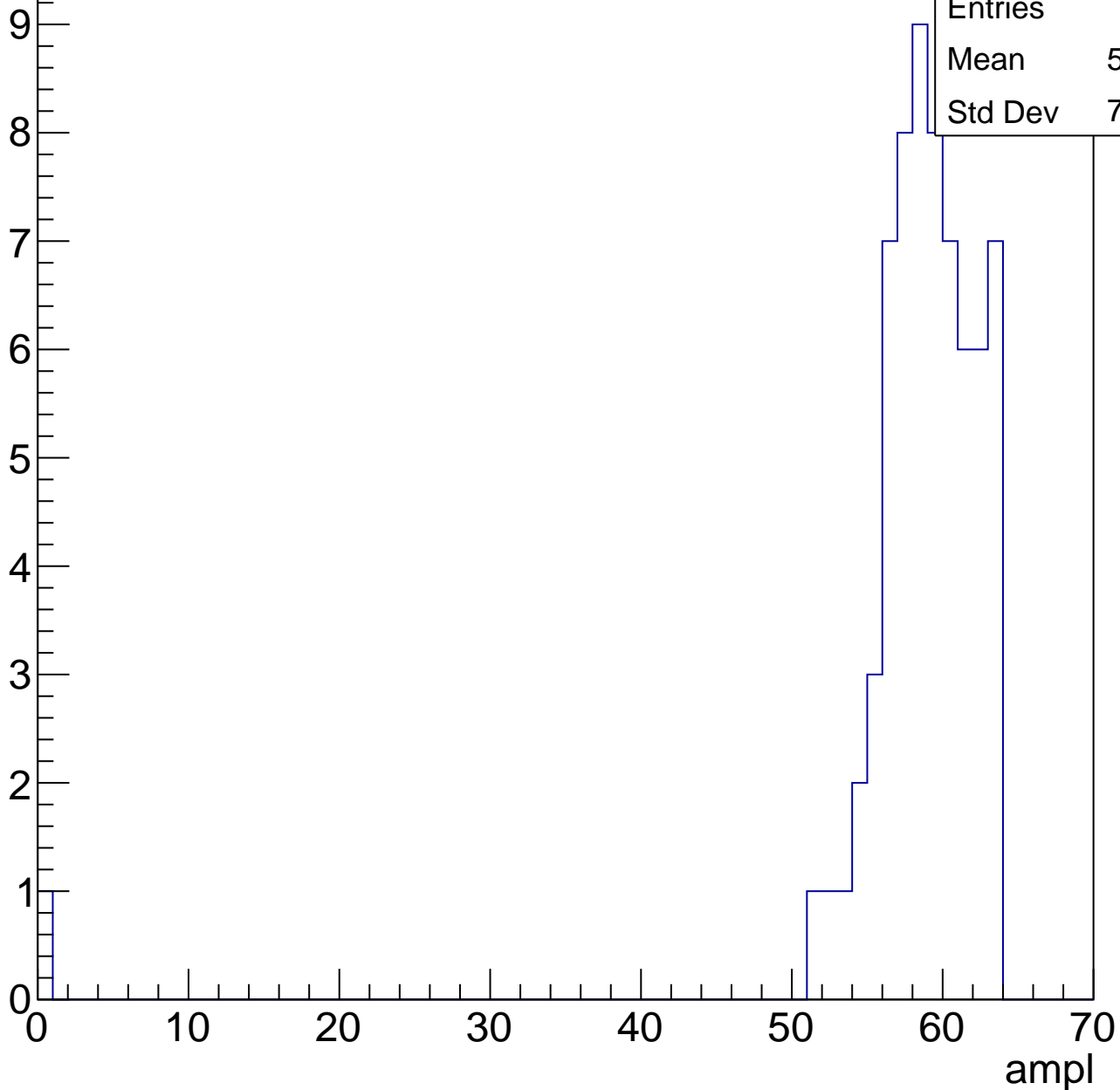


# B1L000S, U23-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

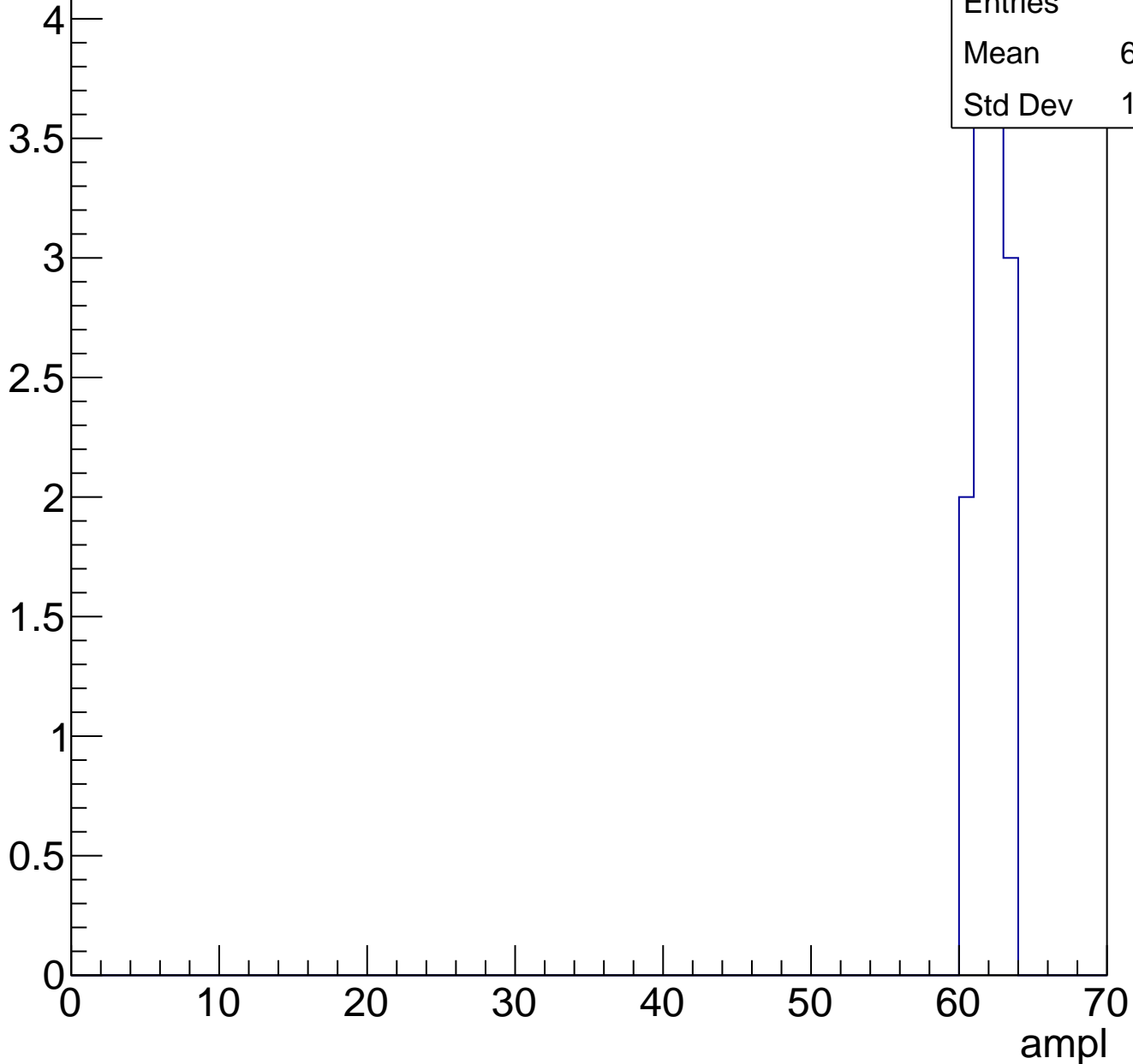
Entries	67
Mean	57.76
Std Dev	7.655



# B1L000S, U23-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch106, adc0

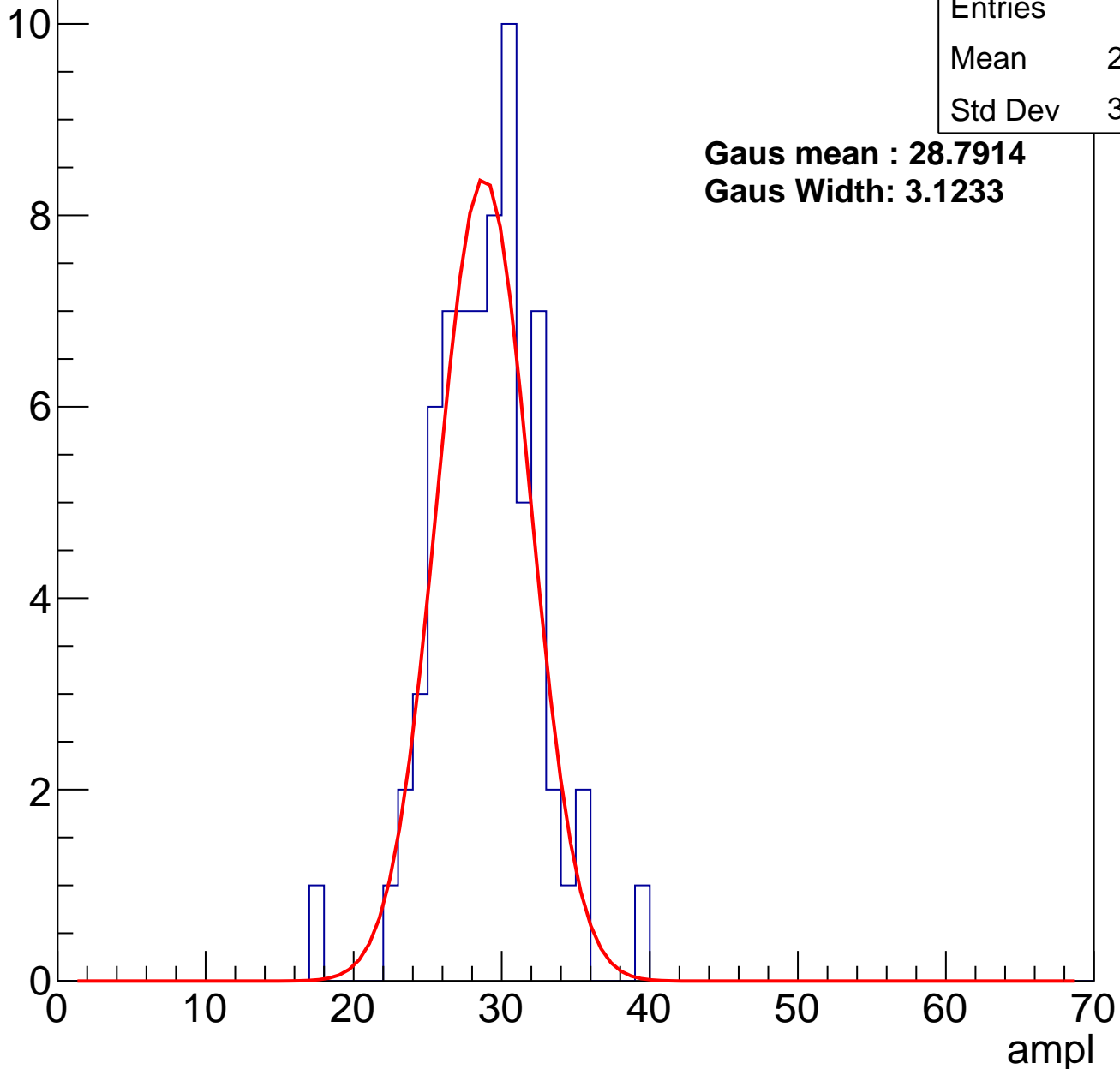
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.49
Std Dev	3.459

**Gaus mean : 28.7914**

**Gaus Width: 3.1233**

Entry



# B1L000S, U23-ch106, adc1

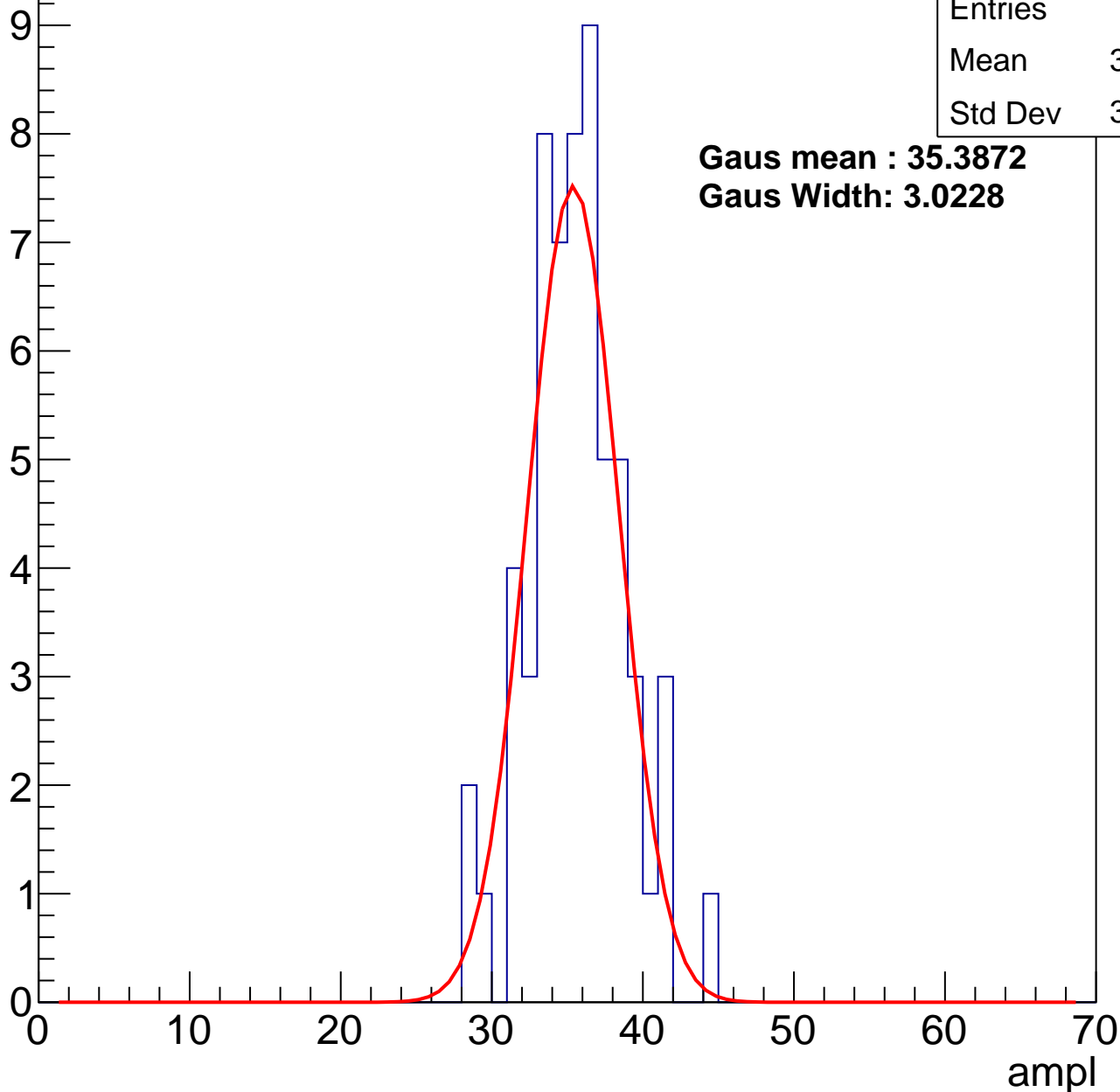
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	35.17
Std Dev	3.158

**Gaus mean : 35.3872**

**Gaus Width: 3.0228**



# B1L000S, U23-ch106, adc2

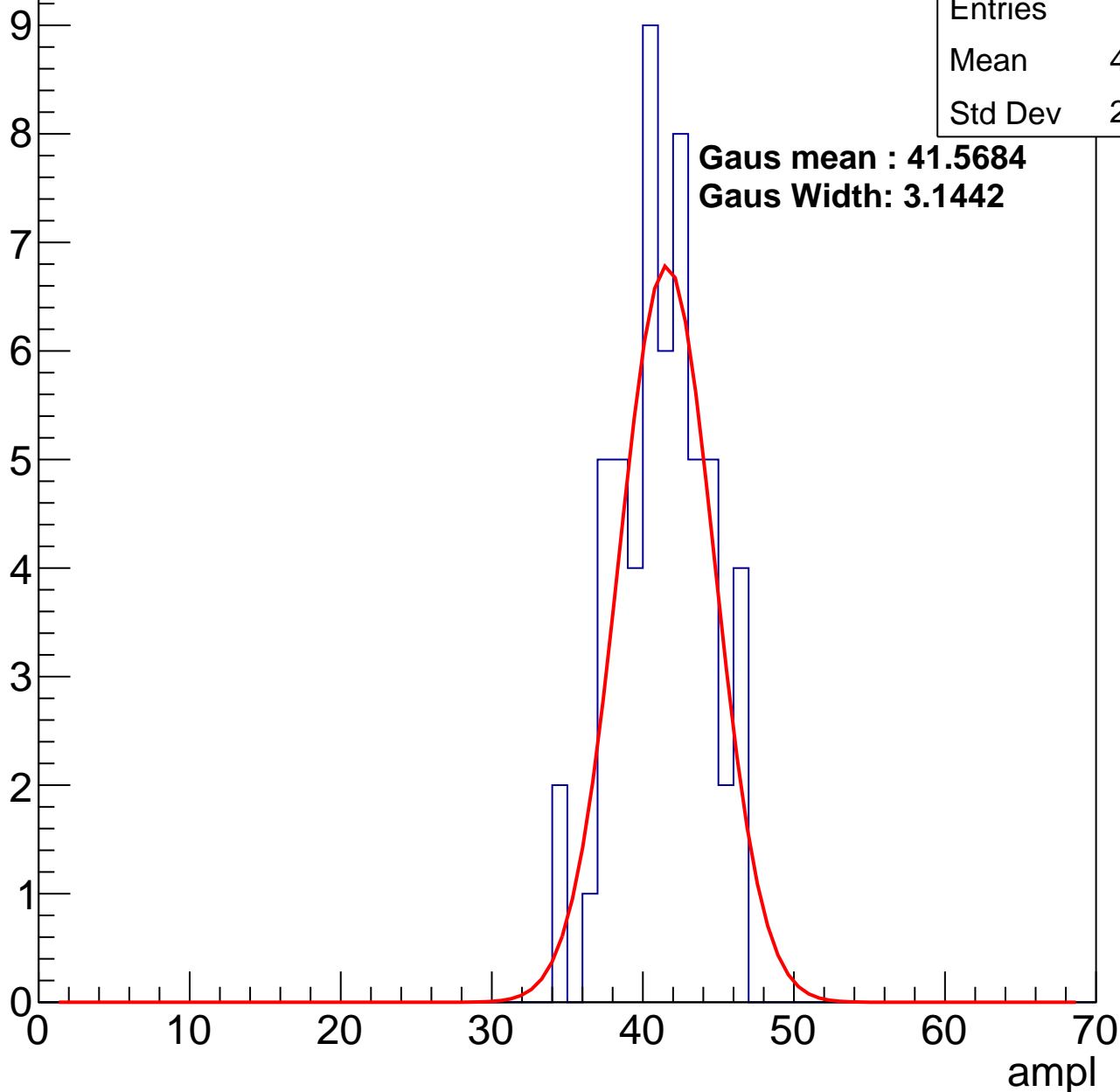
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.82
Std Dev	2.916

**Gaus mean : 41.5684**

**Gaus Width: 3.1442**

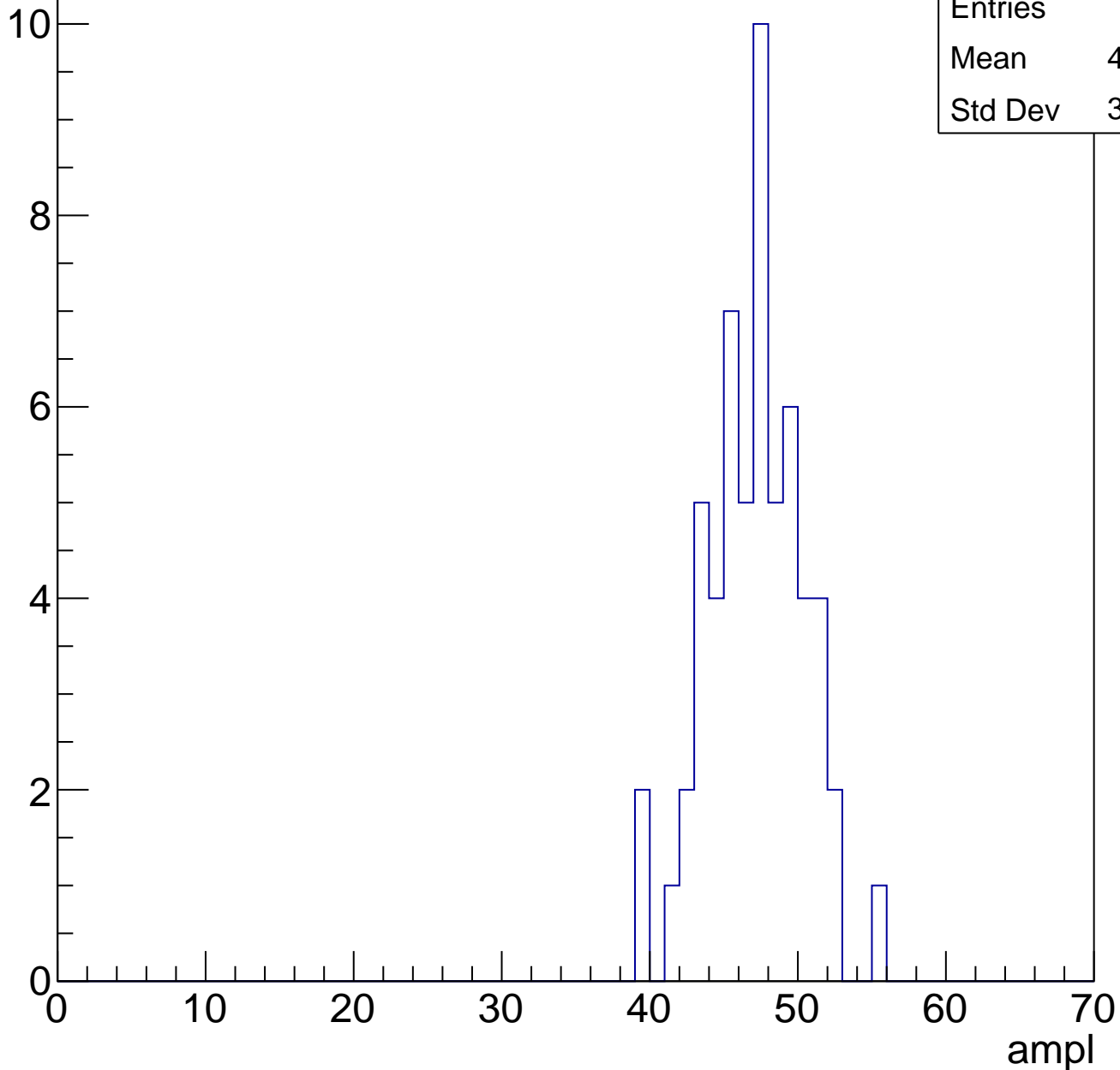


# B1L000S, U23-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	46.66
Std Dev	3.214

Entry

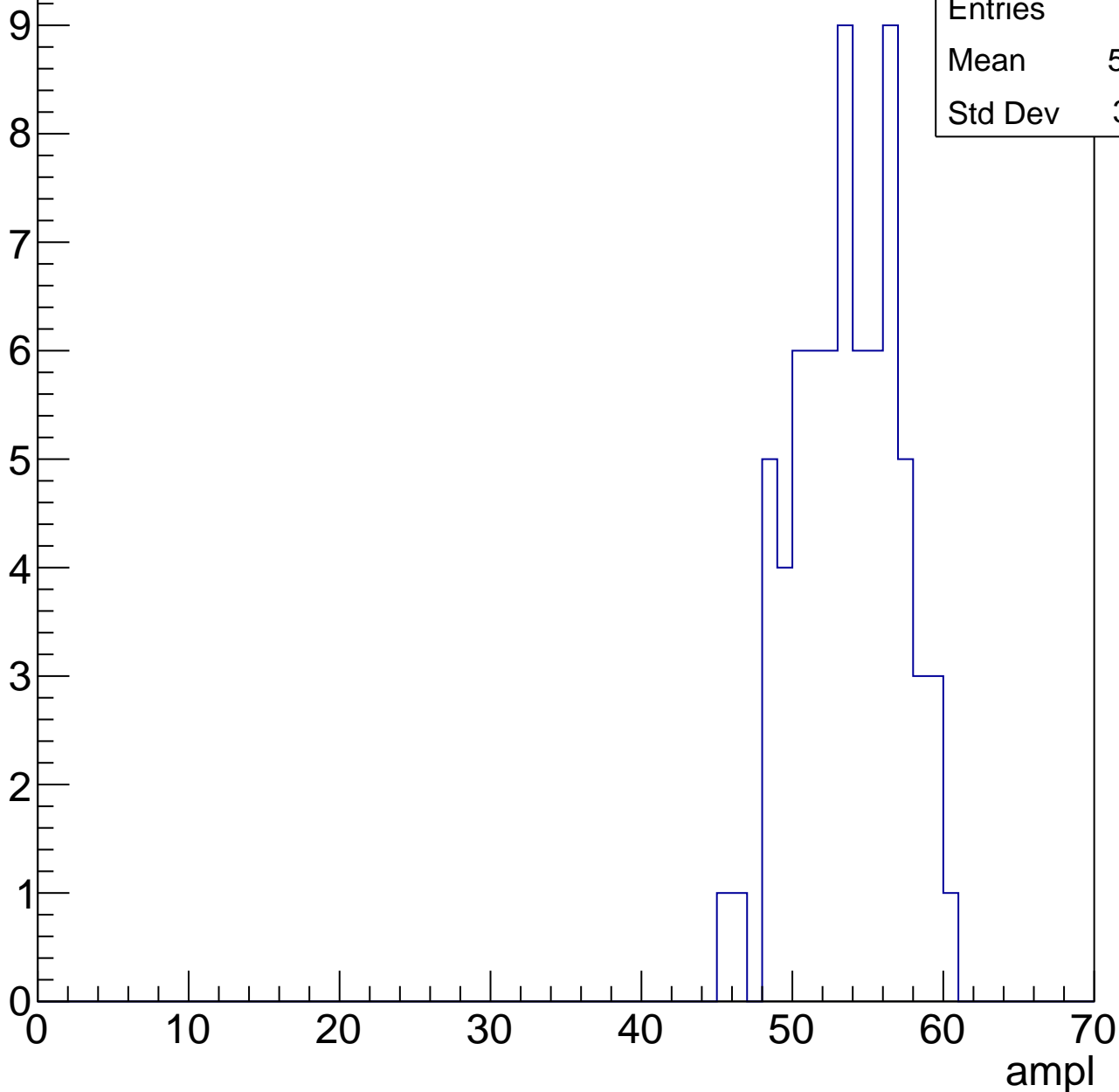


# B1L000S, U23-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

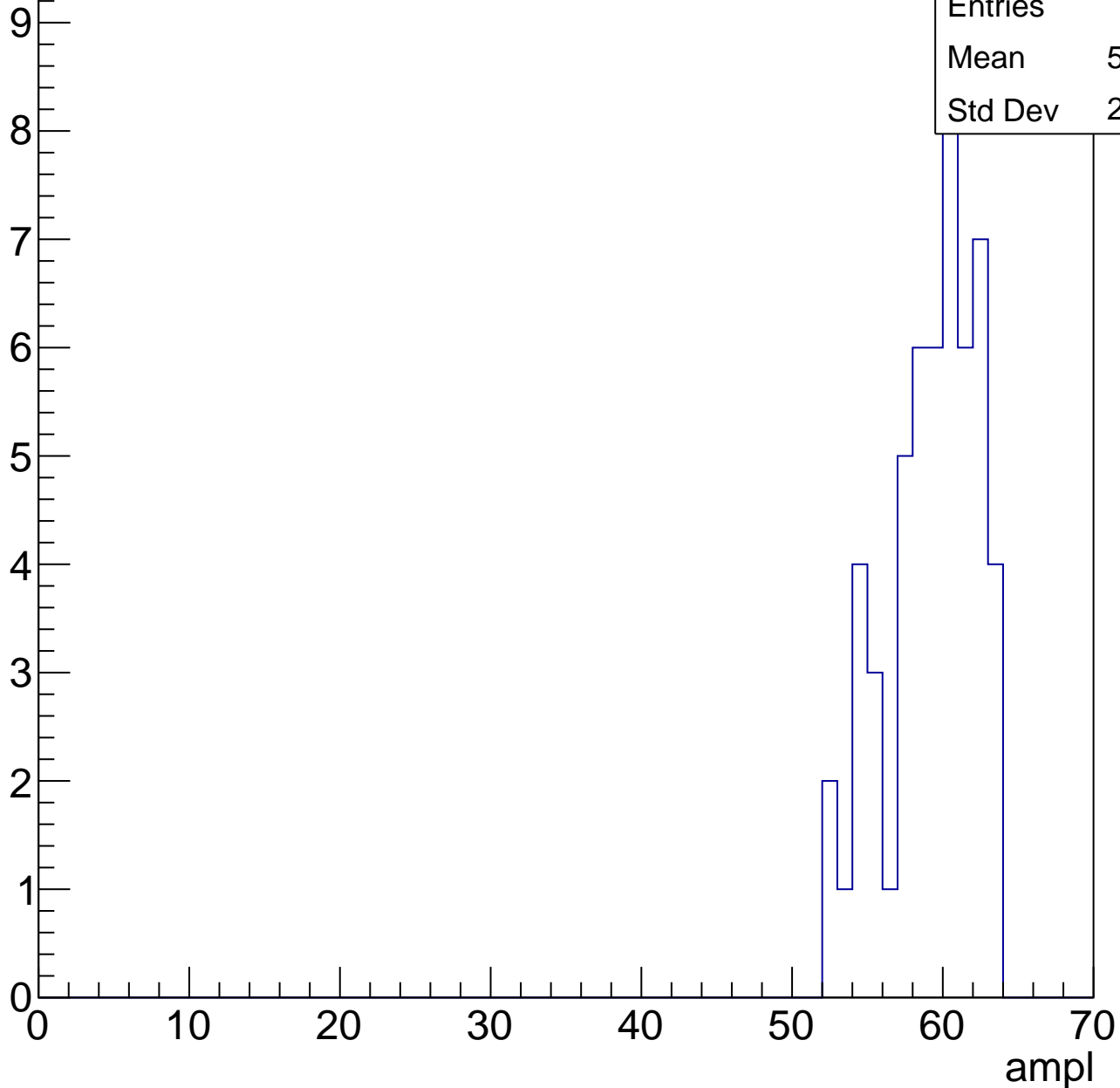
Entries	71
Mean	53.18
Std Dev	3.371



# B1L000S, U23-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

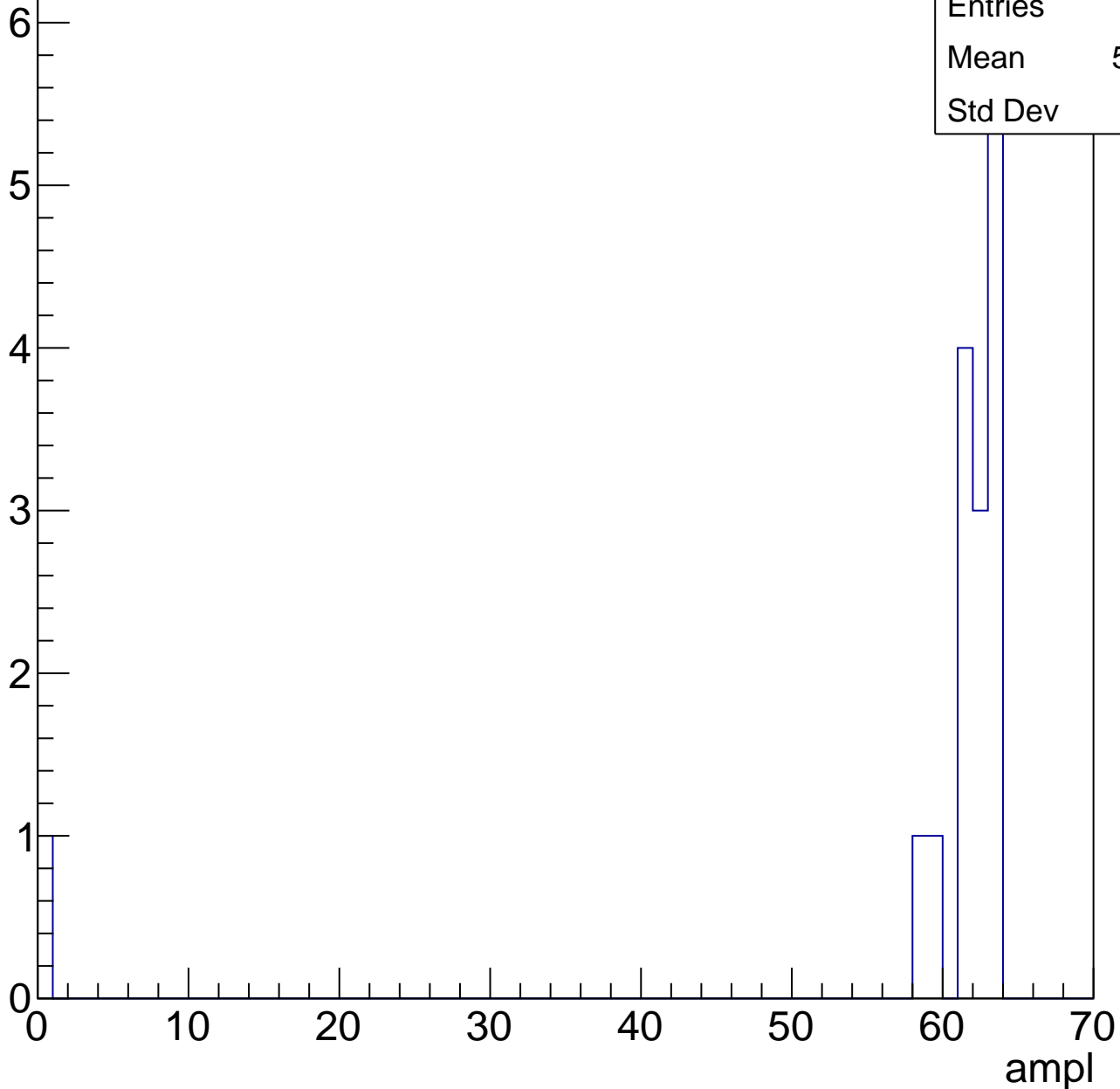


# B1L000S, U23-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.81
Std Dev	15





# B1L000S, U23-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch107, adc0

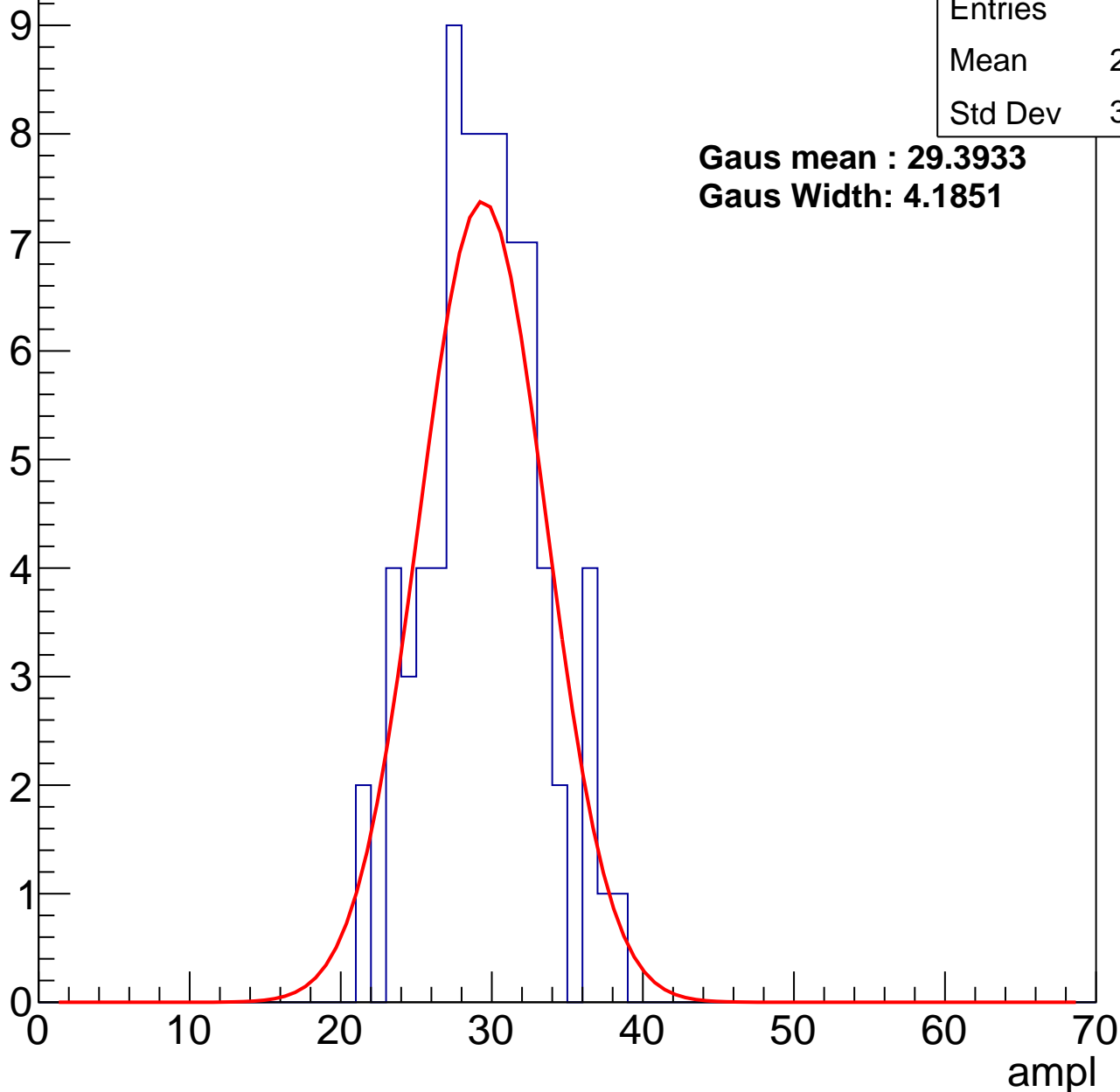
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	29.07
Std Dev	3.708

**Gaus mean : 29.3933**

**Gaus Width: 4.1851**



# B1L000S, U23-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	50
Mean	35.22
Std Dev	2.73

**Gaus mean : 35.6335**

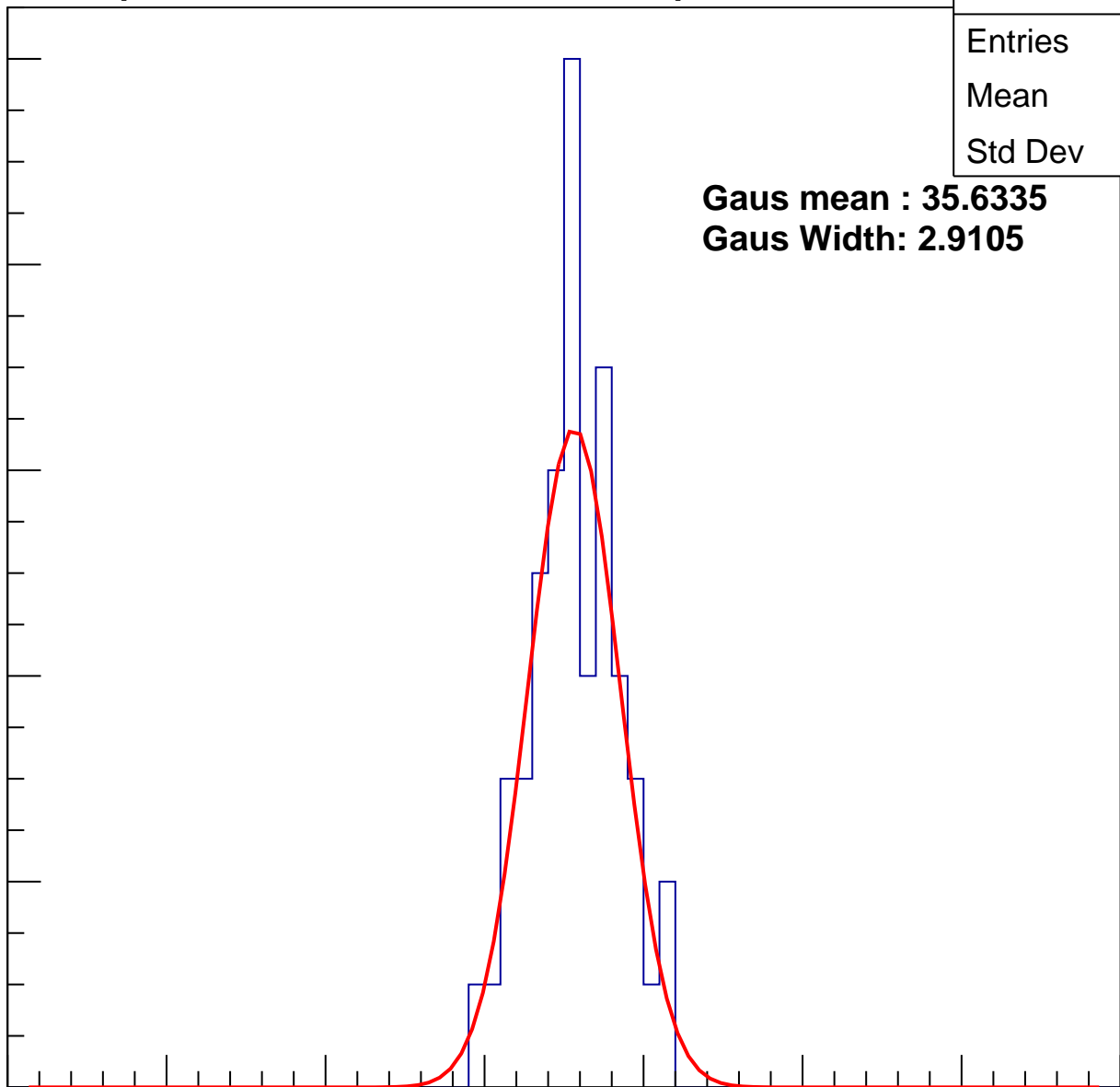
**Gaus Width: 2.9105**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch107, adc2

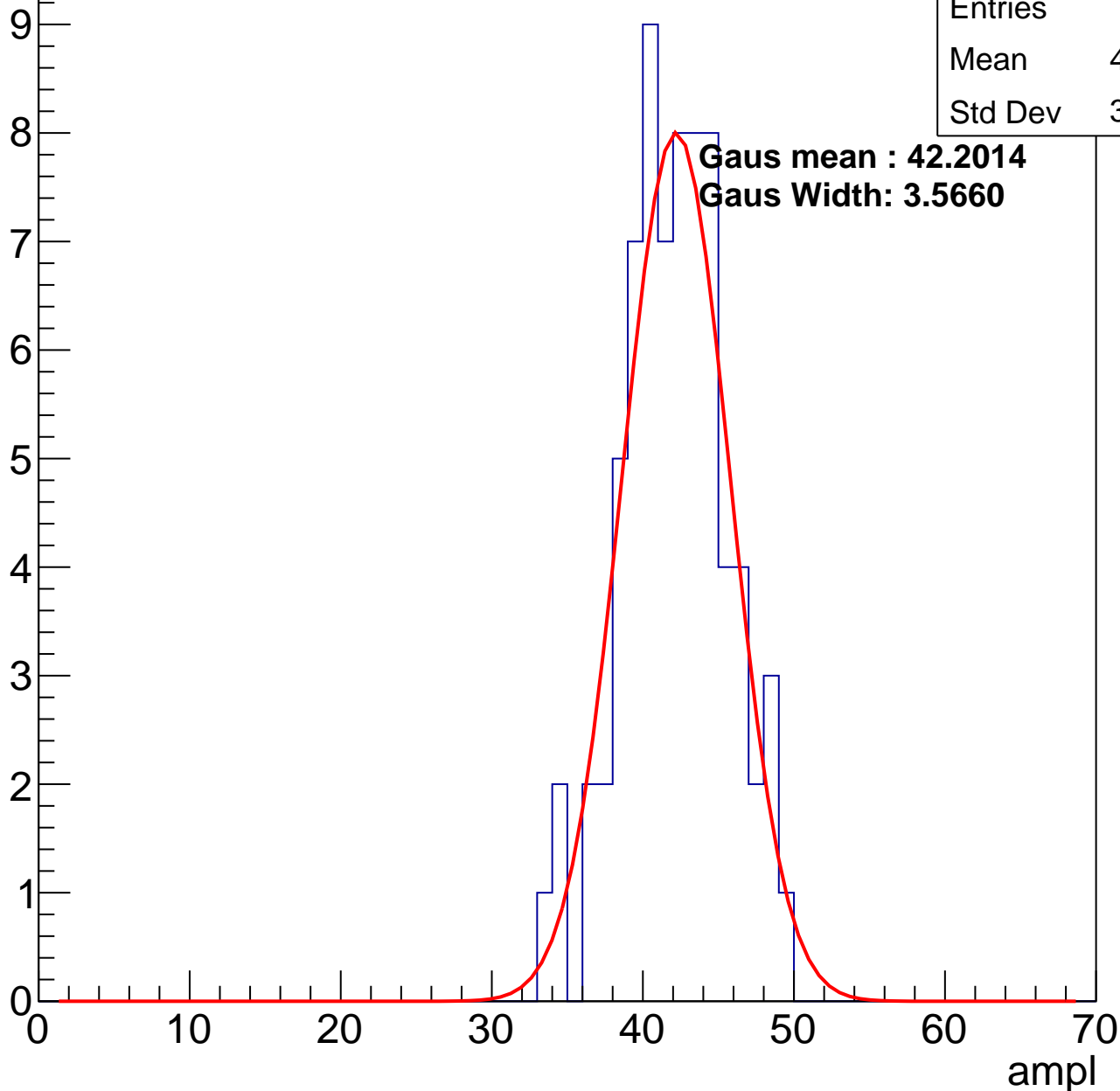
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	41.64
Std Dev	3.422

**Gaus mean : 42.2014**

**Gaus Width: 3.5660**

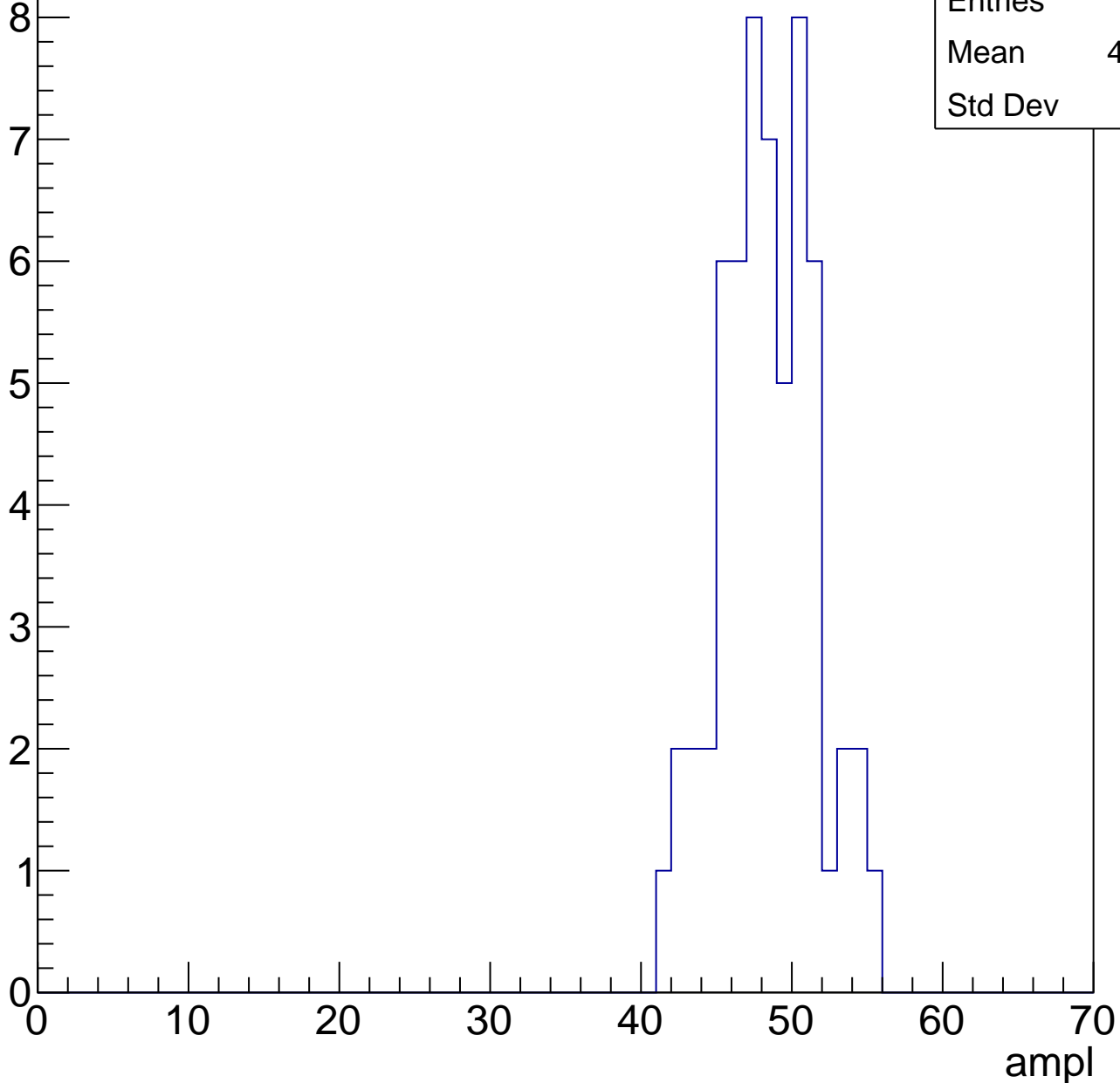


# B1L000S, U23-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.95
Std Dev	3.1

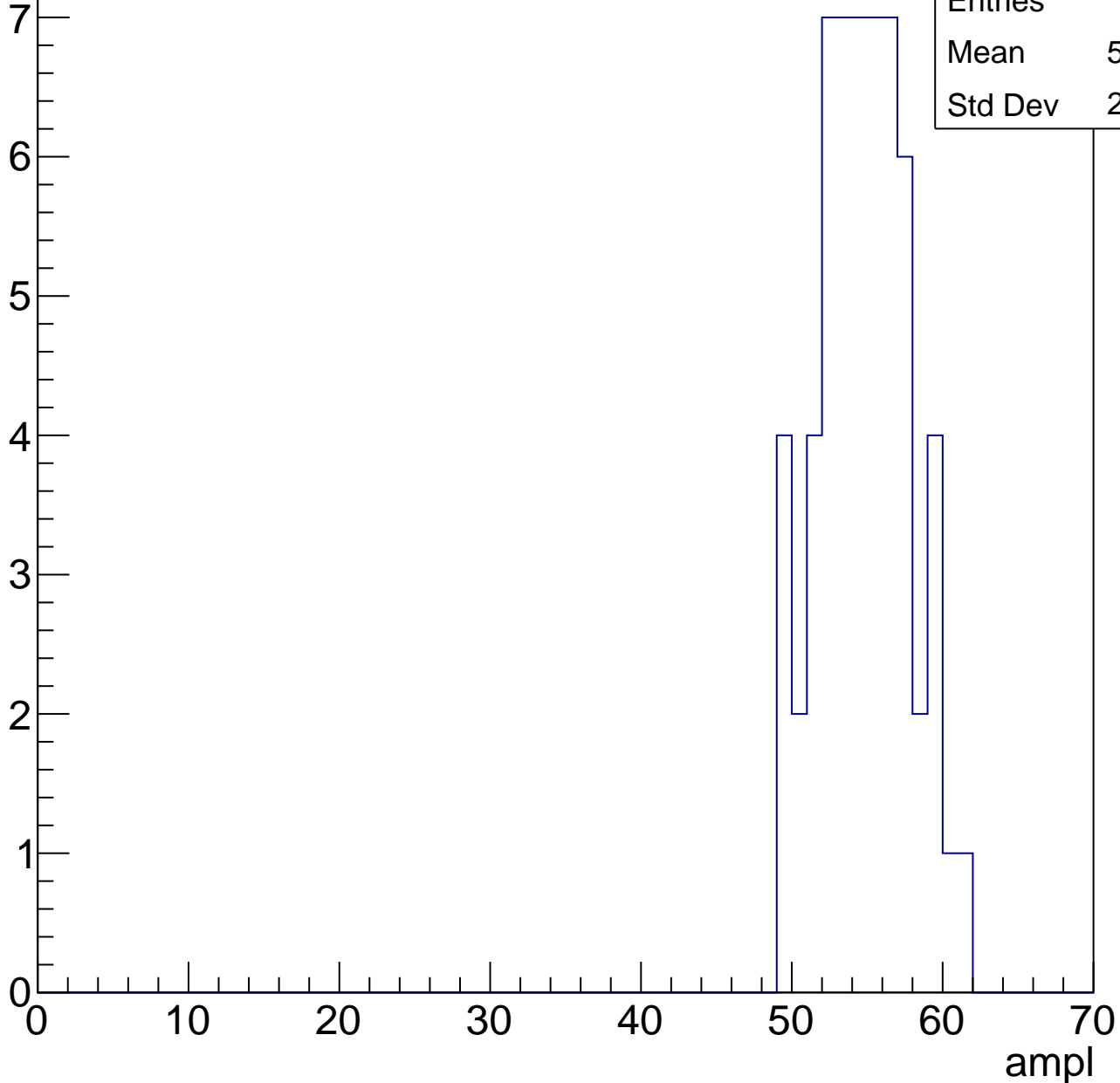


# B1L000S, U23-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

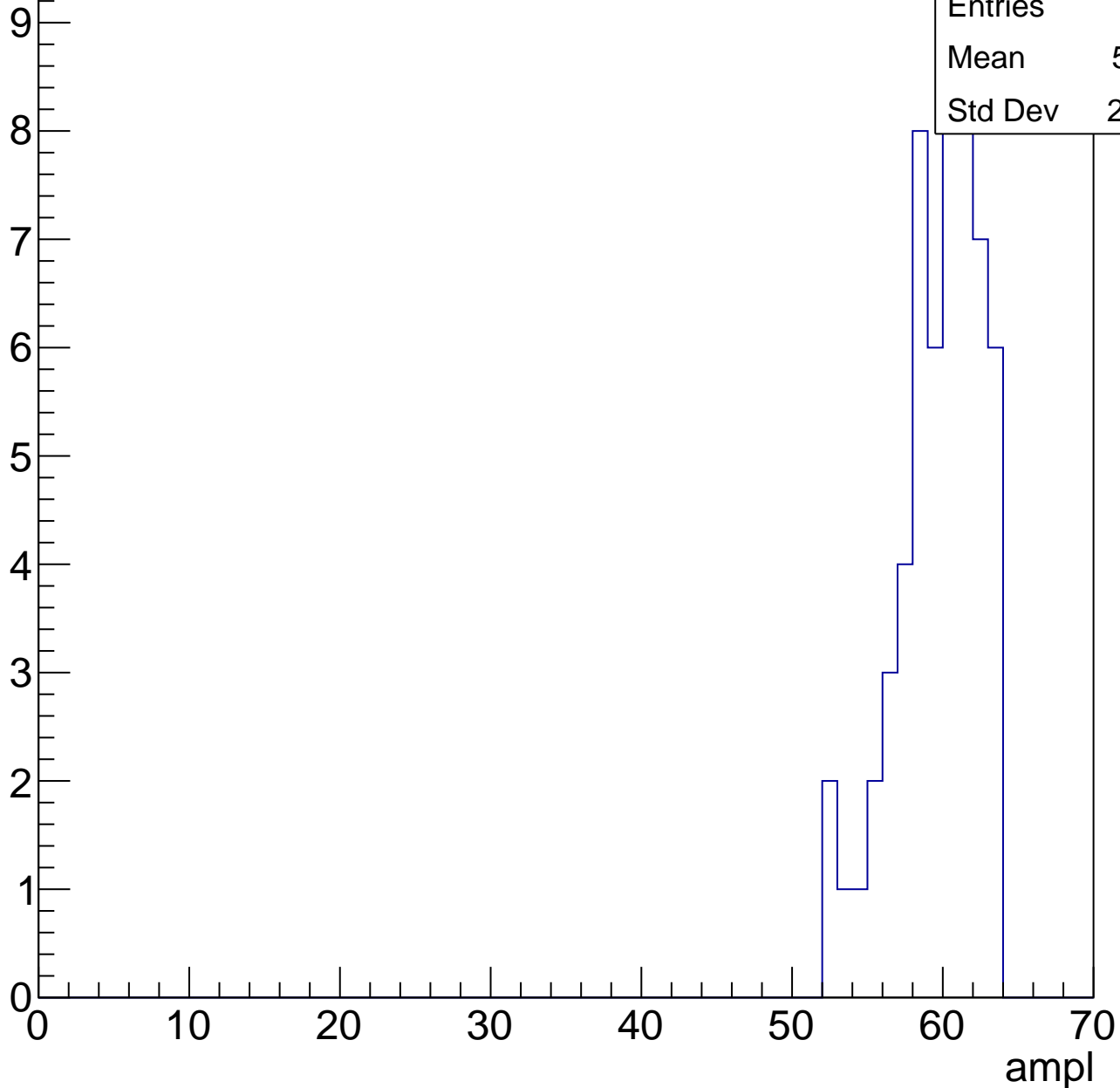
Entries	59
Mean	54.32
Std Dev	2.919



# B1L000S, U23-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

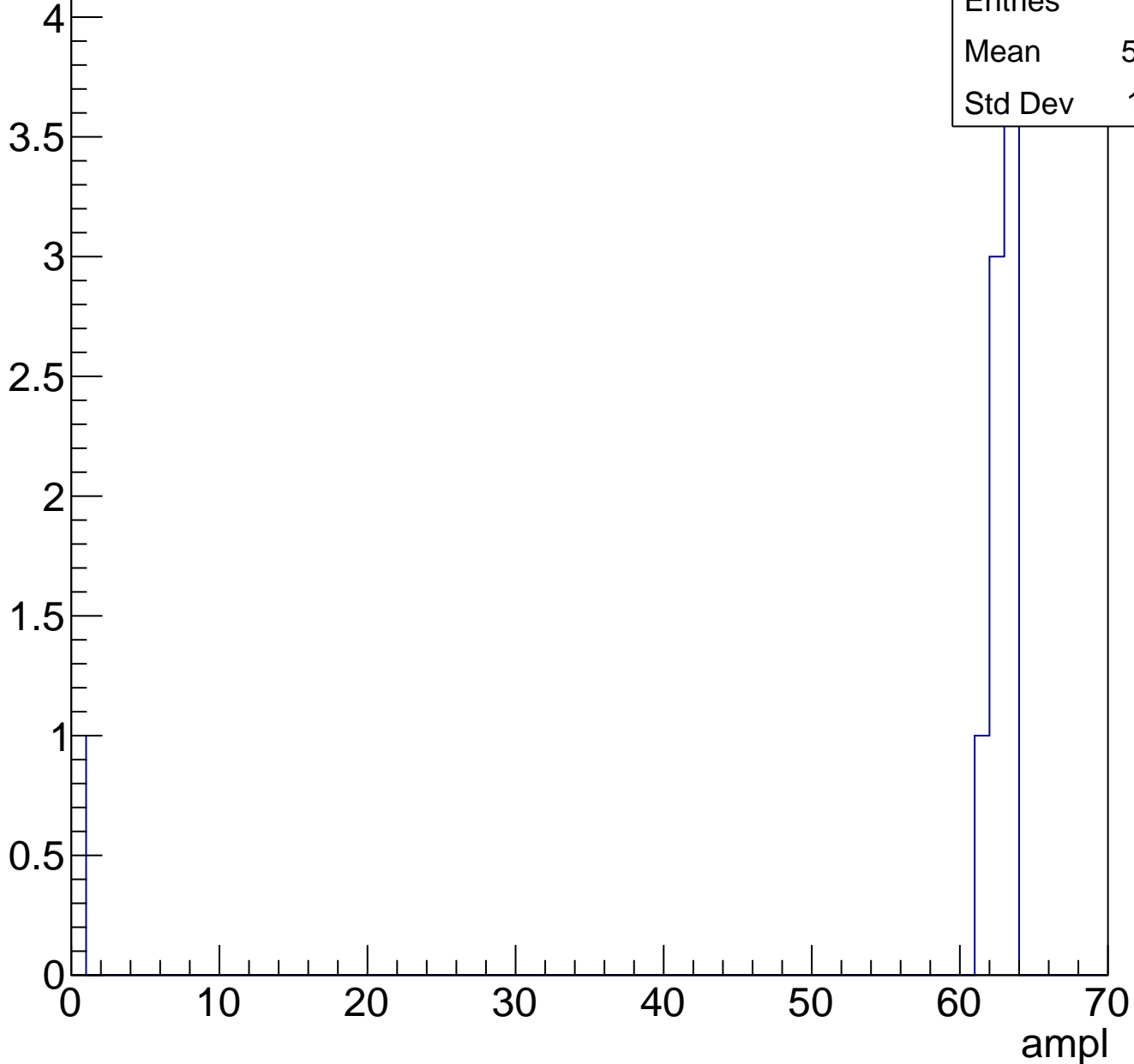


Entries	57
Mean	59.21
Std Dev	2.777

# B1L000S, U23-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch108, adc0

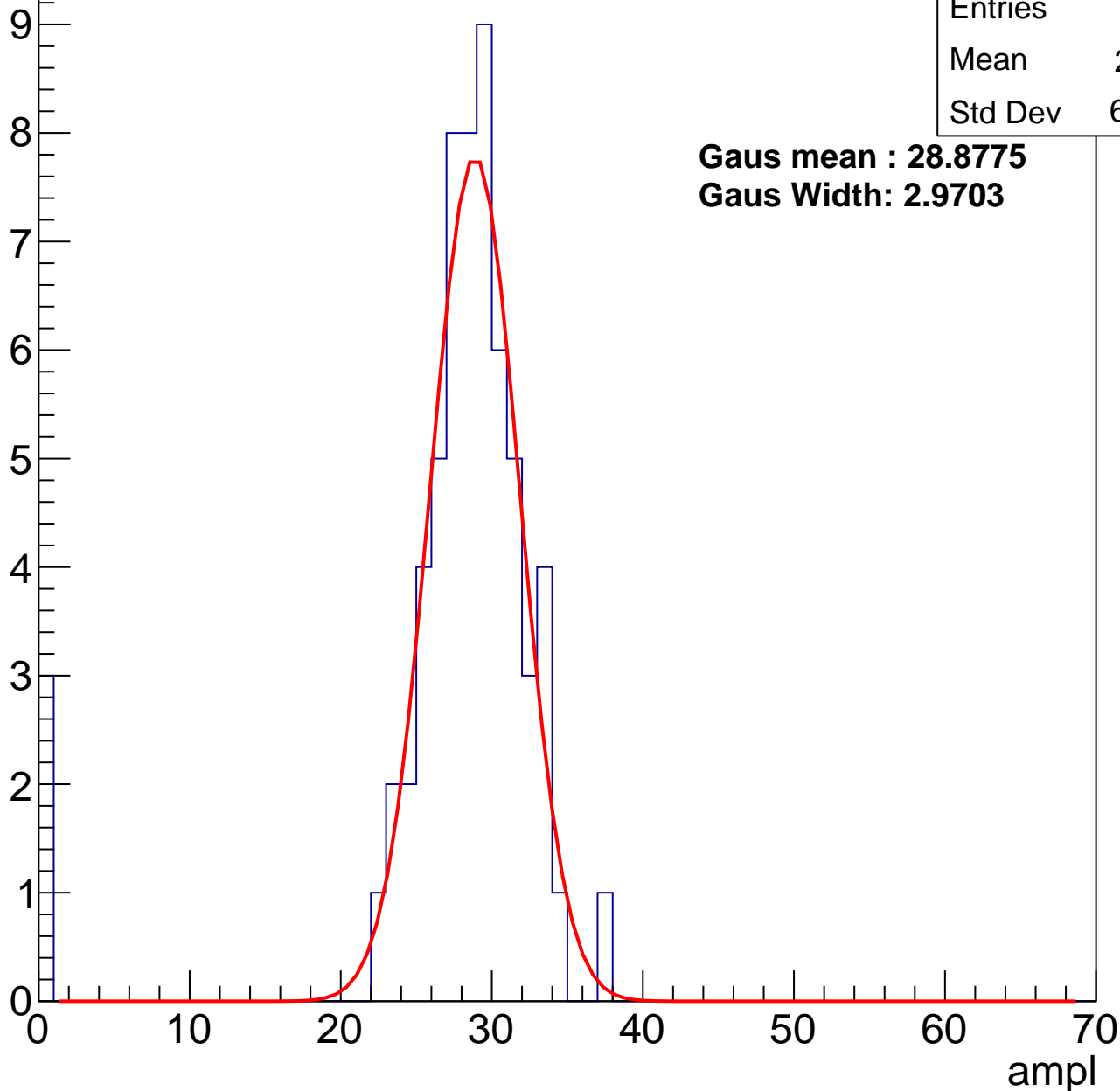
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	27.11
Std Dev	6.754

**Gaus mean : 28.8775**

**Gaus Width: 2.9703**



# B1L000S, U23-ch108, adc1

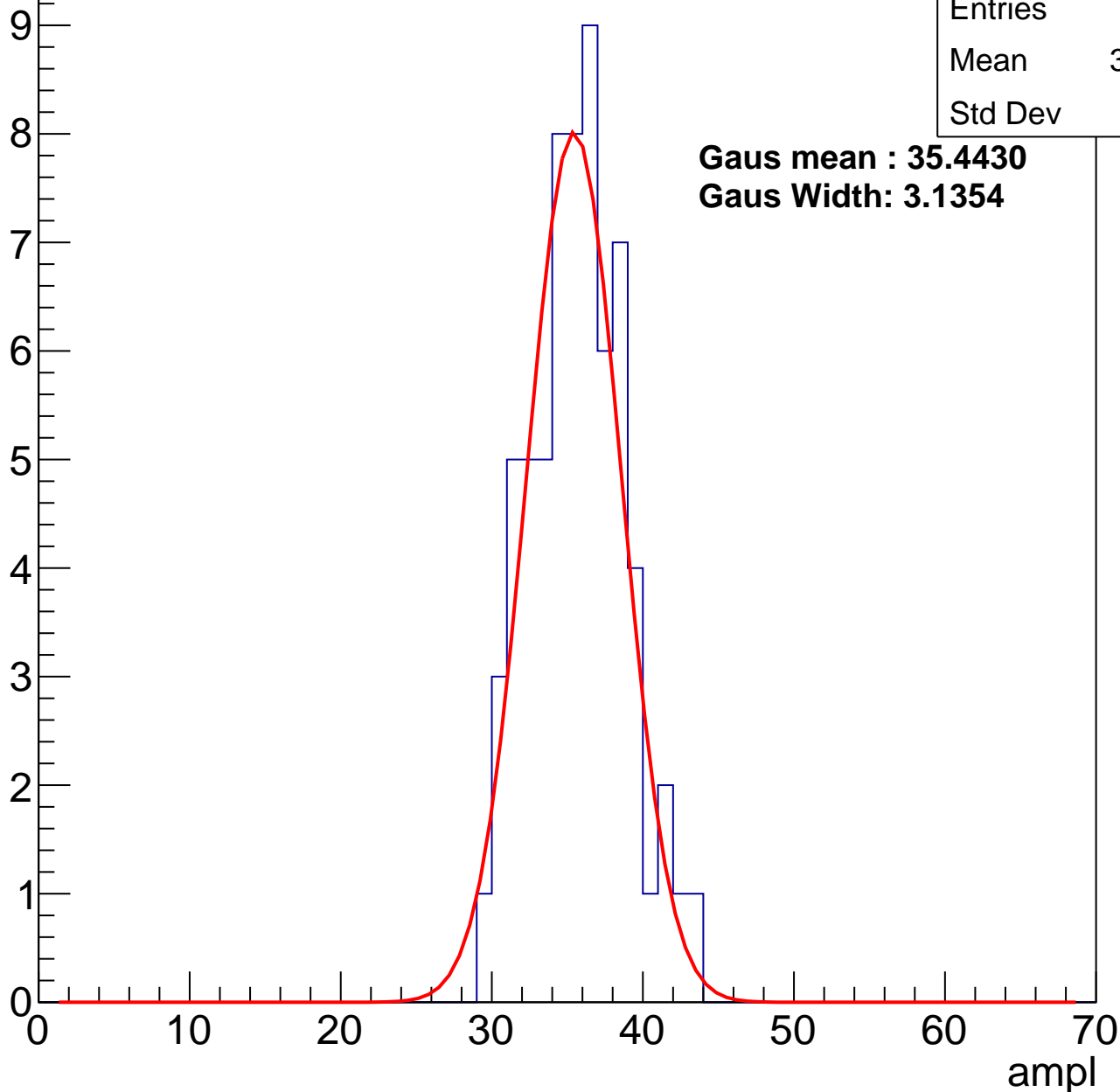
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.24
Std Dev	3.09

**Gaus mean : 35.4430**

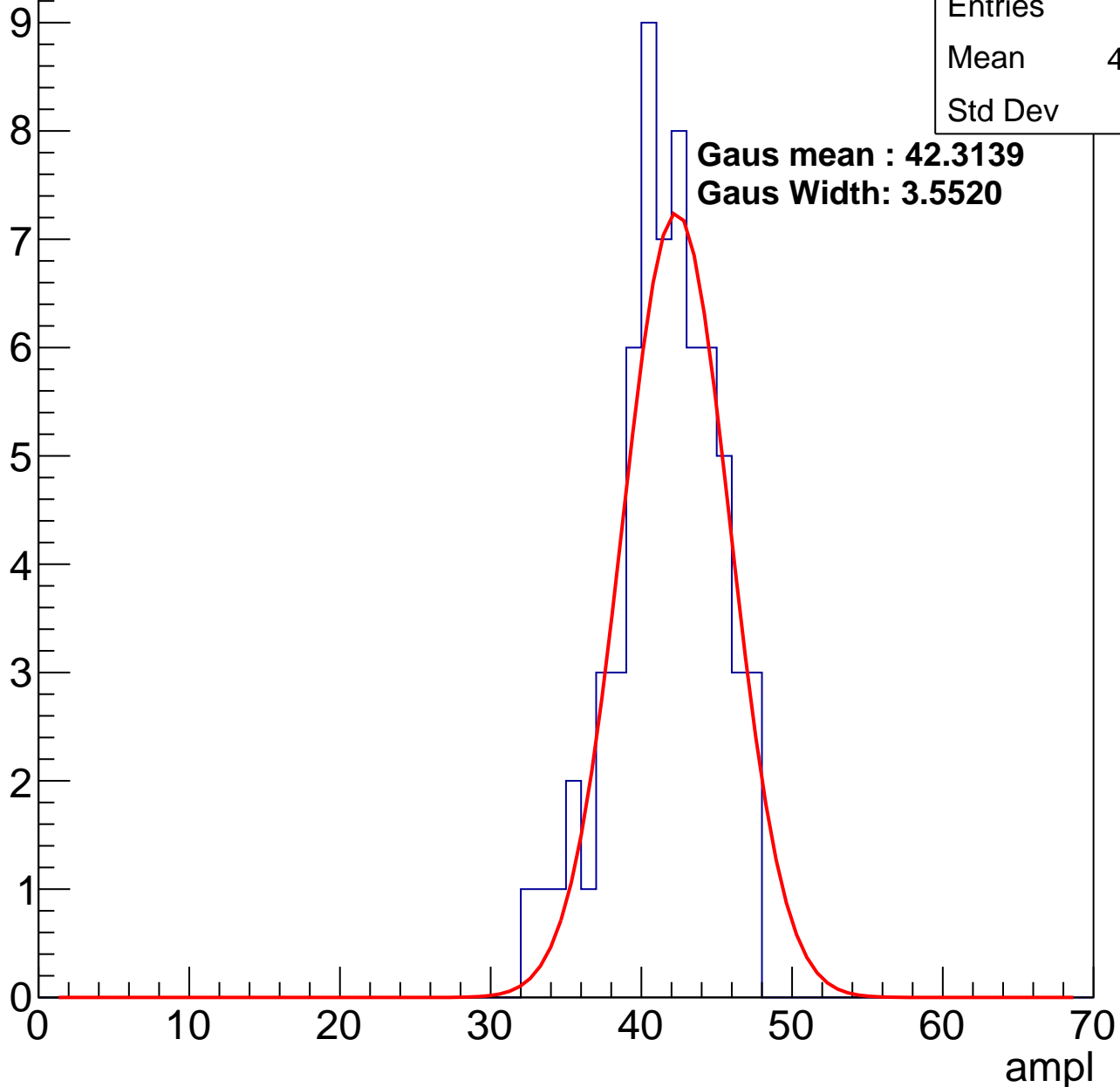
**Gaus Width: 3.1354**



# B1L000S, U23-ch108, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

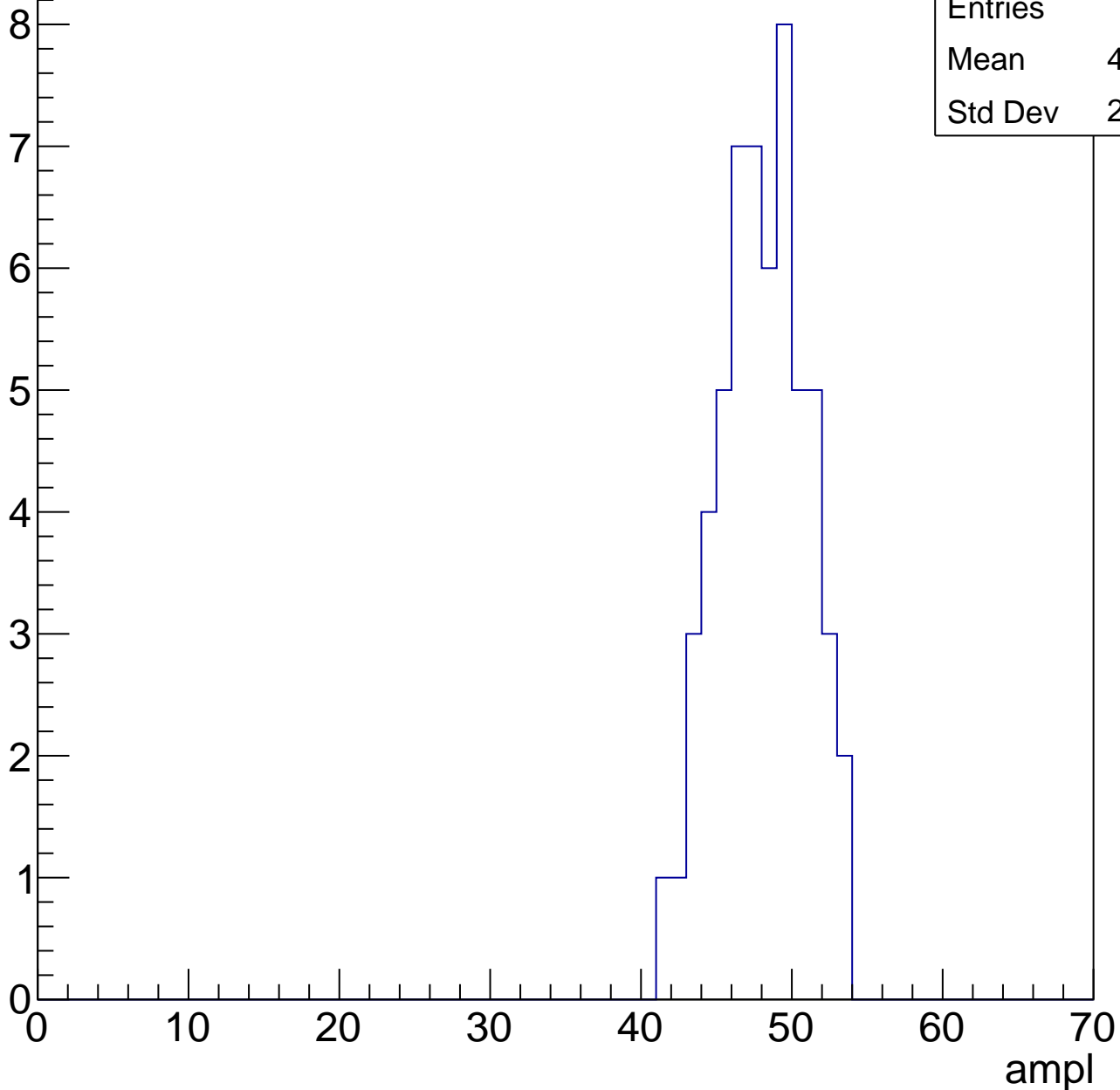


# B1L000S, U23-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.56
Std Dev	2.853

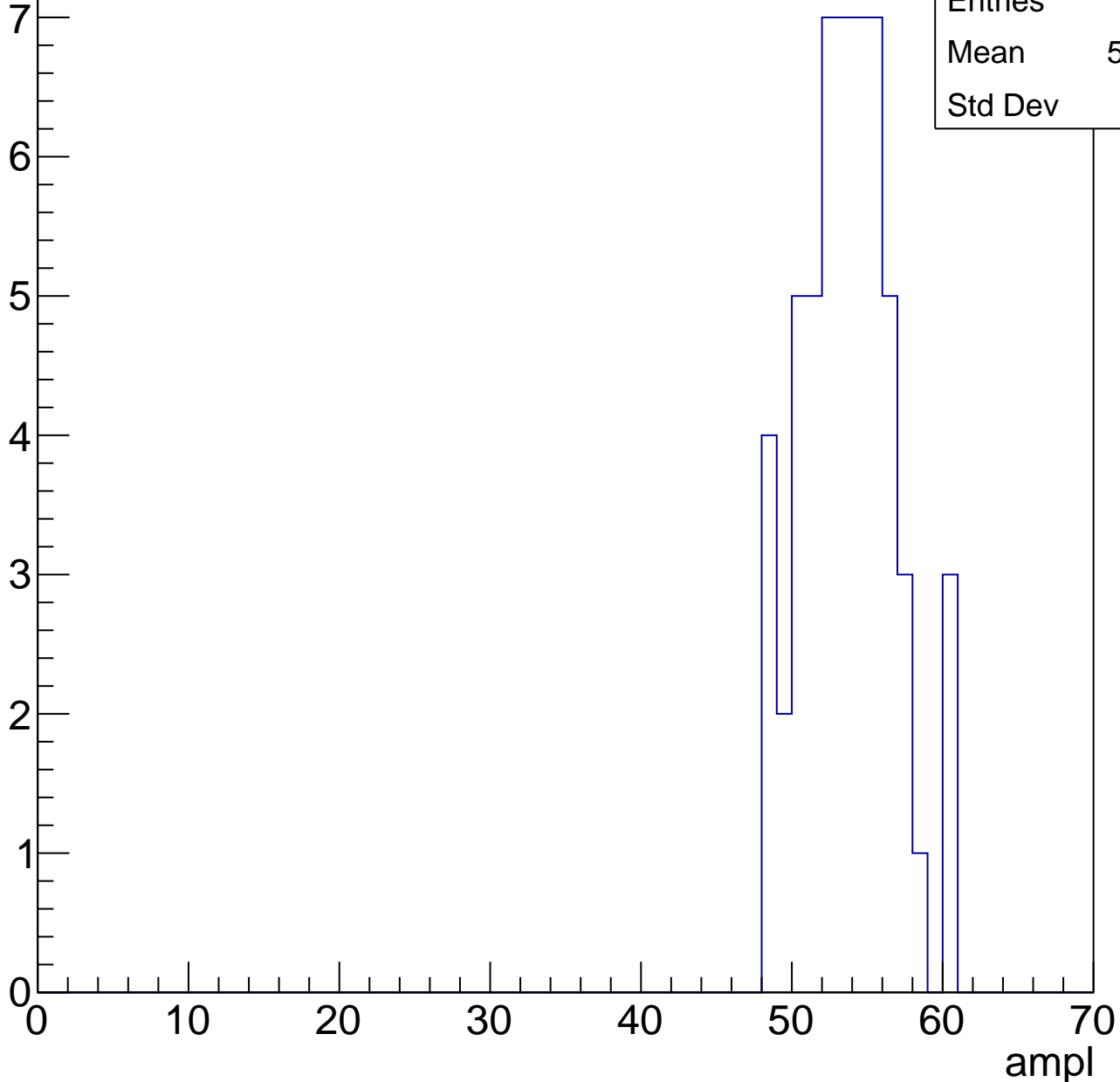


# B1L000S, U23-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	53.25
Std Dev	2.99

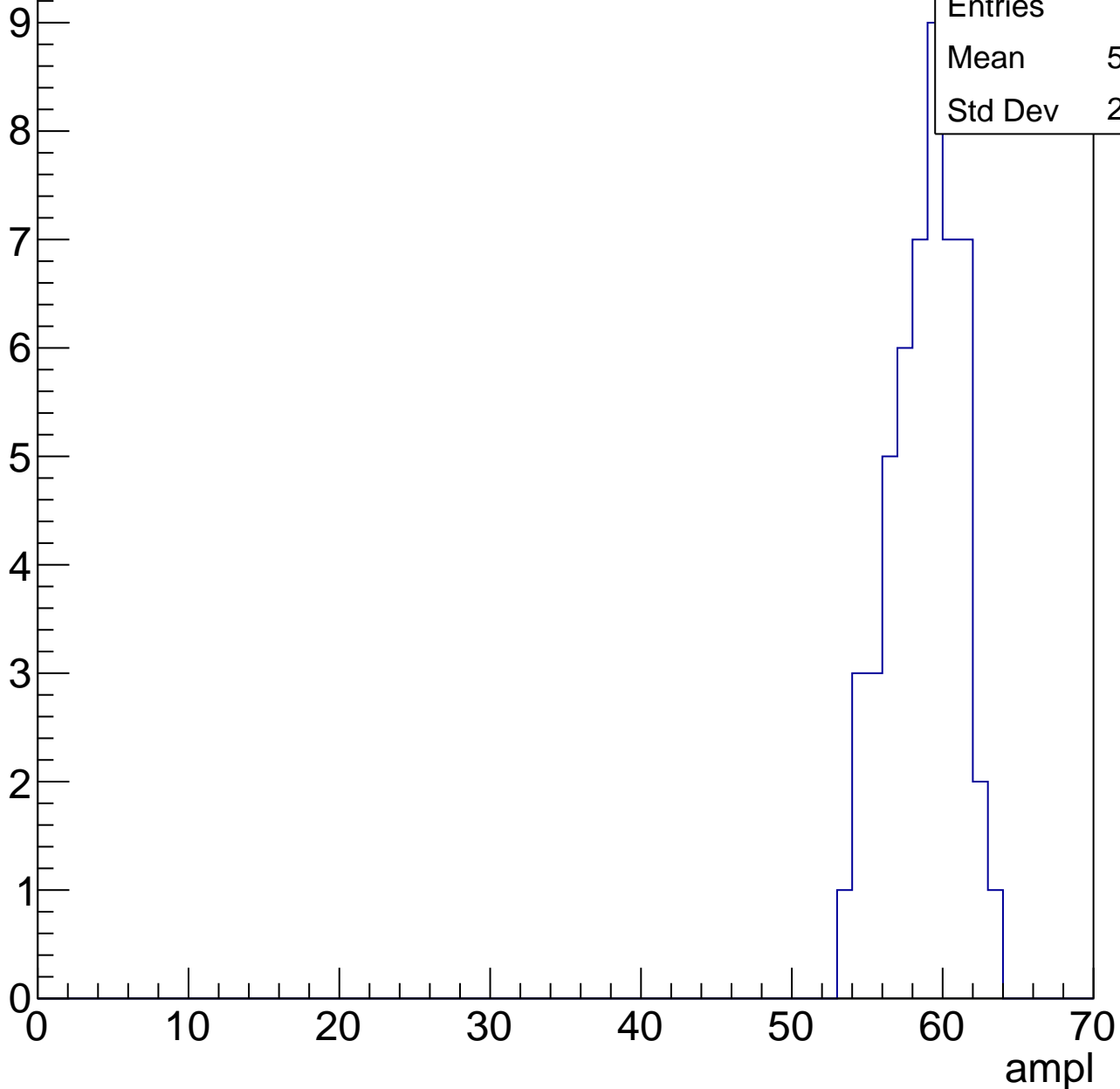


# B1L000S, U23-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.29
Std Dev	2.337

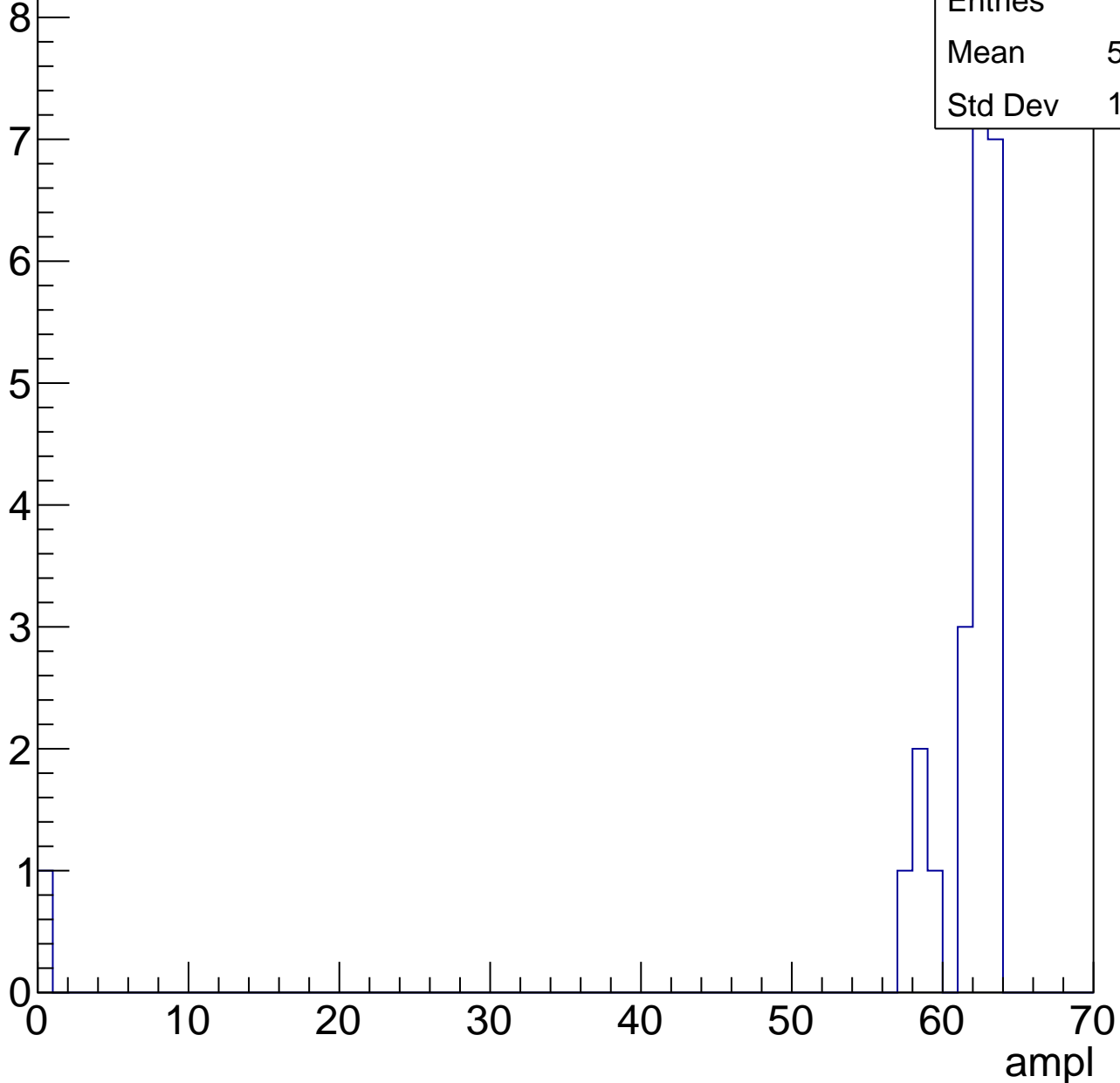


# B1L000S, U23-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	58.78
Std Dev	12.65

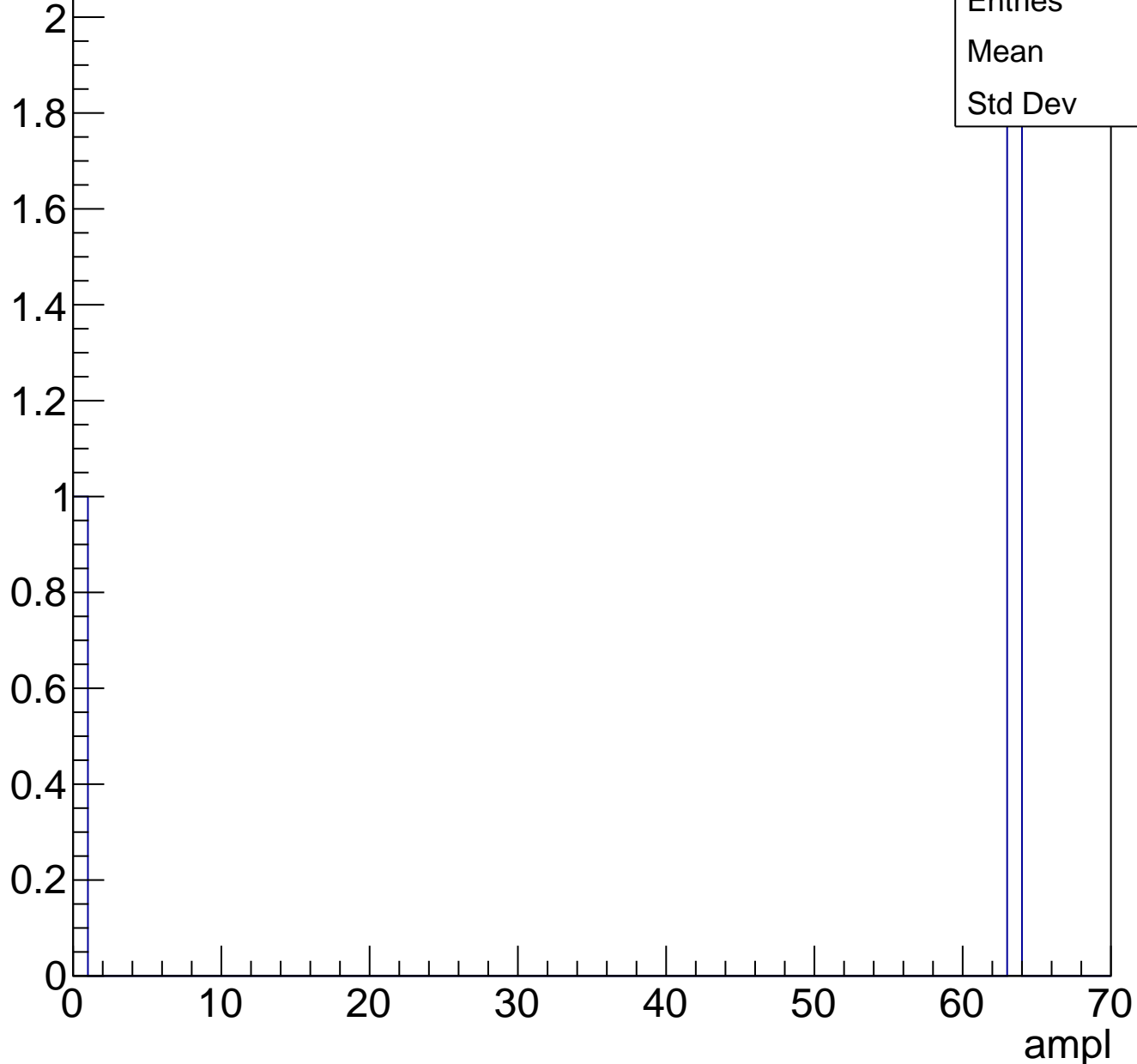




# B1L000S, U23-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	29.93
Std Dev	3.536

**Gaus mean : 30.2151**

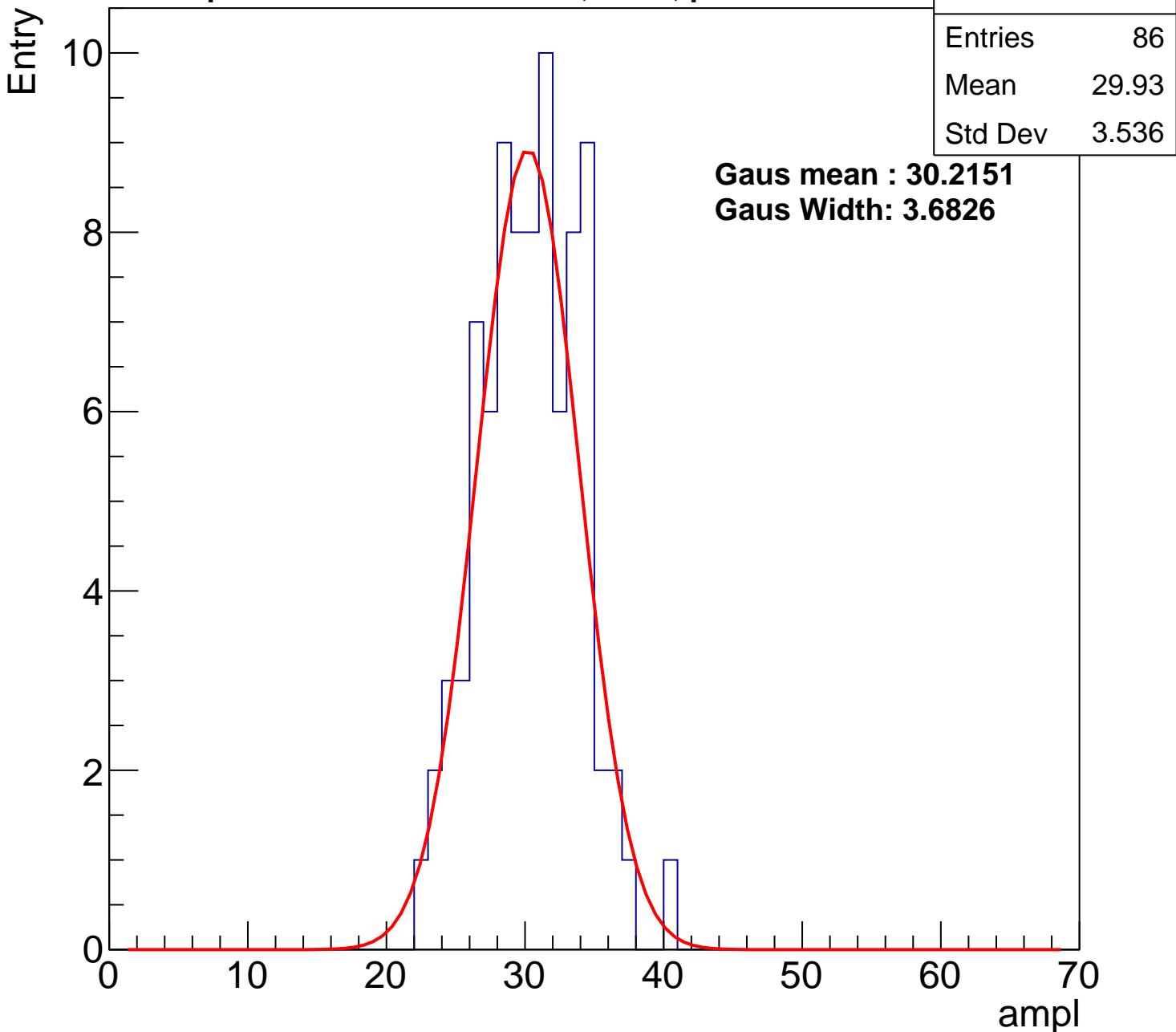
**Gaus Width: 3.6826**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch109, adc1

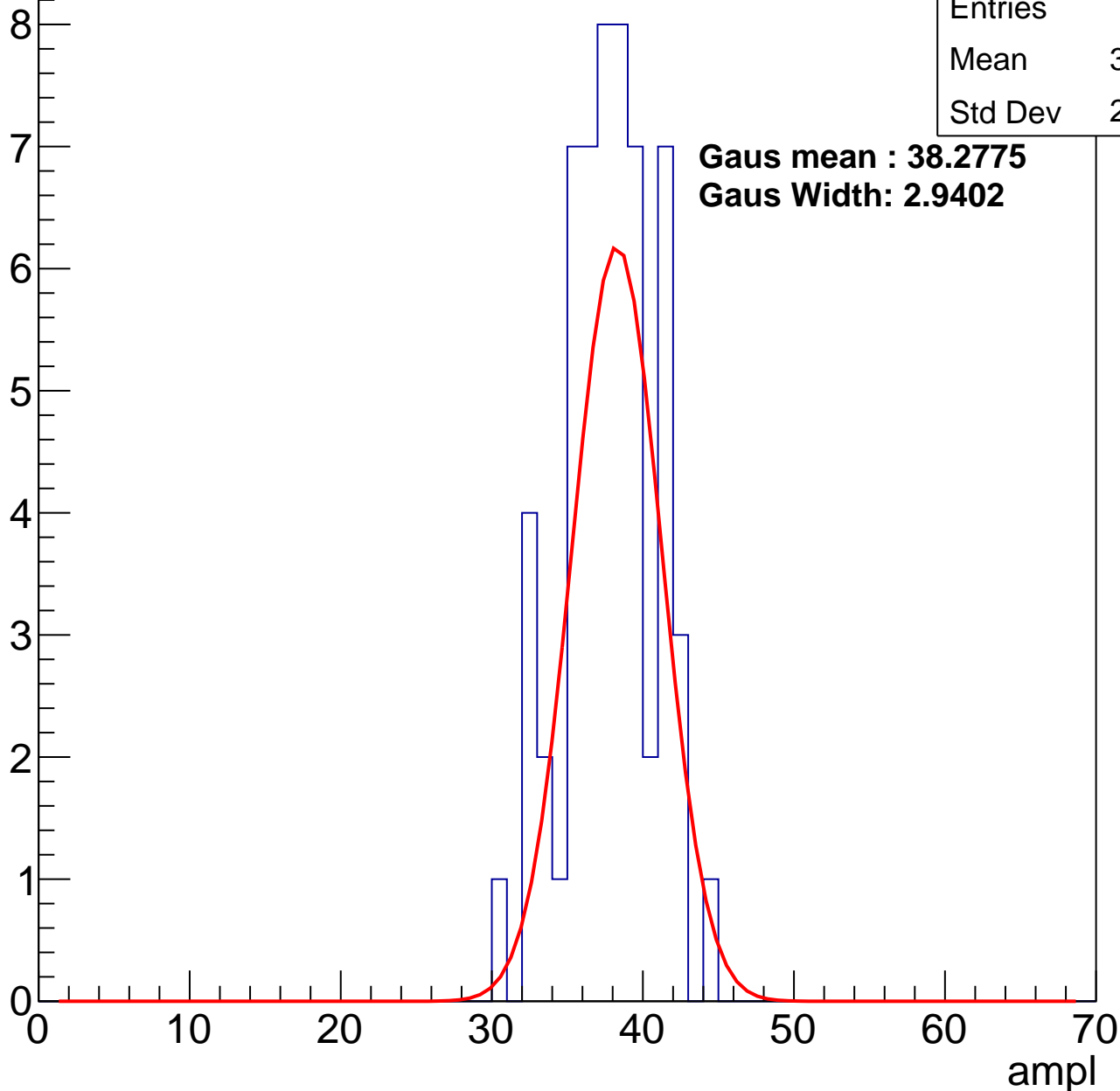
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	37.33
Std Dev	2.962

**Gaus mean : 38.2775**

**Gaus Width: 2.9402**



# B1L000S, U23-ch109, adc2

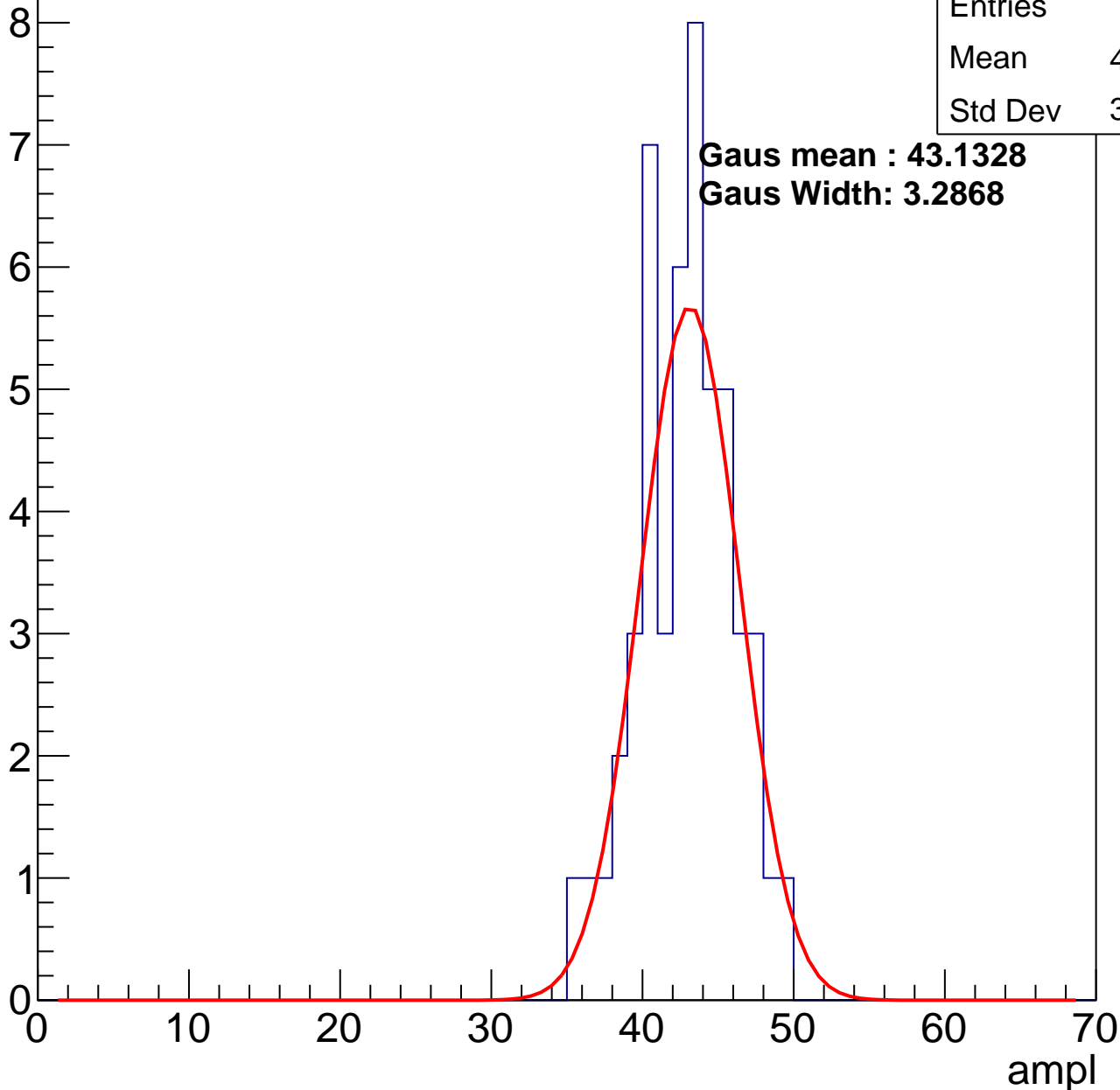
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	42.42
Std Dev	3.073

**Gaus mean : 43.1328**

**Gaus Width: 3.2868**

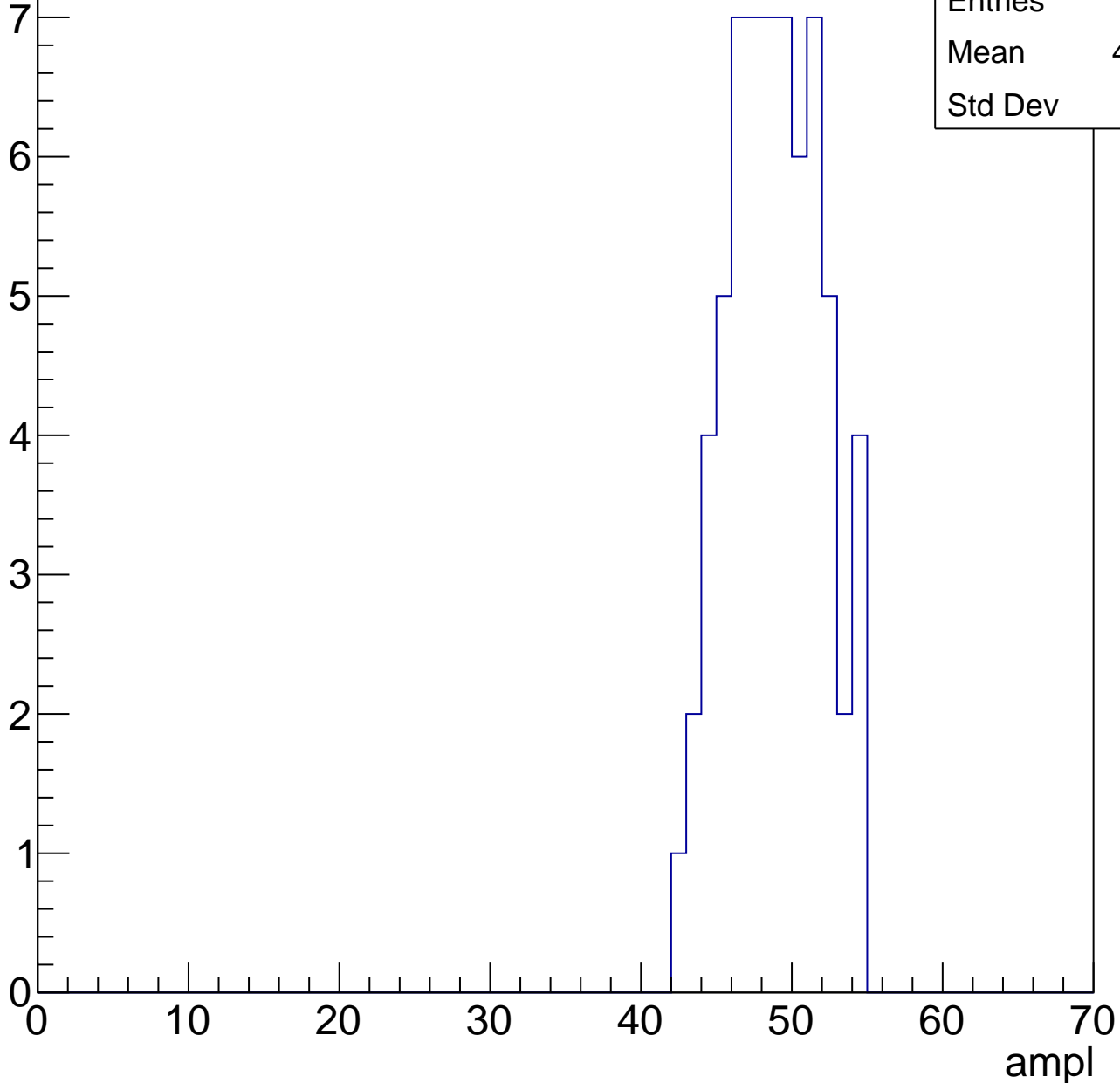


# B1L000S, U23-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.41
Std Dev	3.03

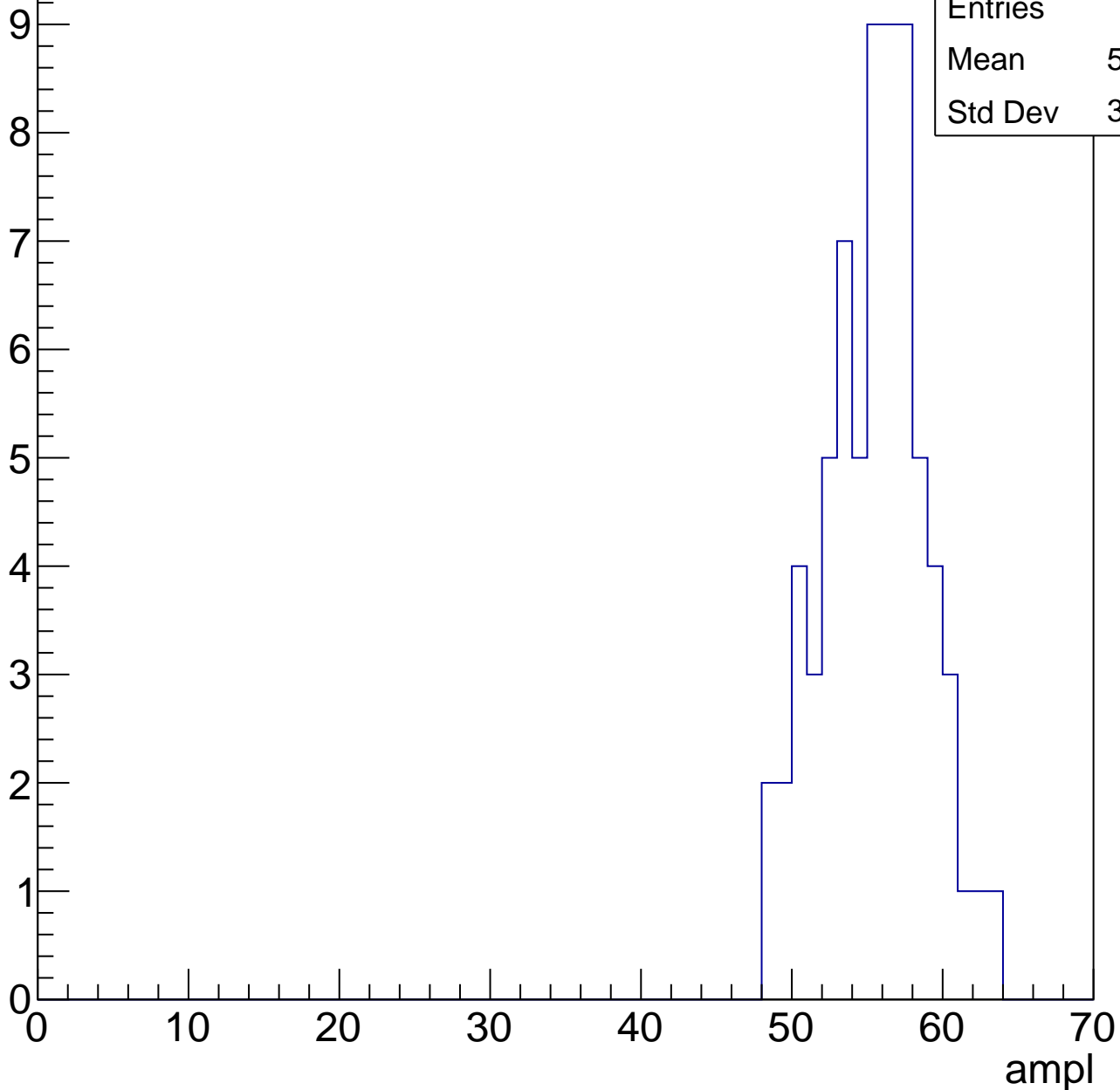


# B1L000S, U23-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

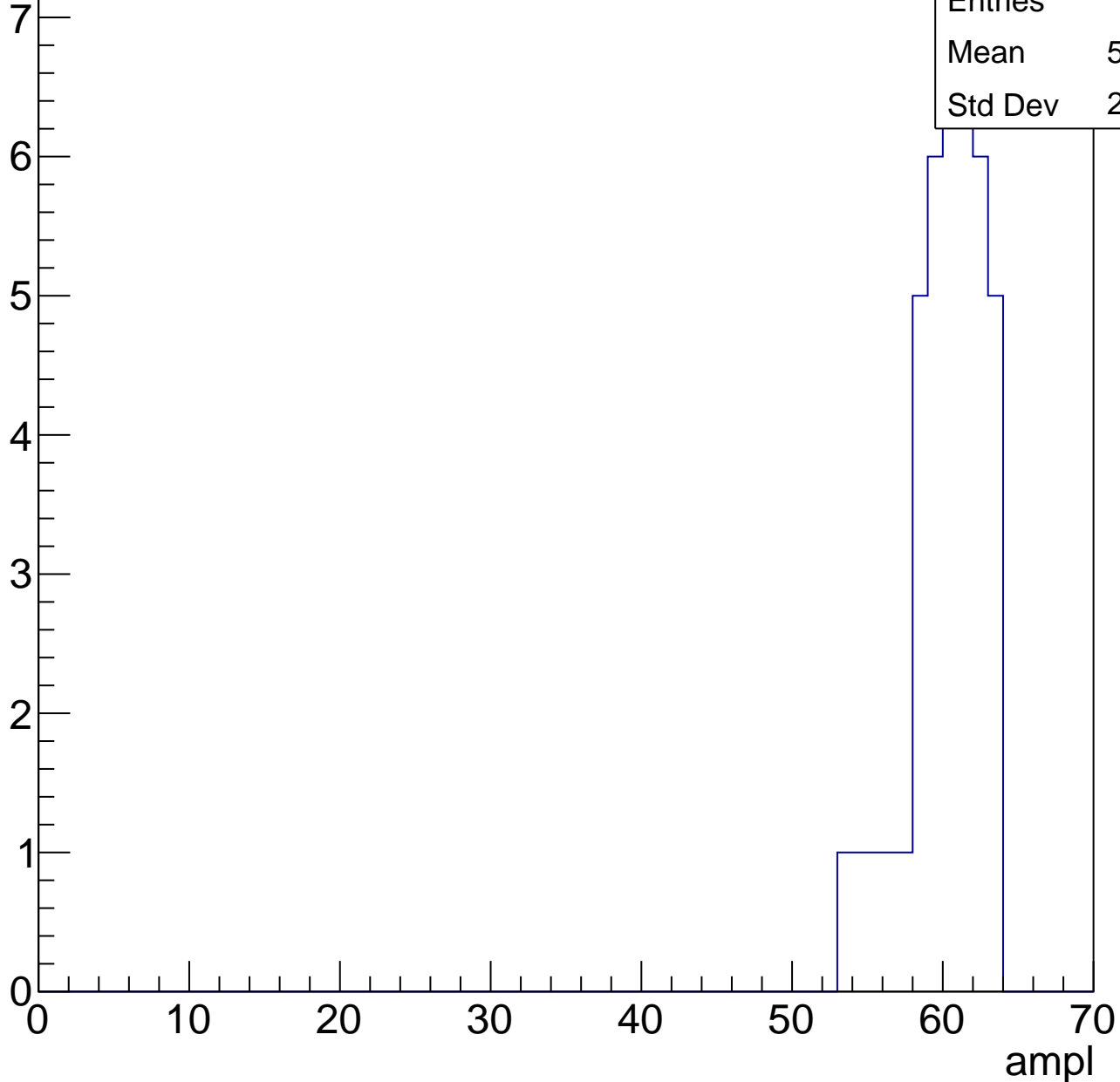
Entries	70
Mean	55.03
Std Dev	3.325



# B1L000S, U23-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

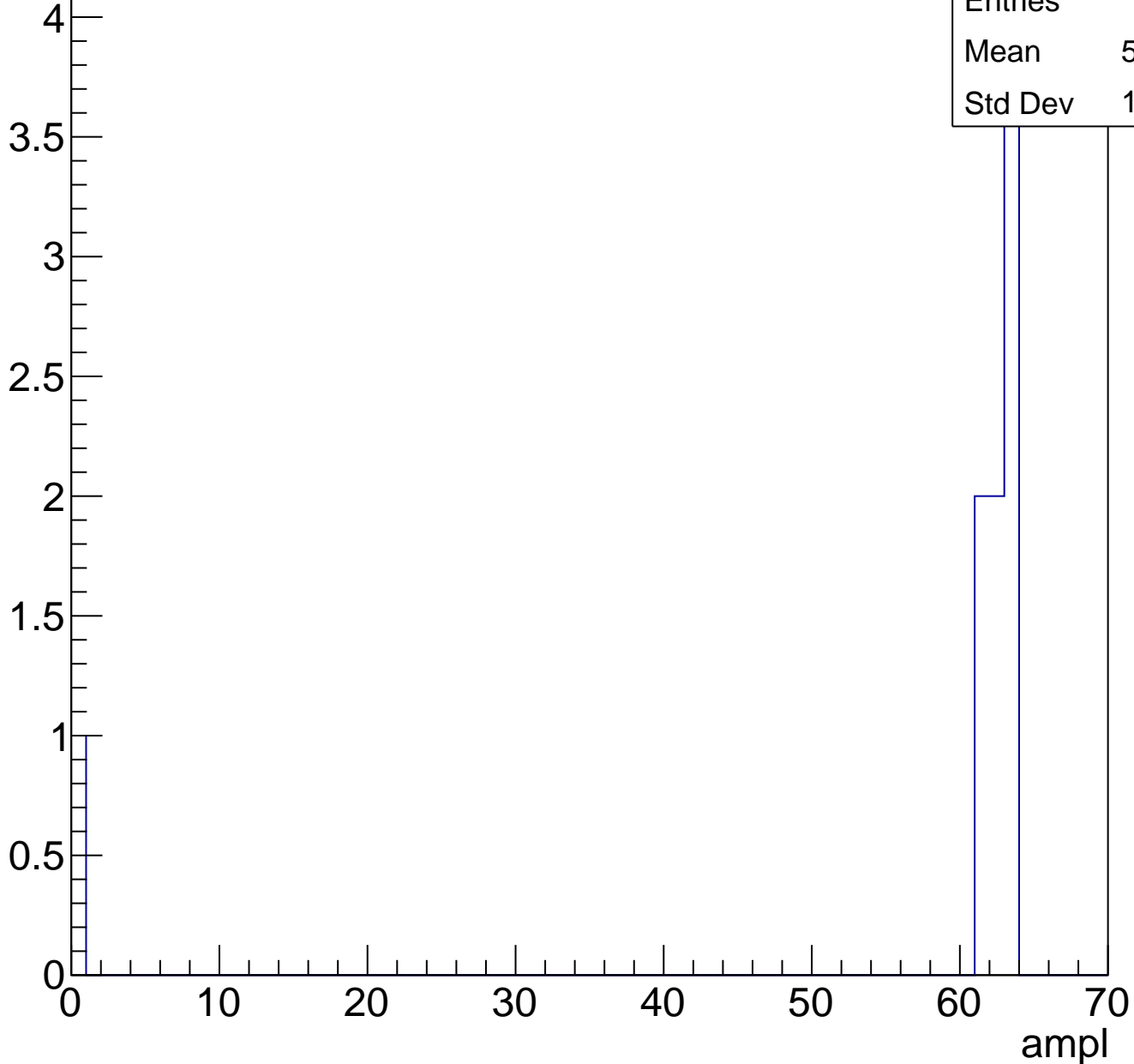


Entries	41
Mean	59.83
Std Dev	2.398

# B1L000S, U23-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch110, adc0

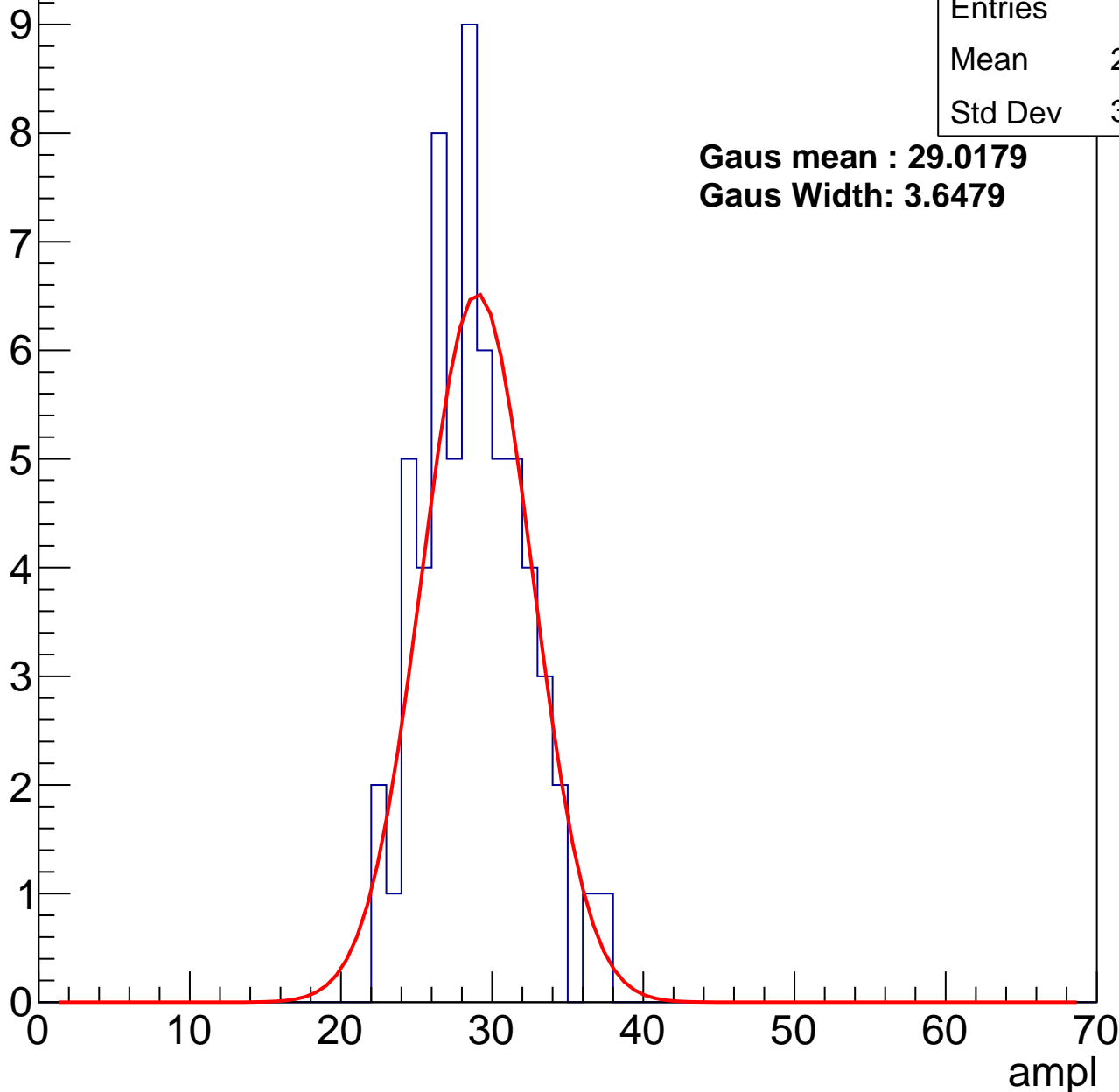
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.34
Std Dev	3.314

**Gaus mean : 29.0179**

**Gaus Width: 3.6479**



# B1L000S, U23-ch110, adc1

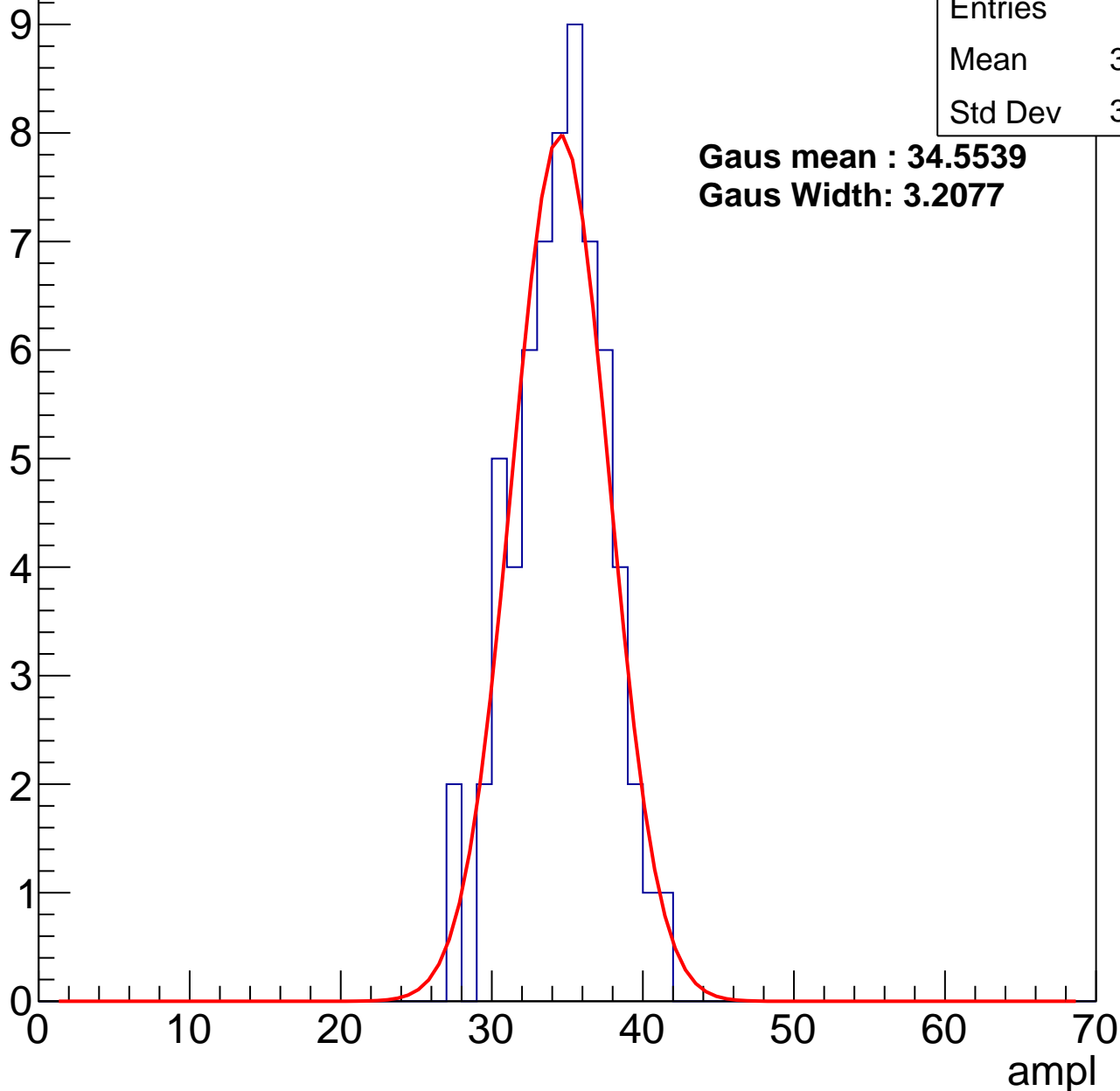
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.08
Std Dev	3.022

**Gaus mean : 34.5539**

**Gaus Width: 3.2077**



# B1L000S, U23-ch110, adc2

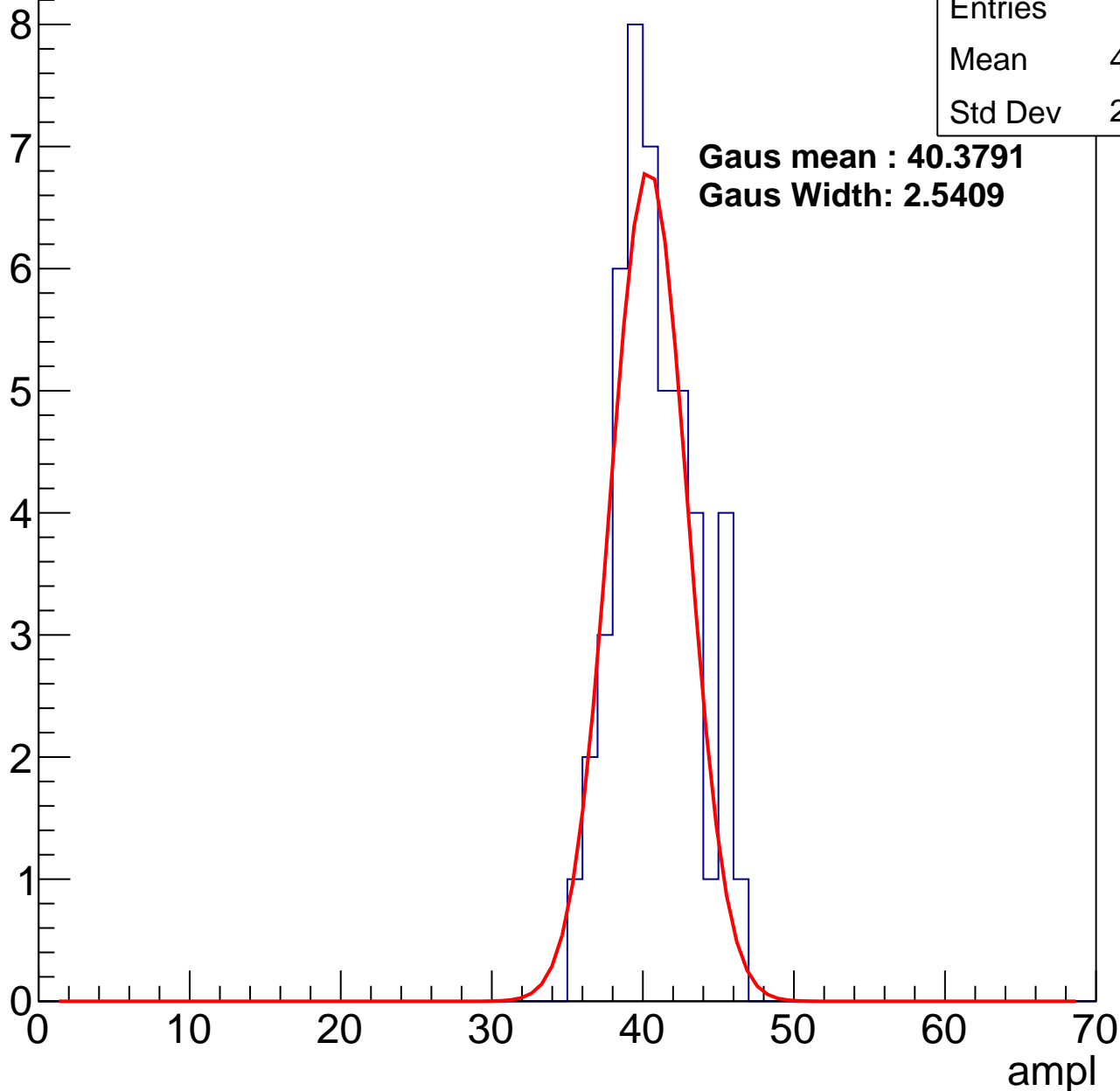
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	40.32
Std Dev	2.626

**Gaus mean : 40.3791**

**Gaus Width: 2.5409**

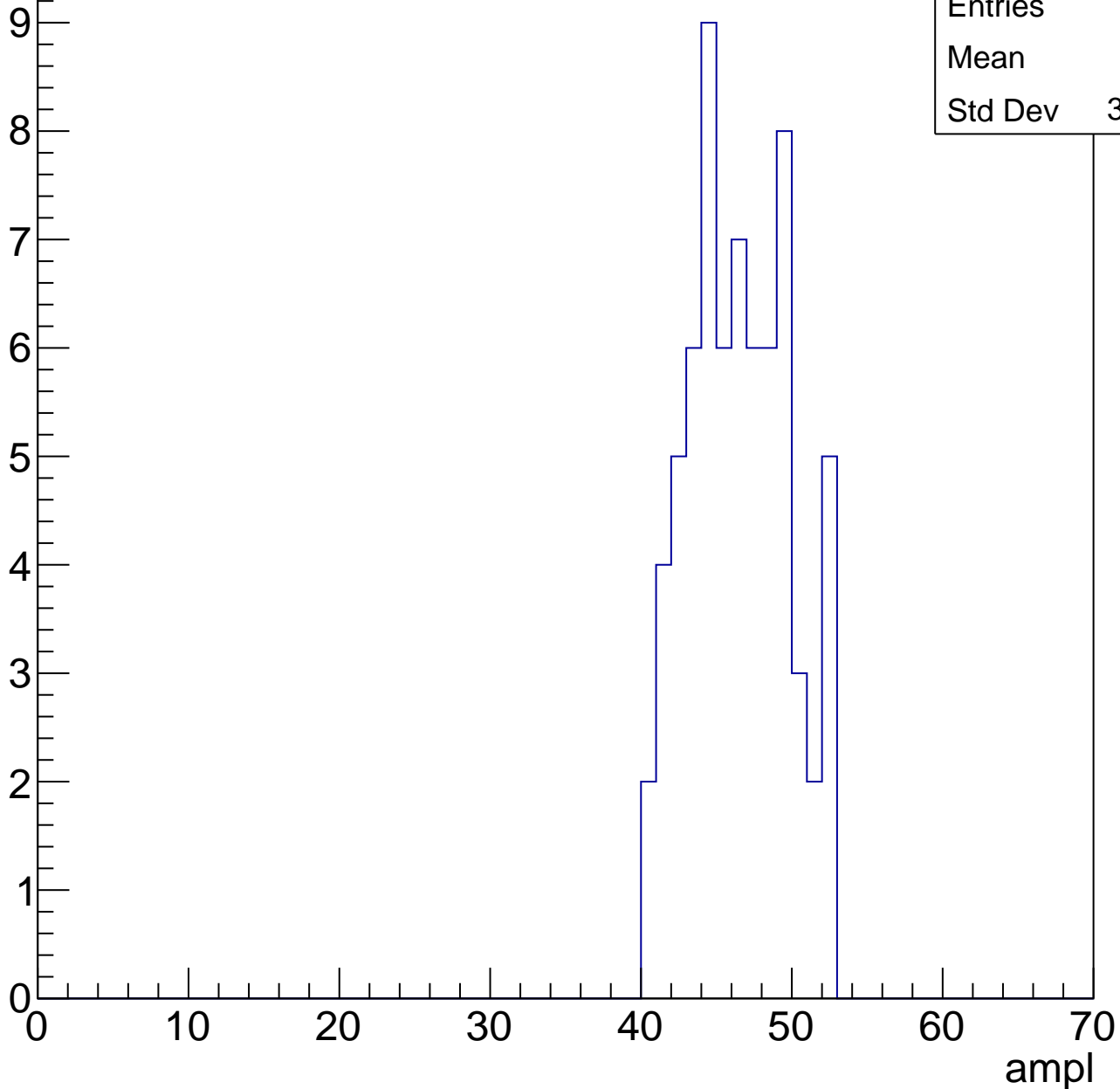


# B1L000S, U23-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	46
Std Dev	3.248

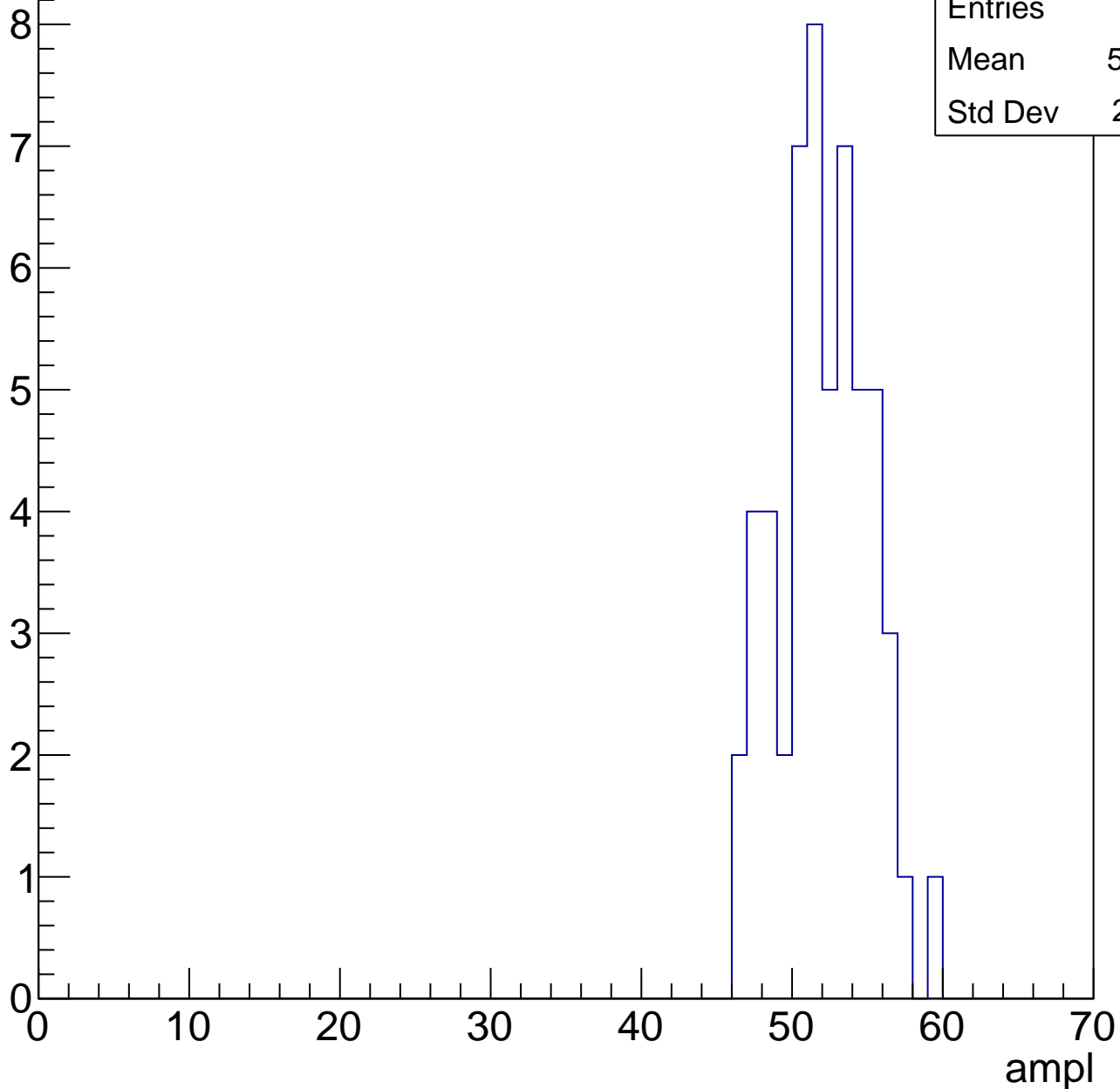


# B1L000S, U23-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	51.63
Std Dev	2.971

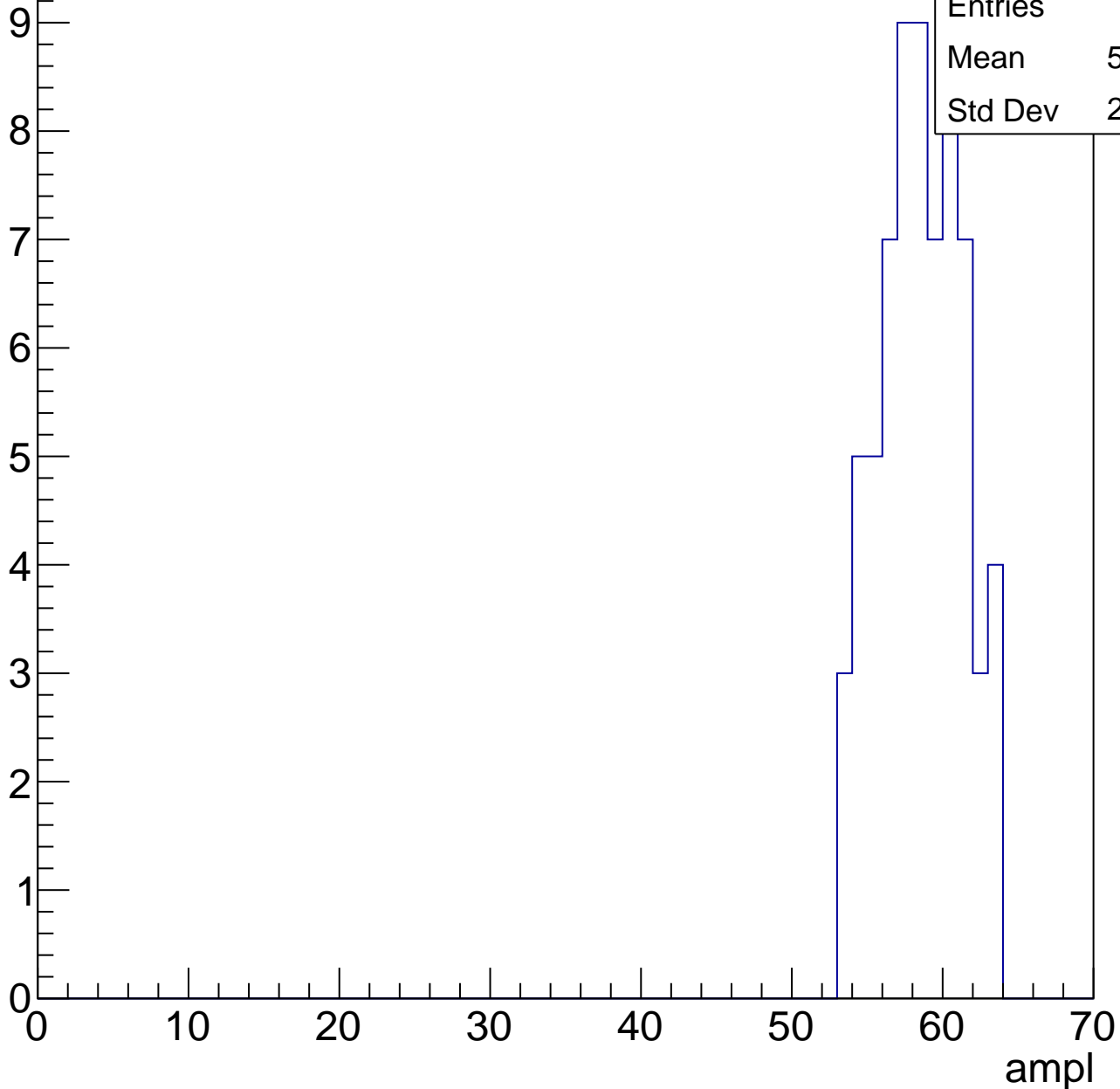


# B1L000S, U23-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	58.07
Std Dev	2.686

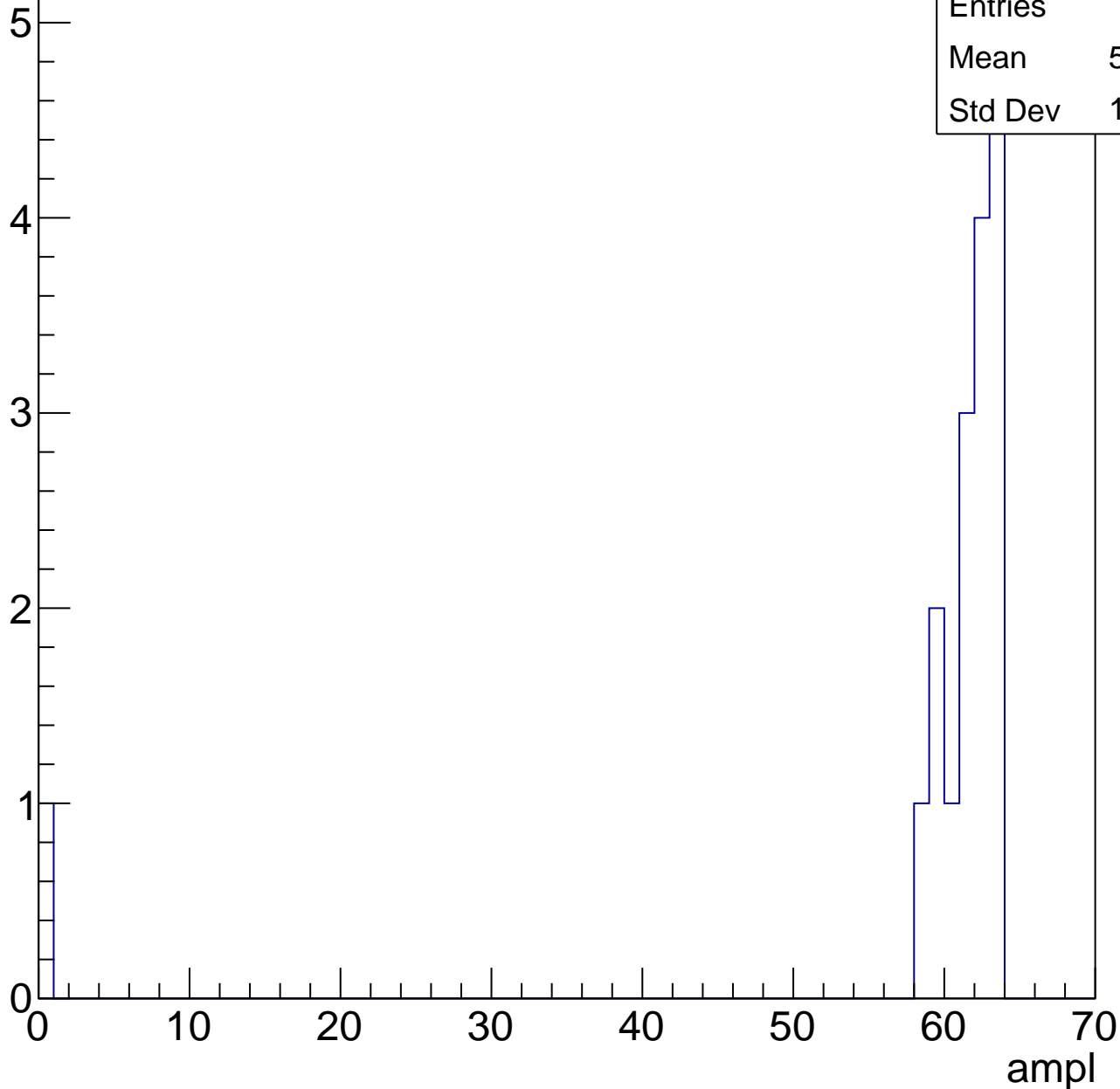


# B1L000S, U23-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.76
Std Dev	14.52

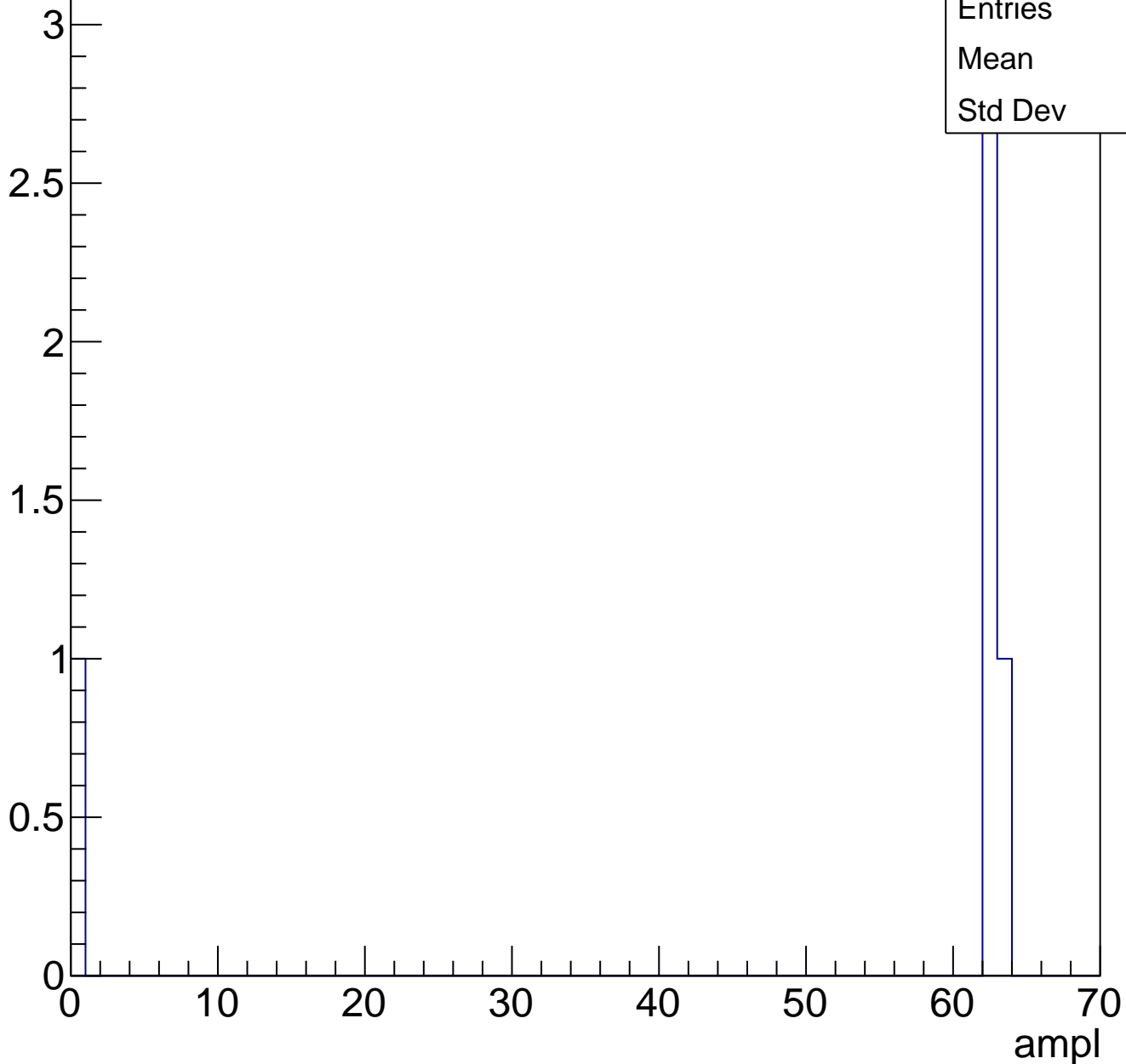




# B1L000S, U23-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	29.65
Std Dev	3.756

**Gaus mean : 30.5729**

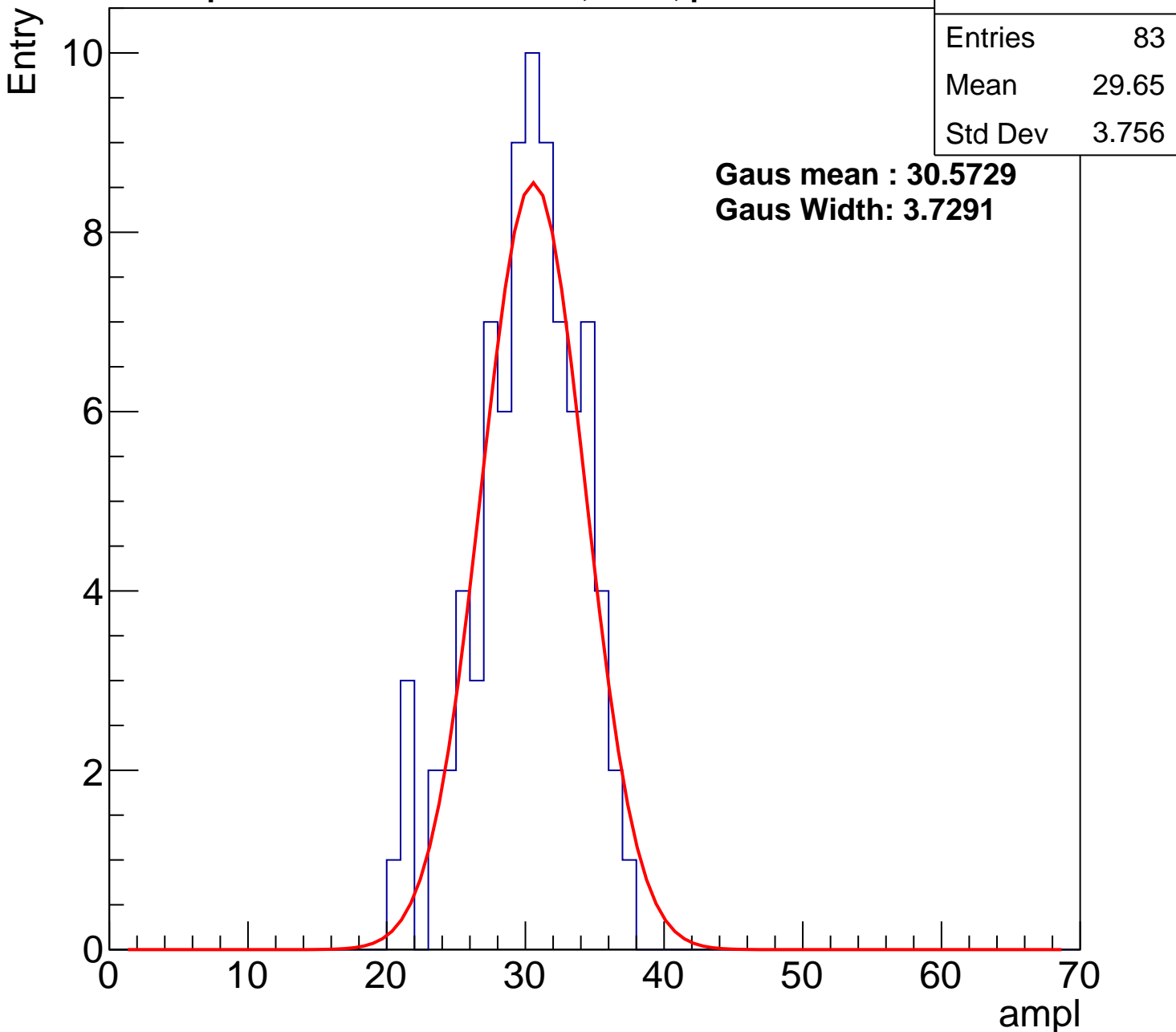
**Gaus Width: 3.7291**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	37.3
Std Dev	2.983

**Gaus mean : 37.5433**

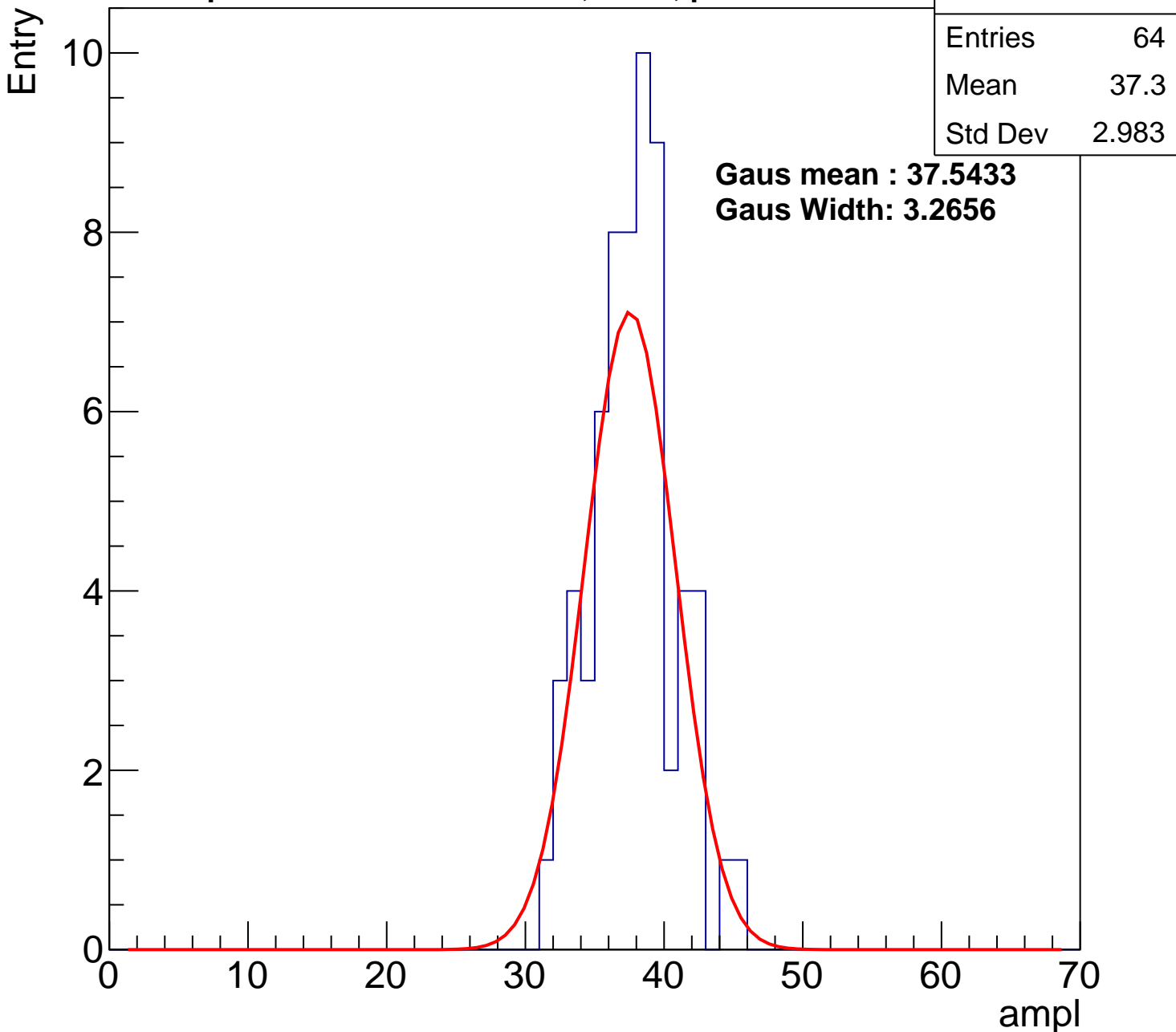
**Gaus Width: 3.2656**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch111, adc2

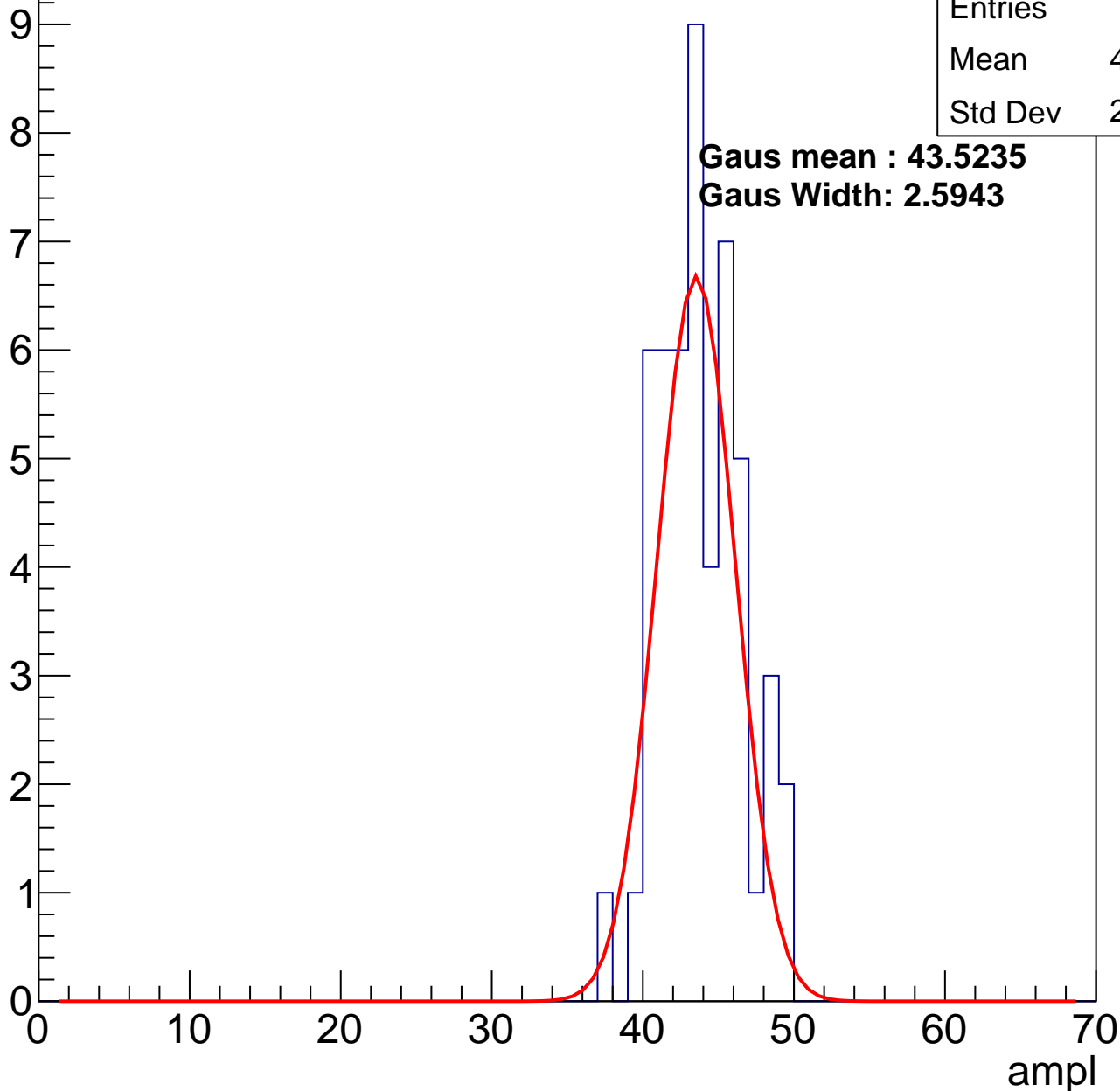
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	43.35
Std Dev	2.692

**Gaus mean : 43.5235**

**Gaus Width: 2.5943**

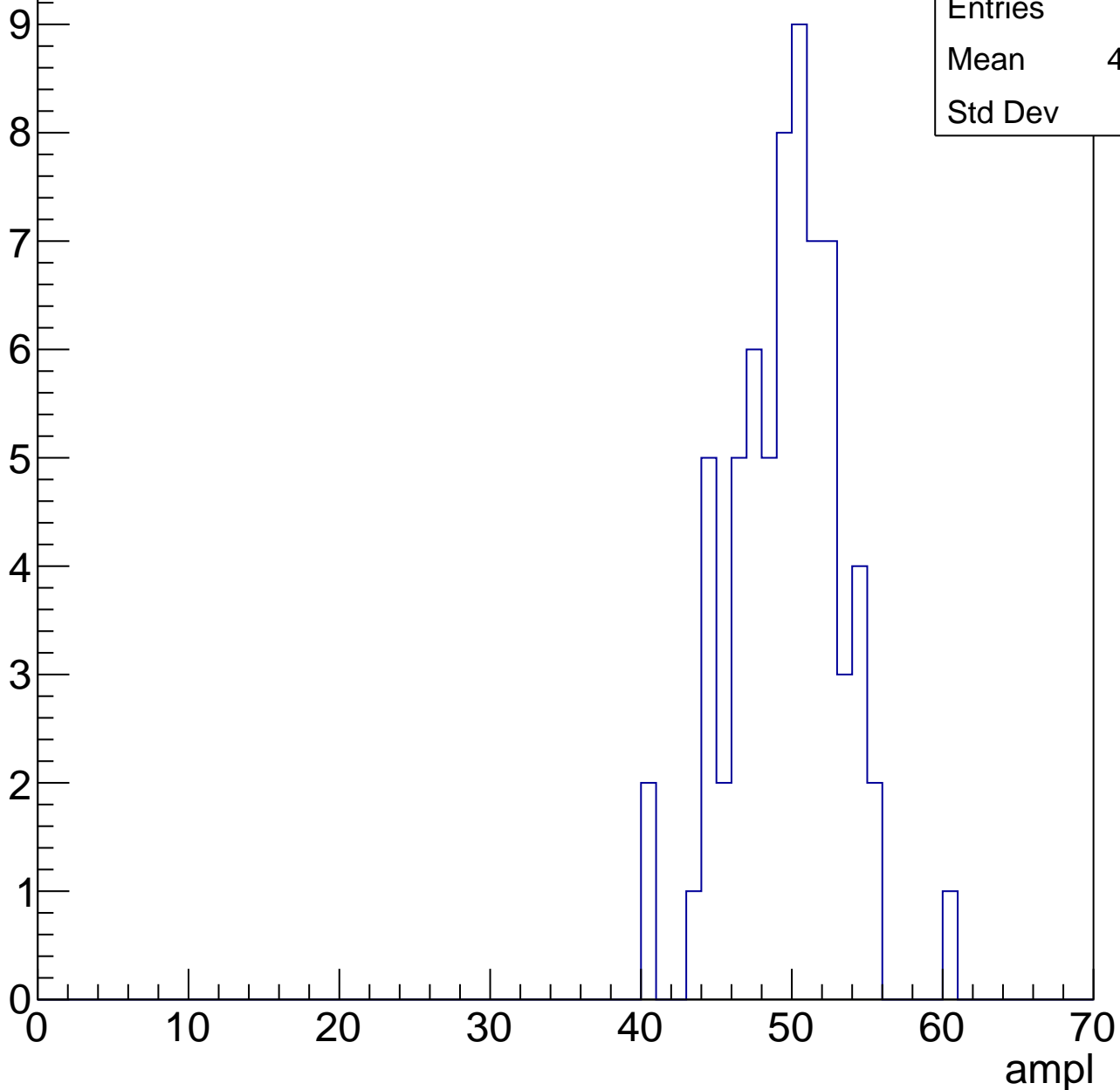


# B1L000S, U23-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	49.15
Std Dev	3.6

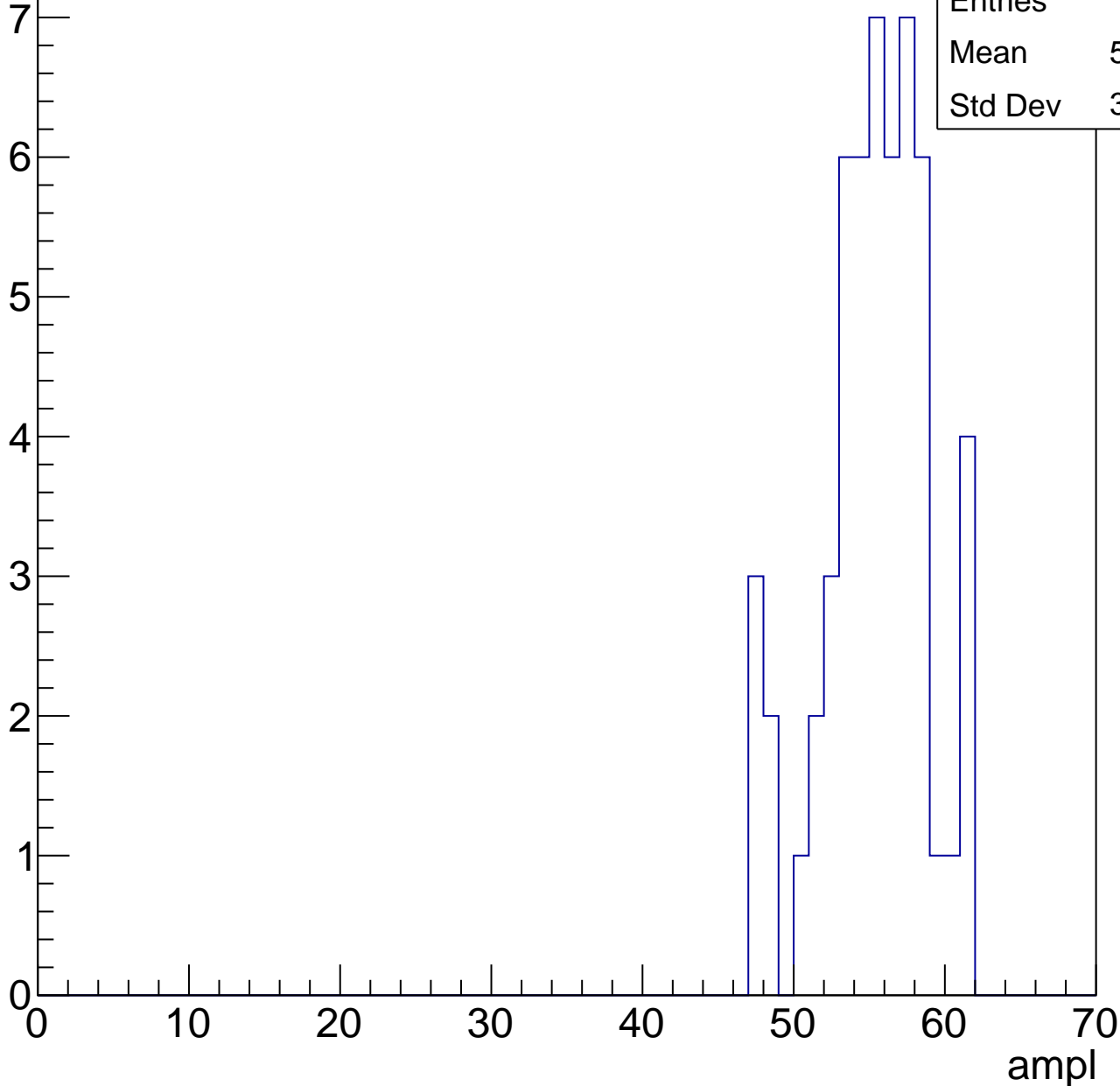


# B1L000S, U23-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

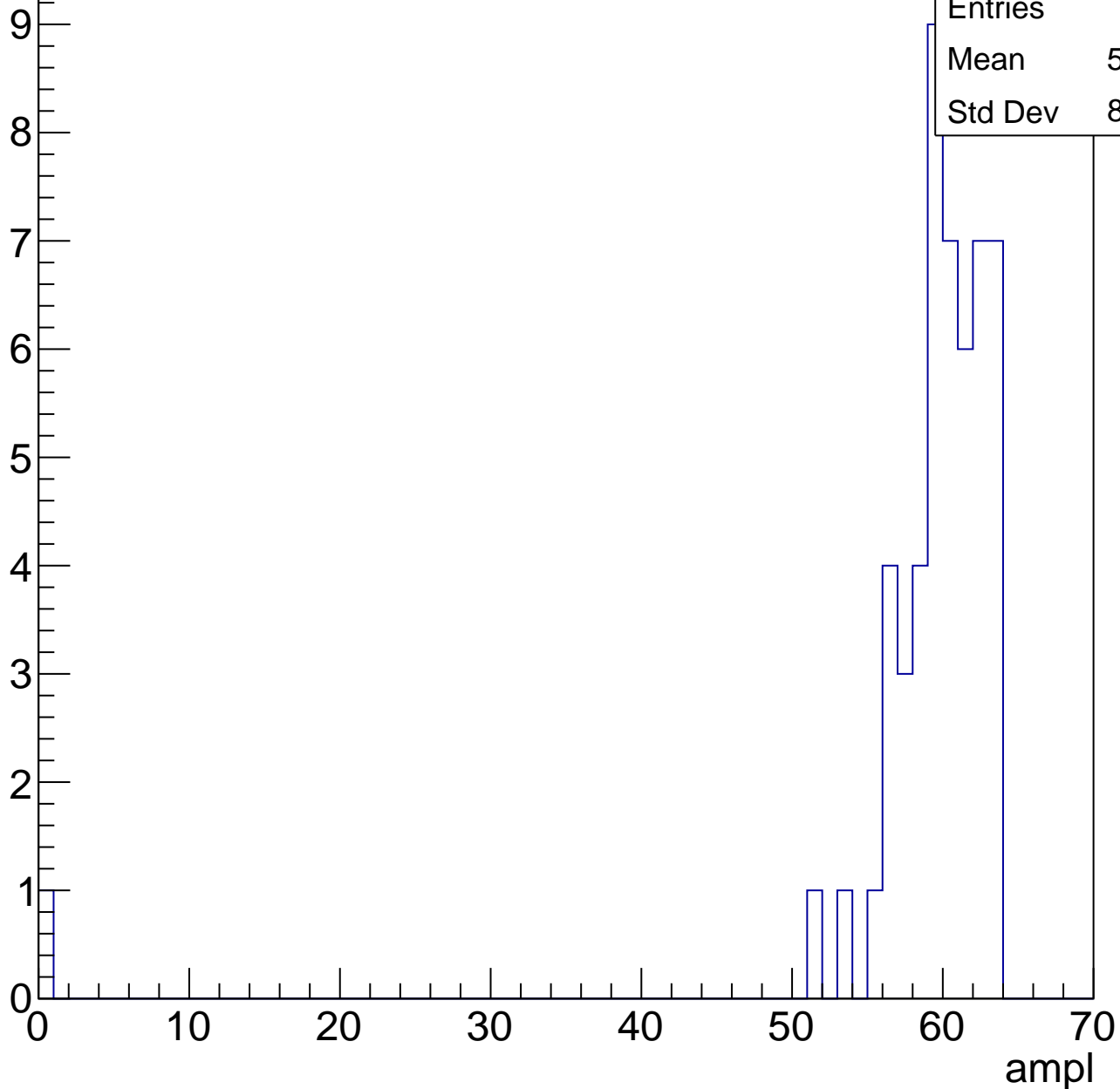
Entries	55
Mean	54.87
Std Dev	3.506



# B1L000S, U23-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

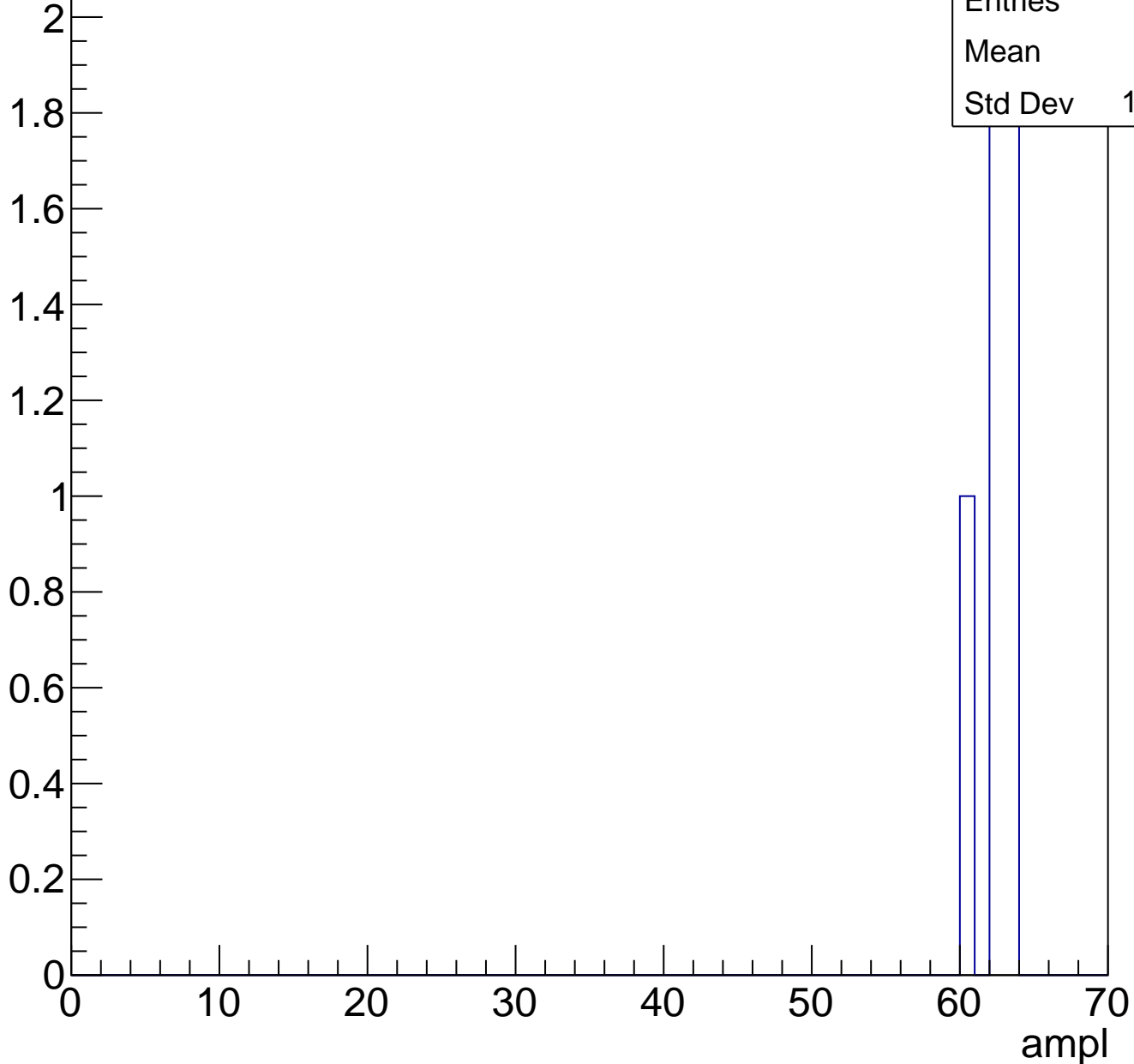
Entry



# B1L000S, U23-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch112, adc0

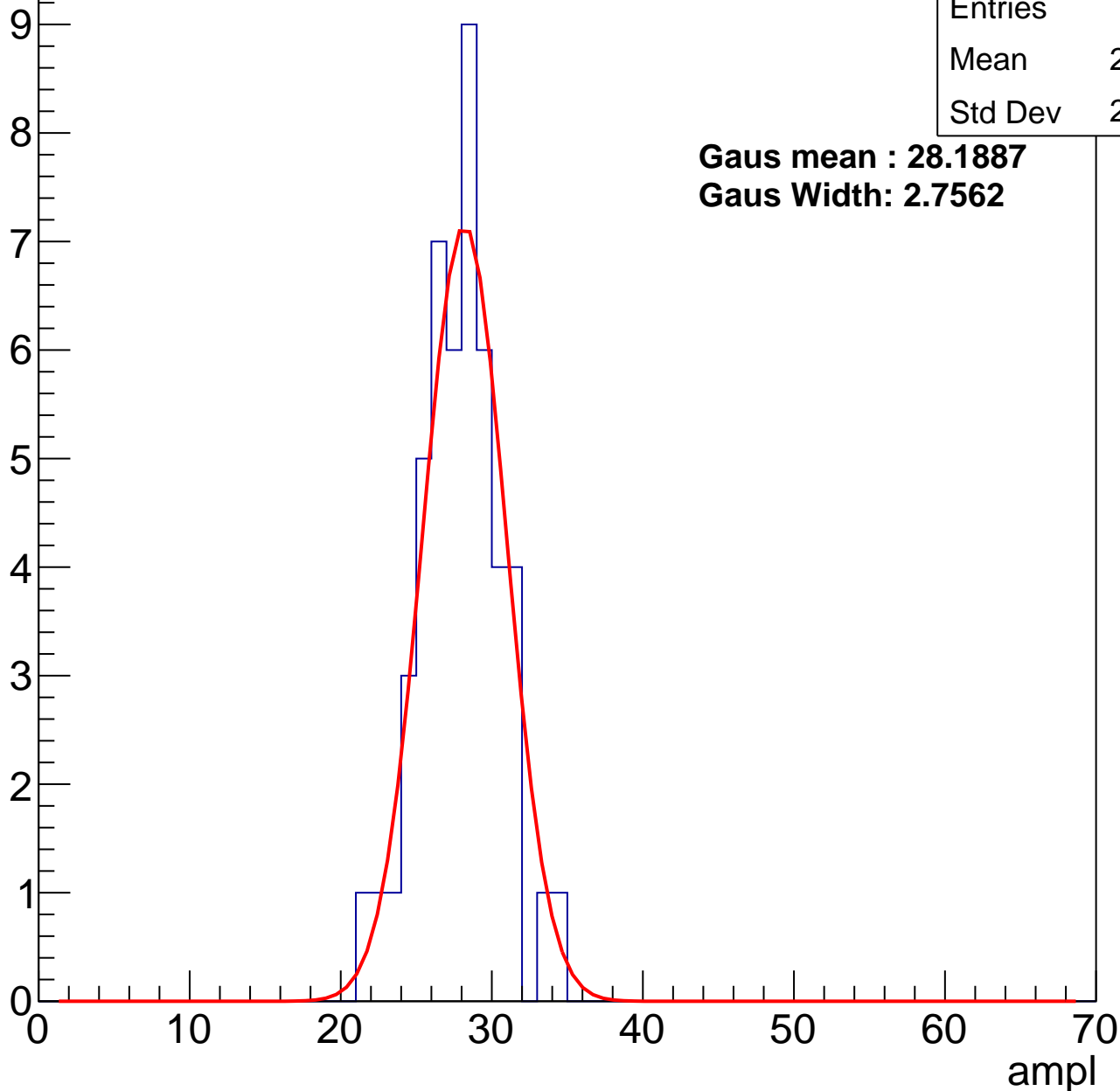
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	27.43
Std Dev	2.634

**Gaus mean : 28.1887**

**Gaus Width: 2.7562**



# B1L000S, U23-ch112, adc1

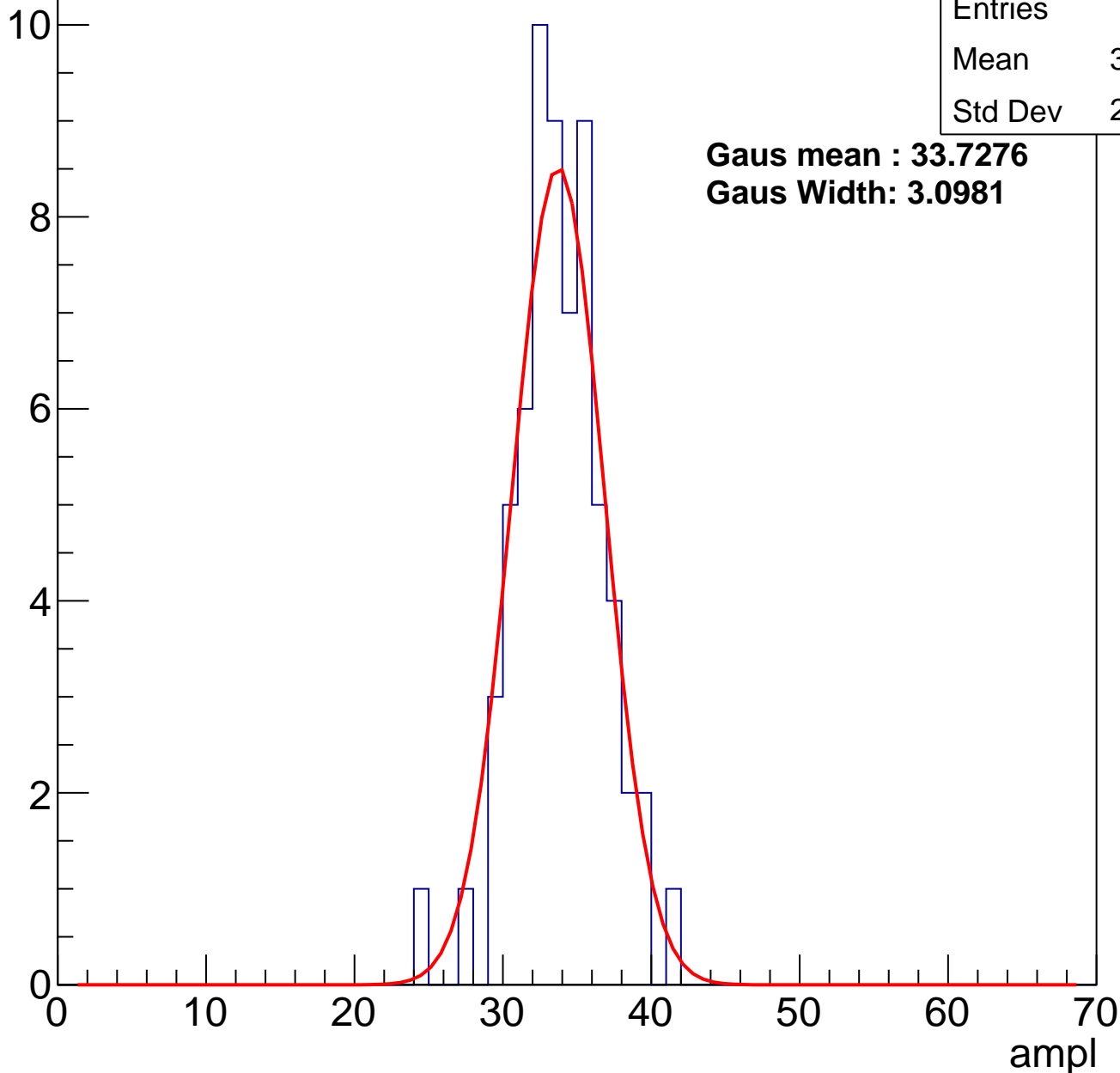
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	33.34
Std Dev	2.973

**Gaus mean : 33.7276**

**Gaus Width: 3.0981**

Entry



# B1L000S, U23-ch112, adc2

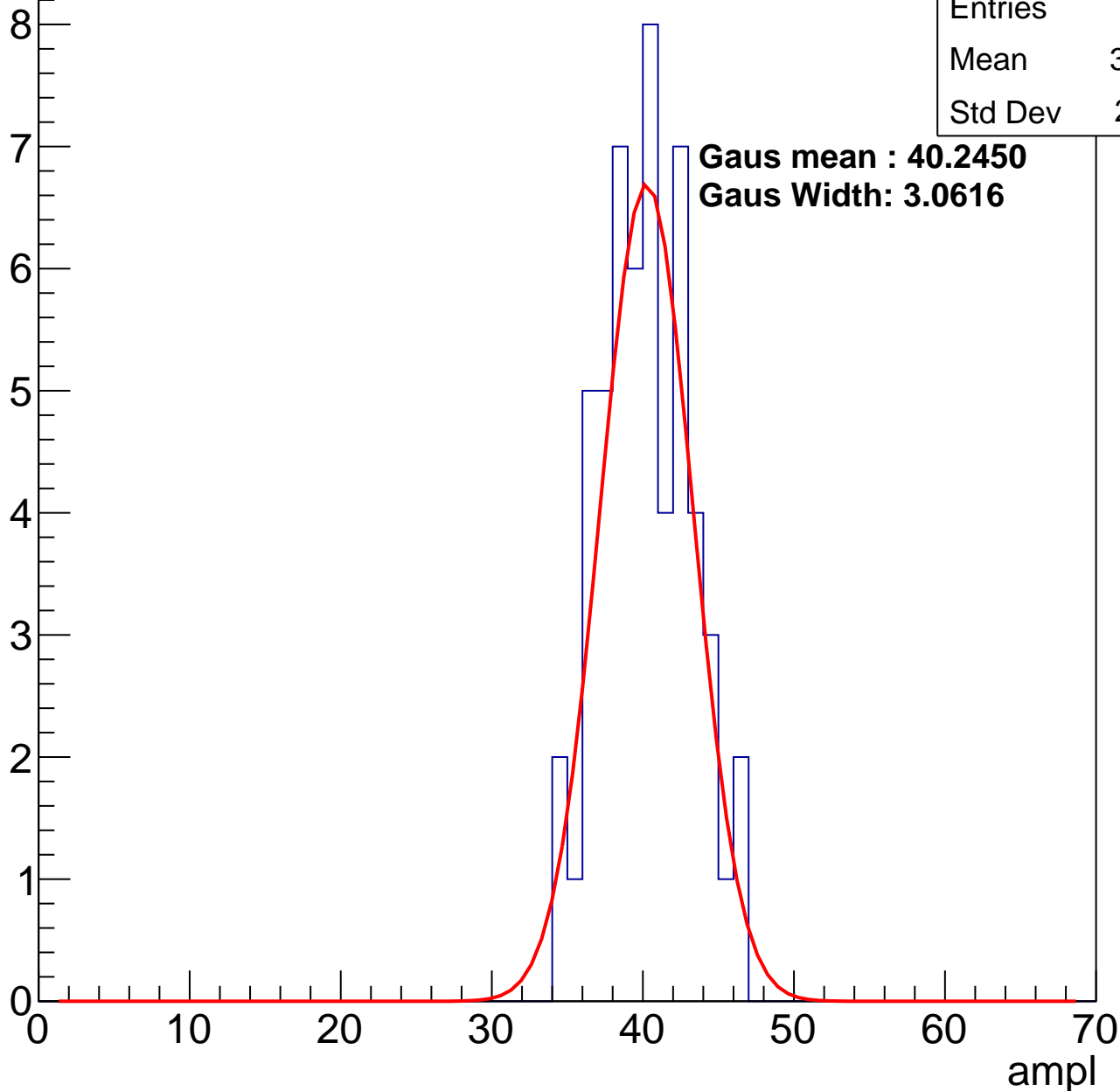
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	39.76
Std Dev	2.911

**Gaus mean : 40.2450**

**Gaus Width: 3.0616**

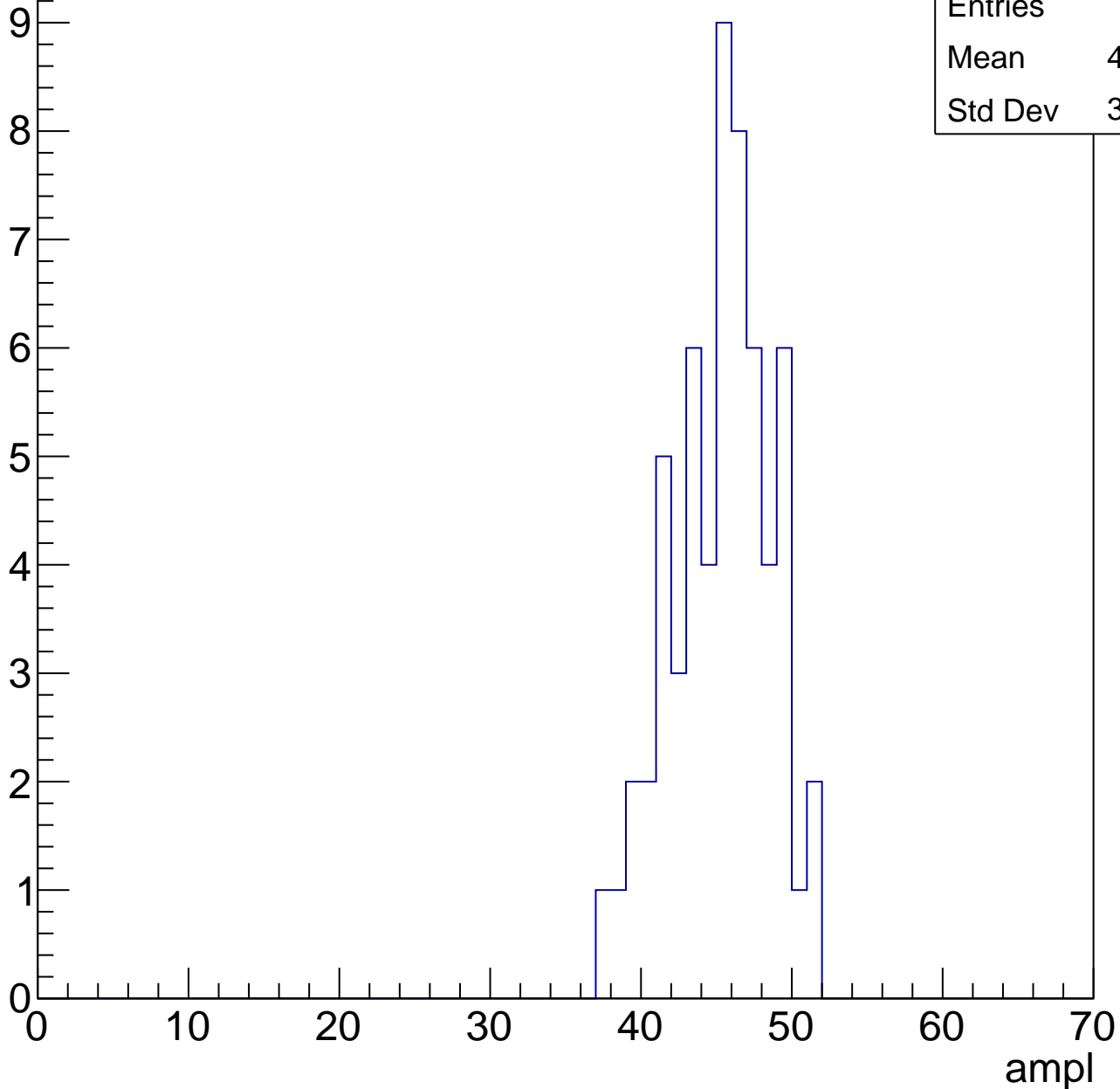


# B1L000S, U23-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	44.85
Std Dev	3.239

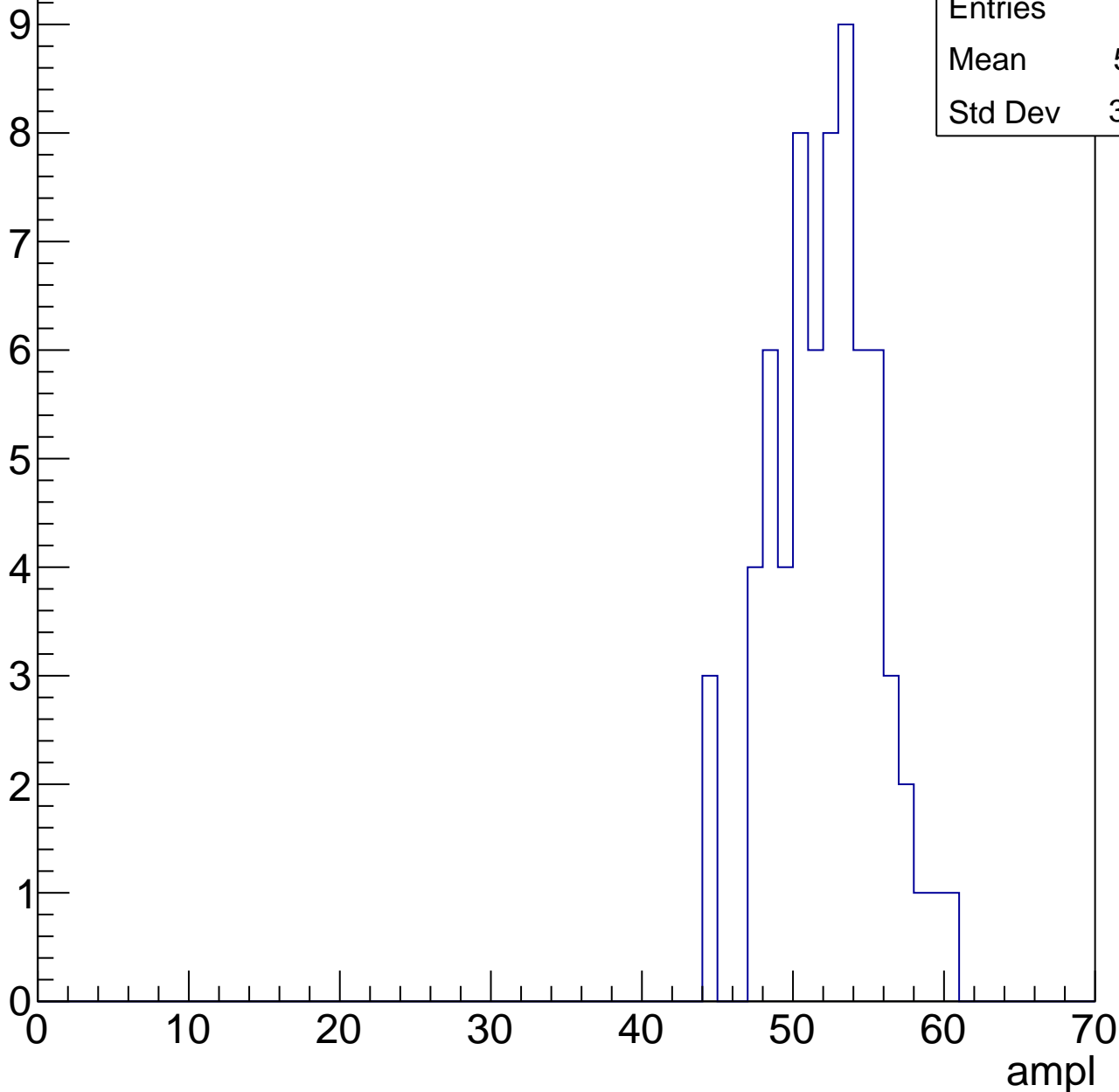


# B1L000S, U23-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	51.71
Std Dev	3.409

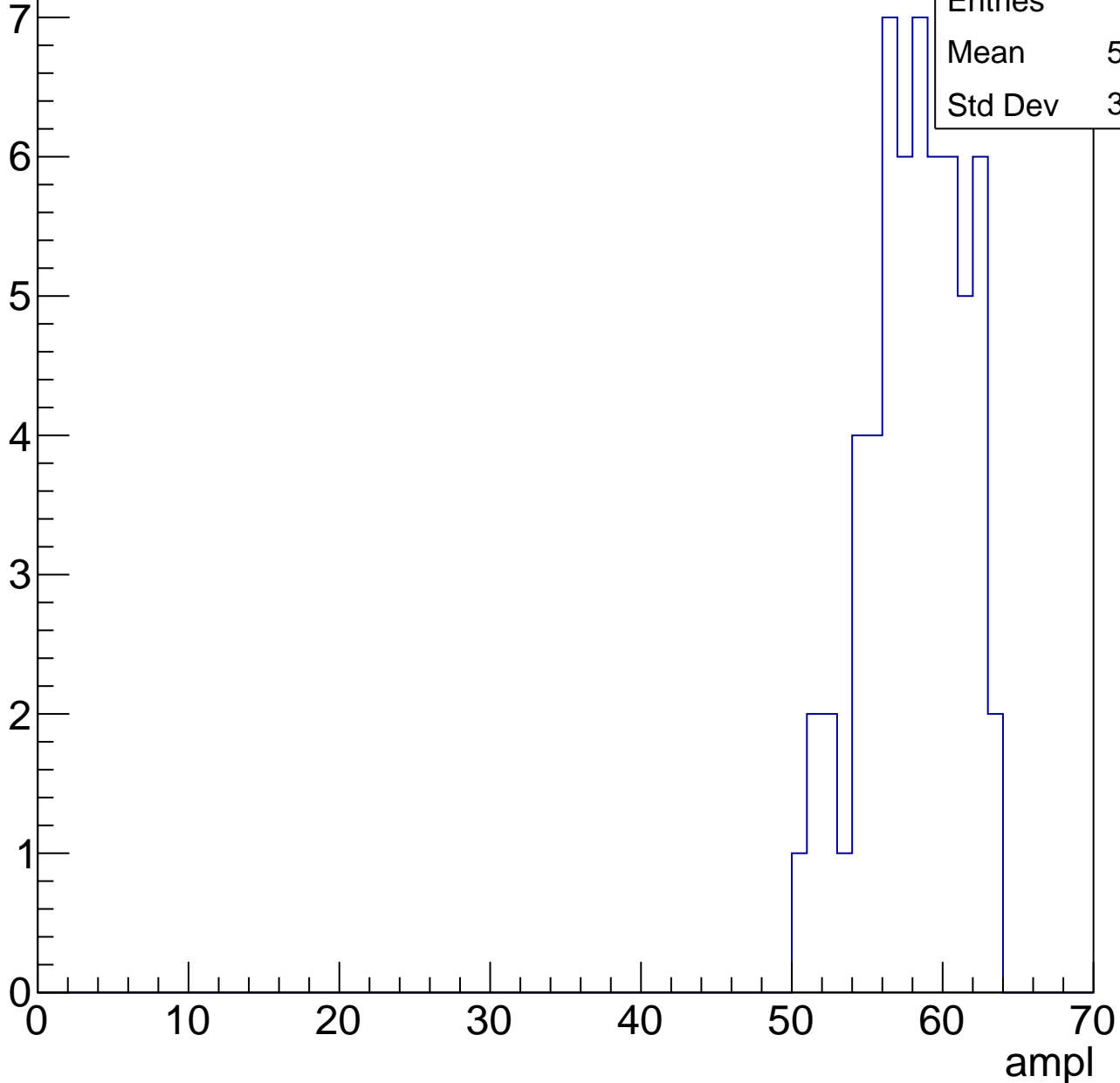


# B1L000S, U23-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.66
Std Dev	3.208

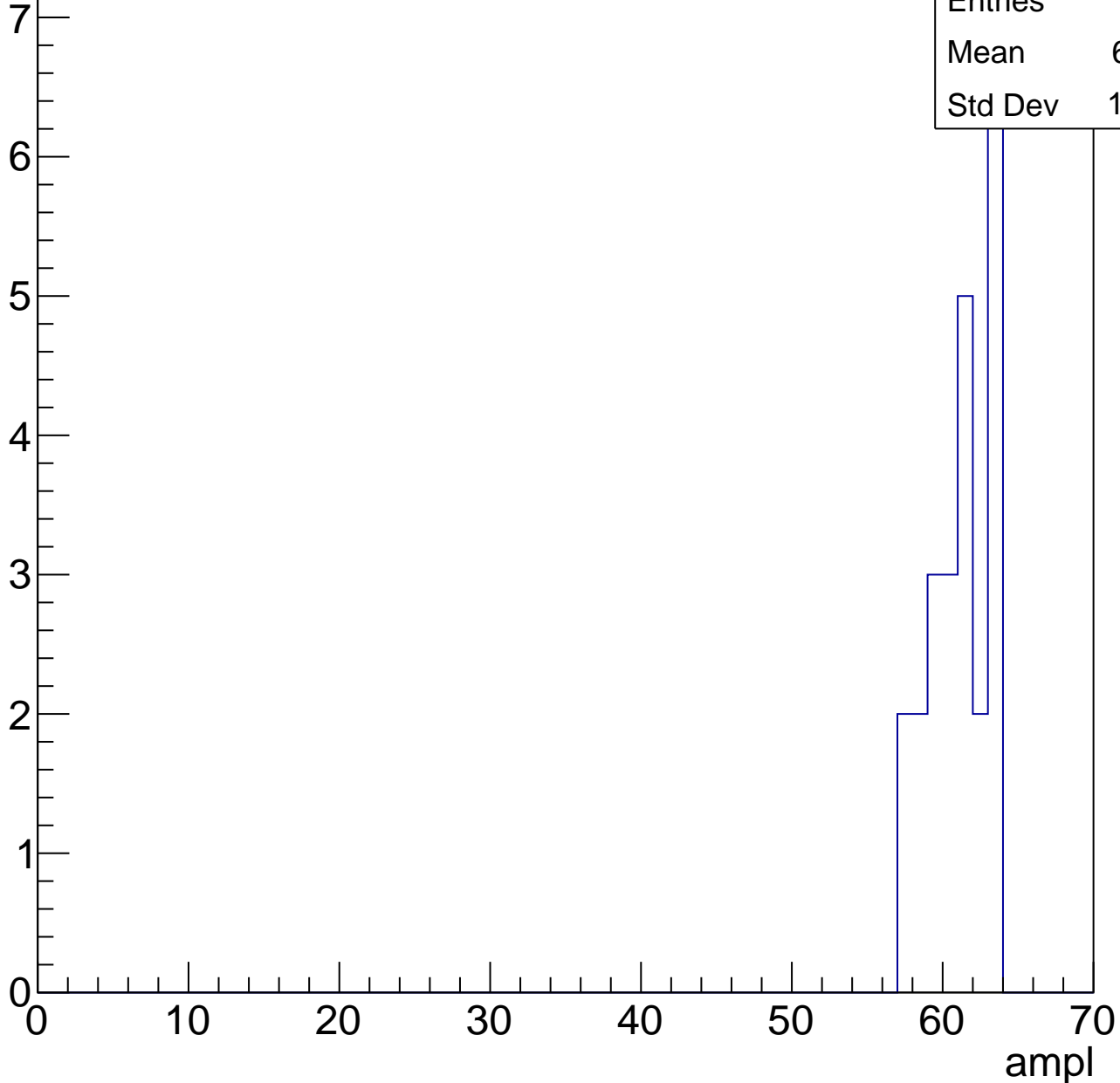


# B1L000S, U23-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	60.71
Std Dev	1.968

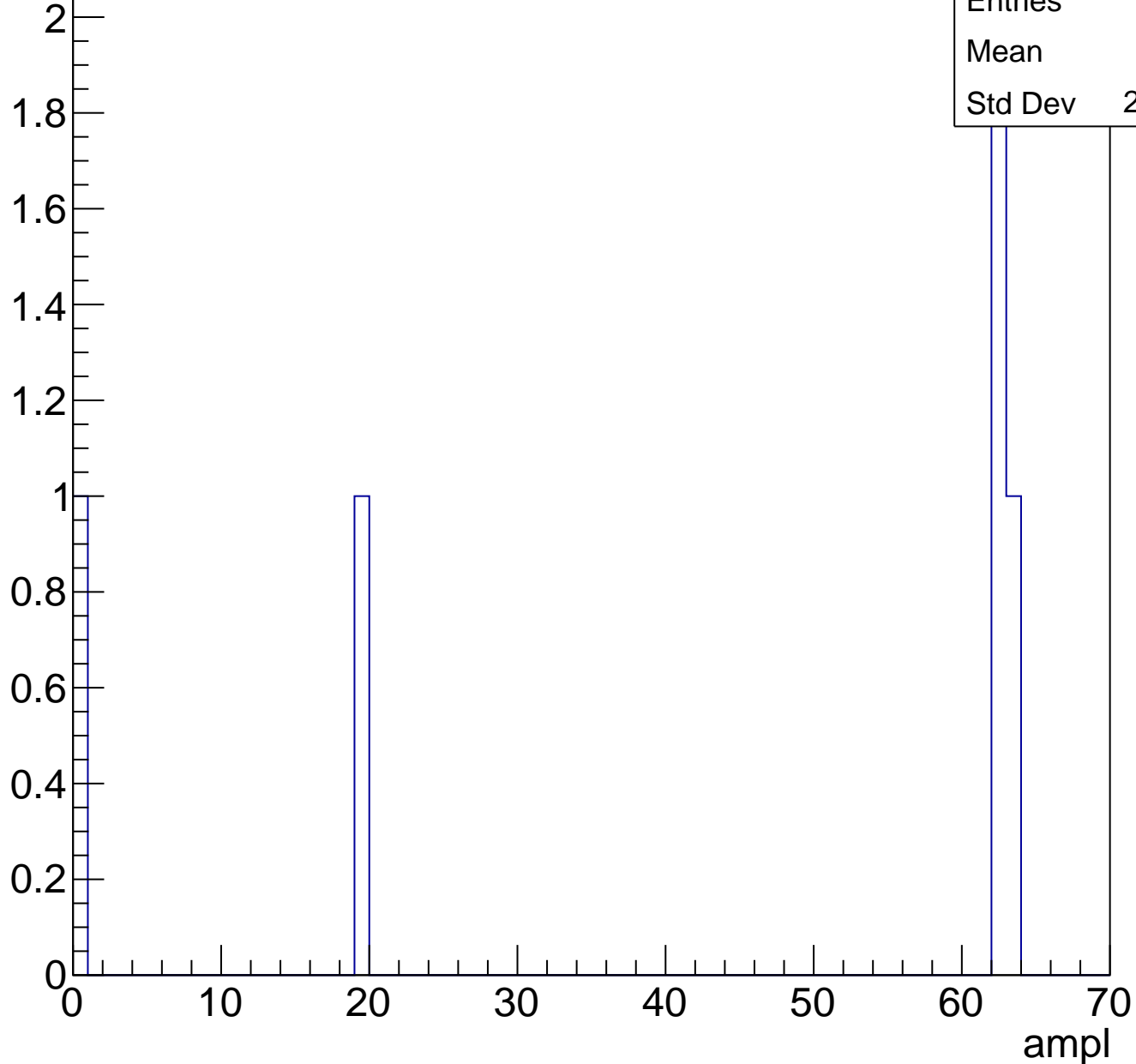




# B1L000S, U23-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	41.2
Std Dev	26.57

# B1L000S, U23-ch113, adc0

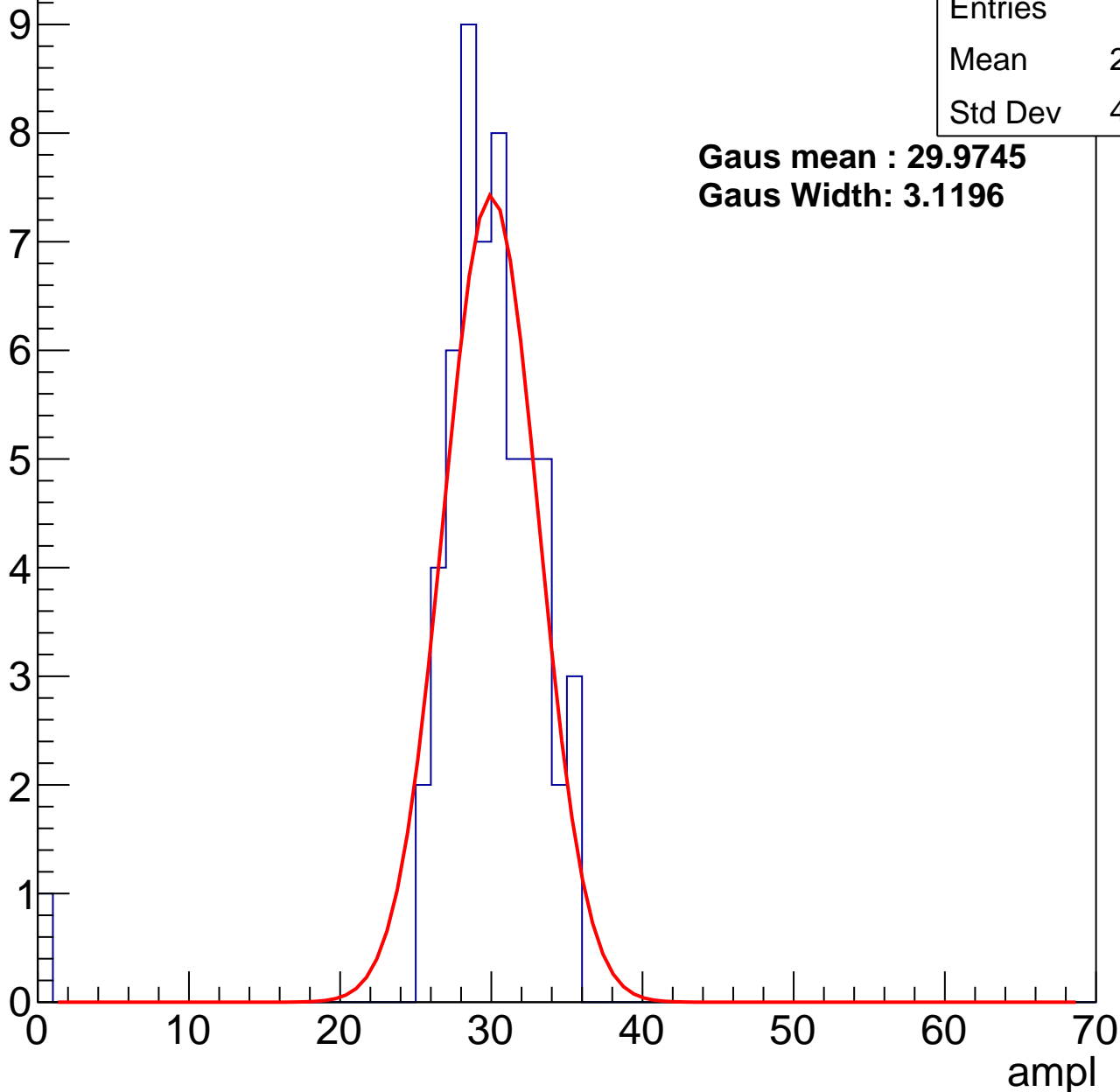
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.19
Std Dev	4.685

**Gaus mean : 29.9745**

**Gaus Width: 3.1196**



# B1L000S, U23-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	36.24
Std Dev	3.259

**Gaus mean : 36.7402**

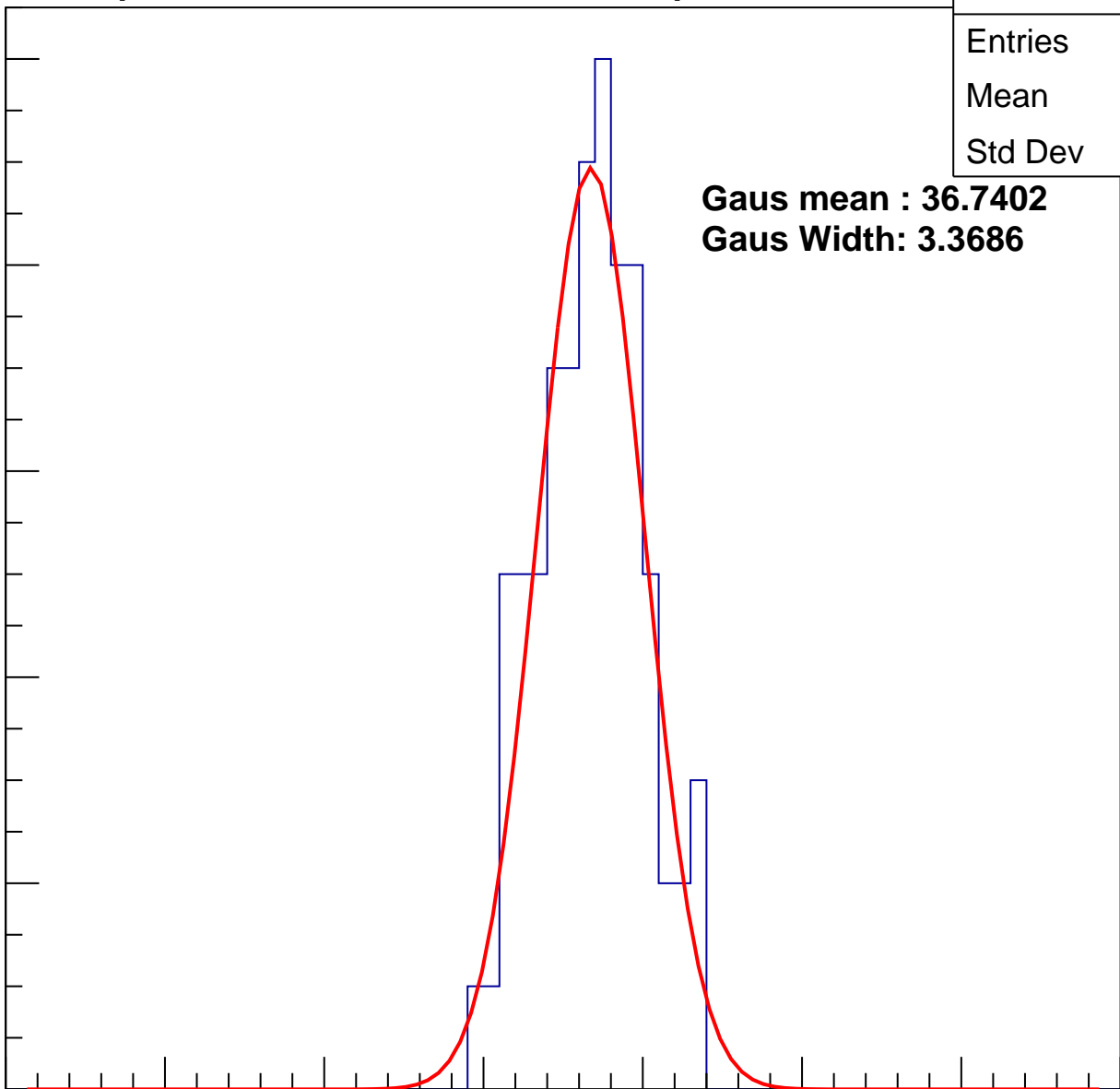
**Gaus Width: 3.3686**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch113, adc2

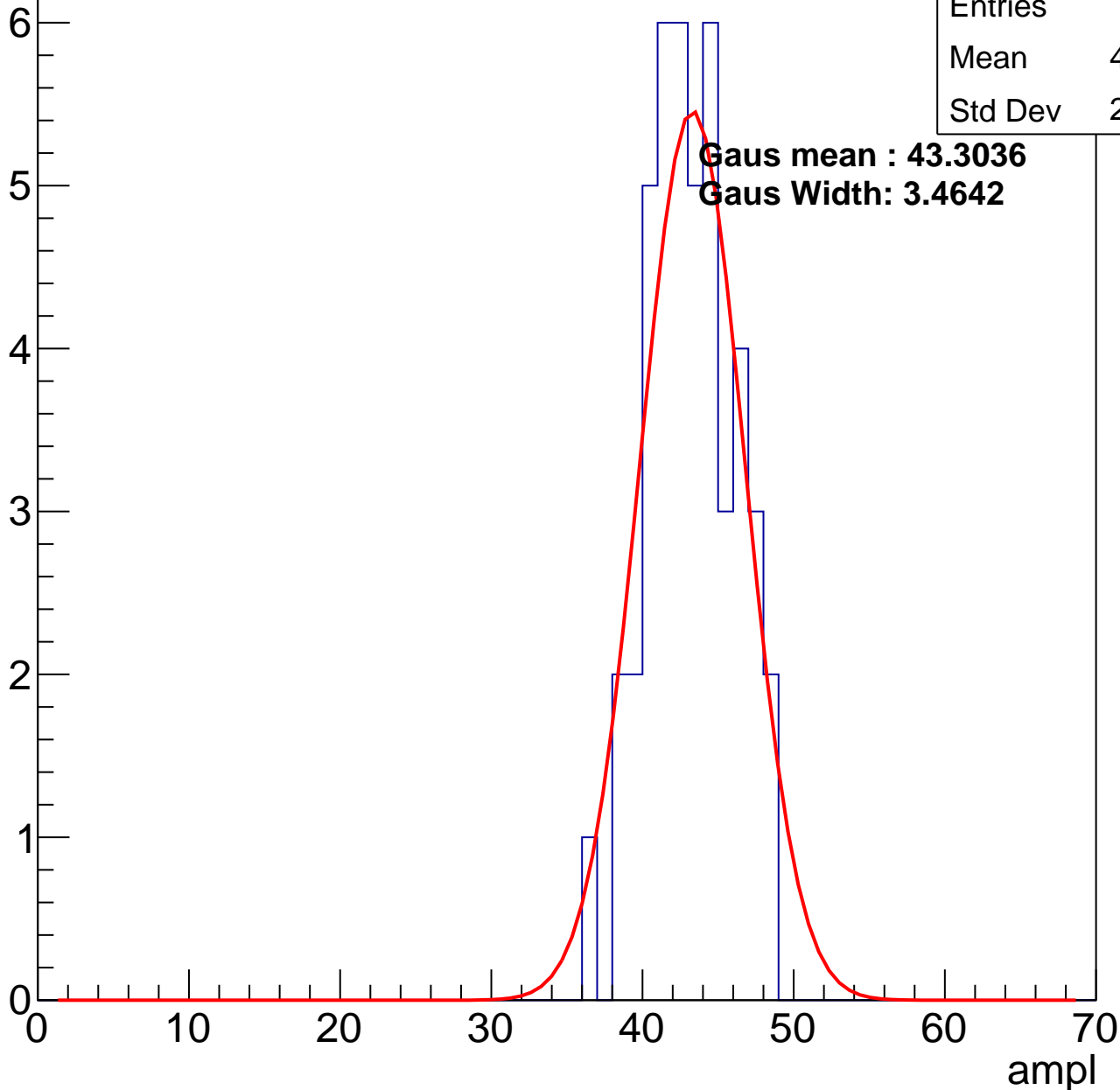
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	42.73
Std Dev	2.808

**Gaus mean : 43.3036**

**Gaus Width: 3.4642**

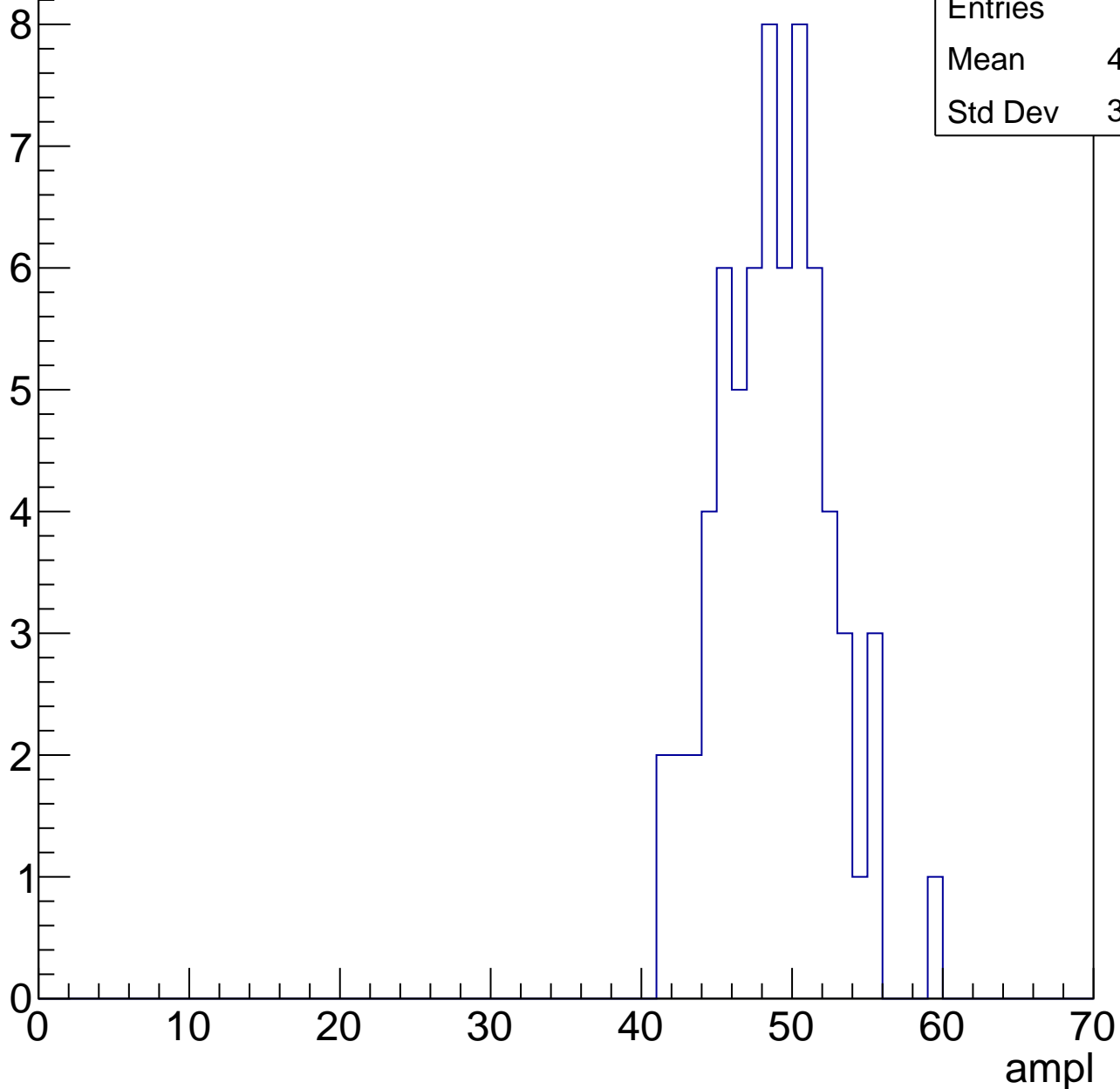


# B1L000S, U23-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	48.34
Std Dev	3.647

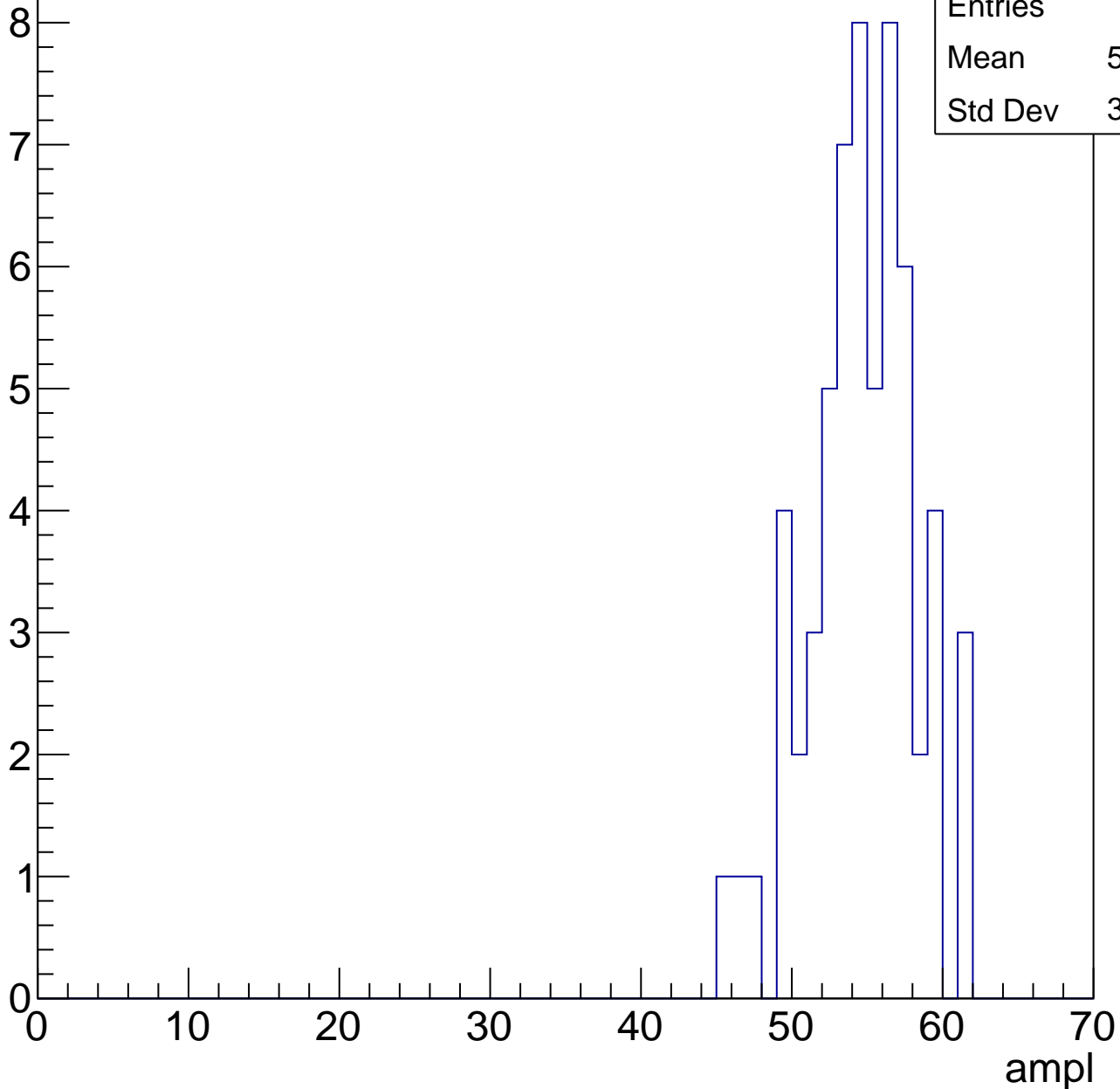


# B1L000S, U23-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.17
Std Dev	3.532

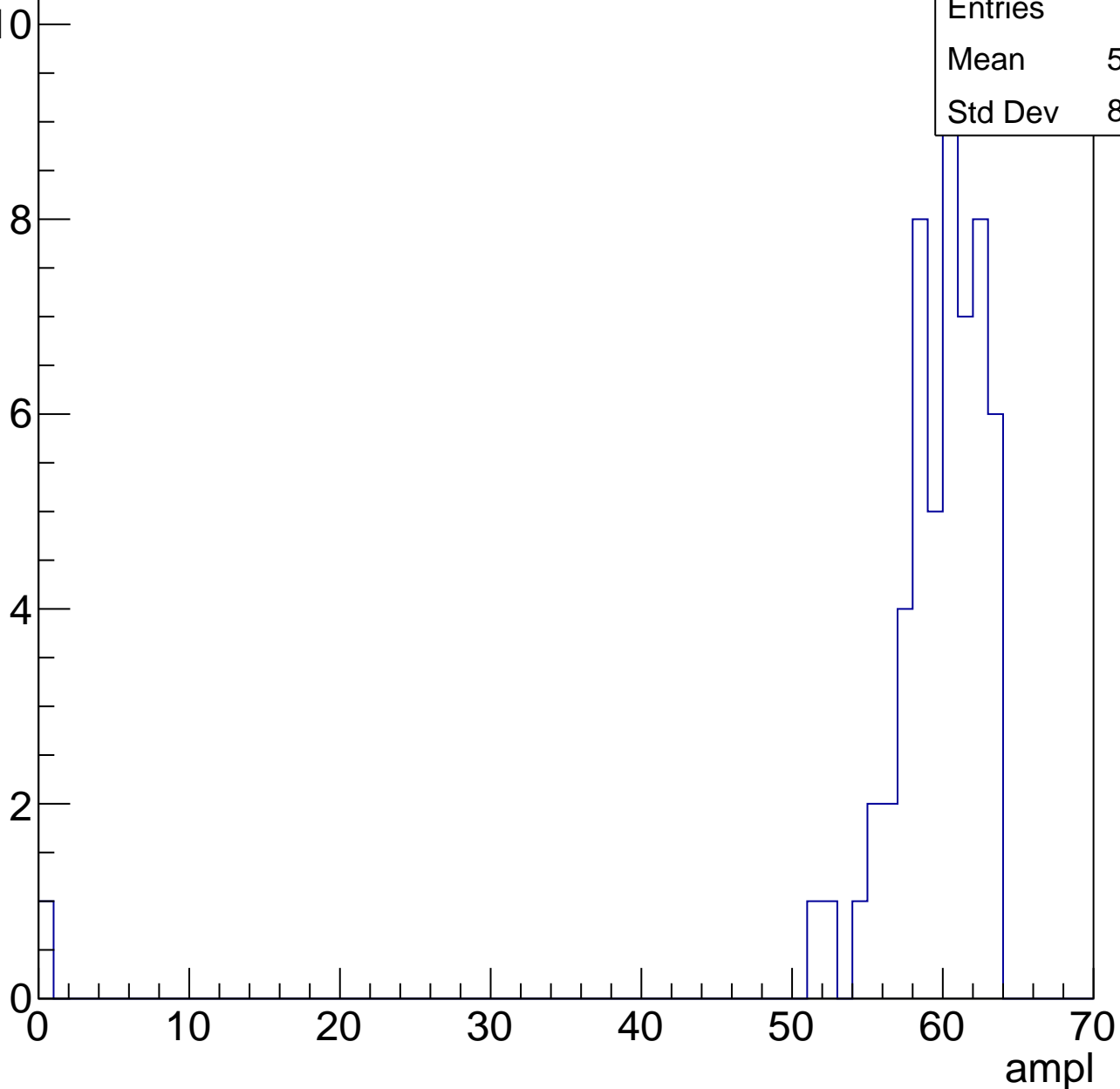


# B1L000S, U23-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

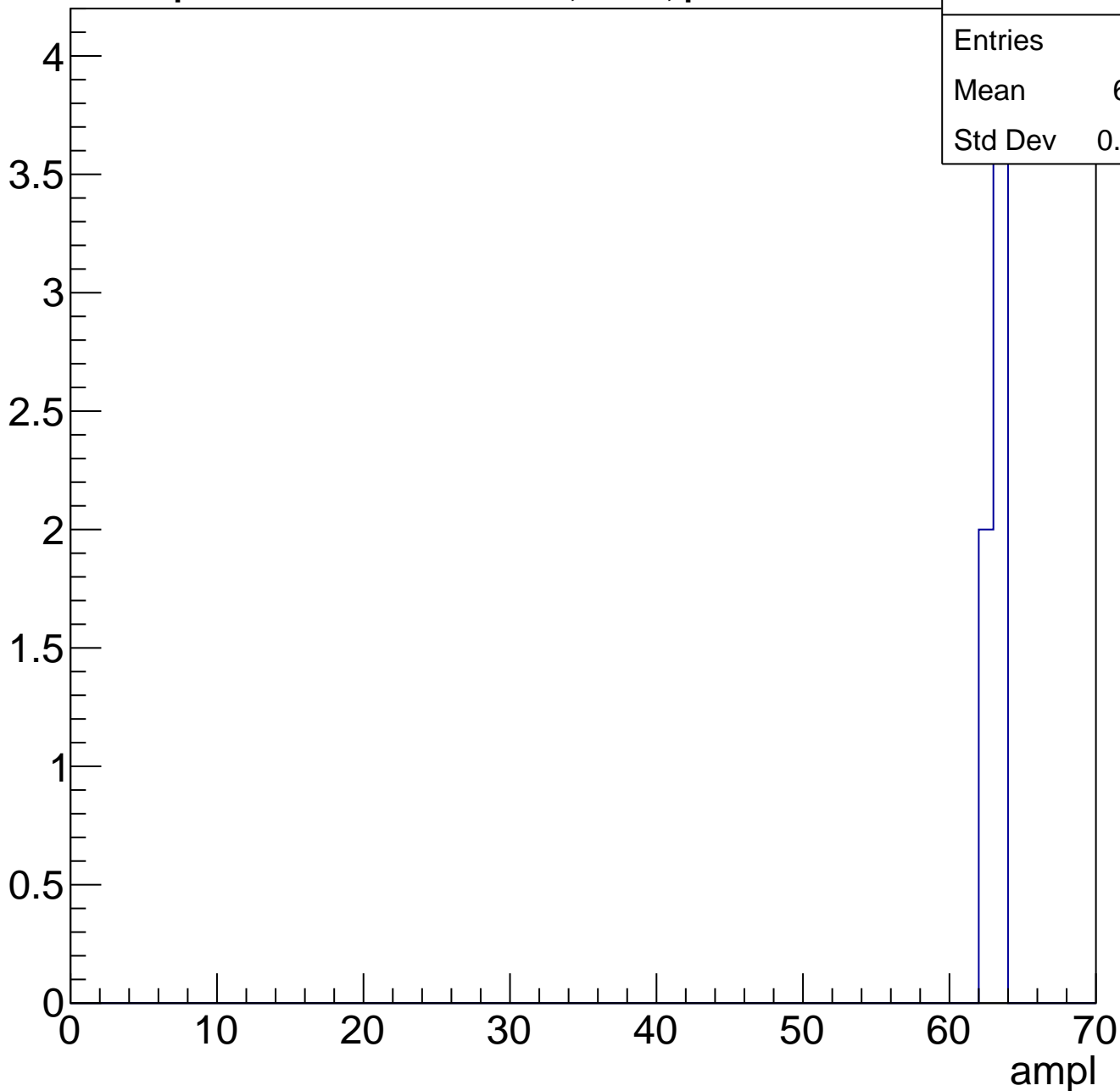
Entries	56
Mean	58.34
Std Dev	8.318



# B1L000S, U23-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



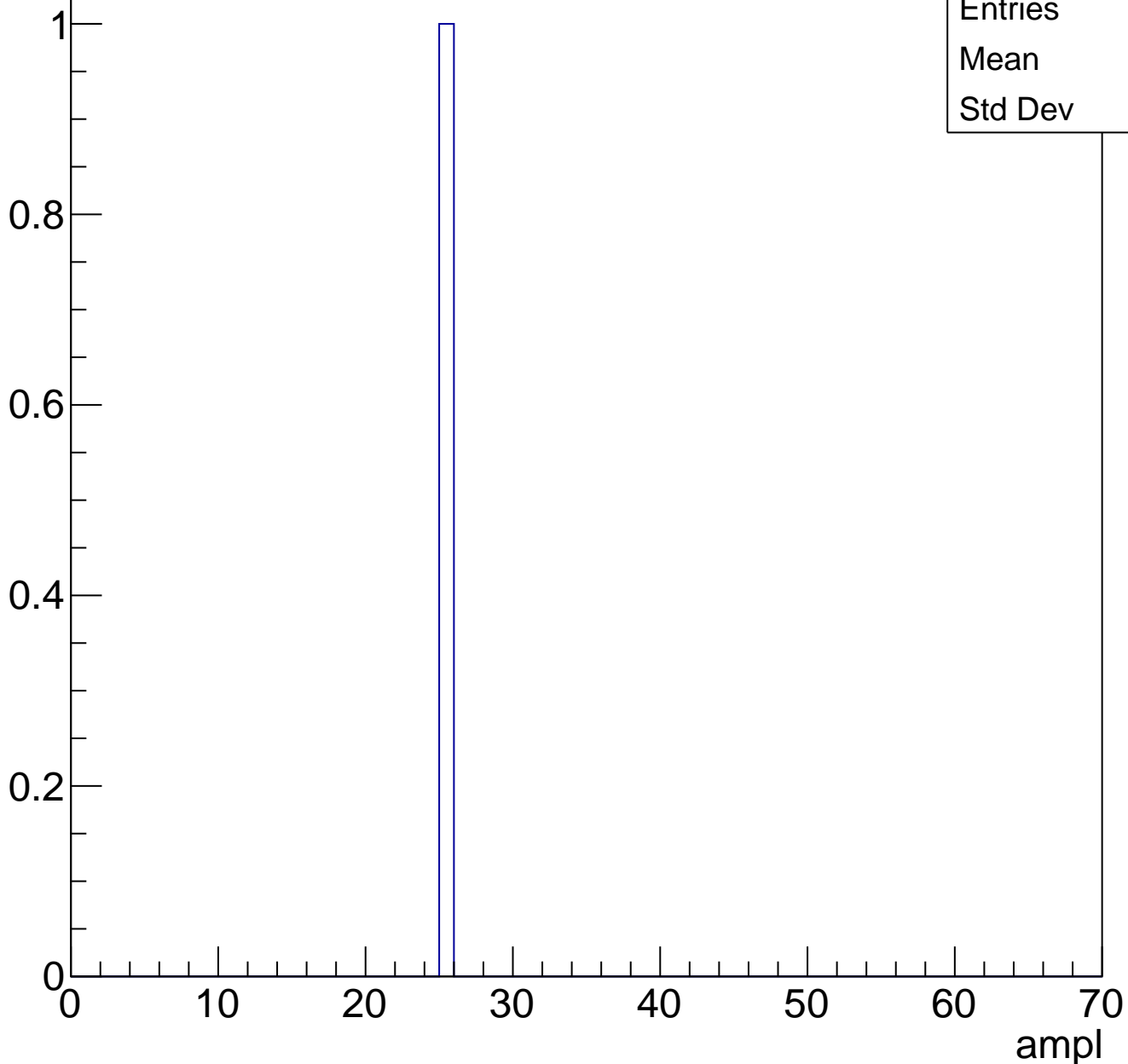
Entries	6
Mean	62.67
Std Dev	0.4714



# B1L000S, U23-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch114, adc0

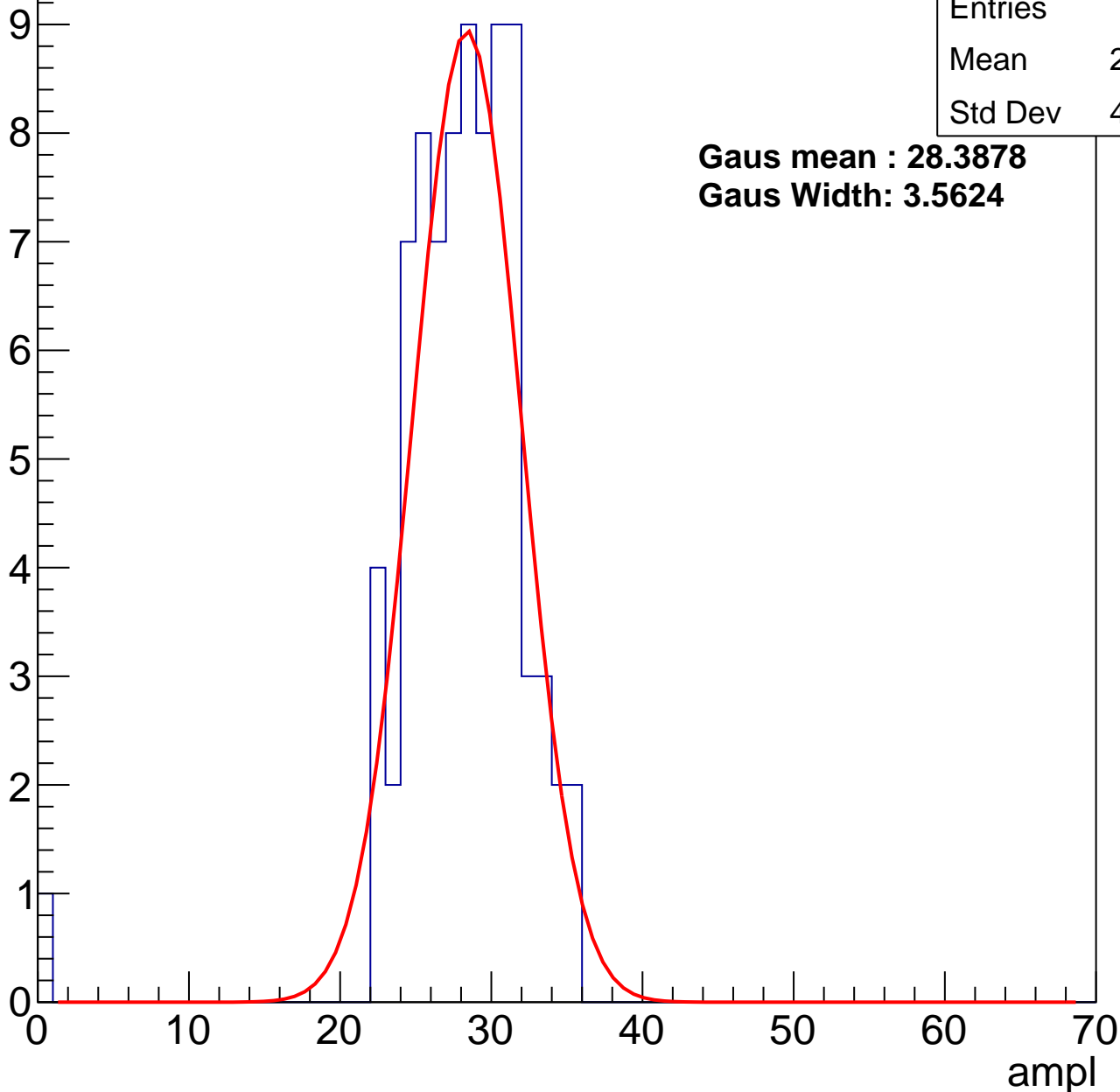
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	27.63
Std Dev	4.424

**Gaus mean : 28.3878**

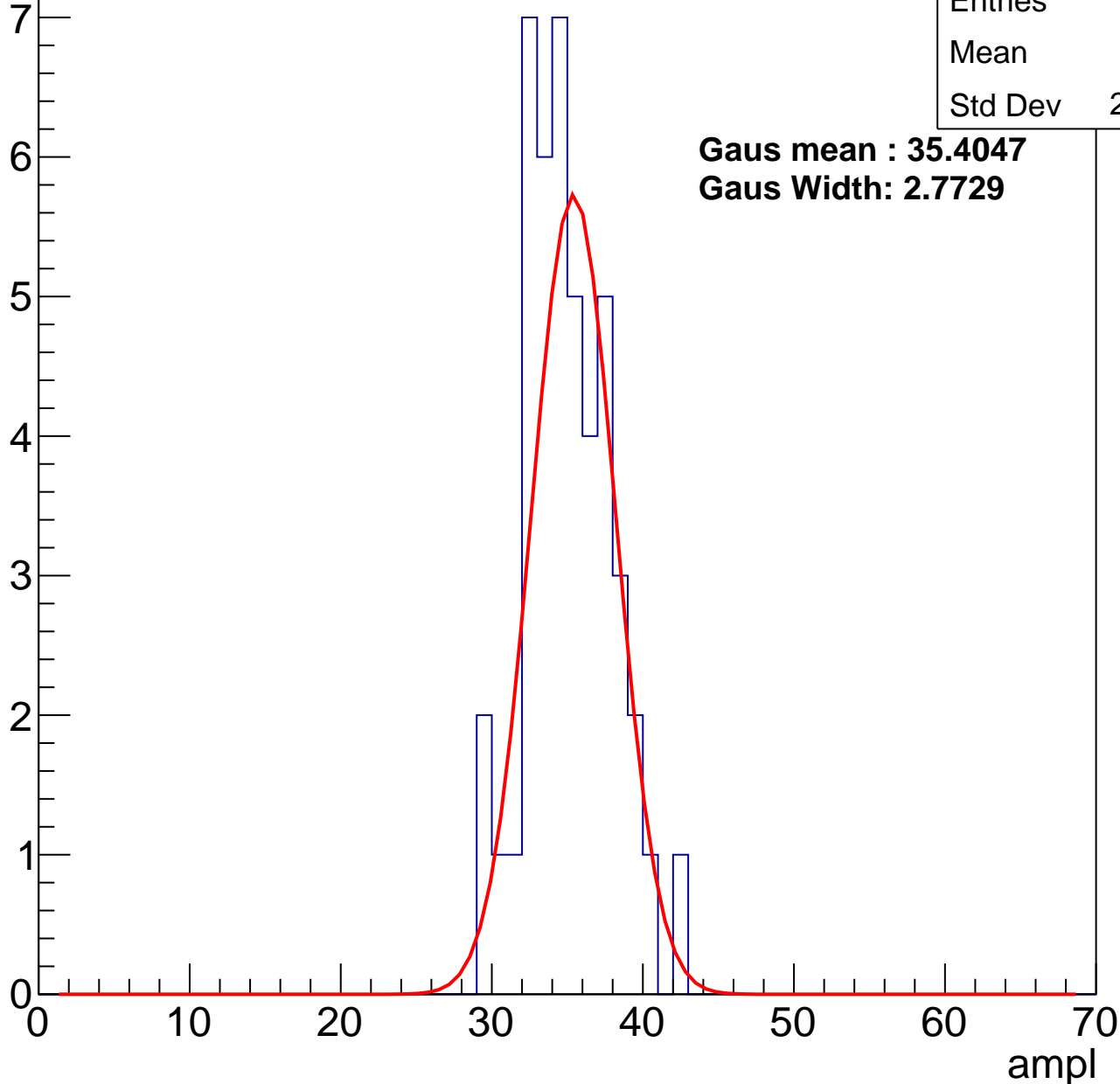
**Gaus Width: 3.5624**



# B1L000S, U23-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch114, adc2

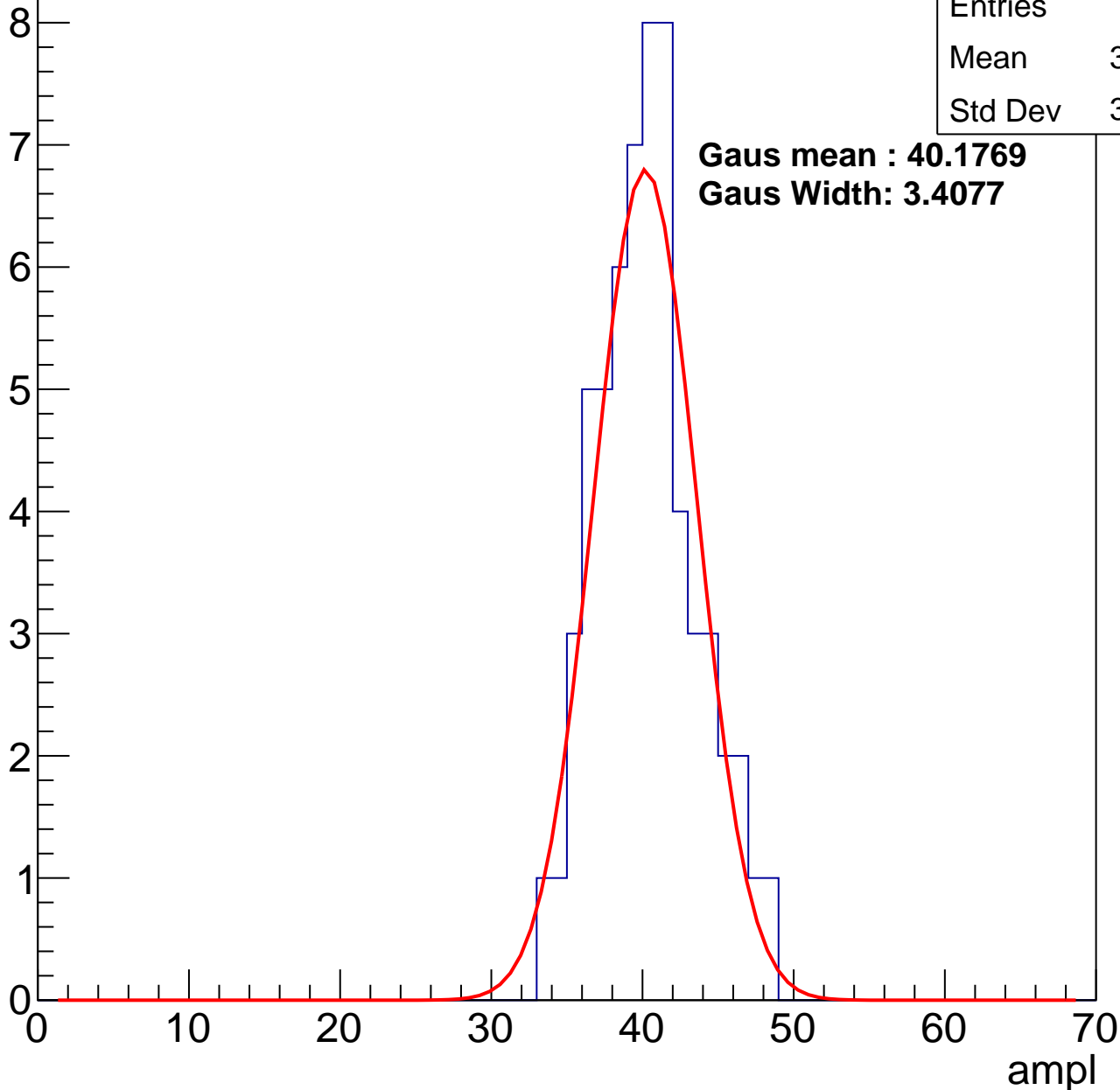
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	39.87
Std Dev	3.289

**Gaus mean : 40.1769**

**Gaus Width: 3.4077**

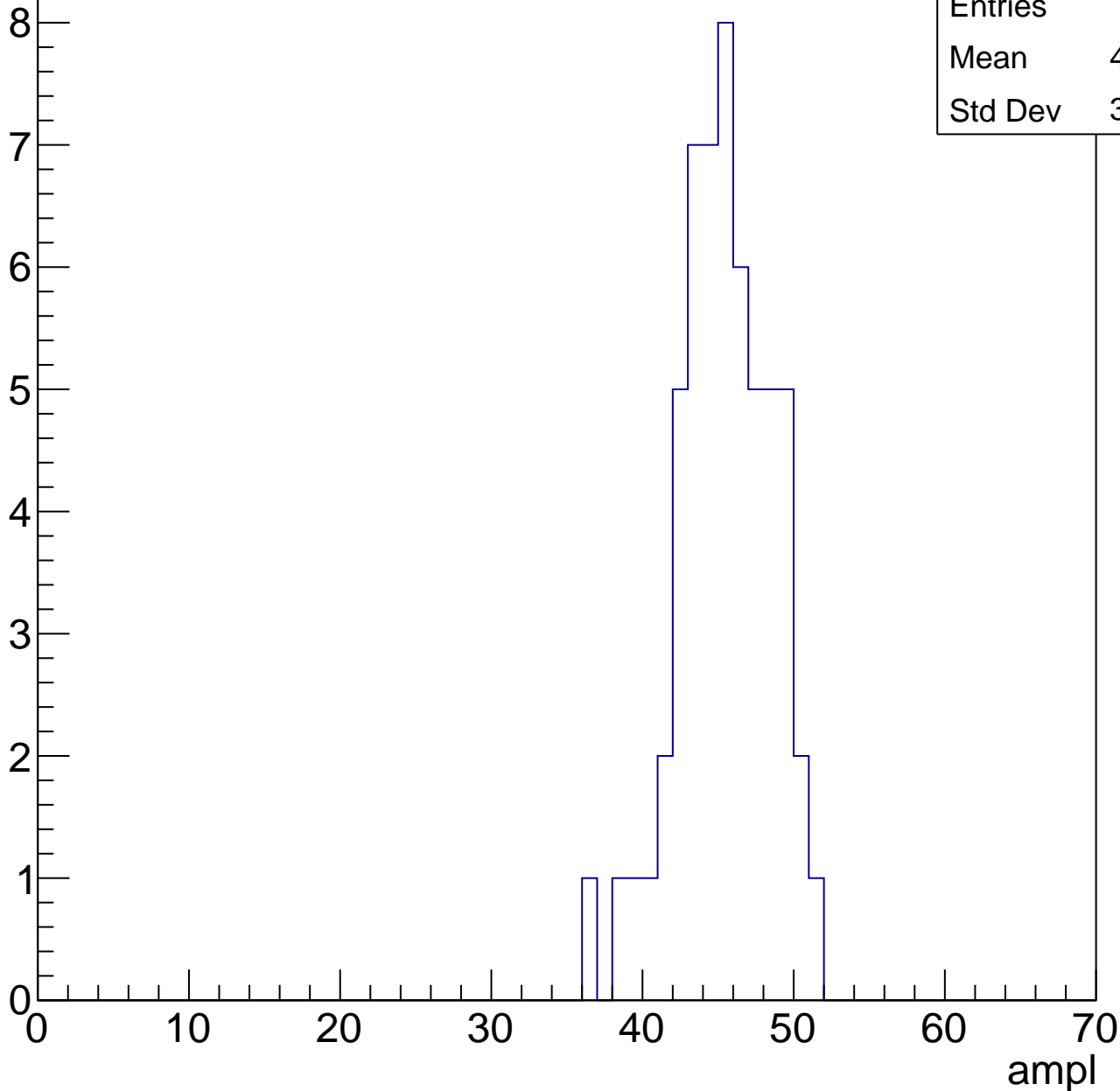


# B1L000S, U23-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	44.93
Std Dev	3.077

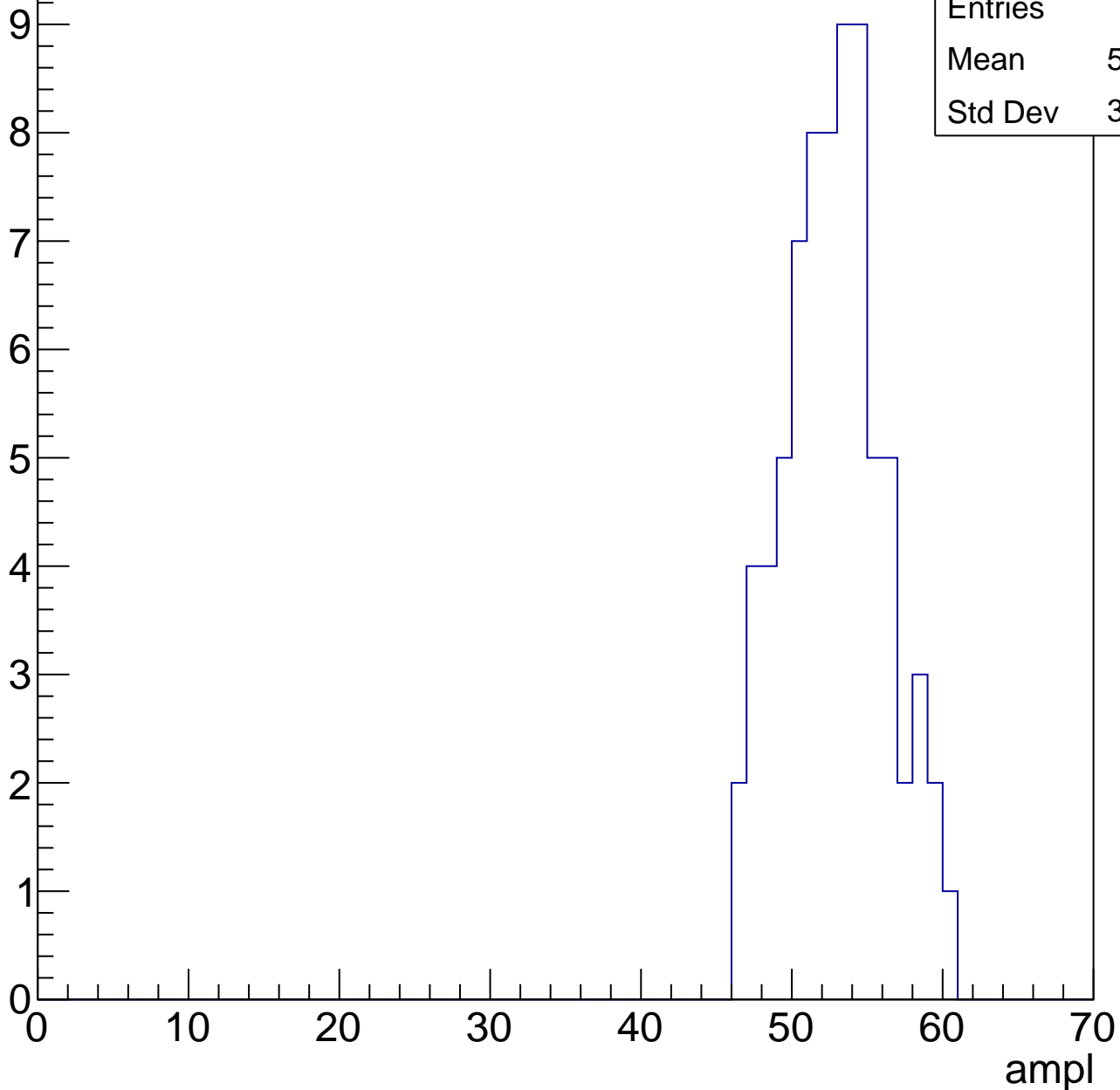


# B1L000S, U23-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	52.36
Std Dev	3.282

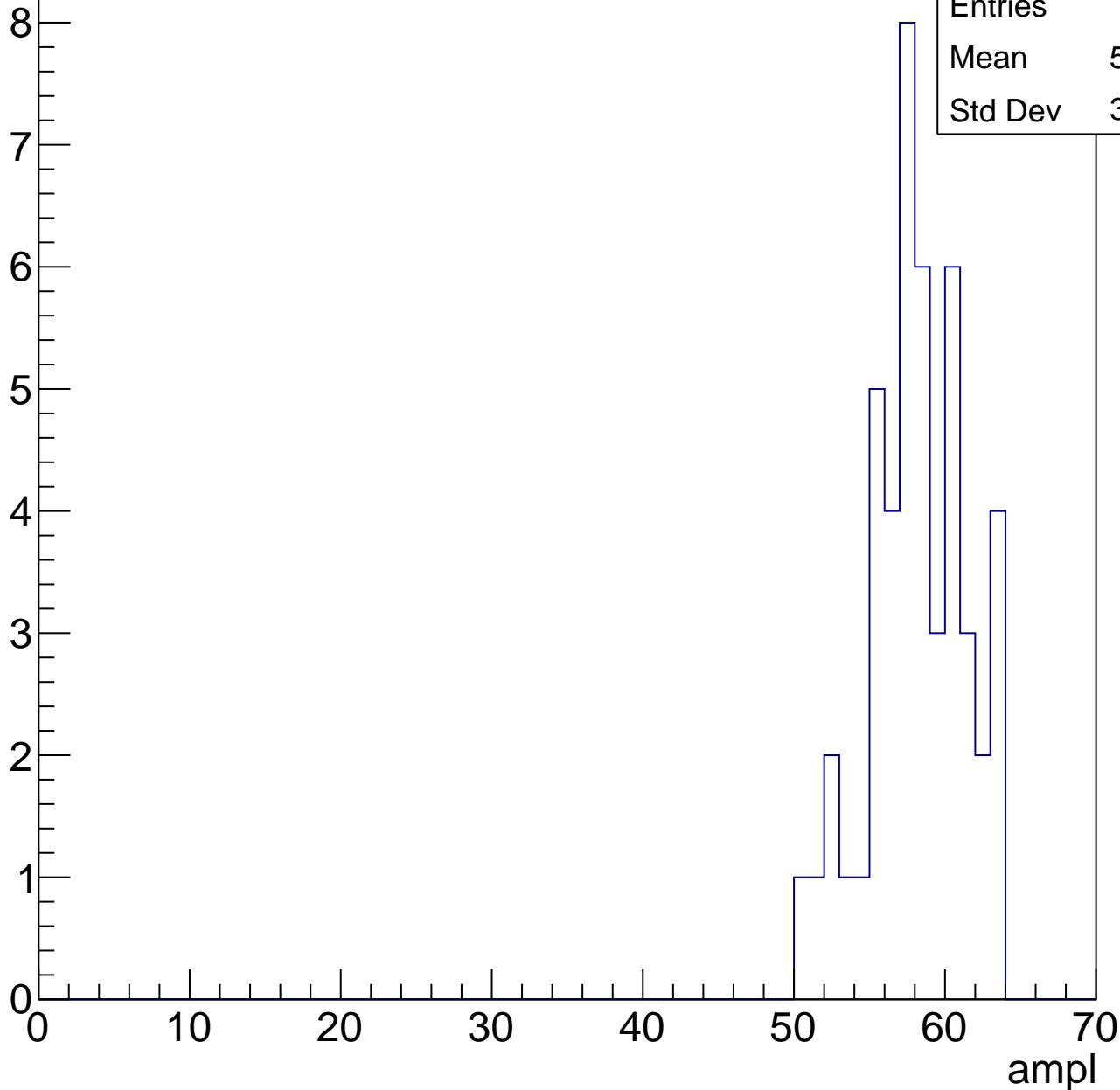


# B1L000S, U23-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	57.68
Std Dev	3.183

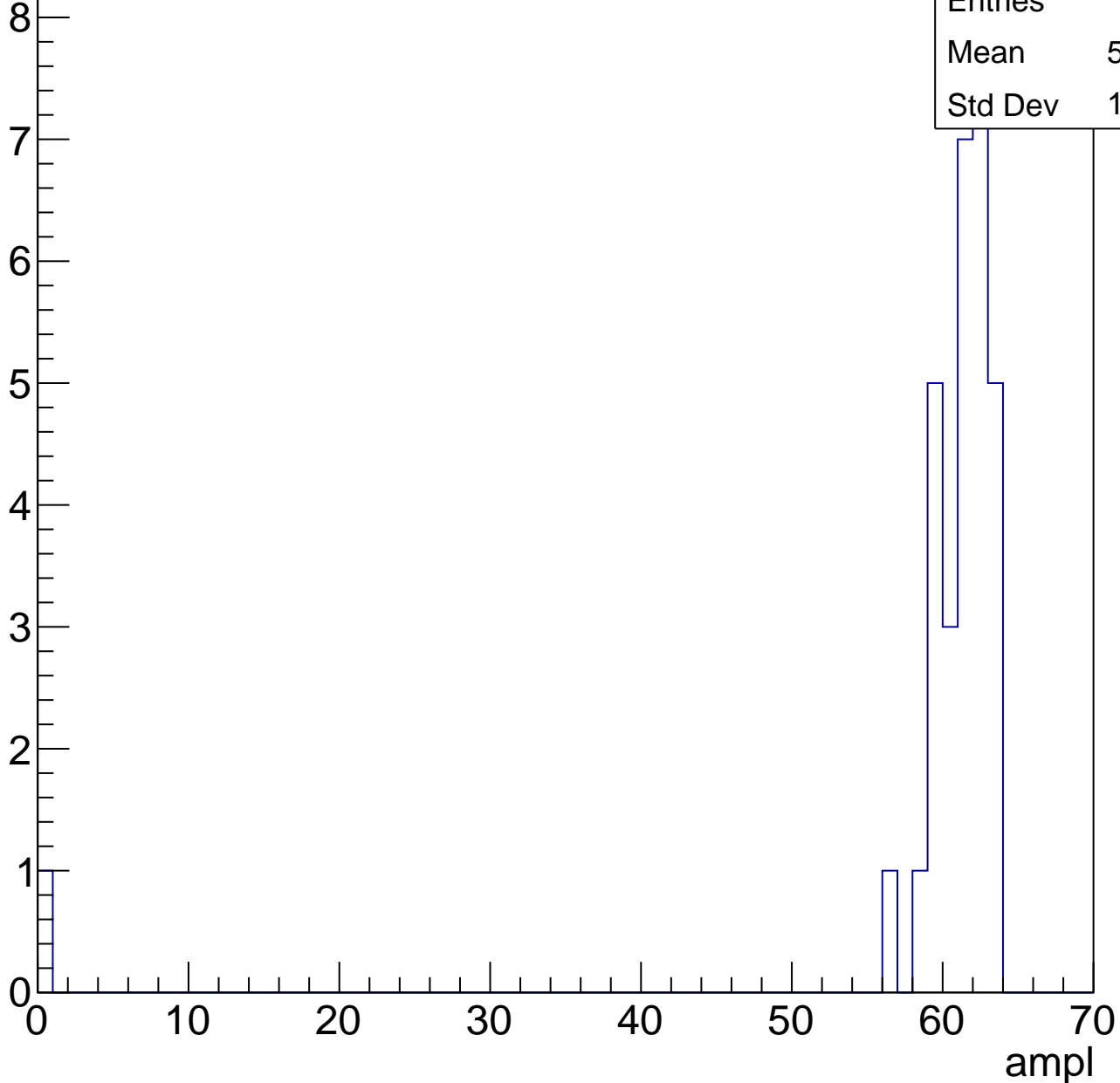


# B1L000S, U23-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	58.94
Std Dev	10.89





# B1L000S, U23-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch115, adc0

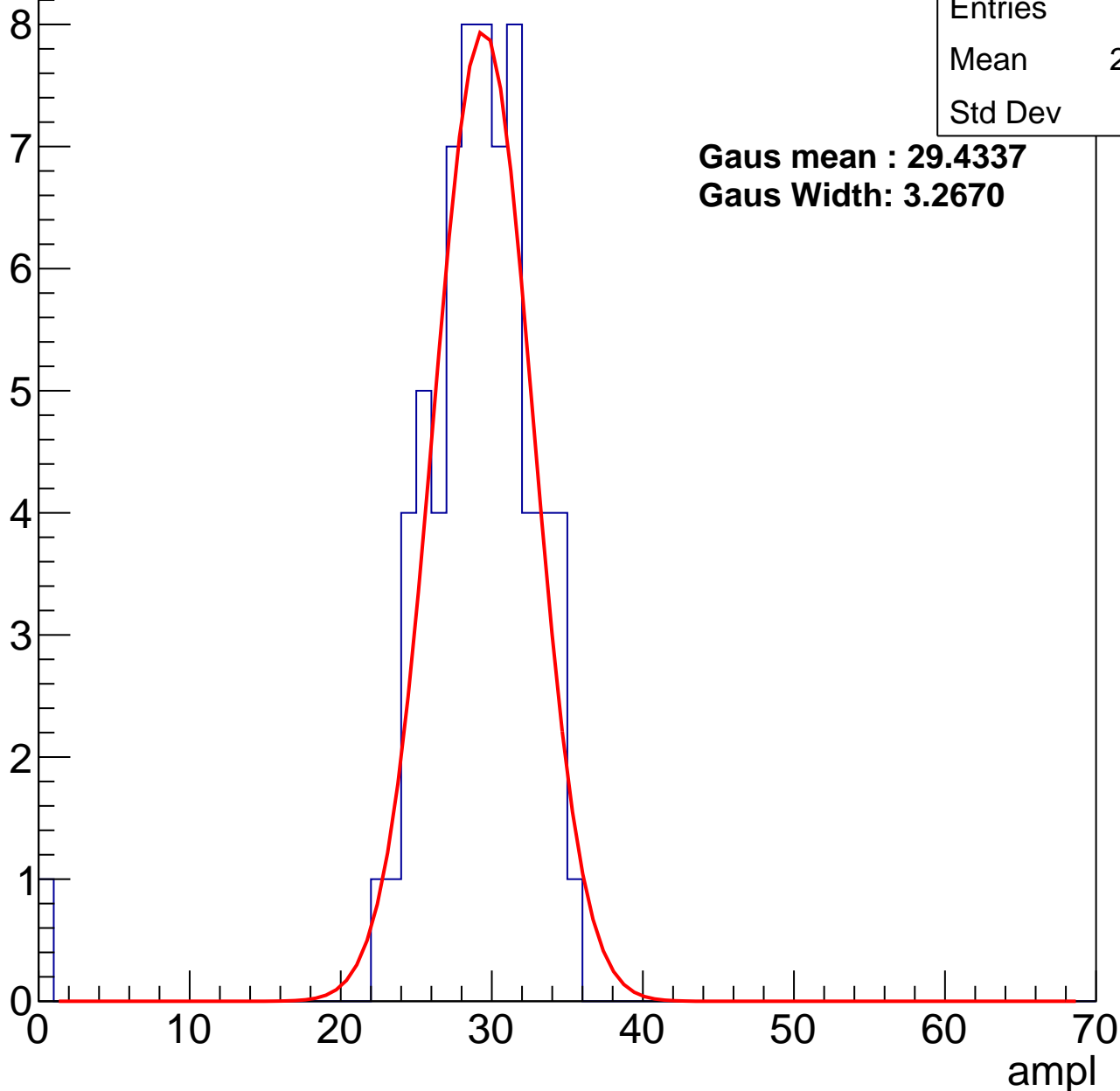
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.42
Std Dev	4.62

**Gaus mean : 29.4337**

**Gaus Width: 3.2670**



# B1L000S, U23-ch115, adc1

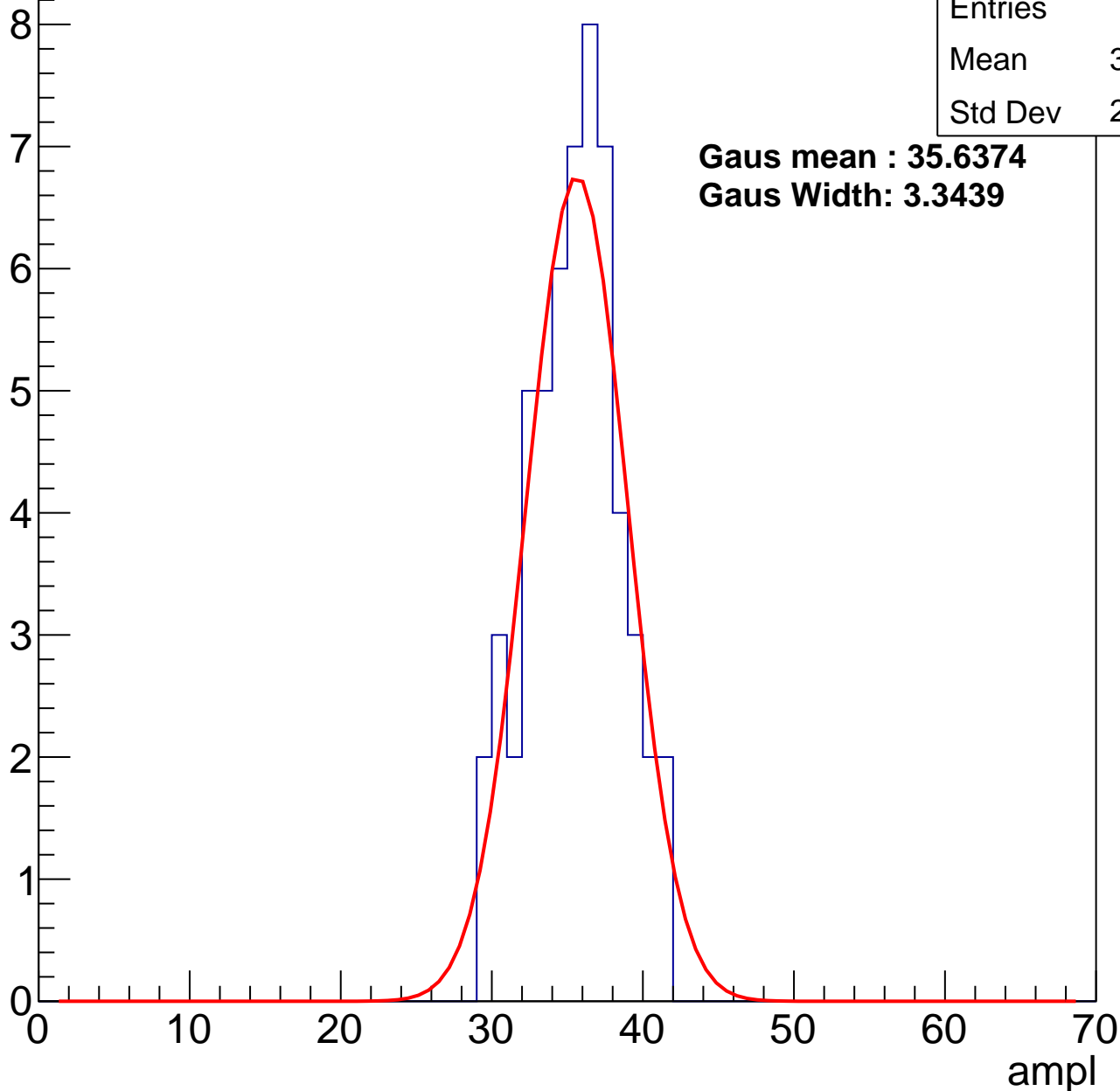
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.04
Std Dev	2.964

**Gaus mean : 35.6374**

**Gaus Width: 3.3439**



# B1L000S, U23-ch115, adc2

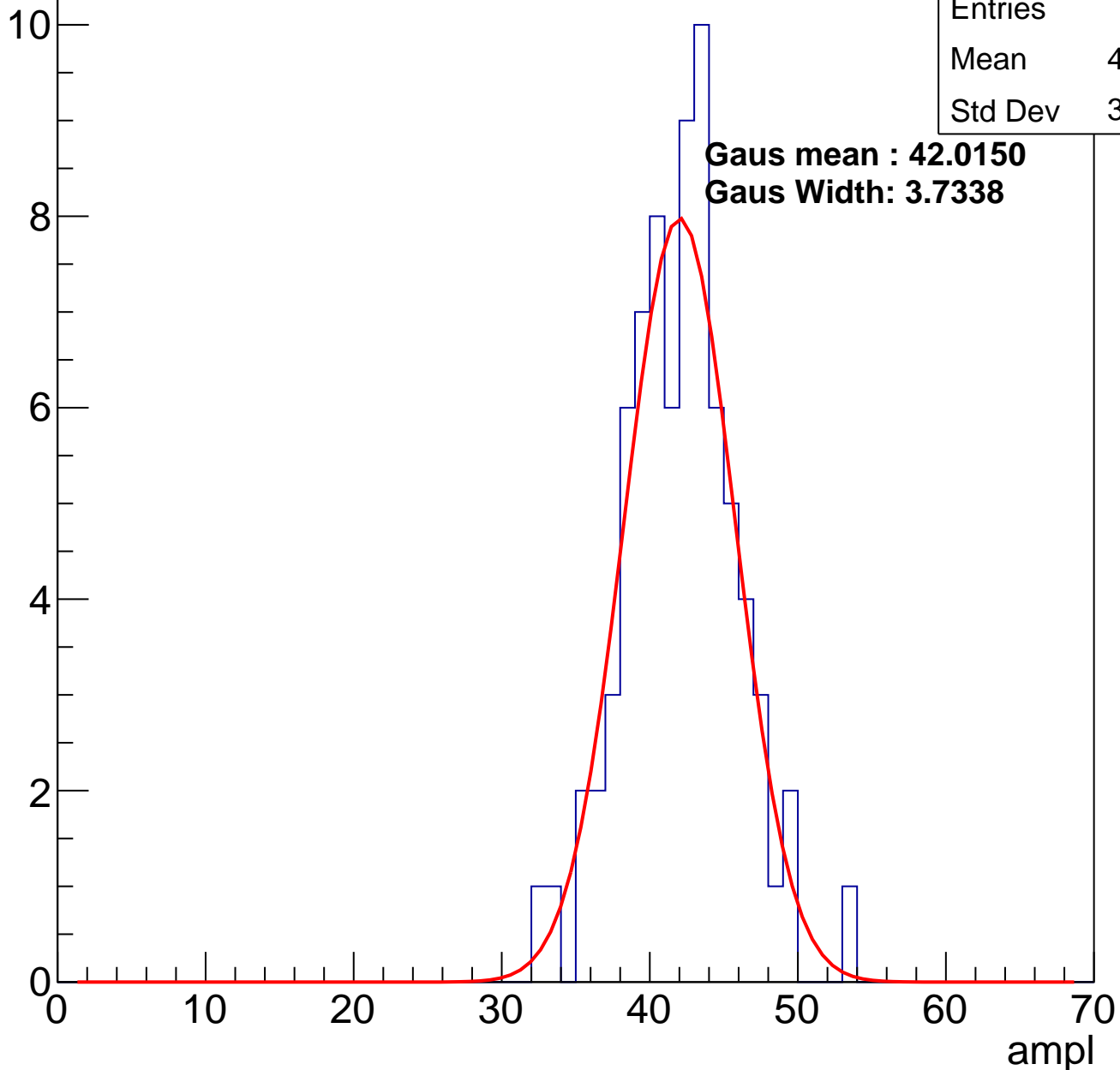
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	41.64
Std Dev	3.755

**Gaus mean : 42.0150**

**Gaus Width: 3.7338**

Entry

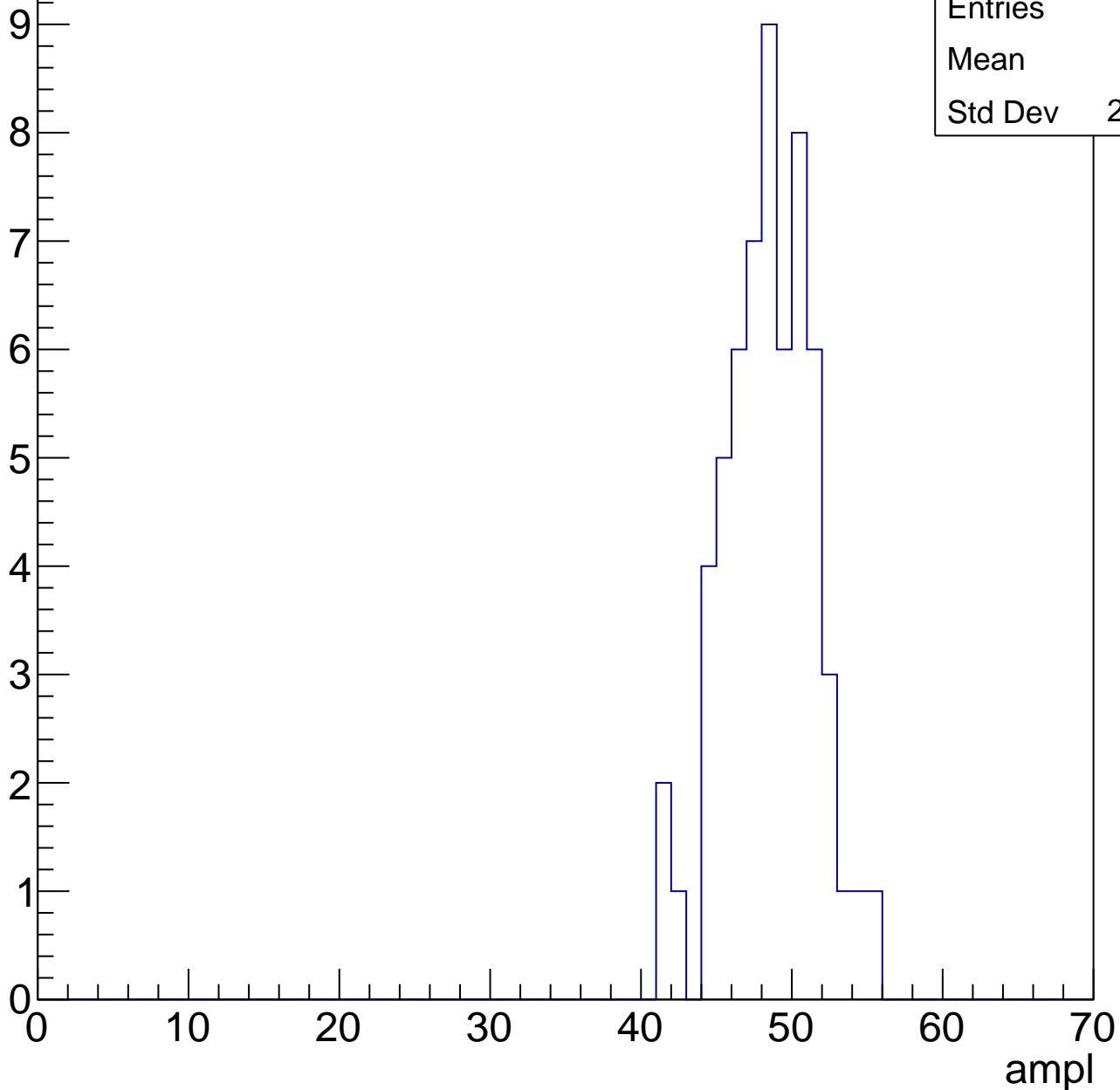


# B1L000S, U23-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

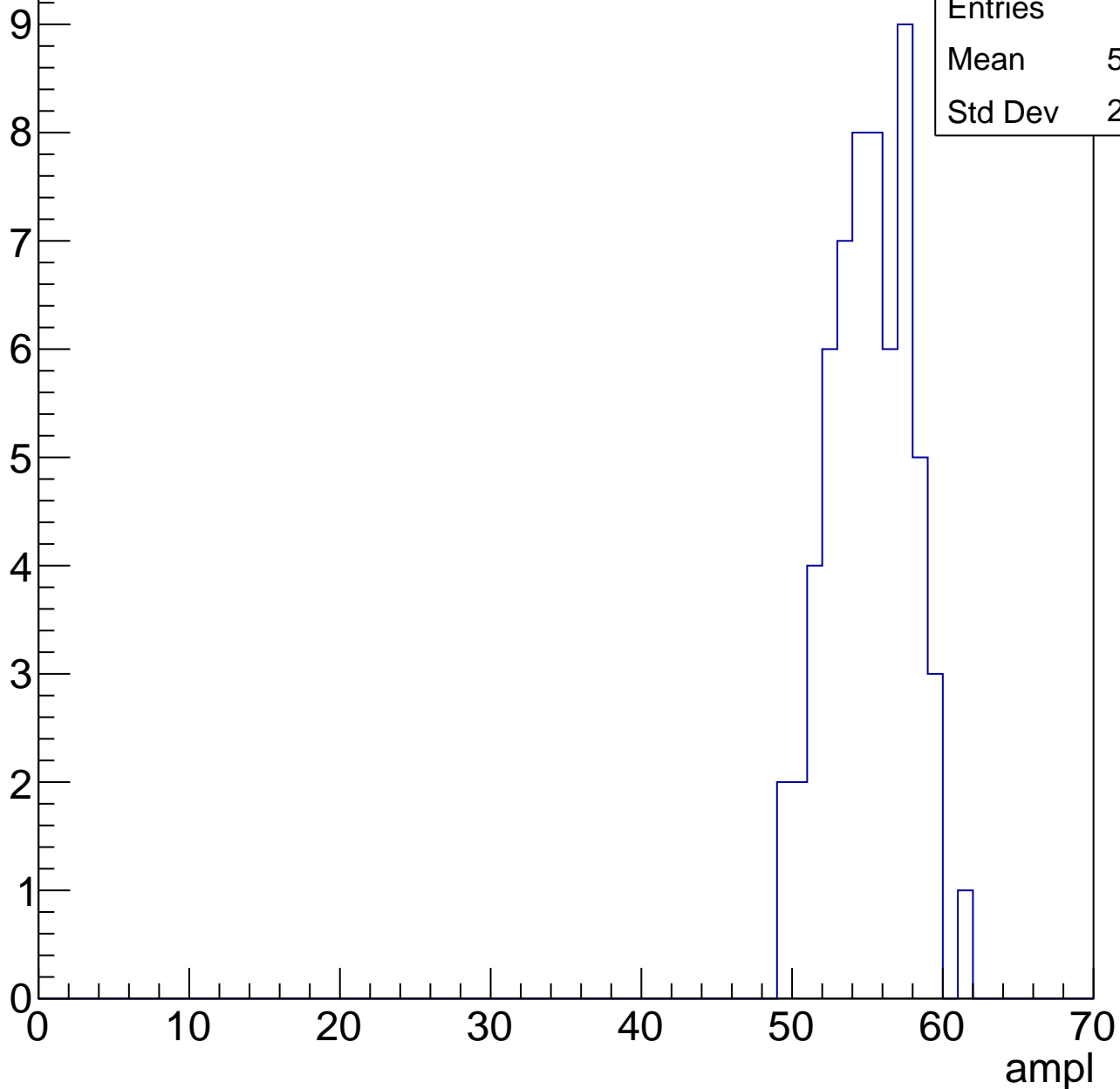
Entries	60
Mean	48
Std Dev	2.955



# B1L000S, U23-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	61
Mean	54.66
Std Dev	2.685

# B1L000S, U23-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	59.8
Std Dev	2.642

Entry

10

8

6

4

2

0

0

10

20

30

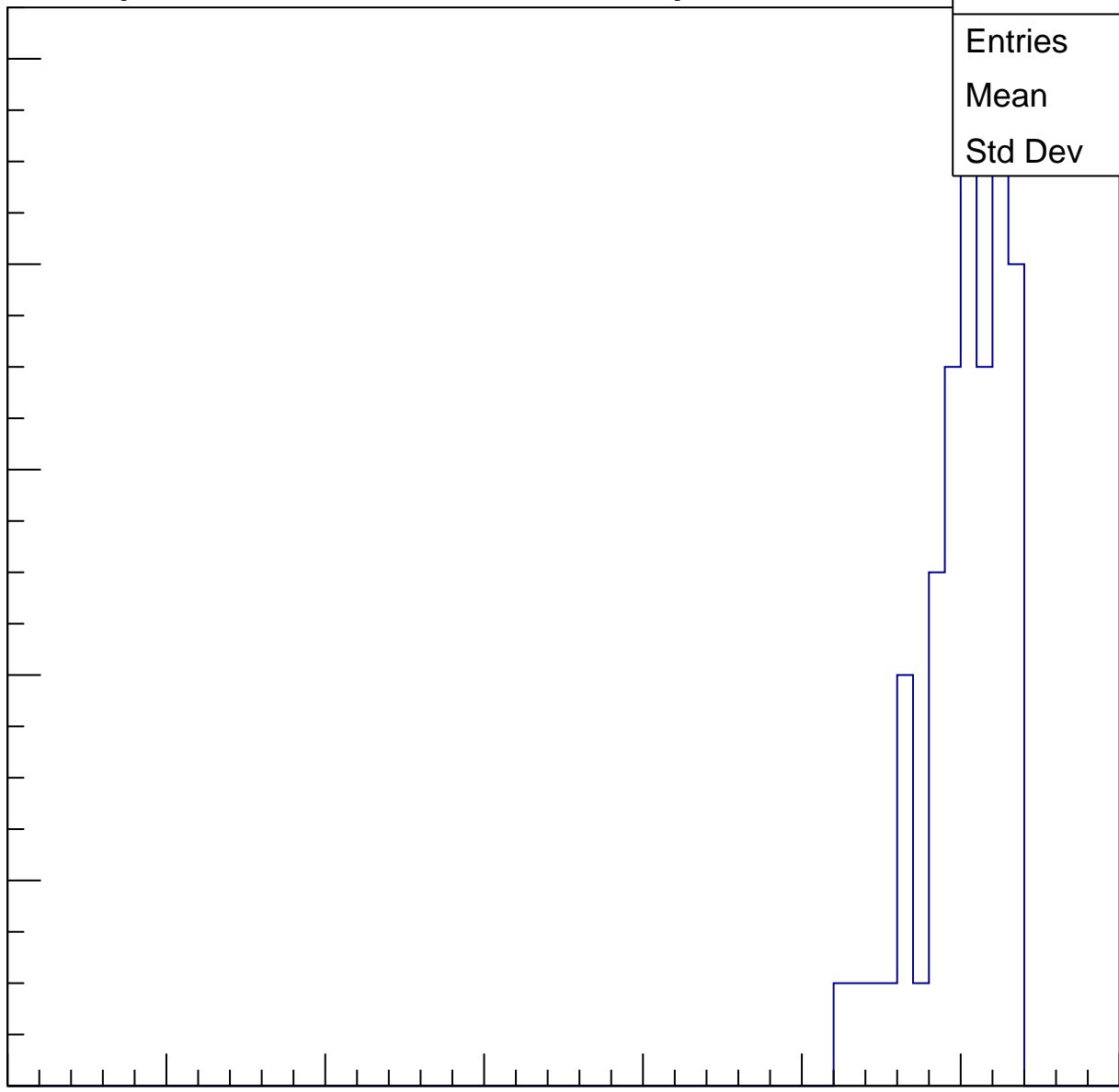
40

50

60

70

ampl



# B1L000S, U23-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

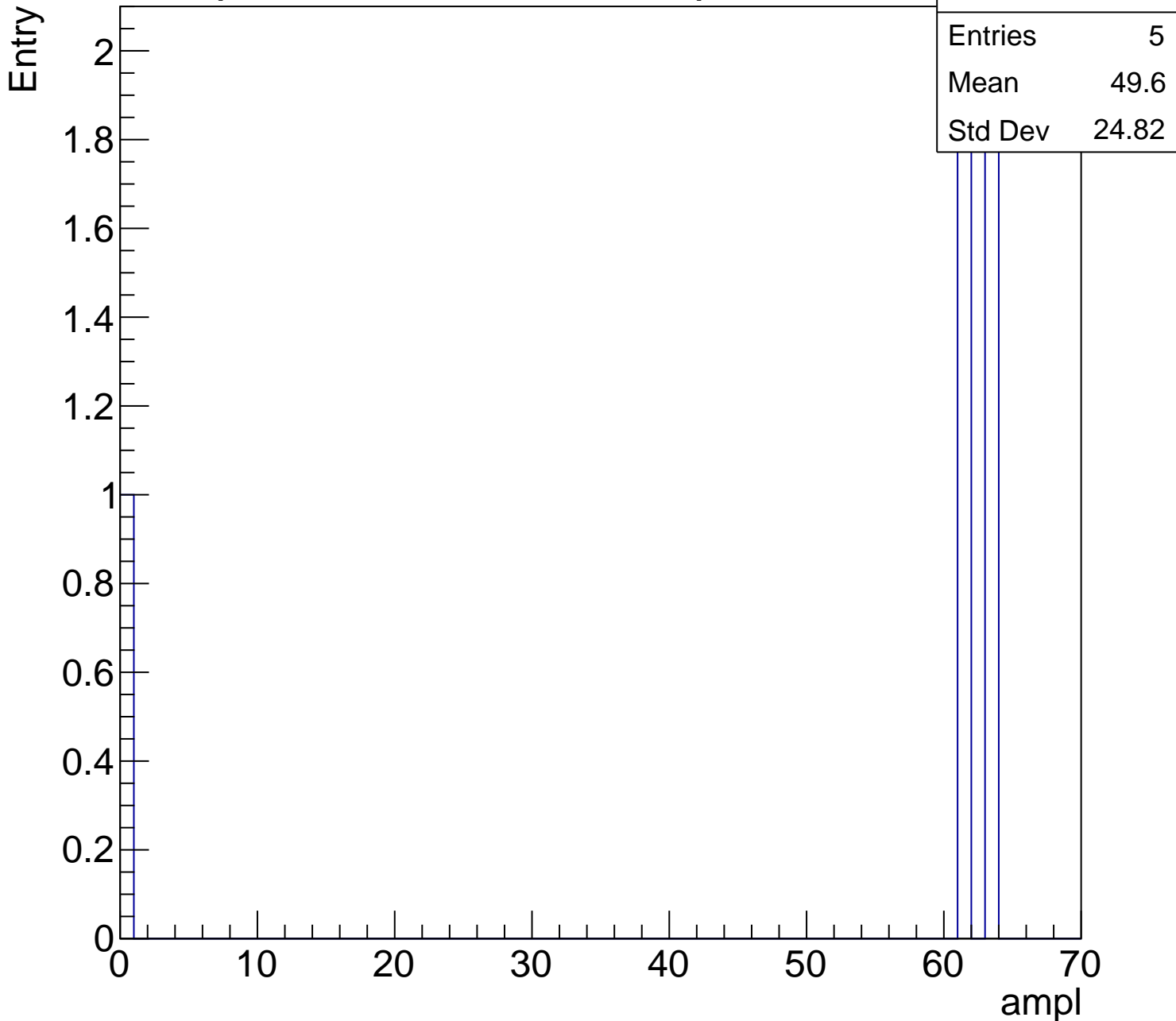
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl





# B1L000S, U23-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch116, adc0

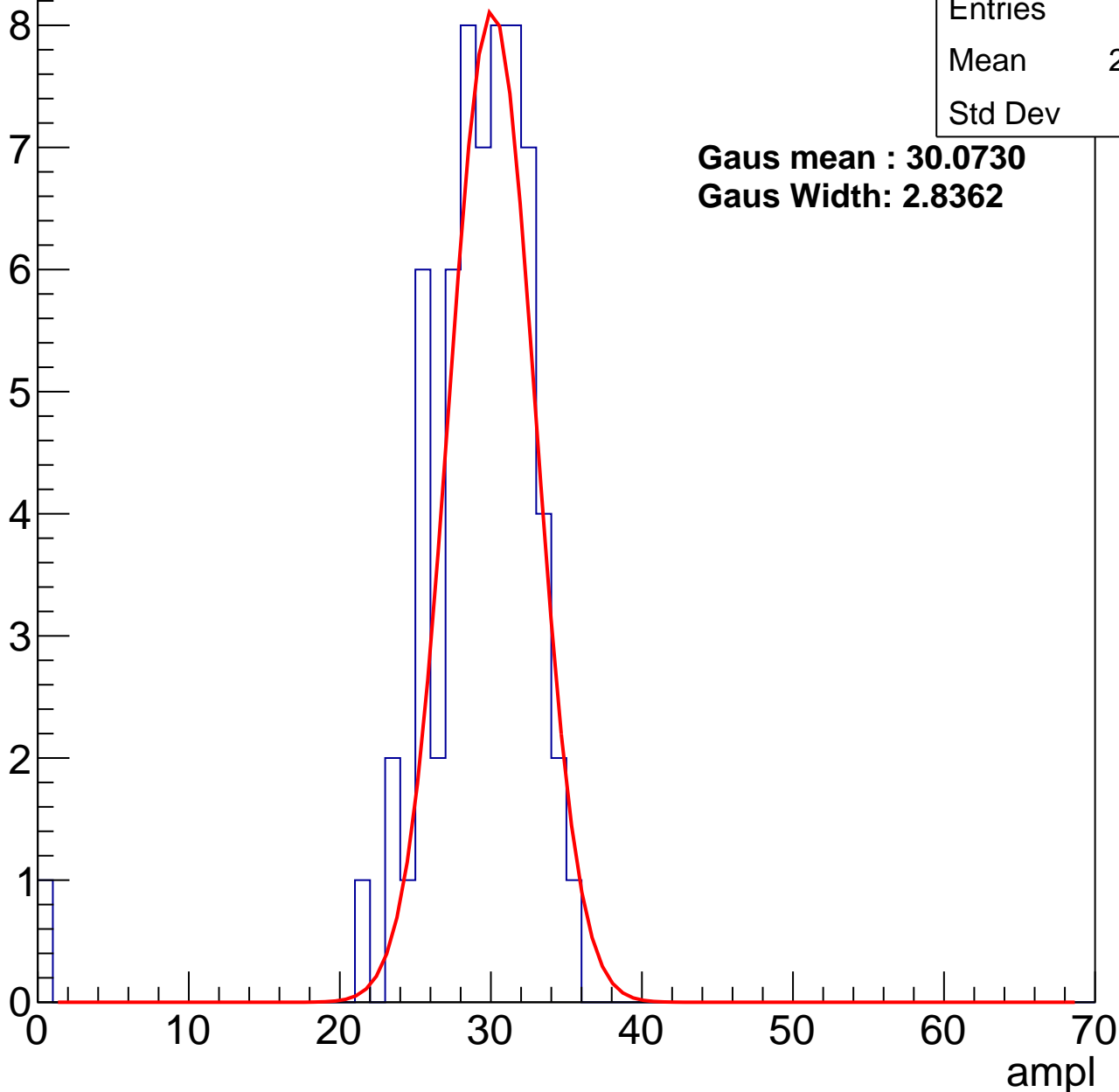
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.58
Std Dev	4.66

**Gaus mean : 30.0730**

**Gaus Width: 2.8362**



# B1L000S, U23-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	36.04
Std Dev	3.513

**Gaus mean : 36.6255**

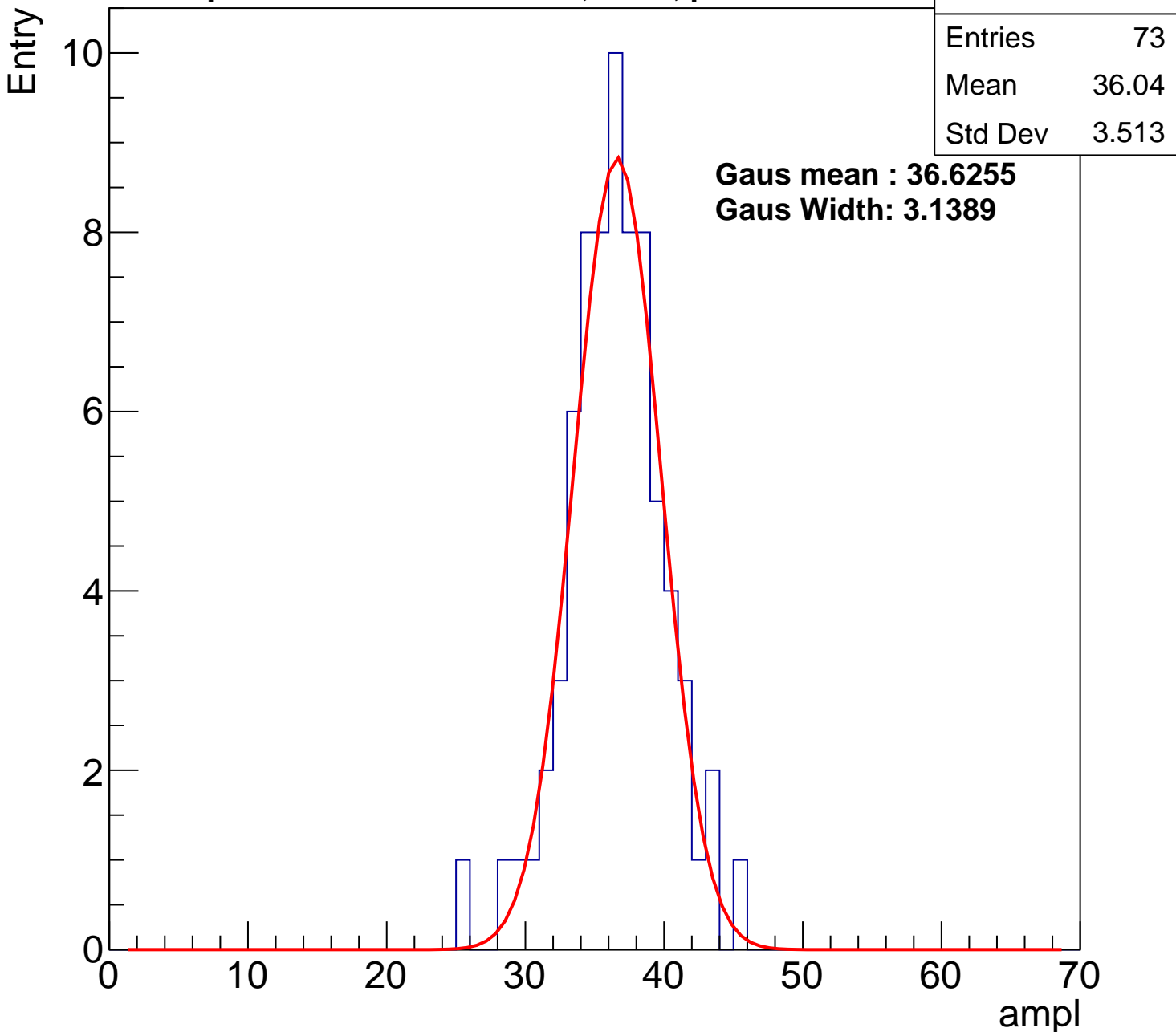
**Gaus Width: 3.1389**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U23-ch116, adc2

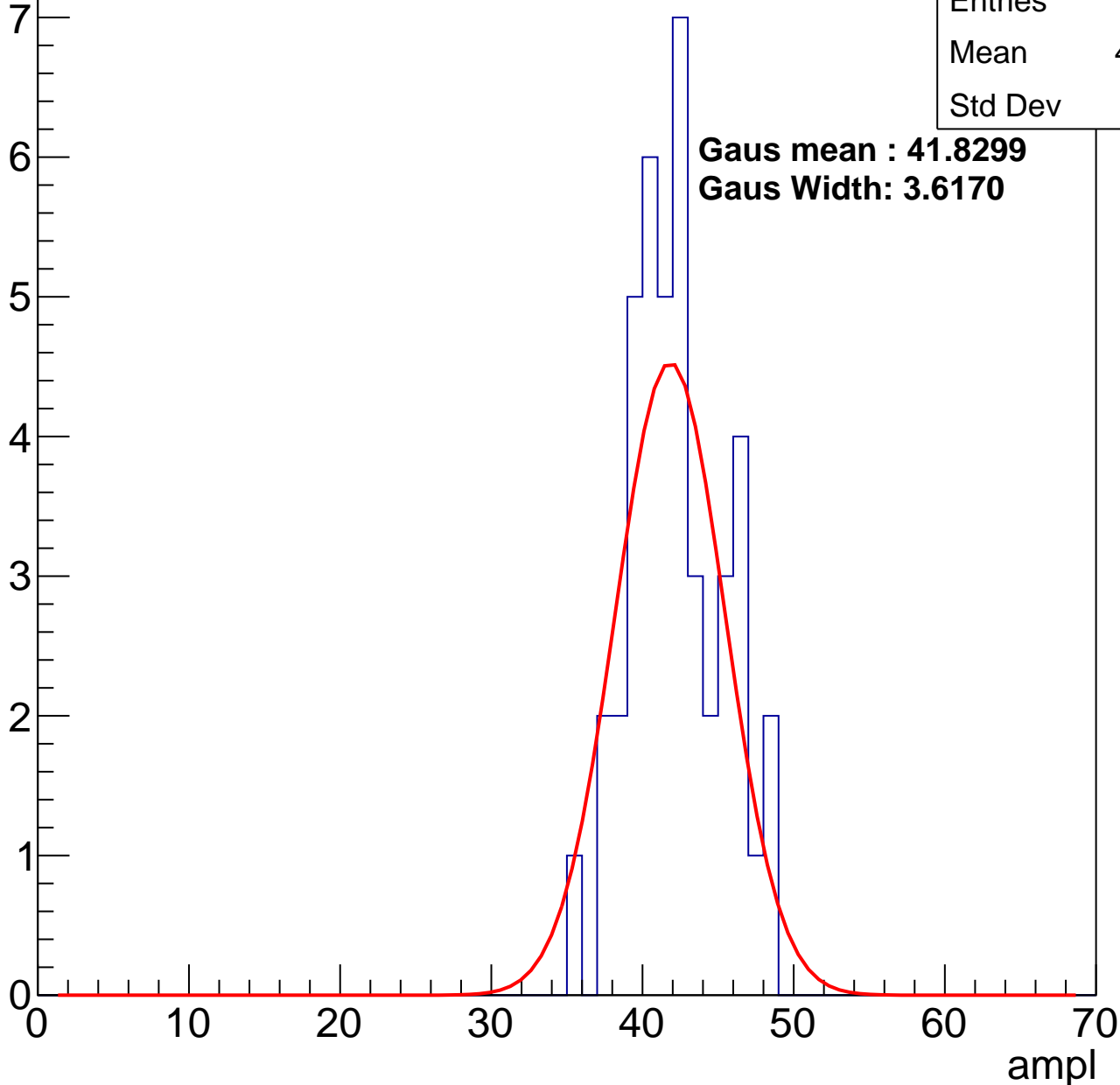
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	41.81
Std Dev	3.06

**Gaus mean : 41.8299**

**Gaus Width: 3.6170**

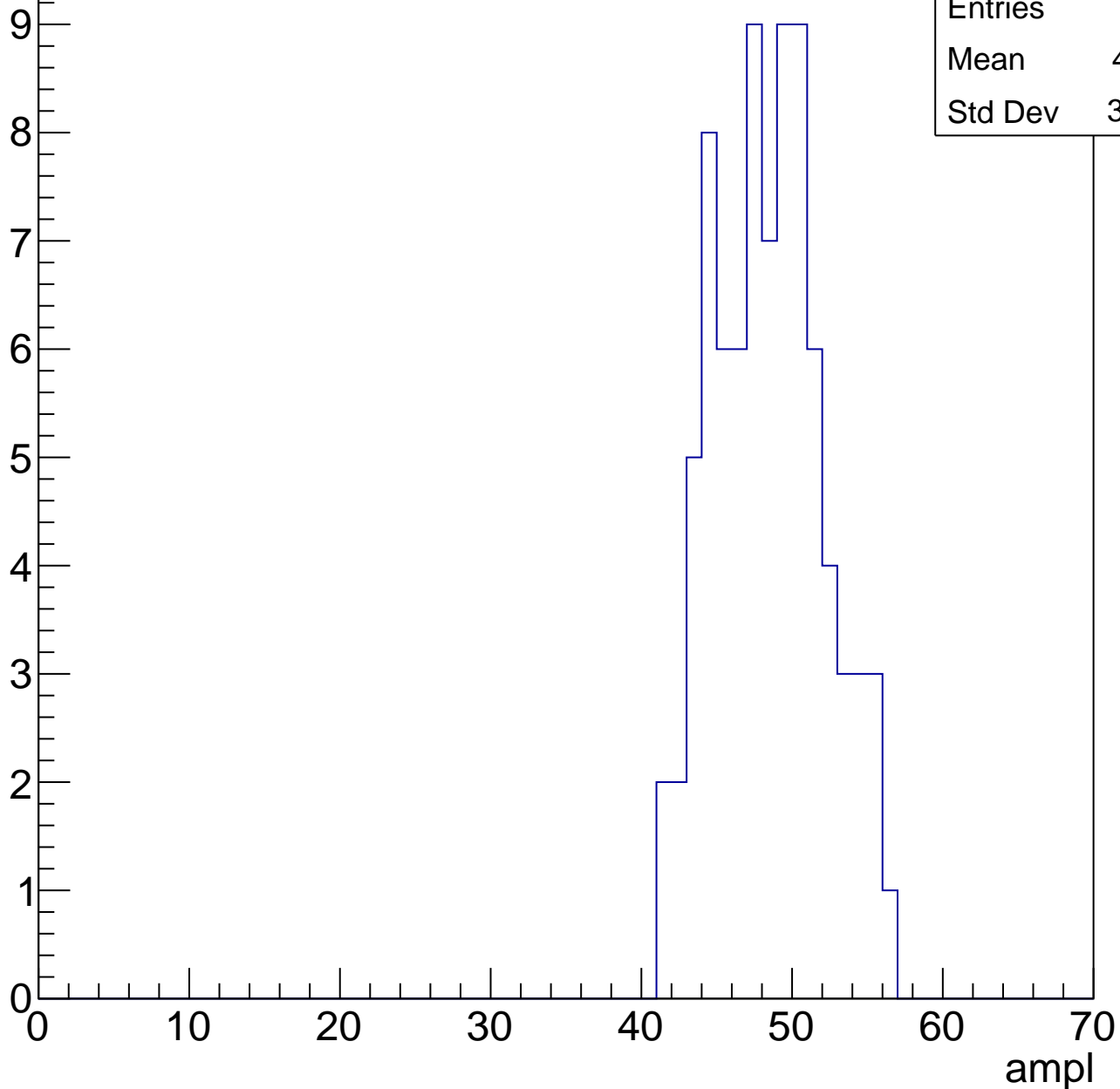


# B1L000S, U23-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	48.01
Std Dev	3.585

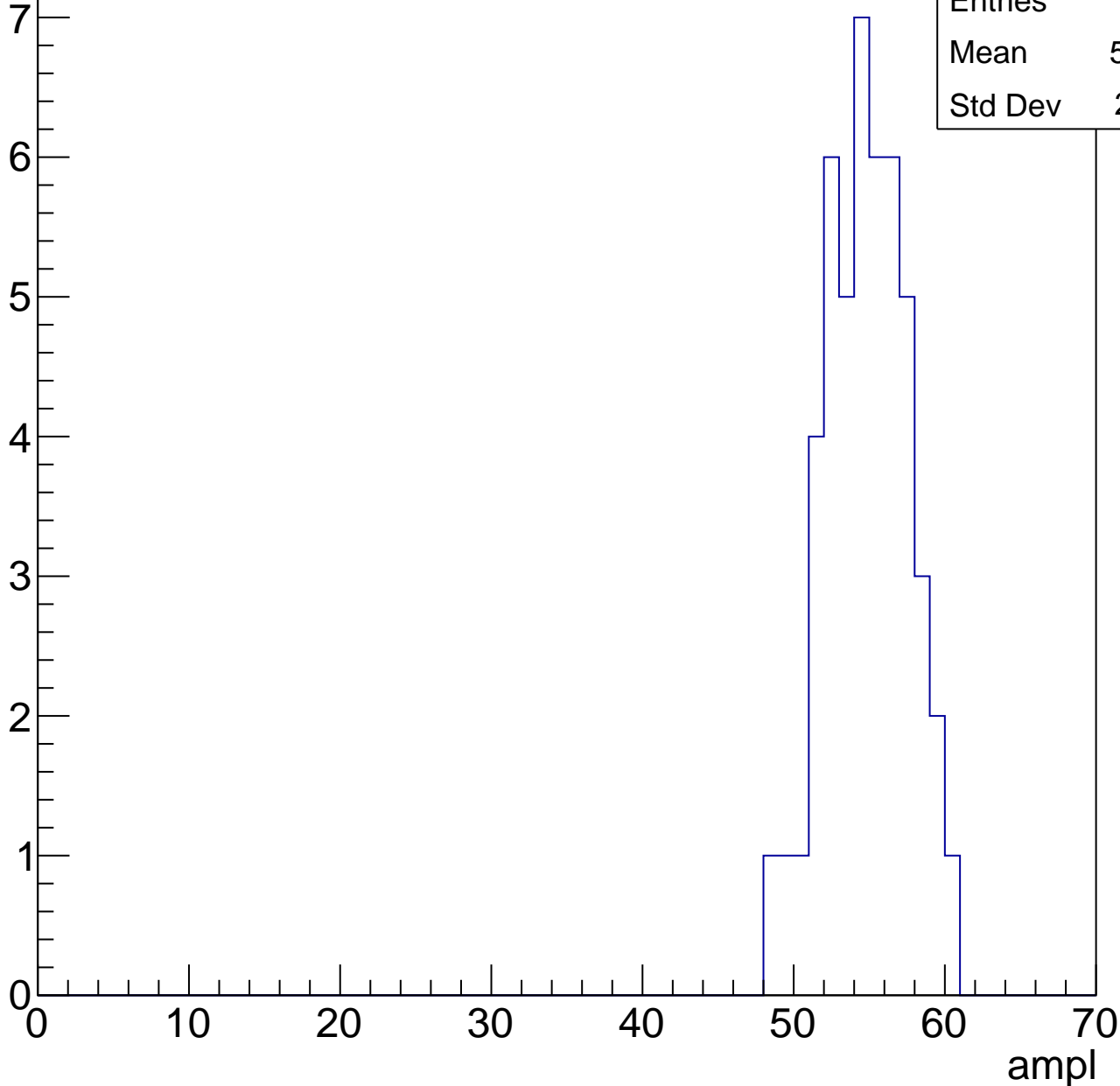


# B1L000S, U23-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	54.35
Std Dev	2.681

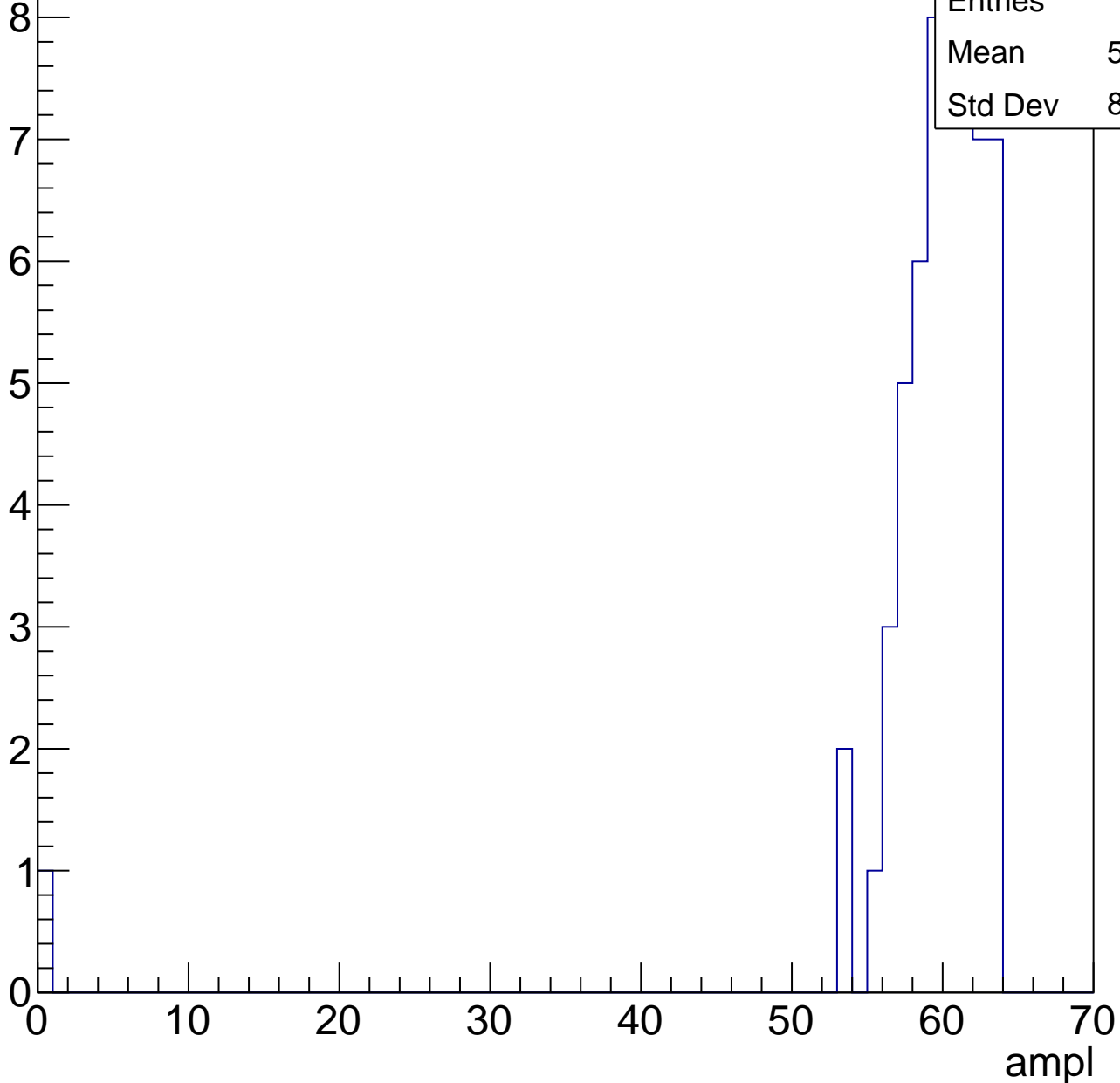


# B1L000S, U23-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

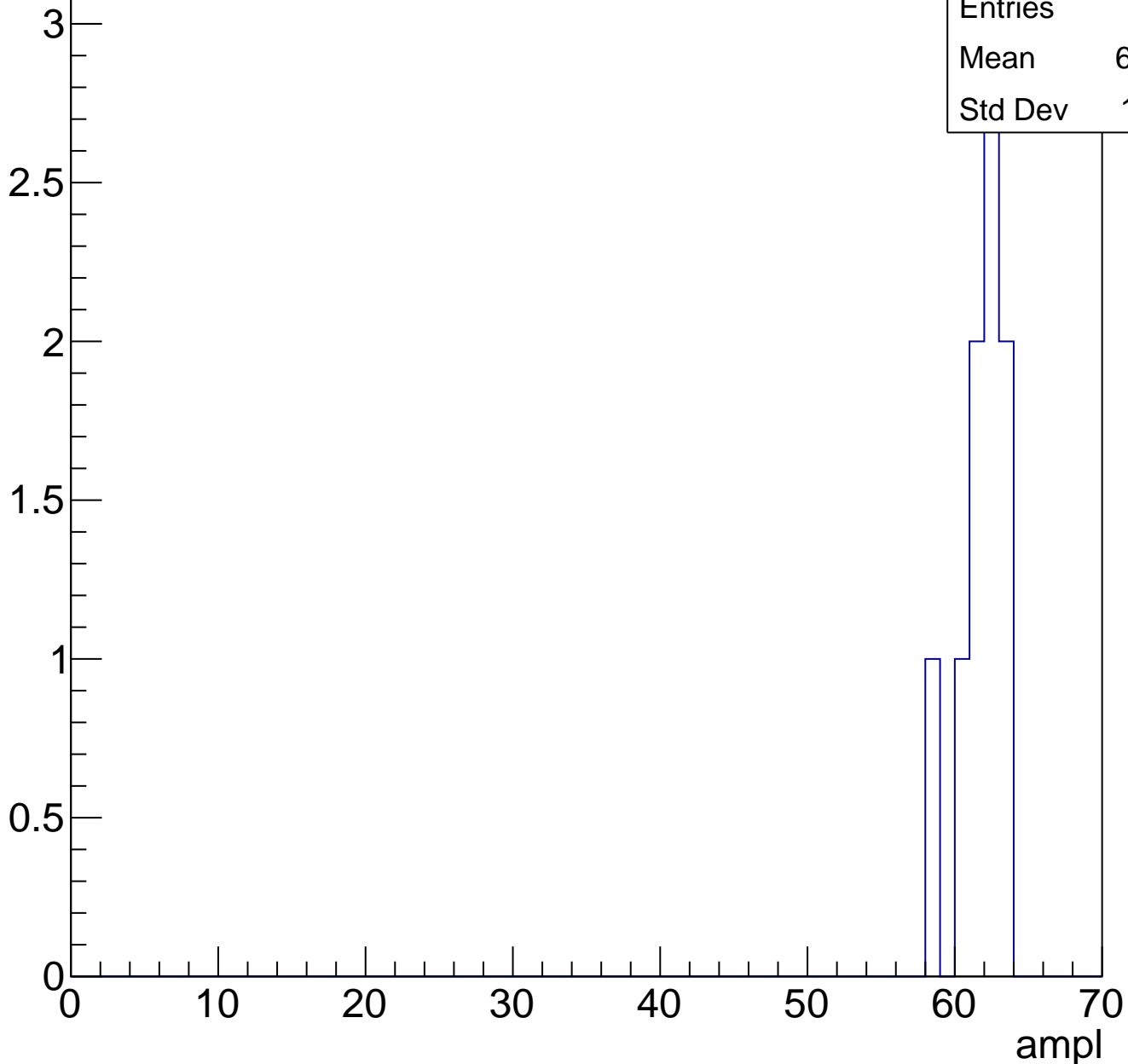
Entries	56
Mean	58.52
Std Dev	8.263



# B1L000S, U23-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	9
Mean	61.33
Std Dev	1.491



# B1L000S, U23-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch117, adc0

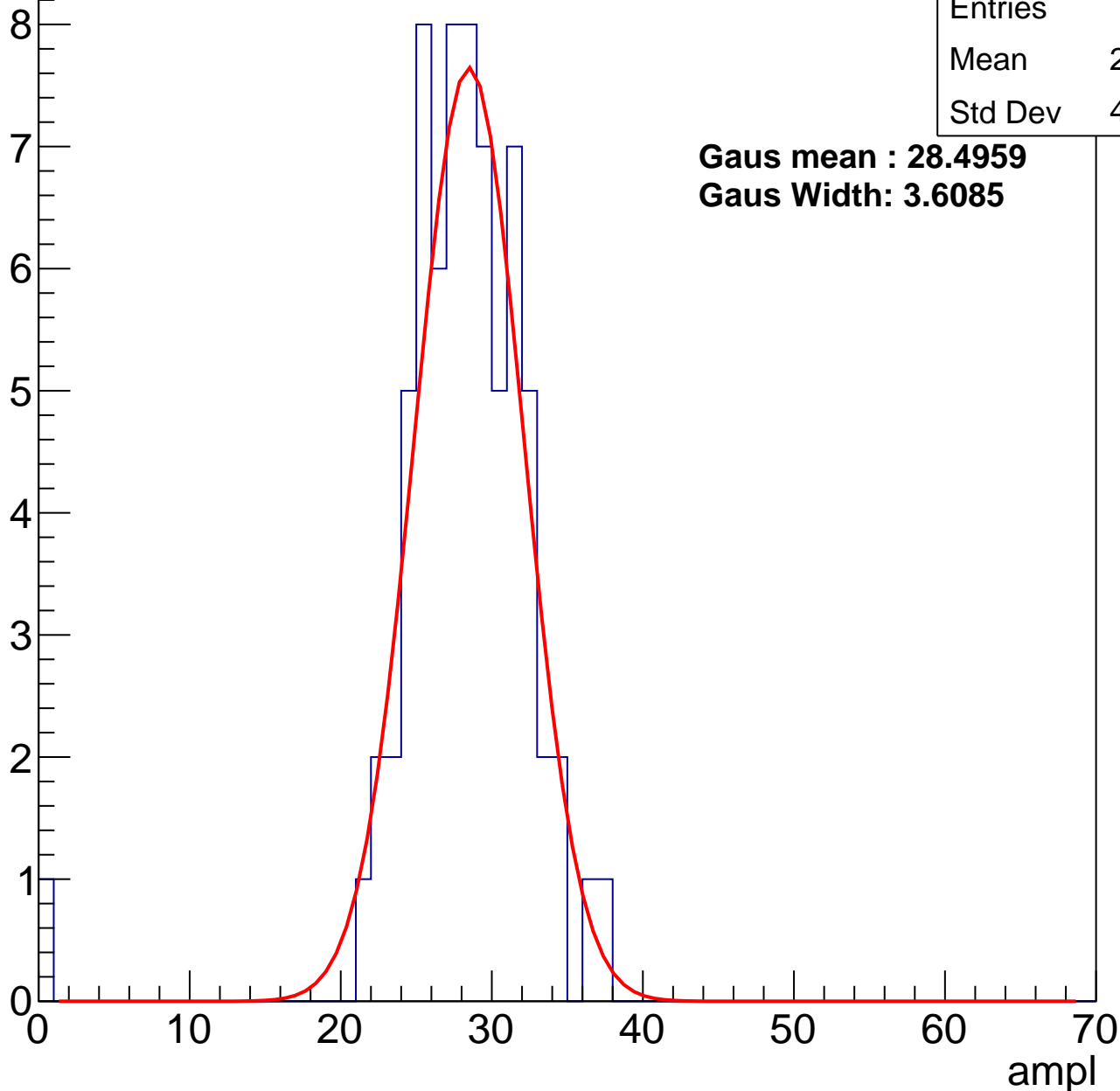
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.66
Std Dev	4.699

**Gaus mean : 28.4959**

**Gaus Width: 3.6085**



# B1L000S, U23-ch117, adc1

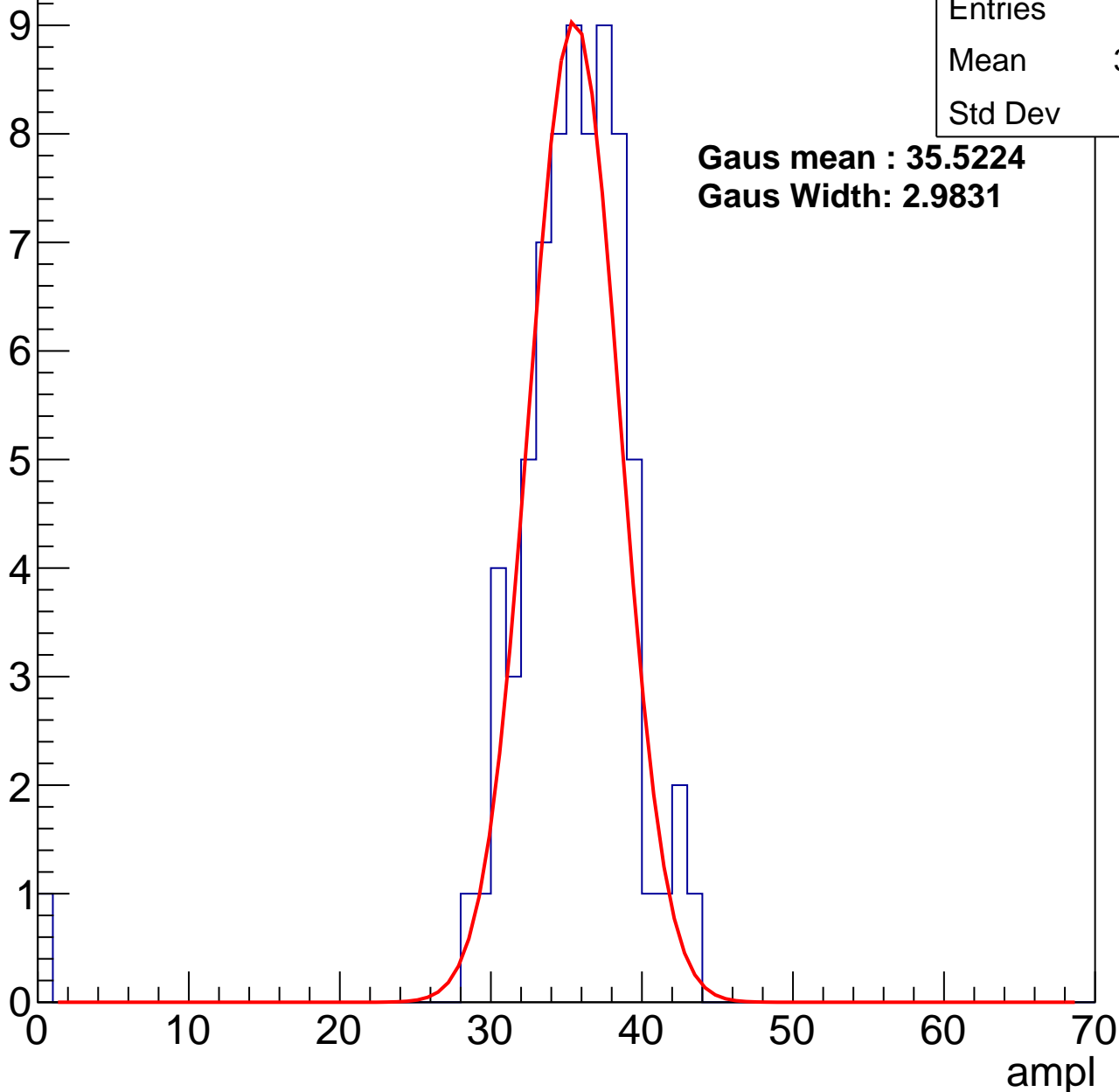
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	34.81
Std Dev	5.14

**Gaus mean : 35.5224**

**Gaus Width: 2.9831**



# B1L000S, U23-ch117, adc2

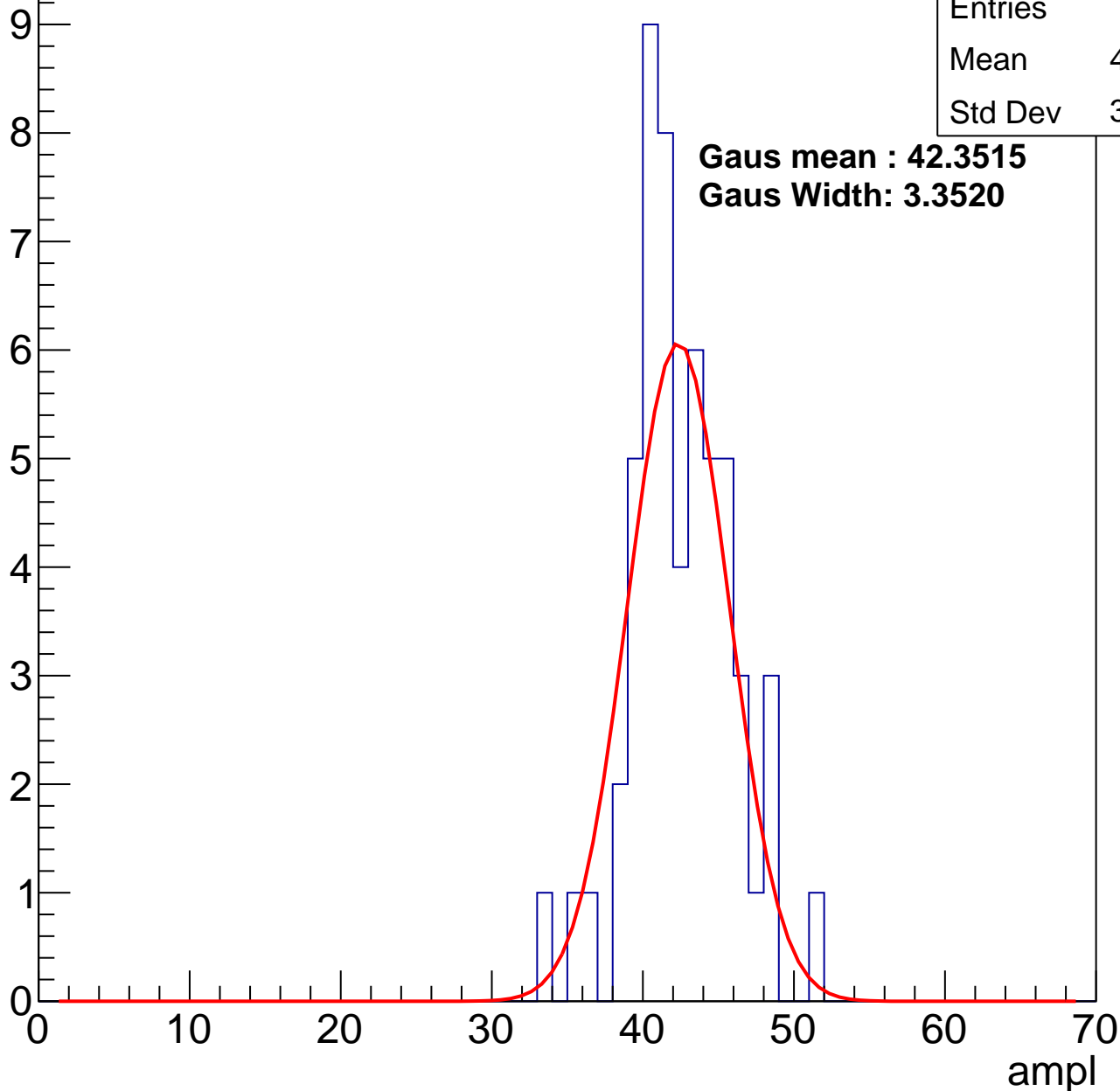
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.07
Std Dev	3.357

**Gaus mean : 42.3515**

**Gaus Width: 3.3520**

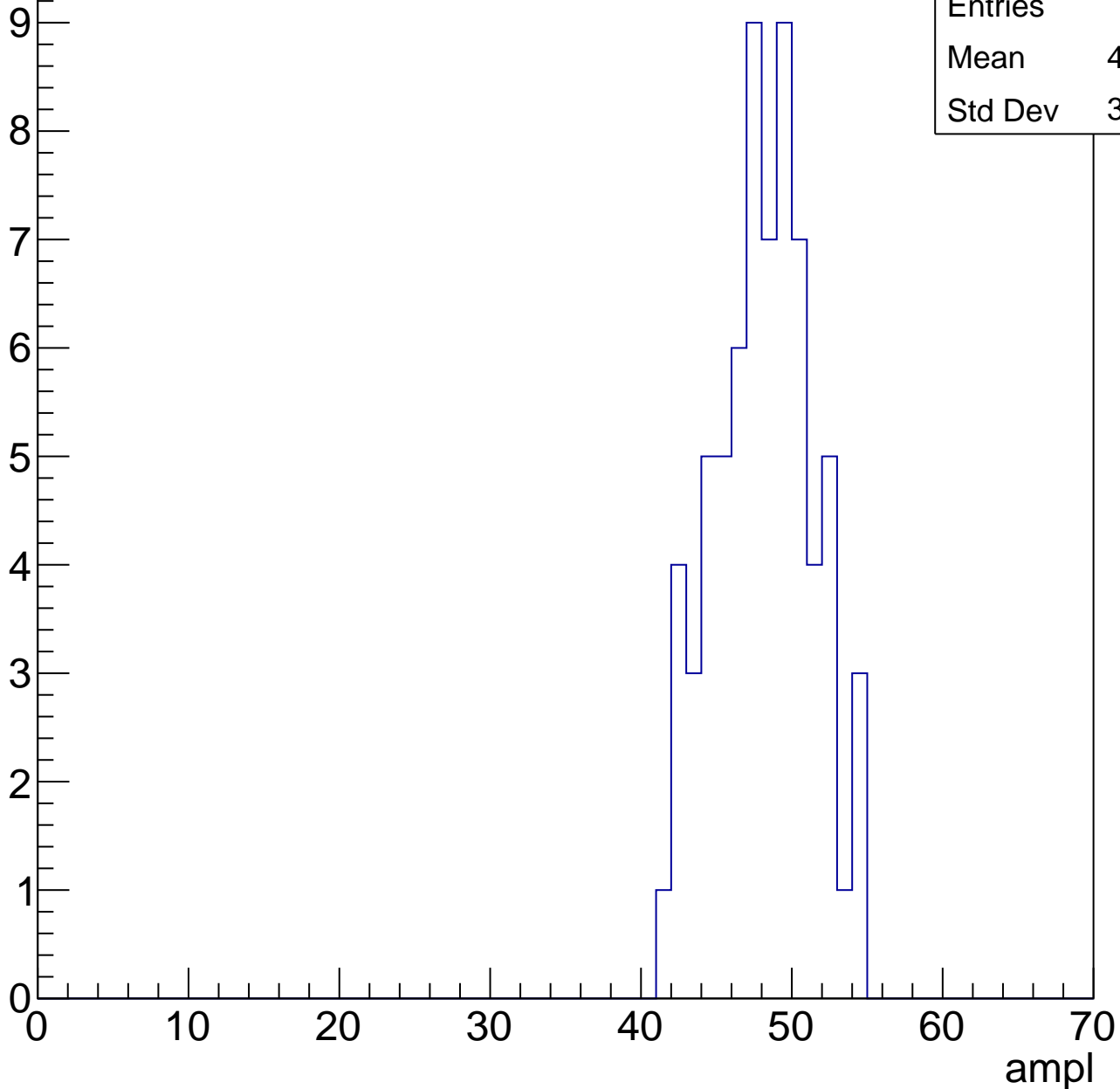


# B1L000S, U23-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.65
Std Dev	3.193

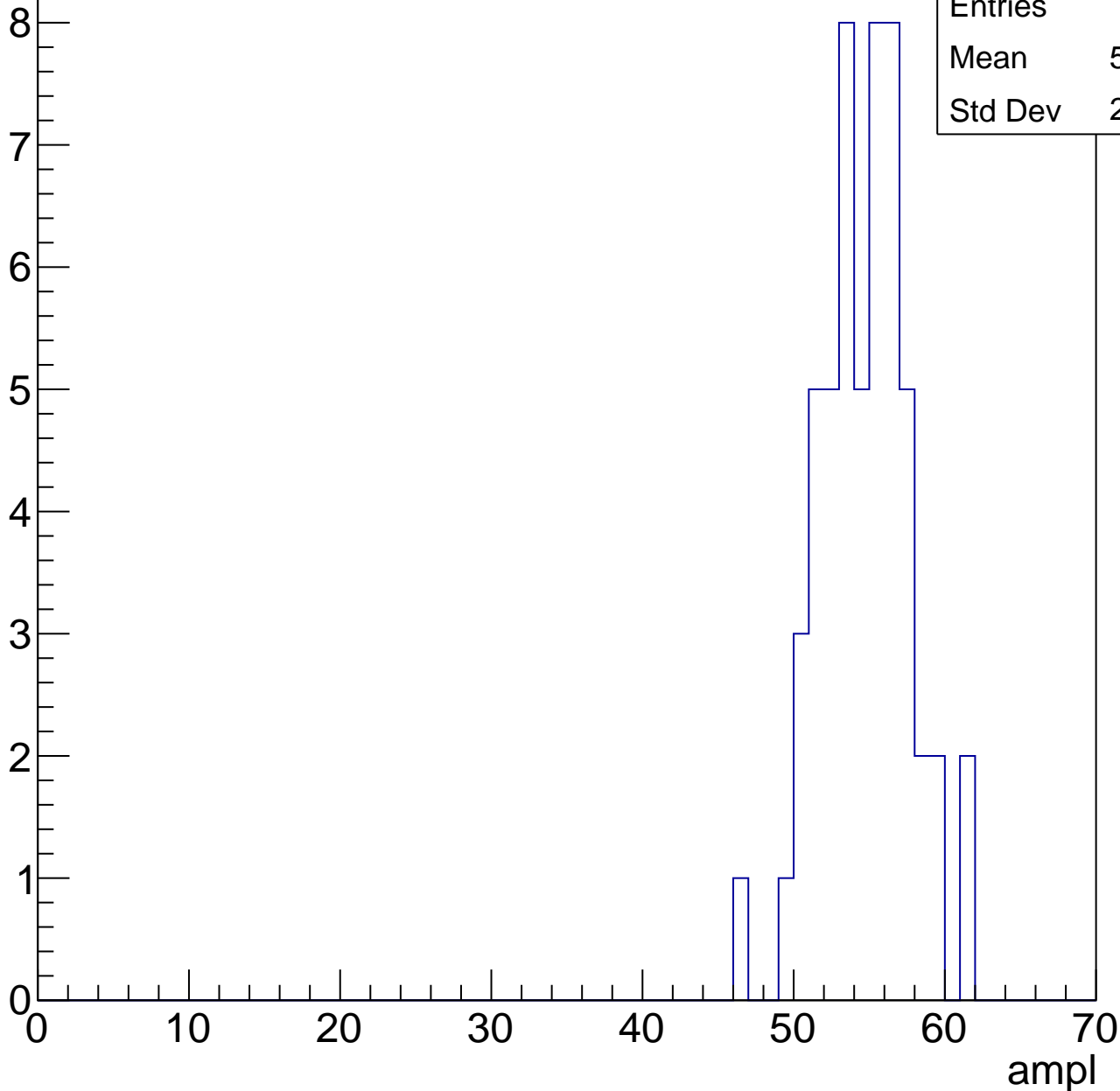


# B1L000S, U23-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	54.24
Std Dev	2.929

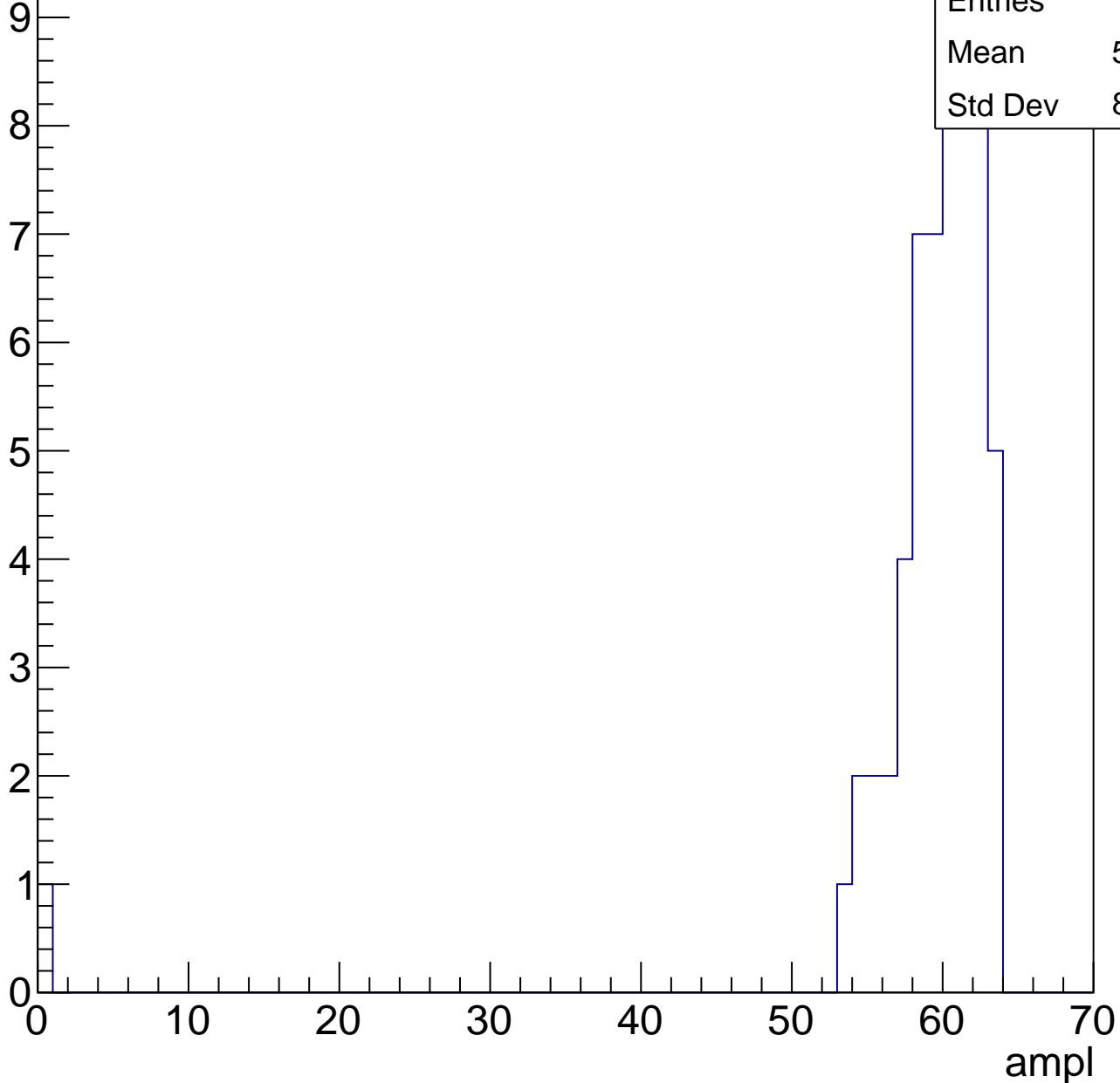


# B1L000S, U23-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	58.41
Std Dev	8.261

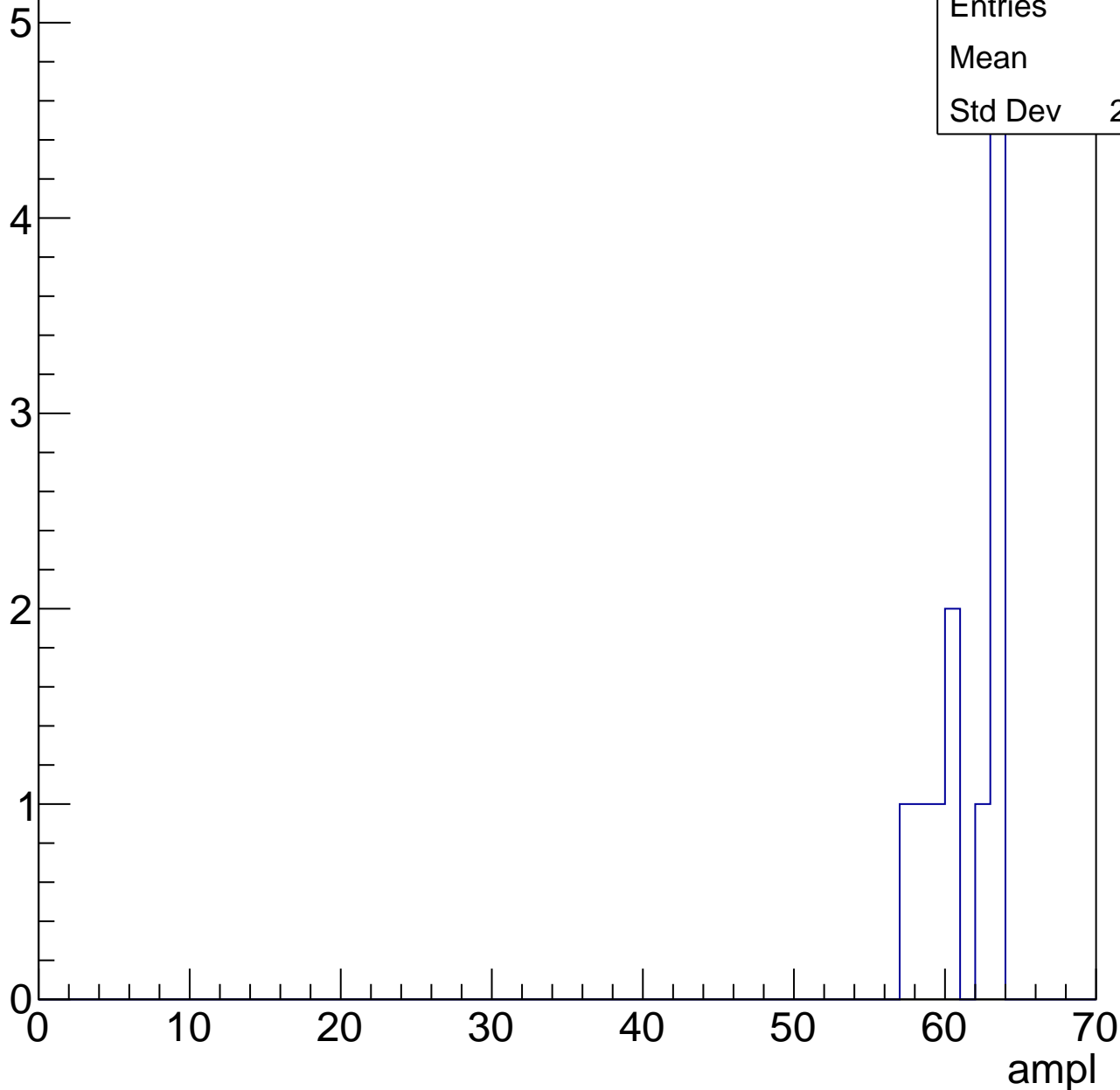


# B1L000S, U23-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	61
Std Dev	2.174





# B1L000S, U23-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch118, adc0

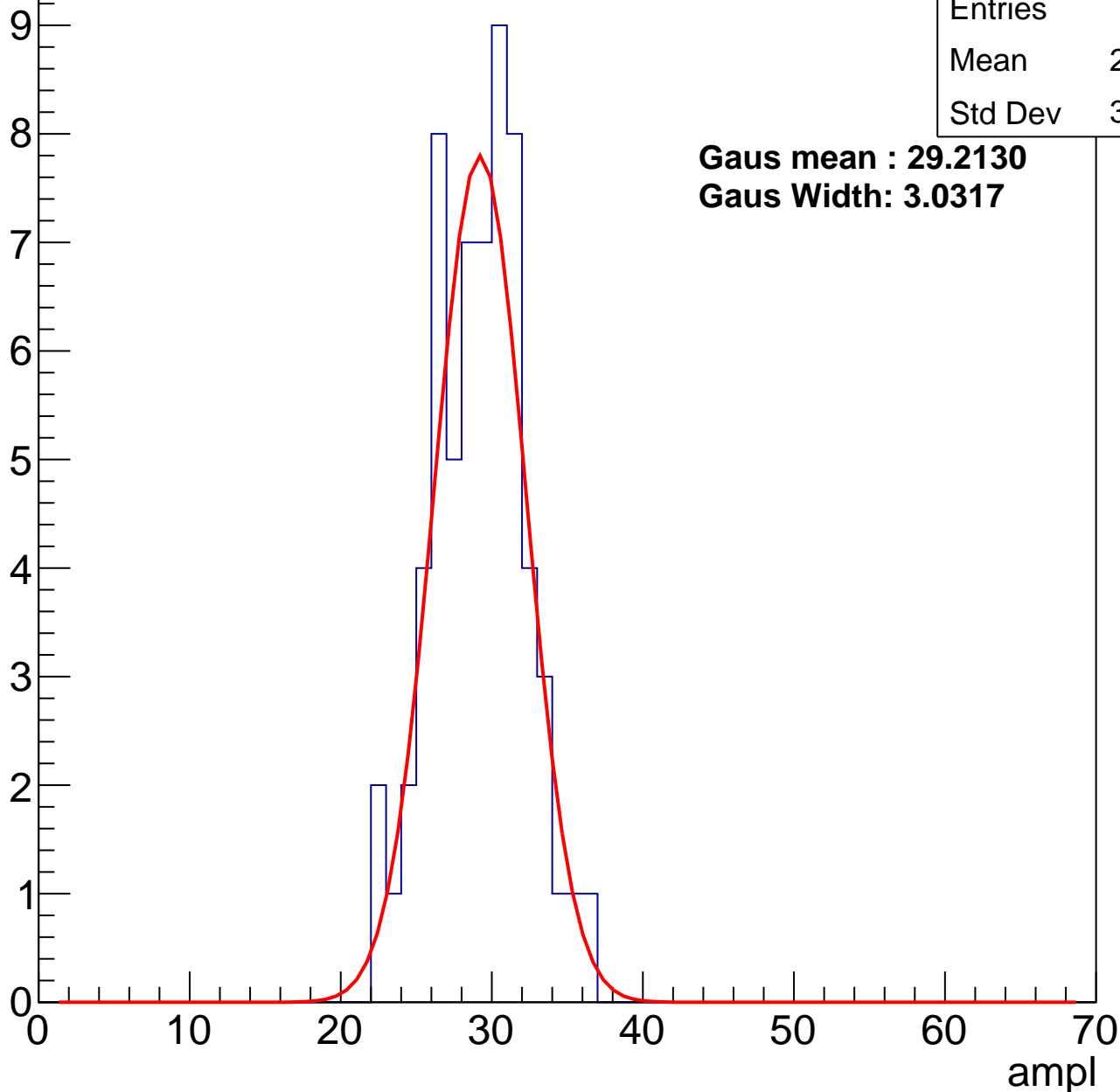
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.68
Std Dev	3.023

**Gaus mean : 29.2130**

**Gaus Width: 3.0317**



# B1L000S, U23-ch118, adc1

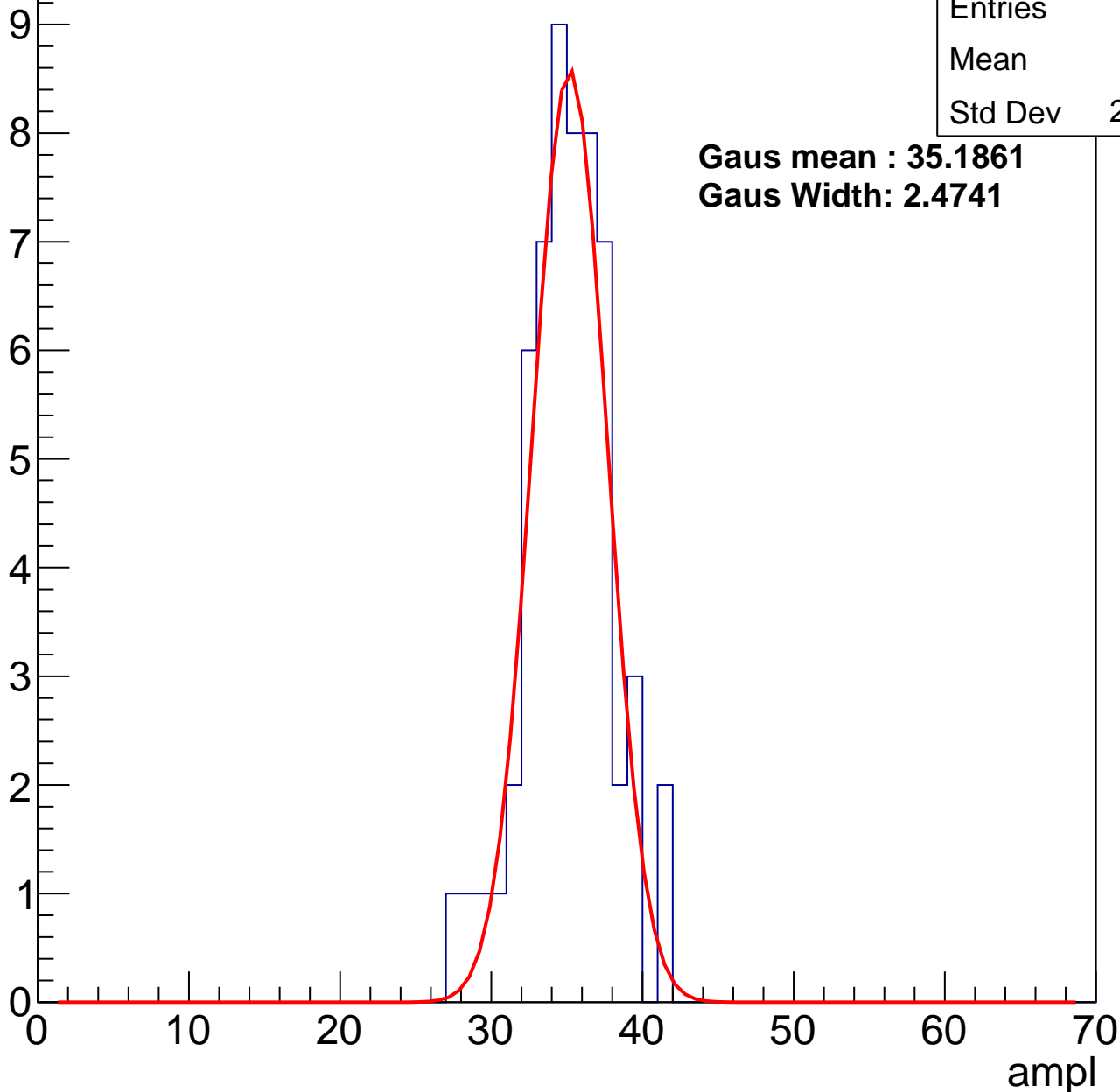
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.6
Std Dev	2.822

**Gaus mean : 35.1861**

**Gaus Width: 2.4741**



# B1L000S, U23-ch118, adc2

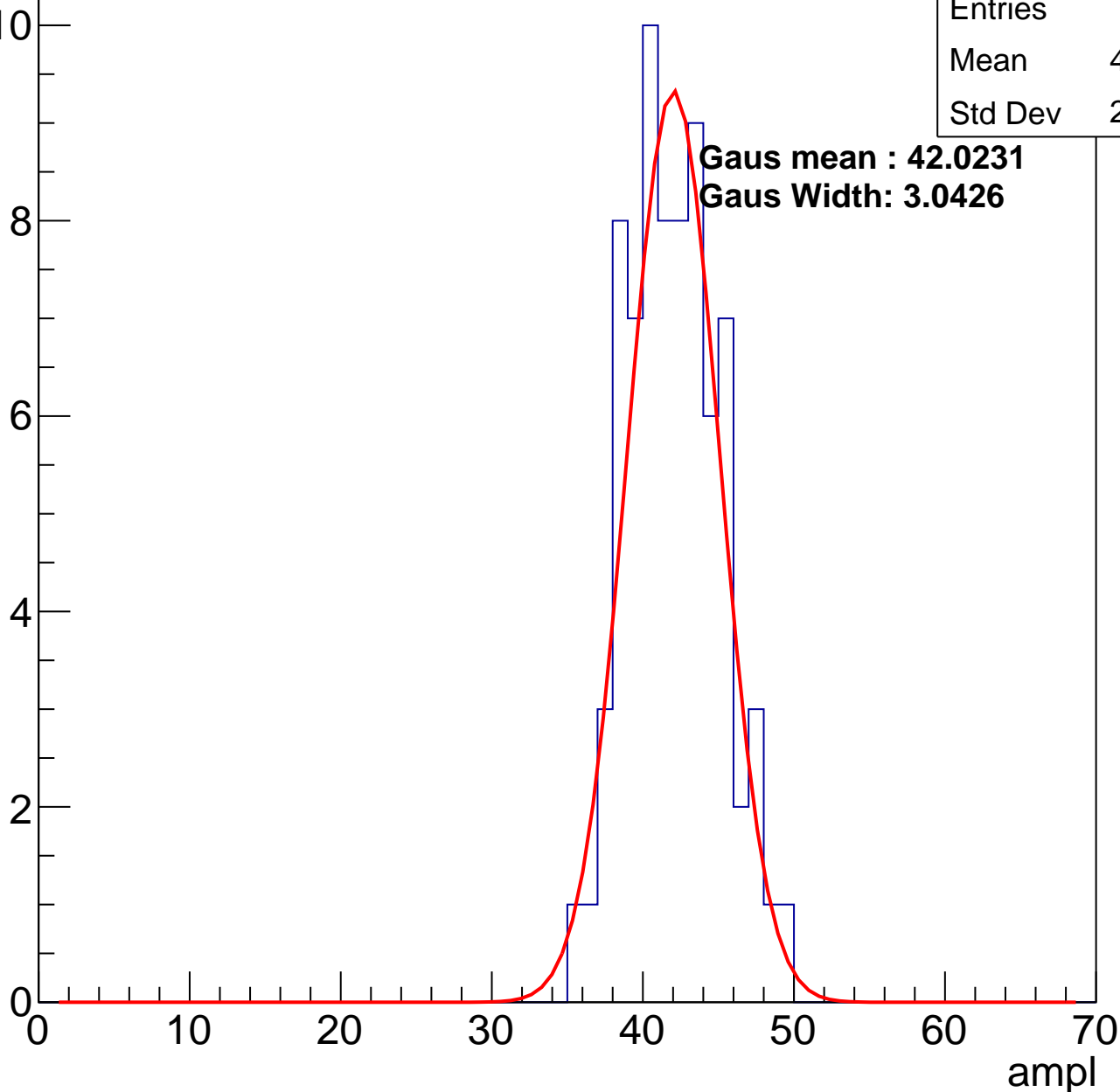
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	41.59
Std Dev	2.998

**Gaus mean : 42.0231**

**Gaus Width: 3.0426**

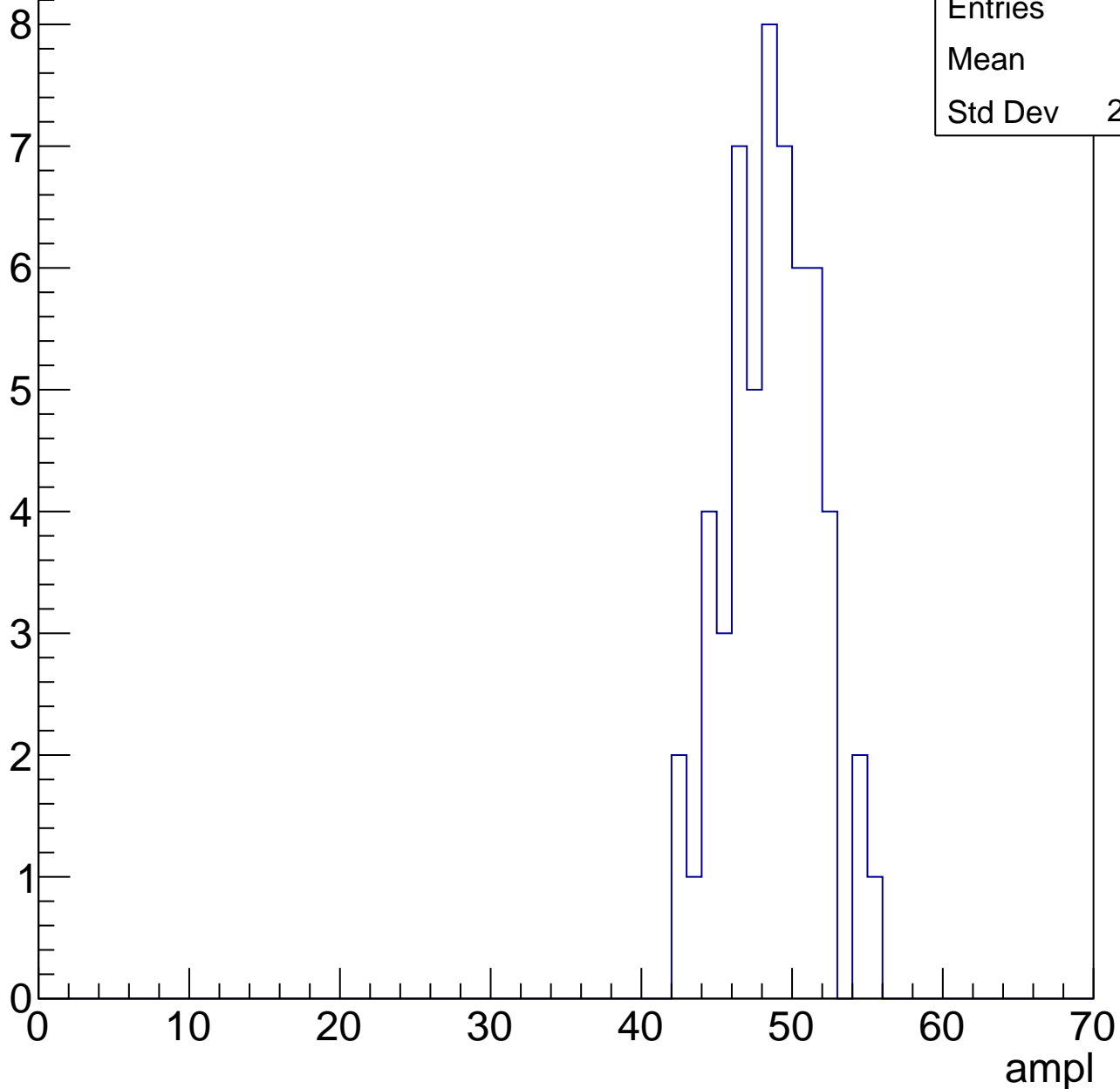


# B1L000S, U23-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	48.2
Std Dev	2.955

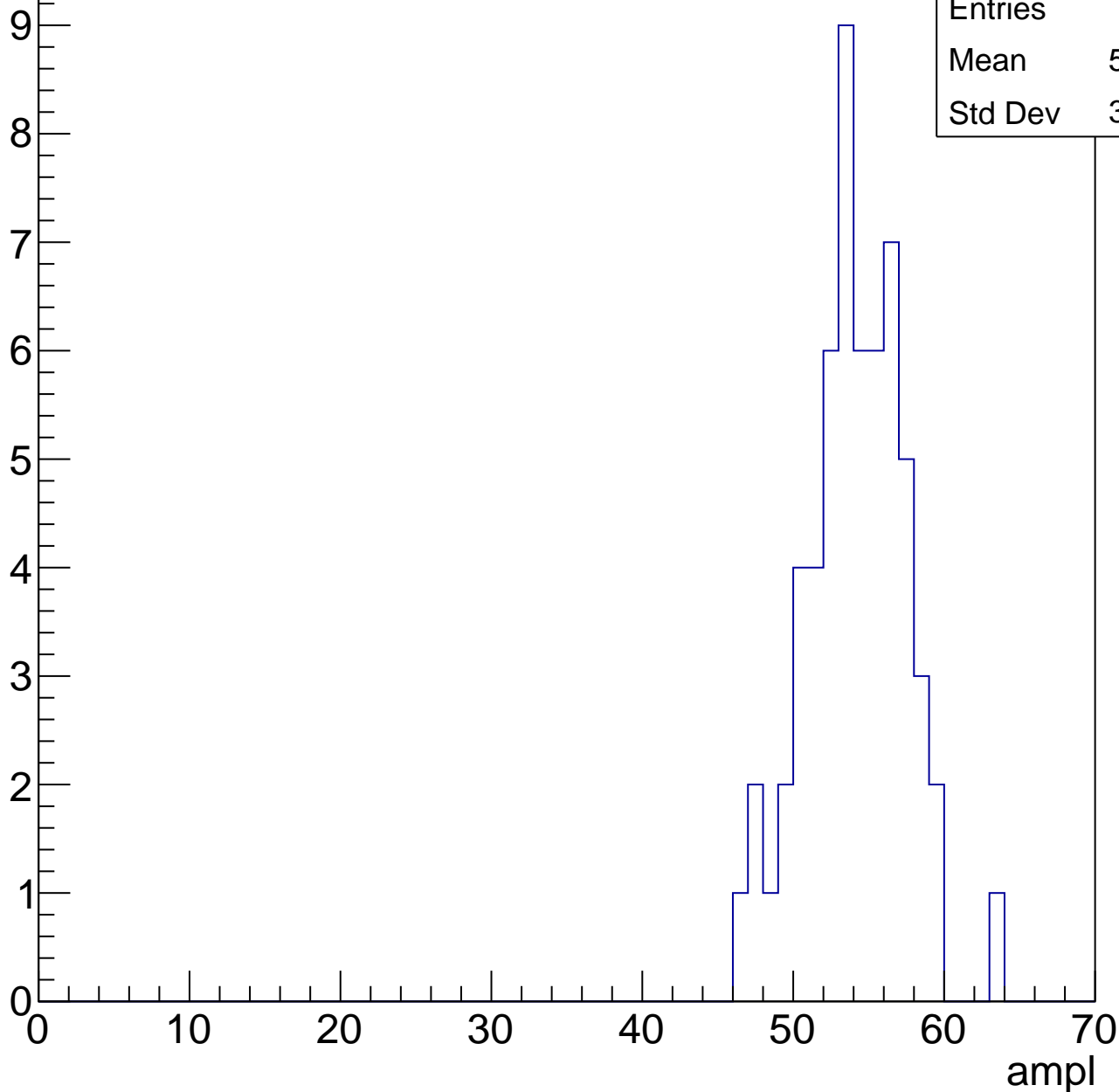


# B1L000S, U23-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.64
Std Dev	3.272

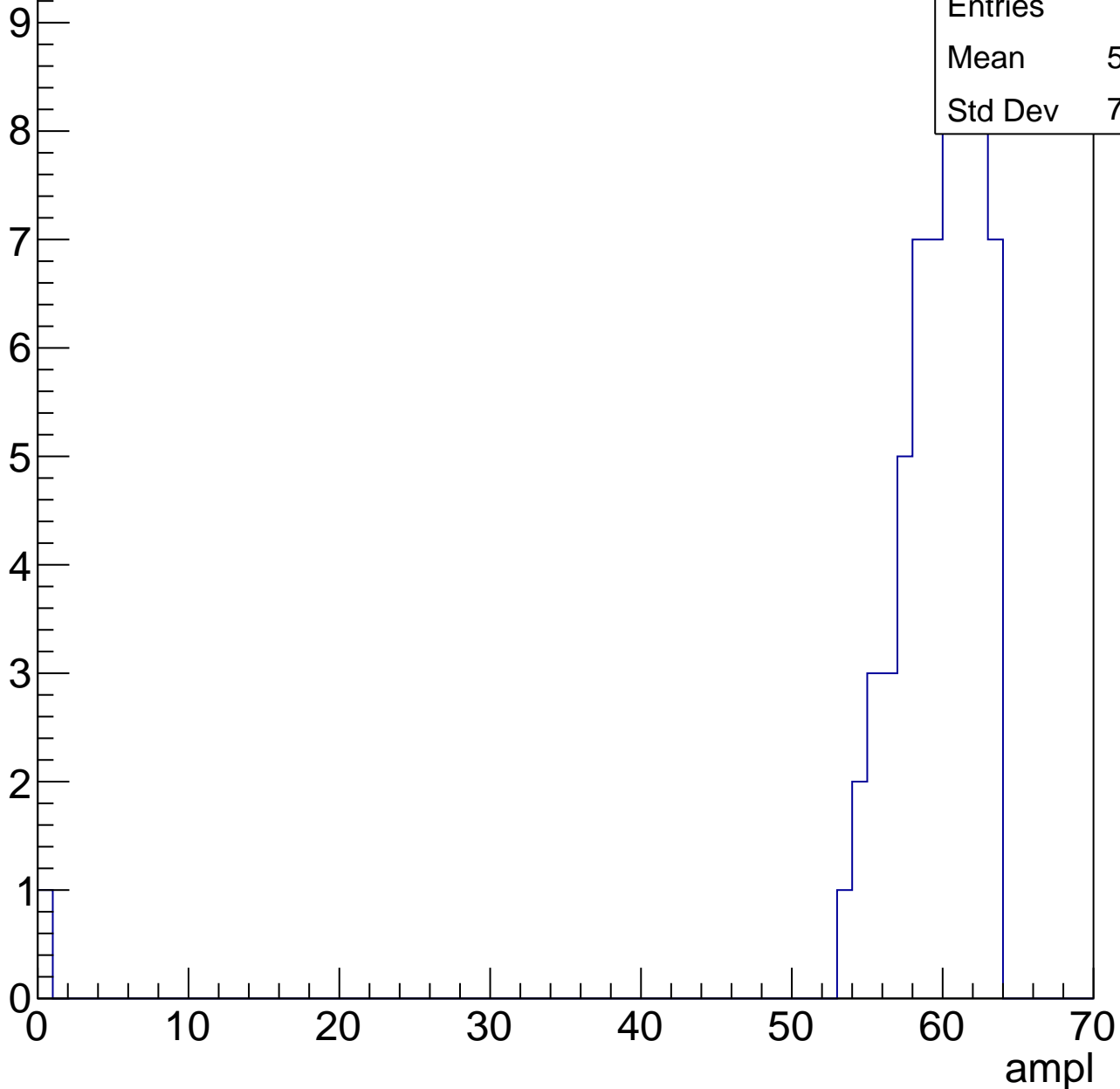


# B1L000S, U23-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	58.47
Std Dev	7.916



# B1L000S, U23-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.29
Std Dev	1.385

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch119, adc0

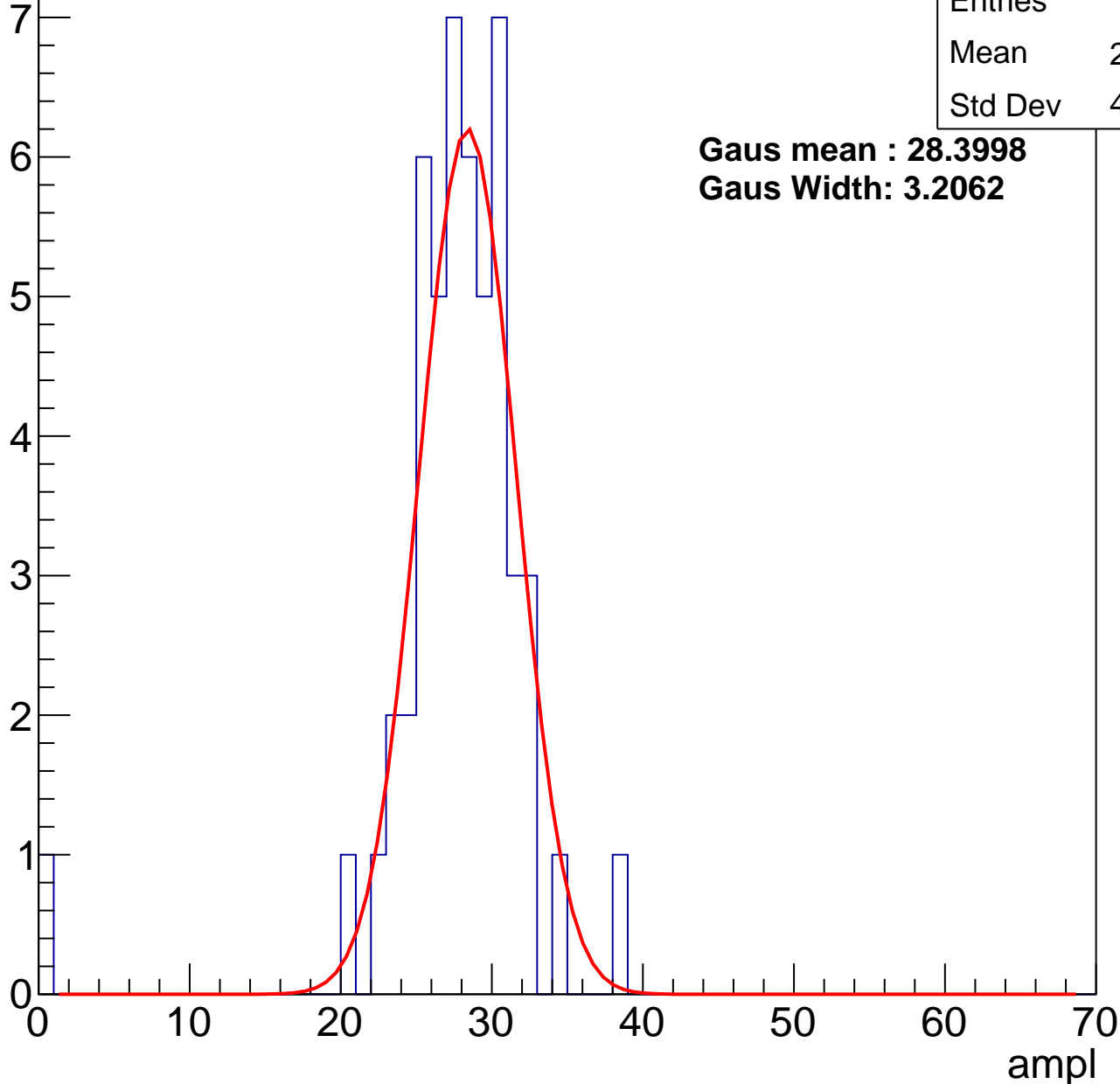
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	27.24
Std Dev	4.973

**Gaus mean : 28.3998**

**Gaus Width: 3.2062**



# B1L000S, U23-ch119, adc1

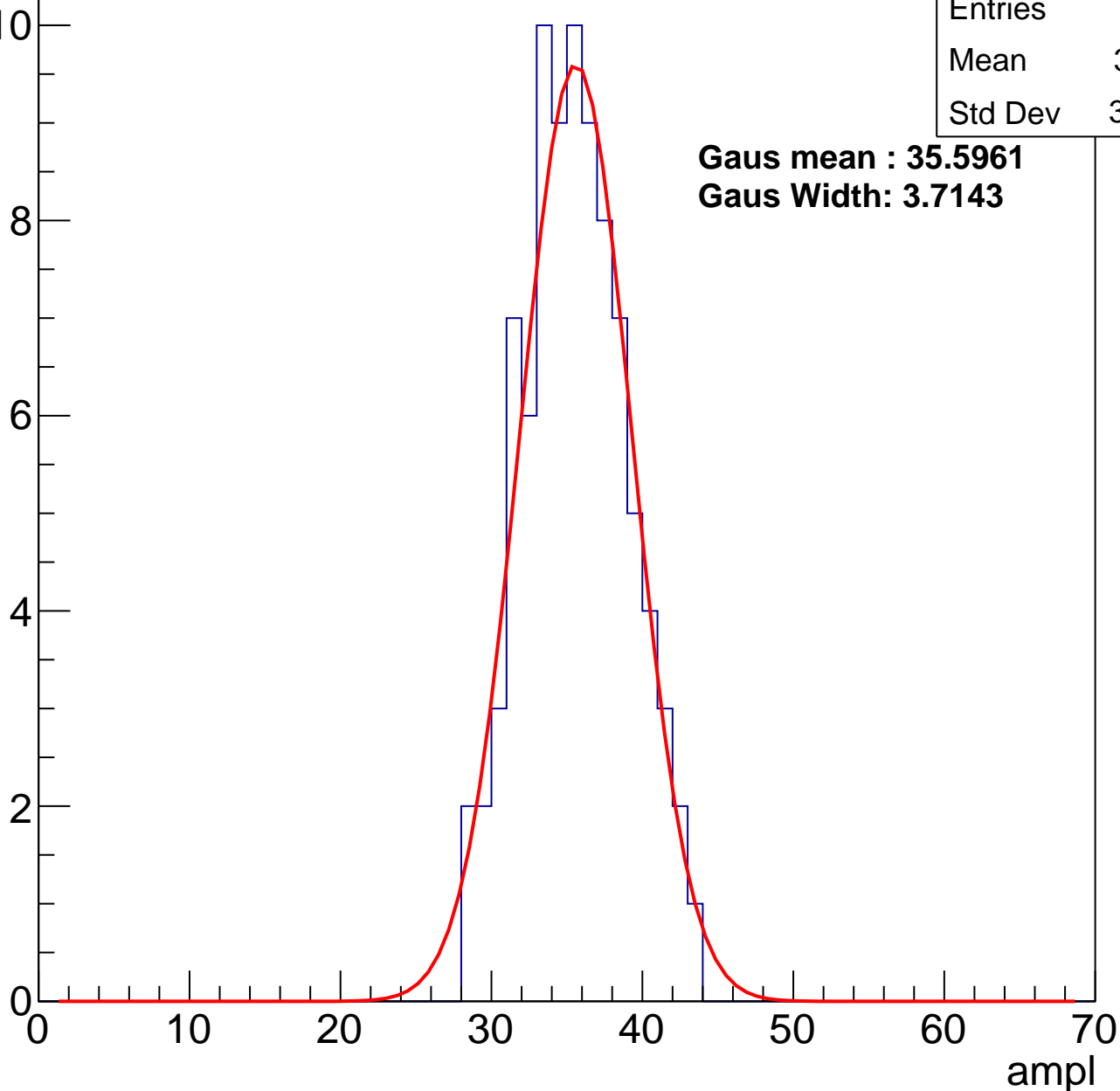
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	88
Mean	35.11
Std Dev	3.393

**Gaus mean : 35.5961**

**Gaus Width: 3.7143**



# B1L000S, U23-ch119, adc2

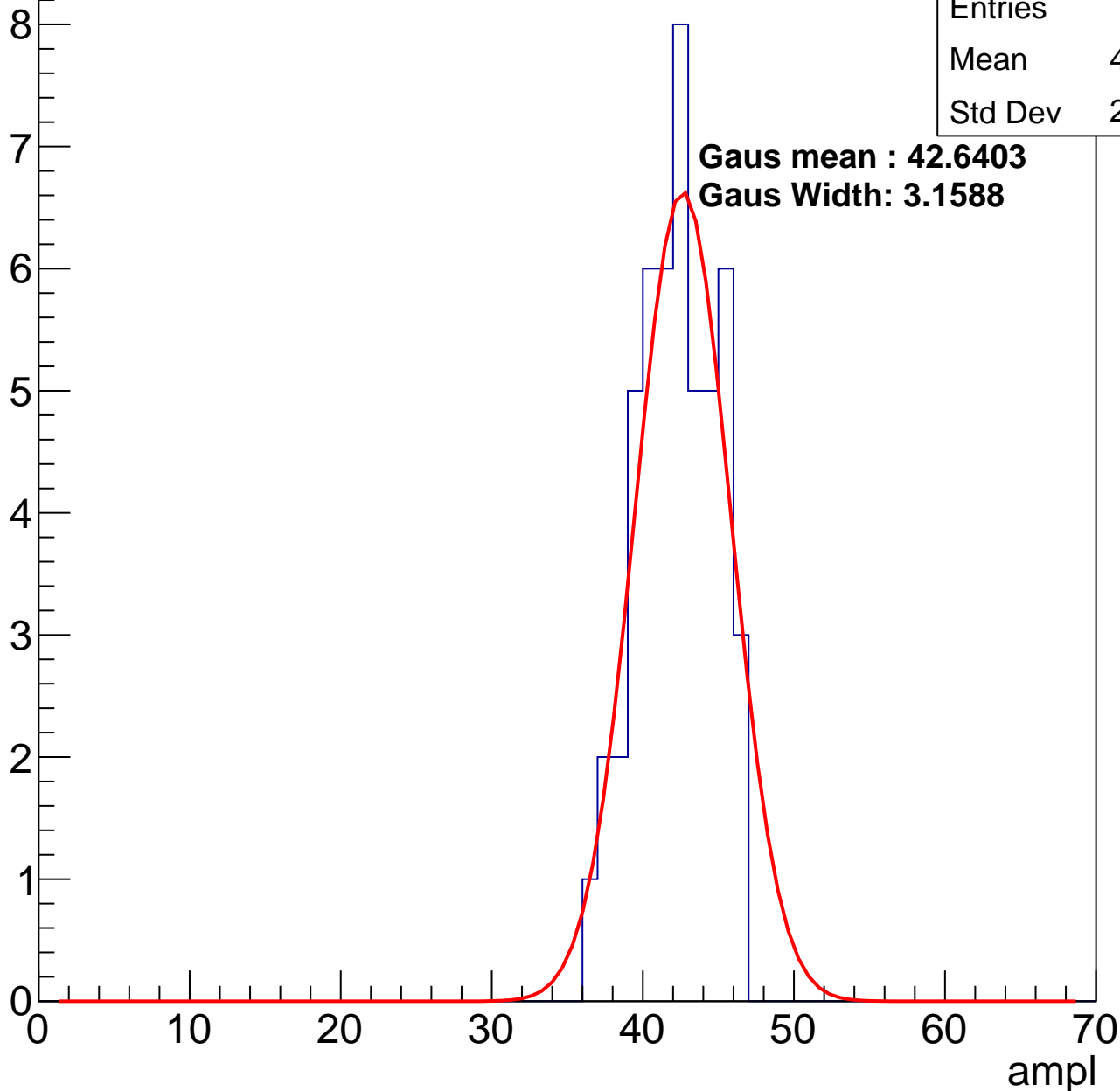
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	41.76
Std Dev	2.544

**Gaus mean : 42.6403**

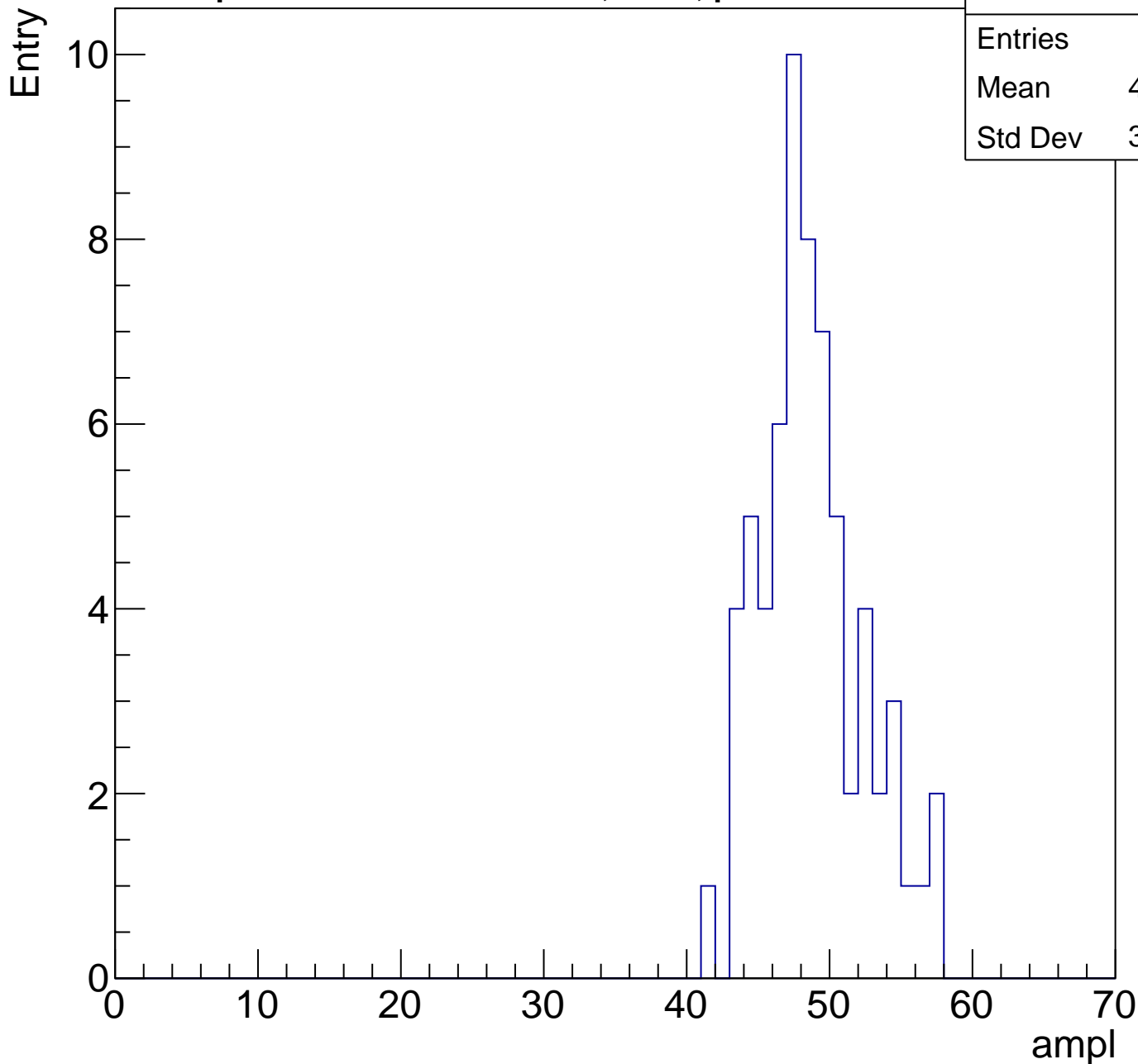
**Gaus Width: 3.1588**



# B1L000S, U23-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	48.29
Std Dev	3.585

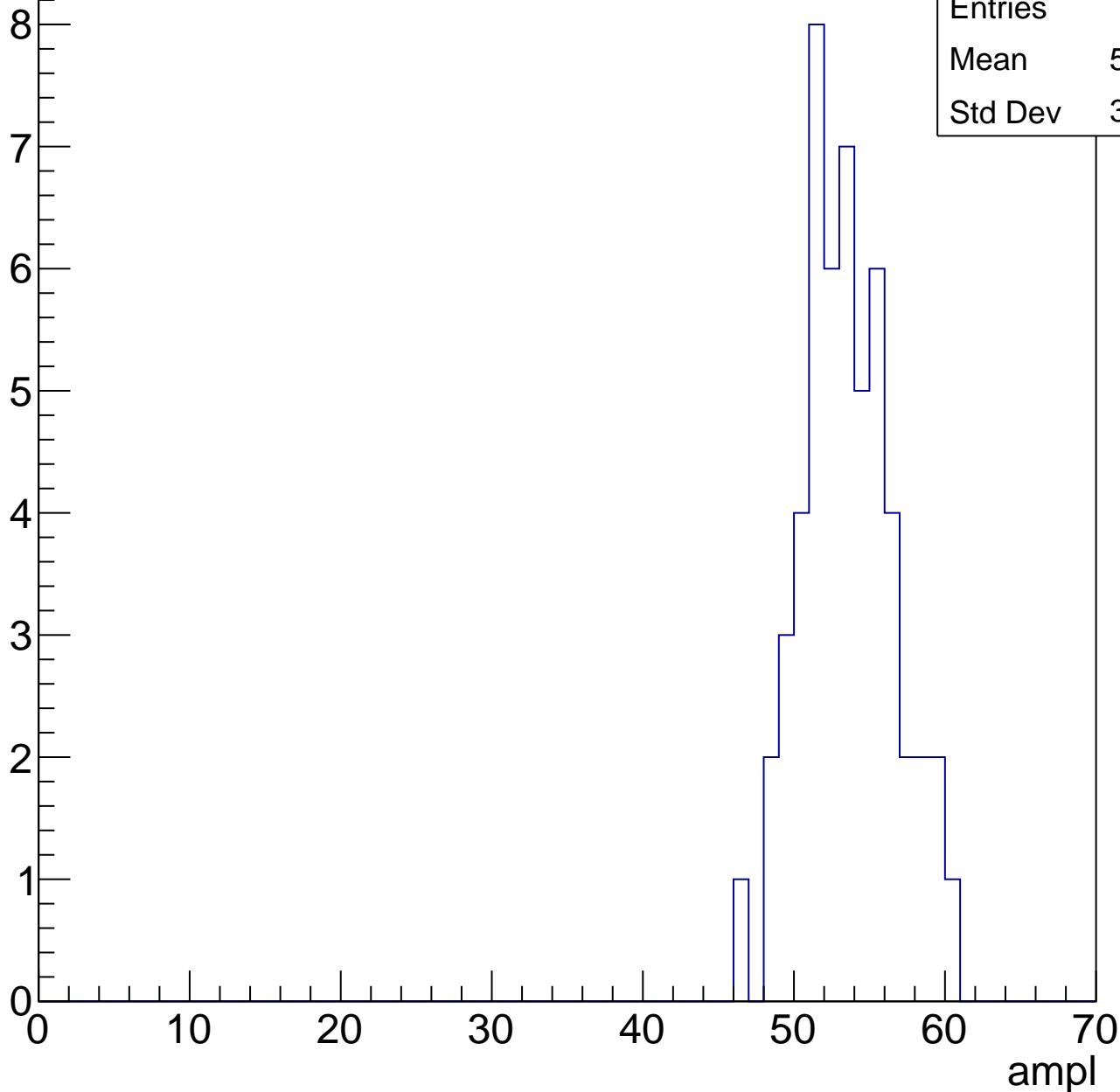


# B1L000S, U23-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

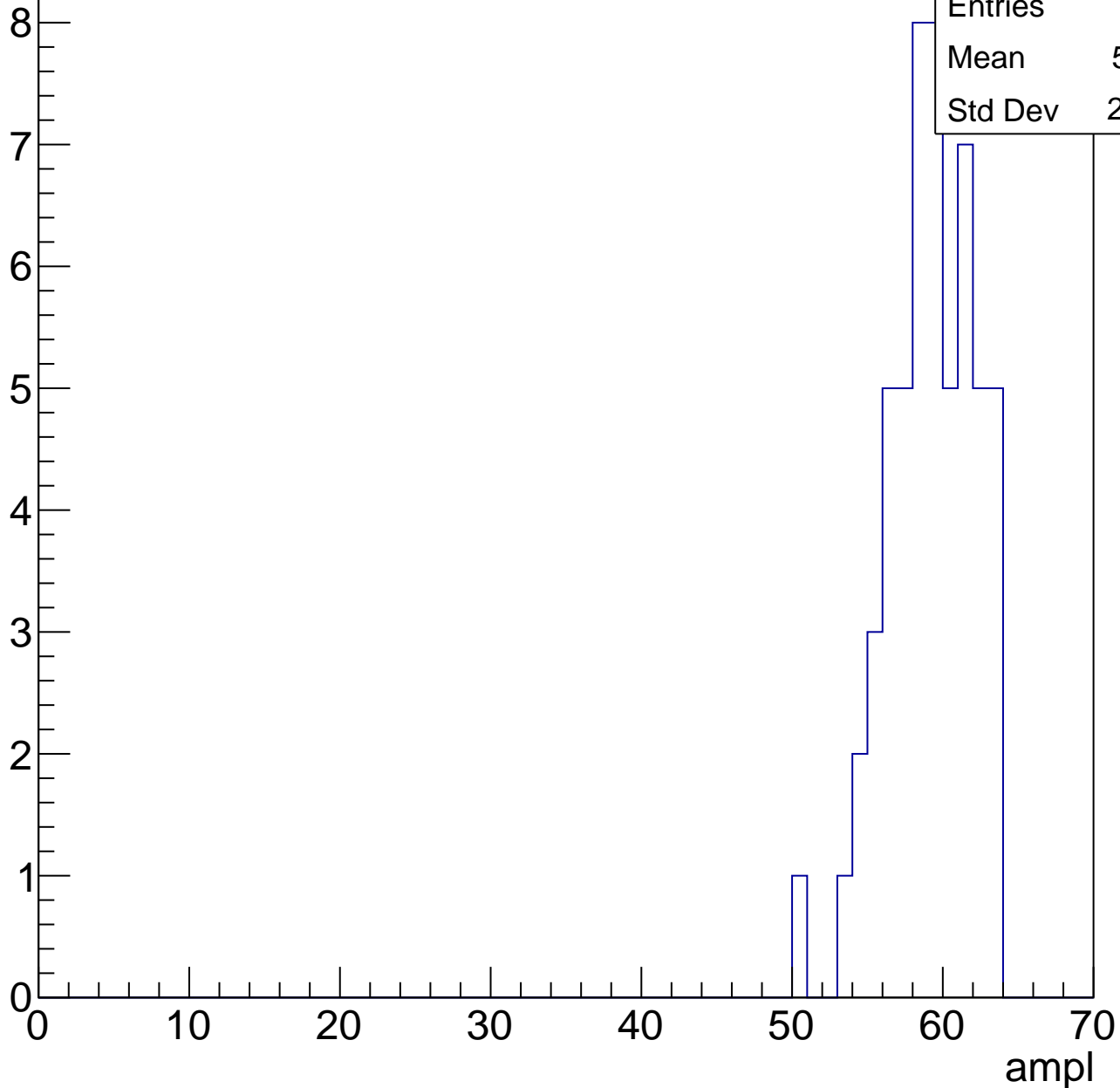
Entries	53
Mean	53.06
Std Dev	3.037



# B1L000S, U23-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

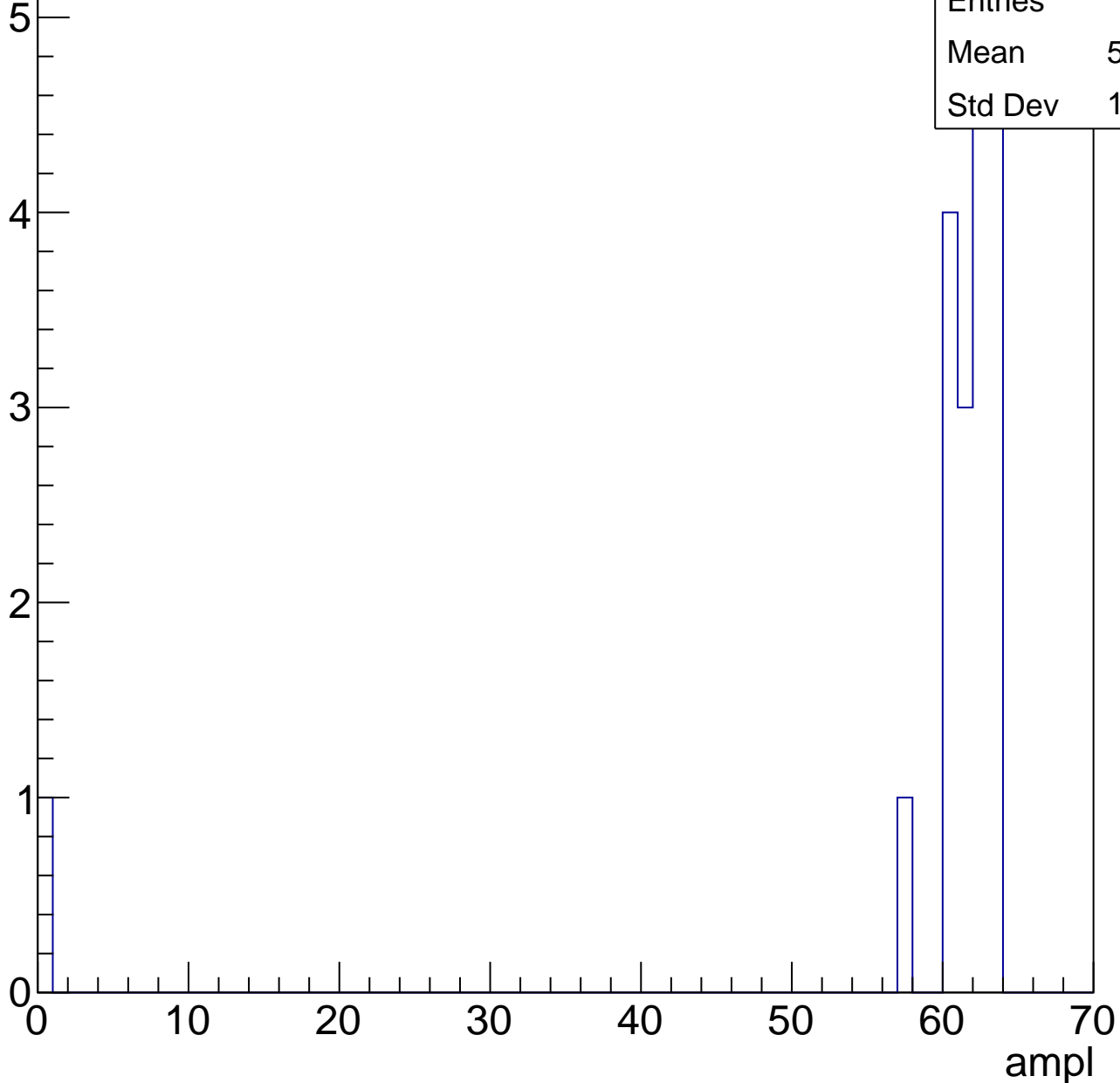


# B1L000S, U23-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.16
Std Dev	13.79





# B1L000S, U23-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch120, adc0

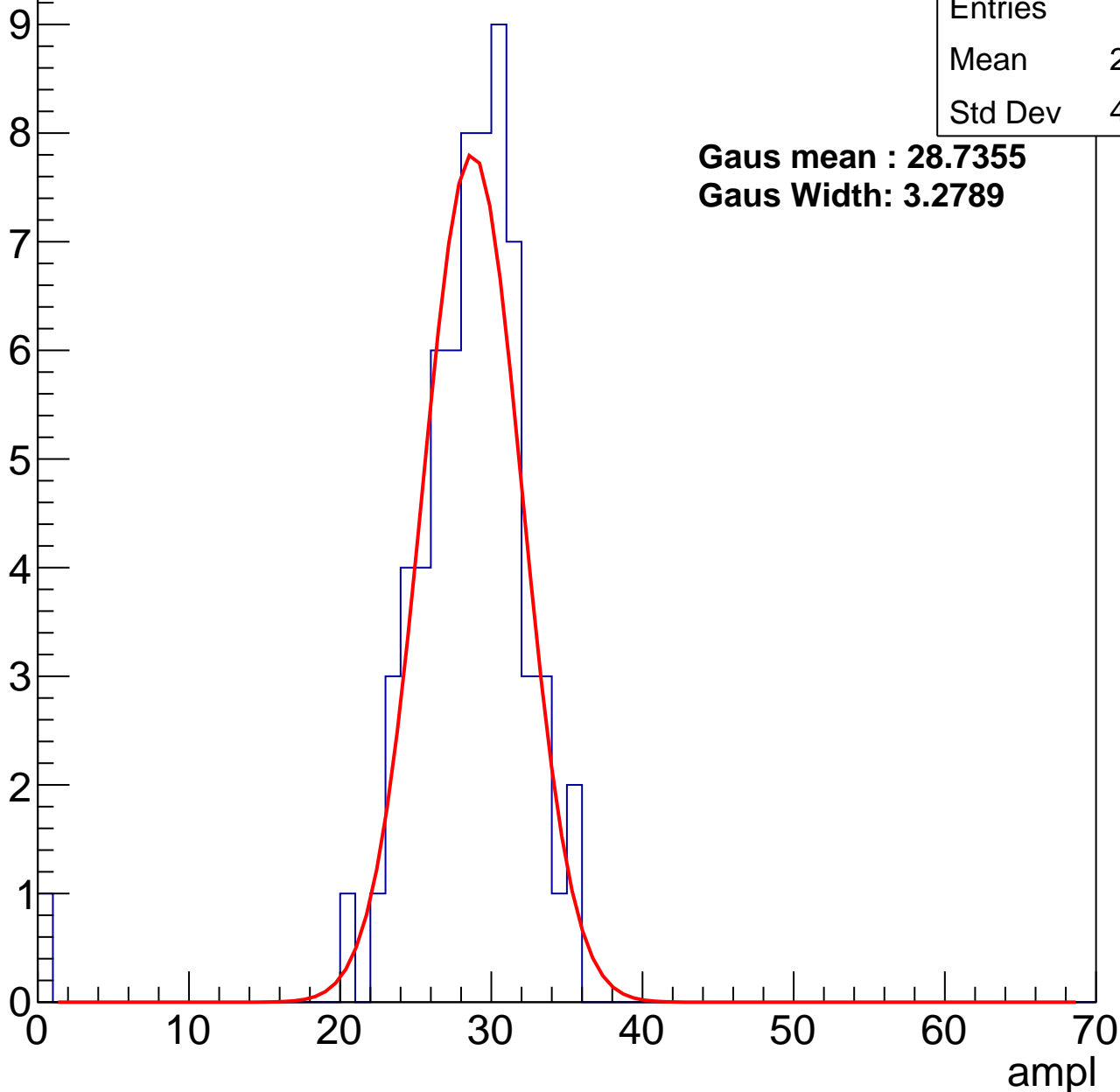
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	27.87
Std Dev	4.655

**Gaus mean : 28.7355**

**Gaus Width: 3.2789**



# B1L000S, U23-ch120, adc1

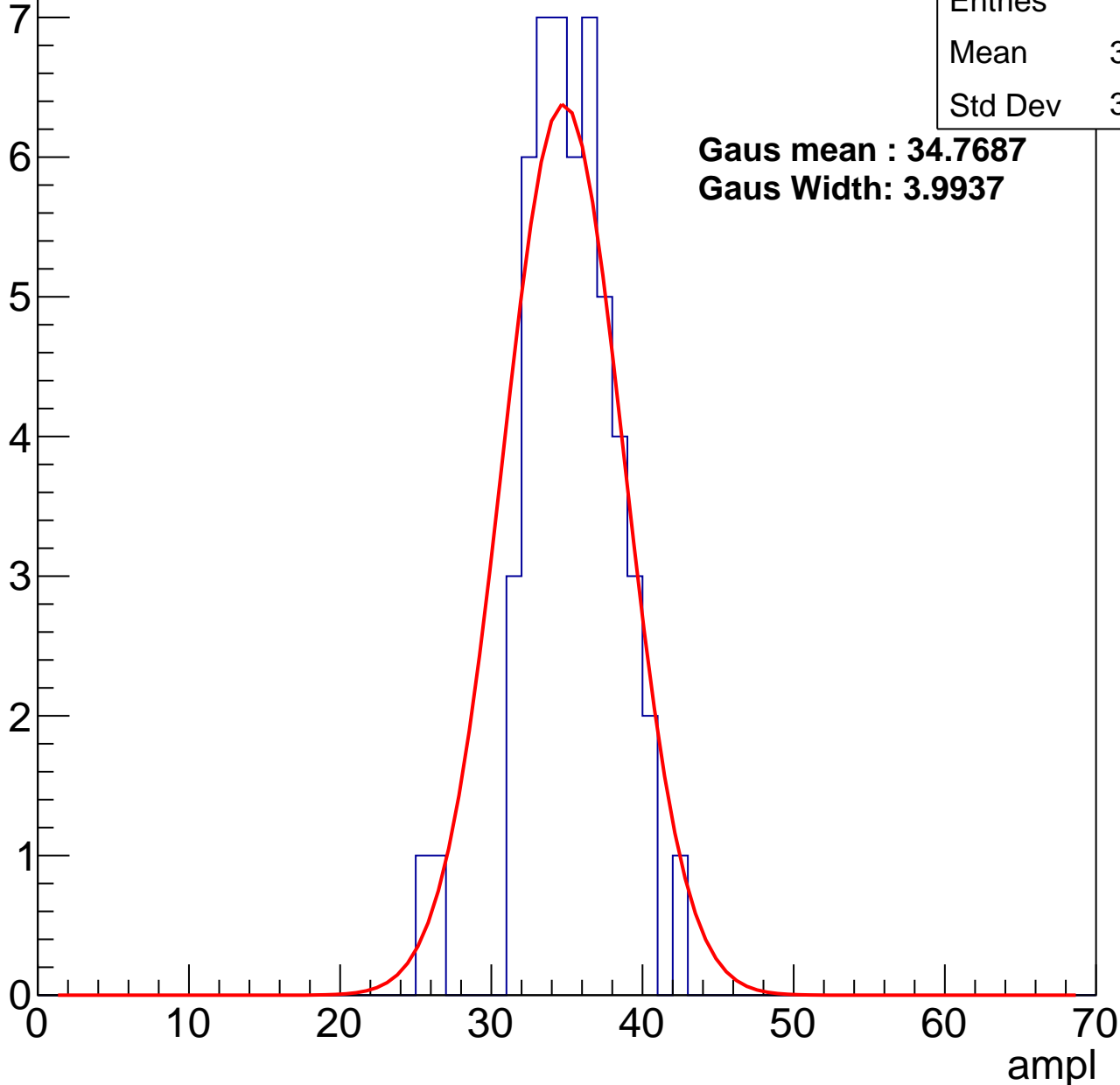
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.77
Std Dev	3.148

**Gaus mean : 34.7687**

**Gaus Width: 3.9937**



# B1L000S, U23-ch120, adc2

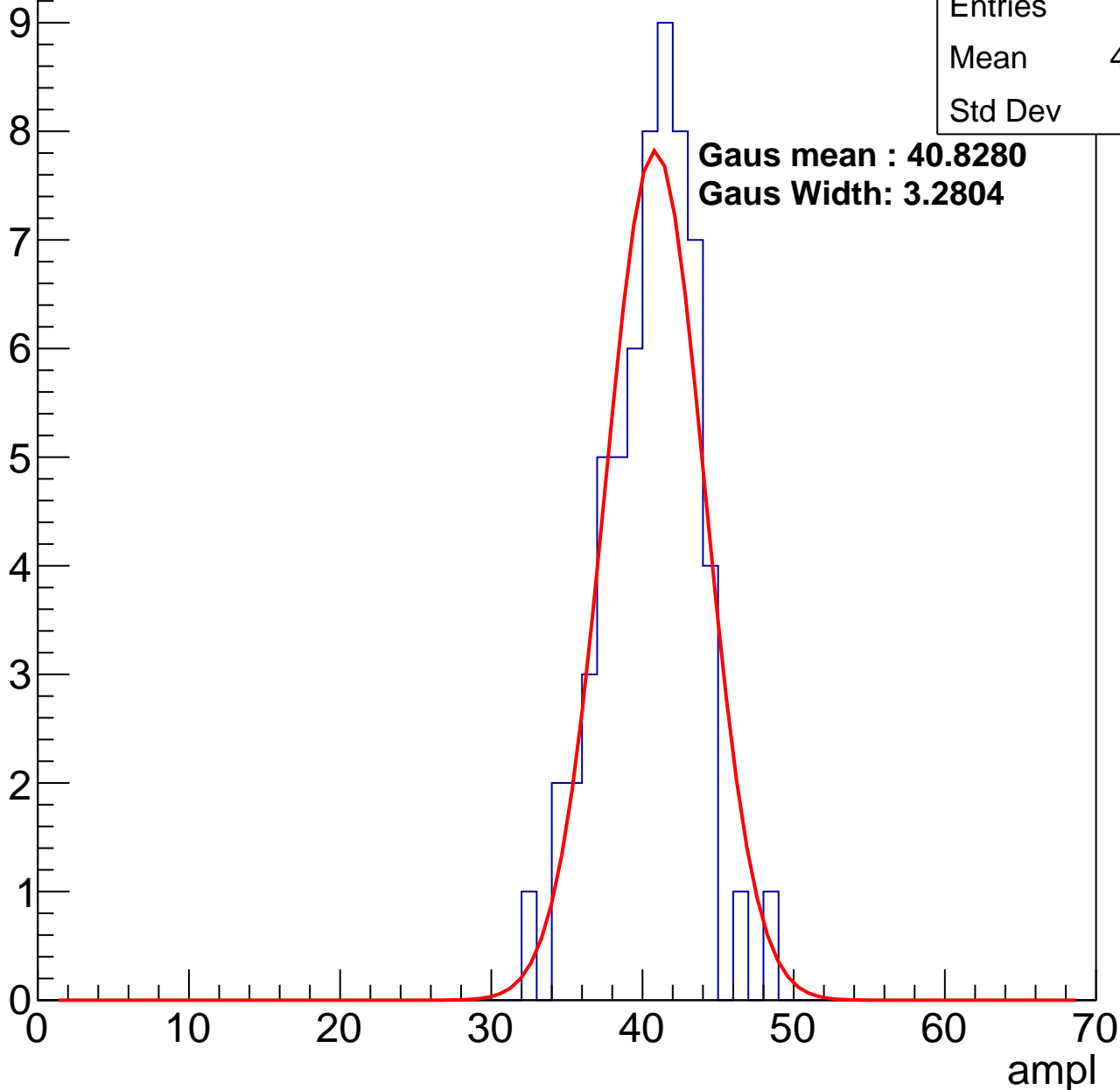
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.05
Std Dev	3.04

**Gaus mean : 40.8280**

**Gaus Width: 3.2804**

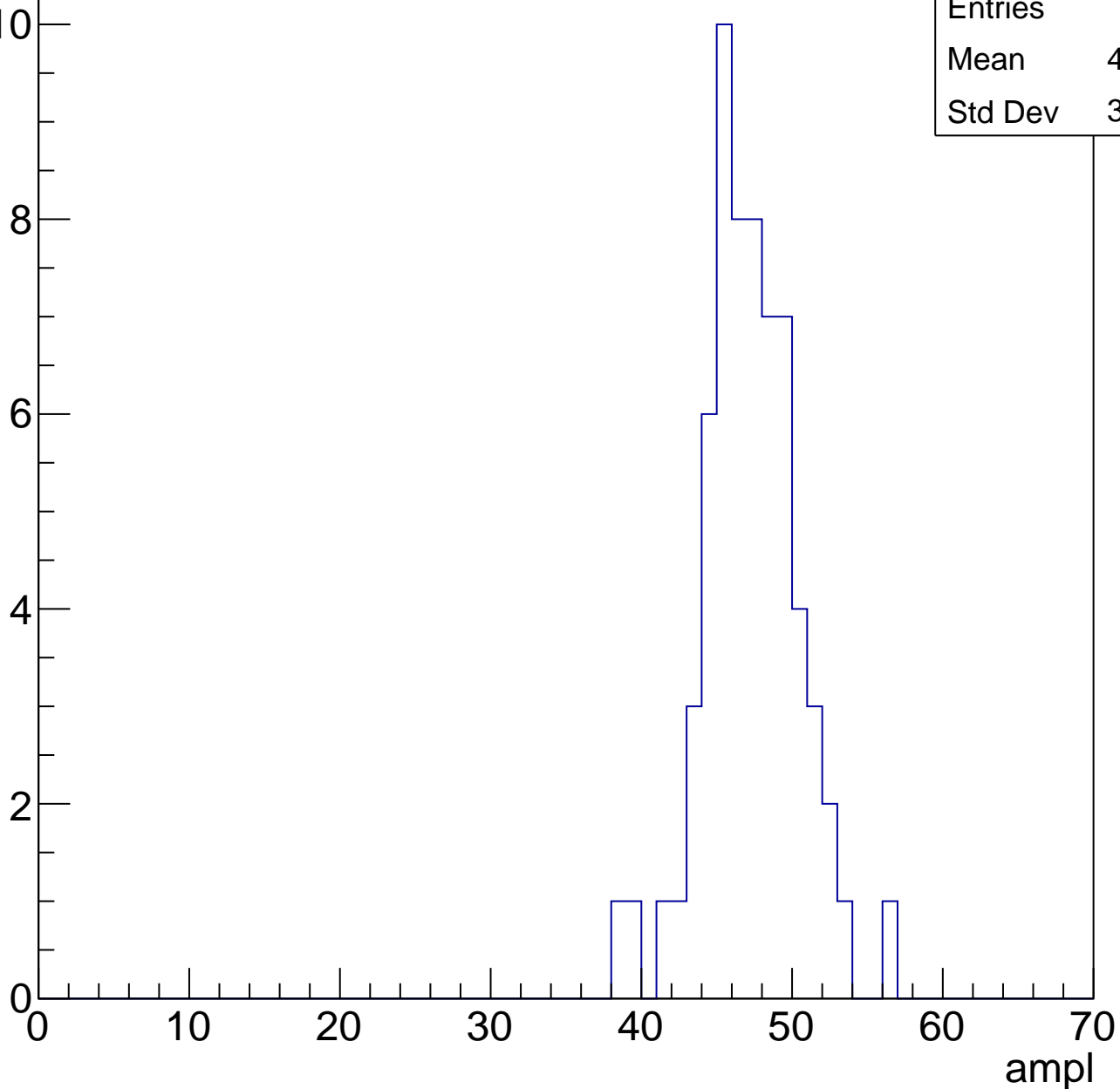


# B1L000S, U23-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.75
Std Dev	3.157

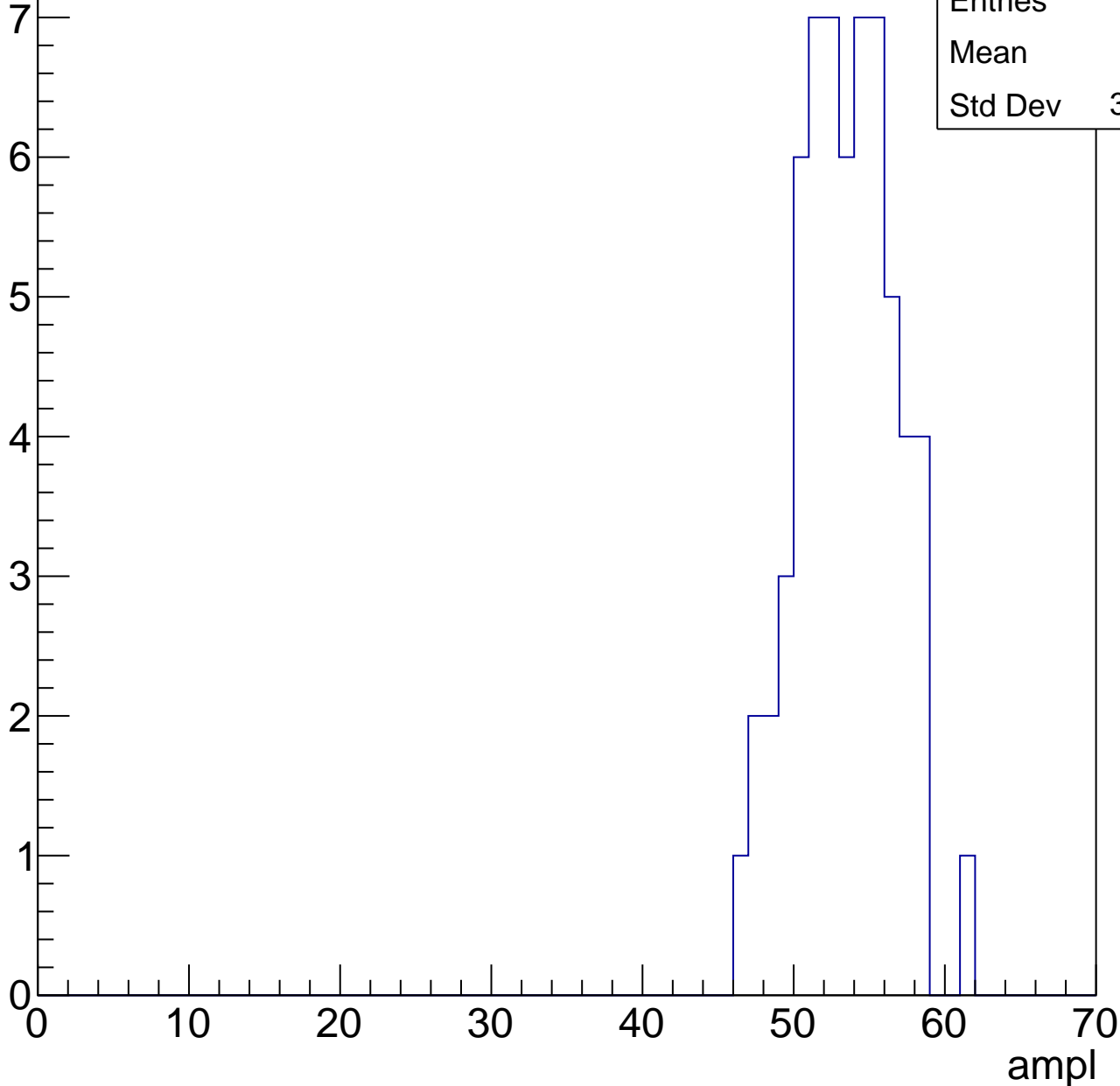


# B1L000S, U23-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	53
Std Dev	3.152

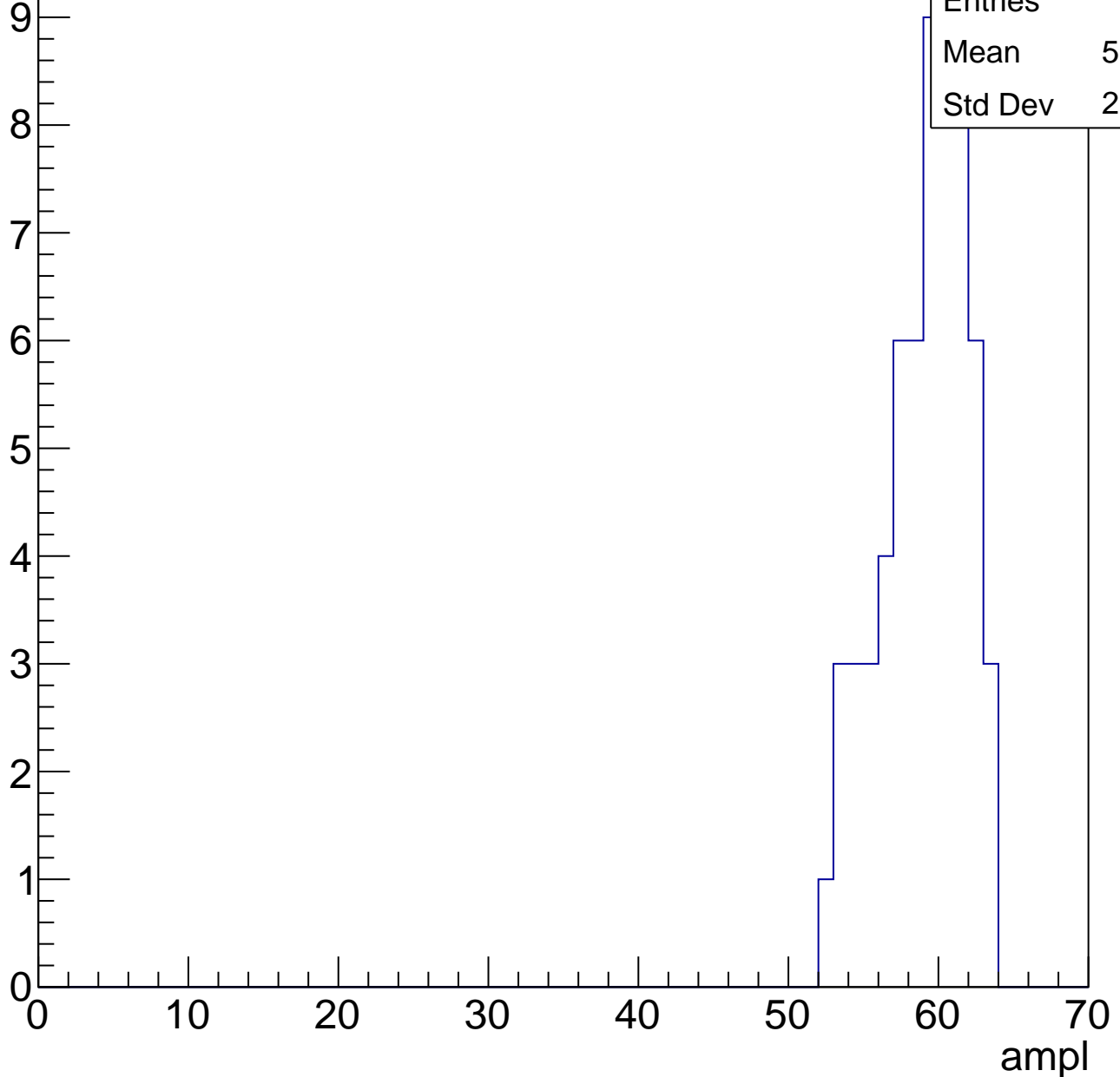


# B1L000S, U23-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.53
Std Dev	2.813

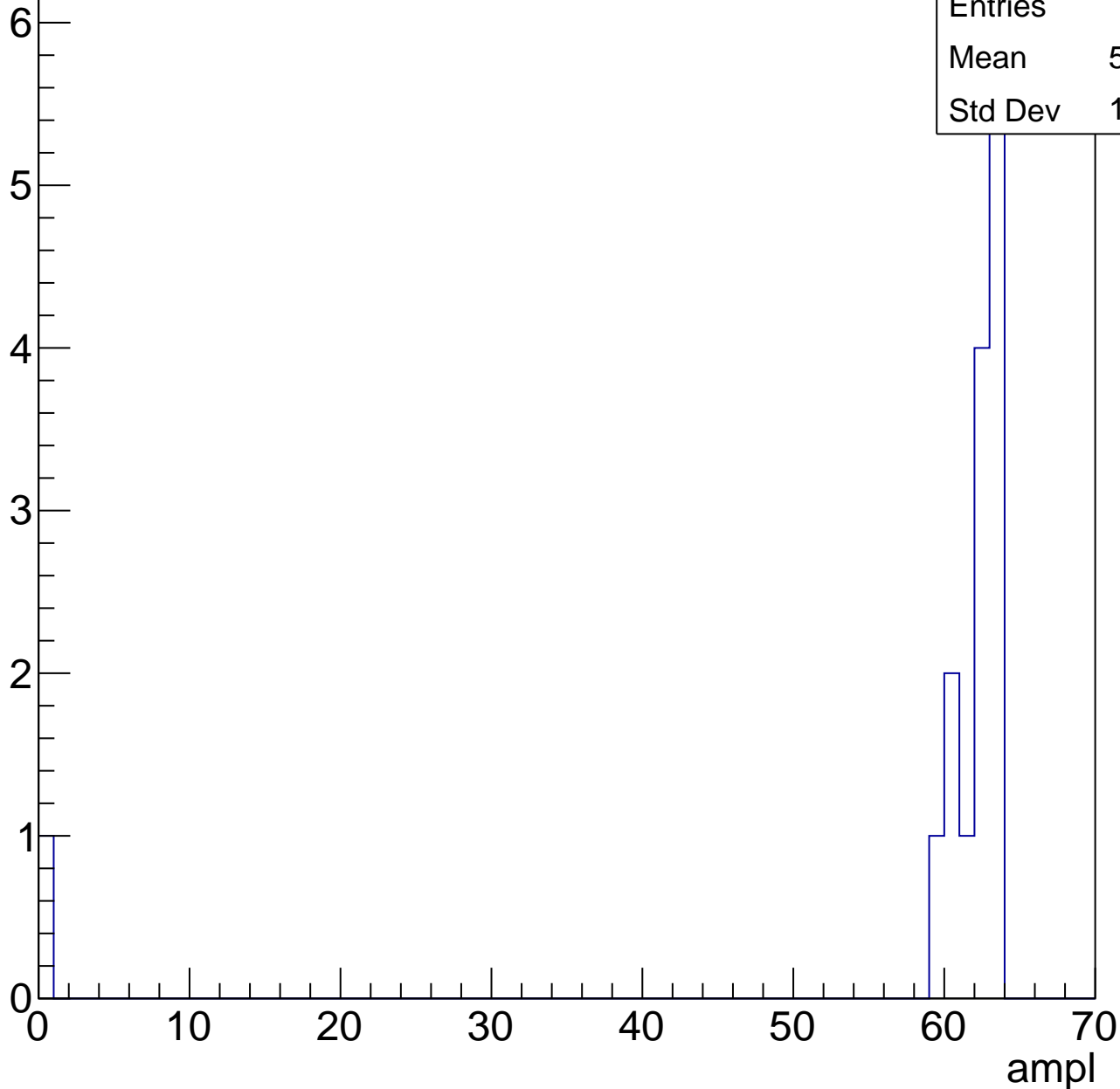


# B1L000S, U23-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.73
Std Dev	15.48





# B1L000S, U23-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U23-ch121, adc0

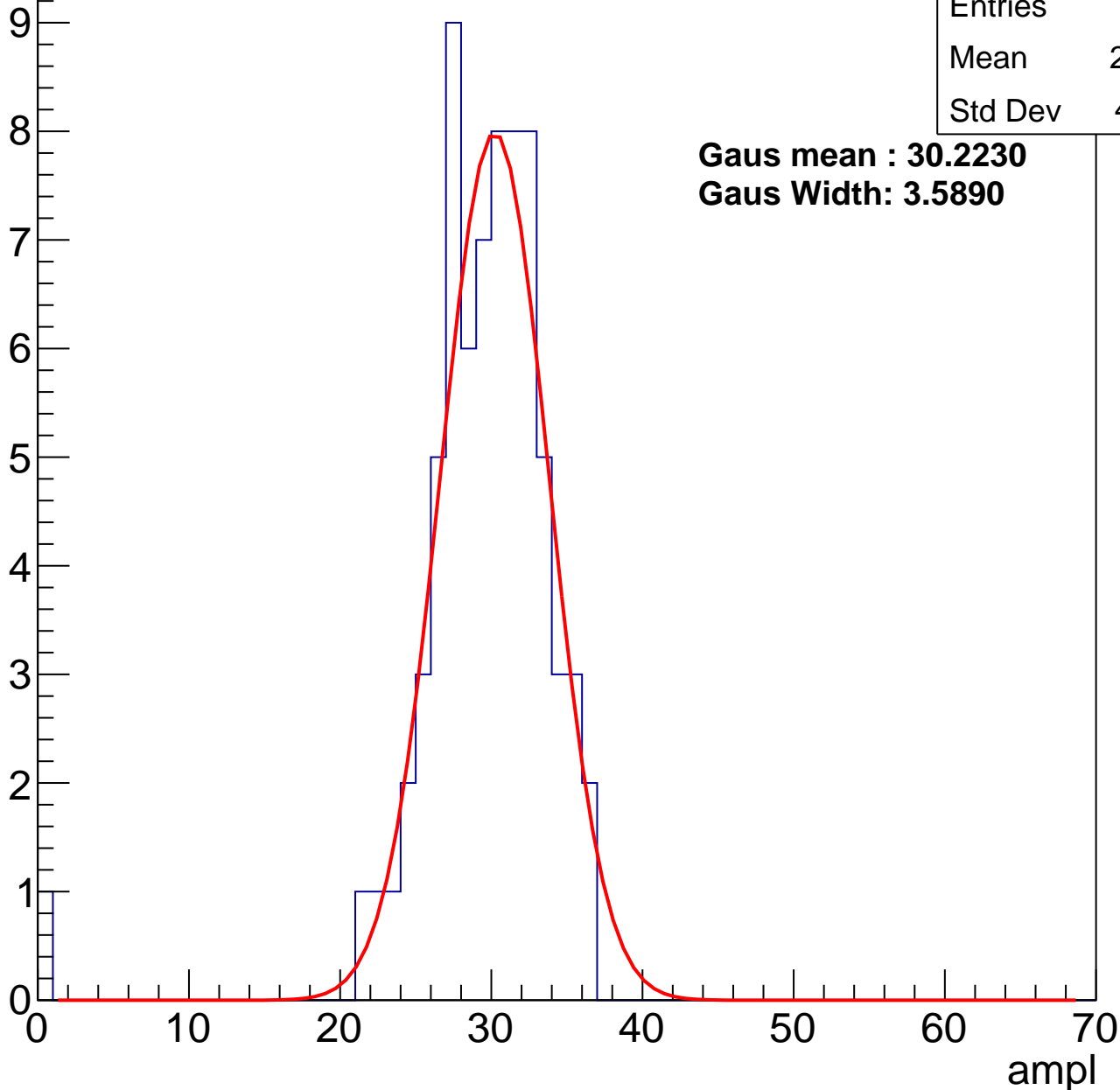
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	29.05
Std Dev	4.751

**Gaus mean : 30.2230**

**Gaus Width: 3.5890**



# B1L000S, U23-ch121, adc1

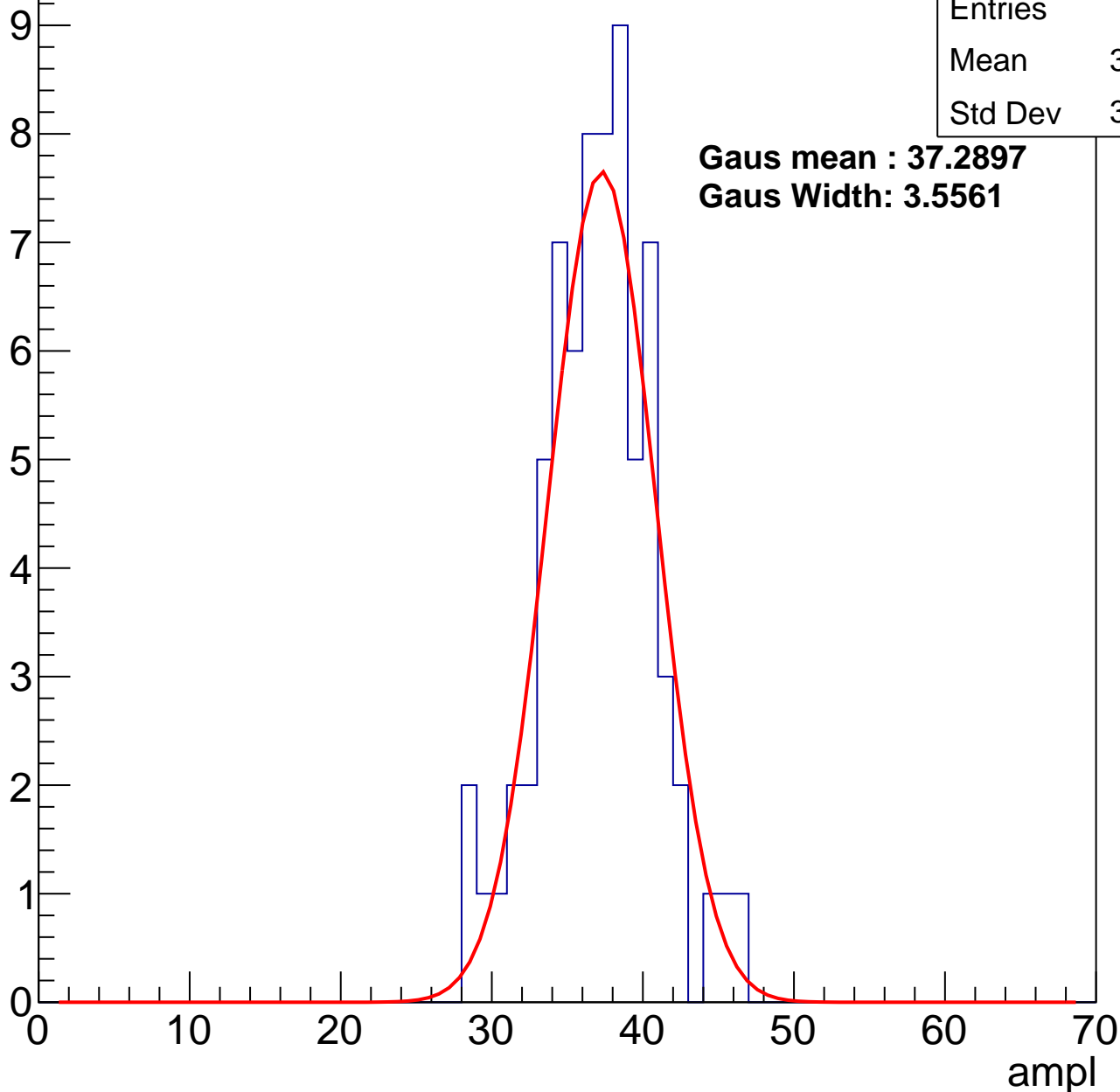
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	36.58
Std Dev	3.645

**Gaus mean : 37.2897**

**Gaus Width: 3.5561**



# B1L000S, U23-ch121, adc2

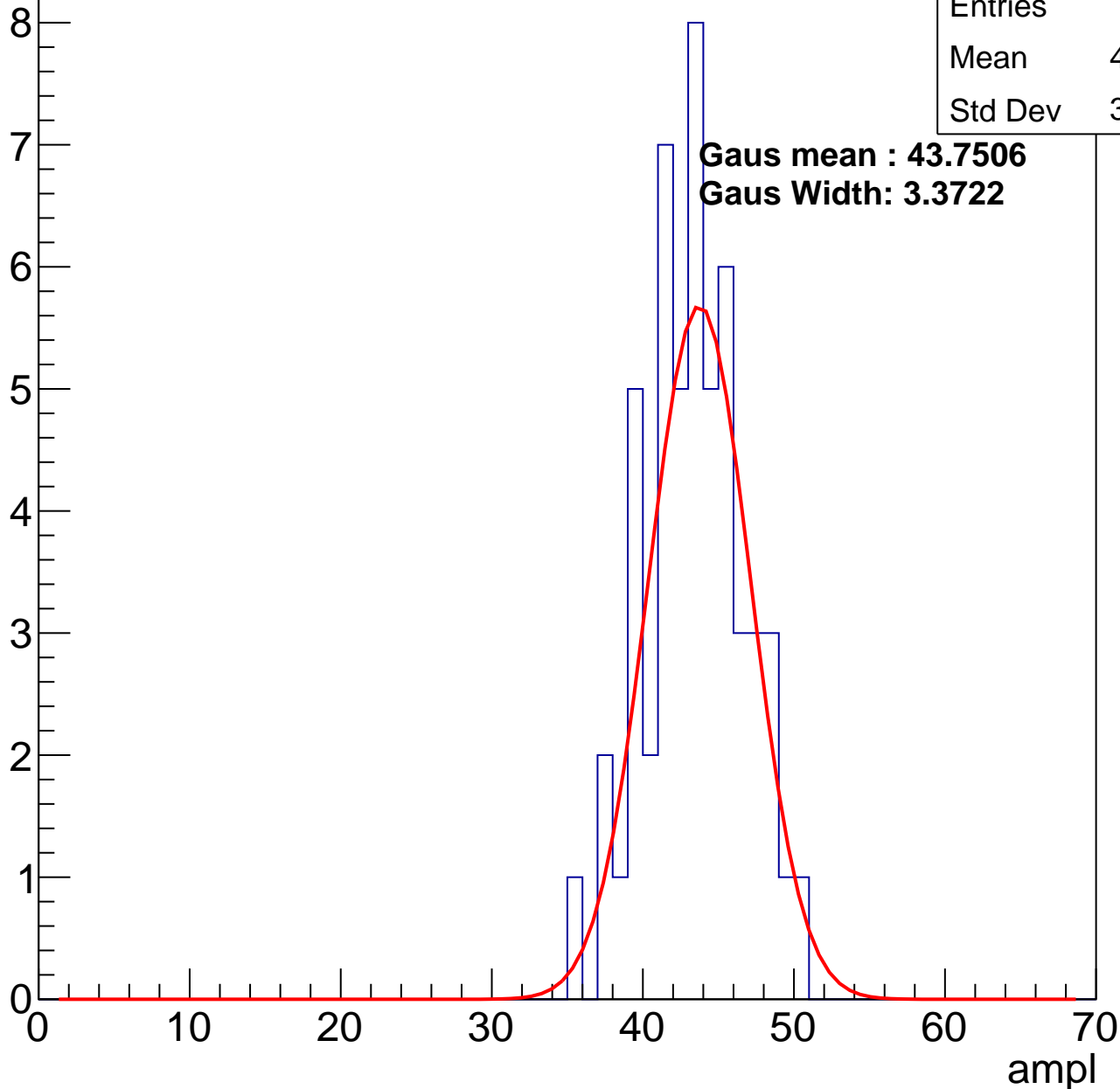
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	42.92
Std Dev	3.238

**Gaus mean : 43.7506**

**Gaus Width: 3.3722**

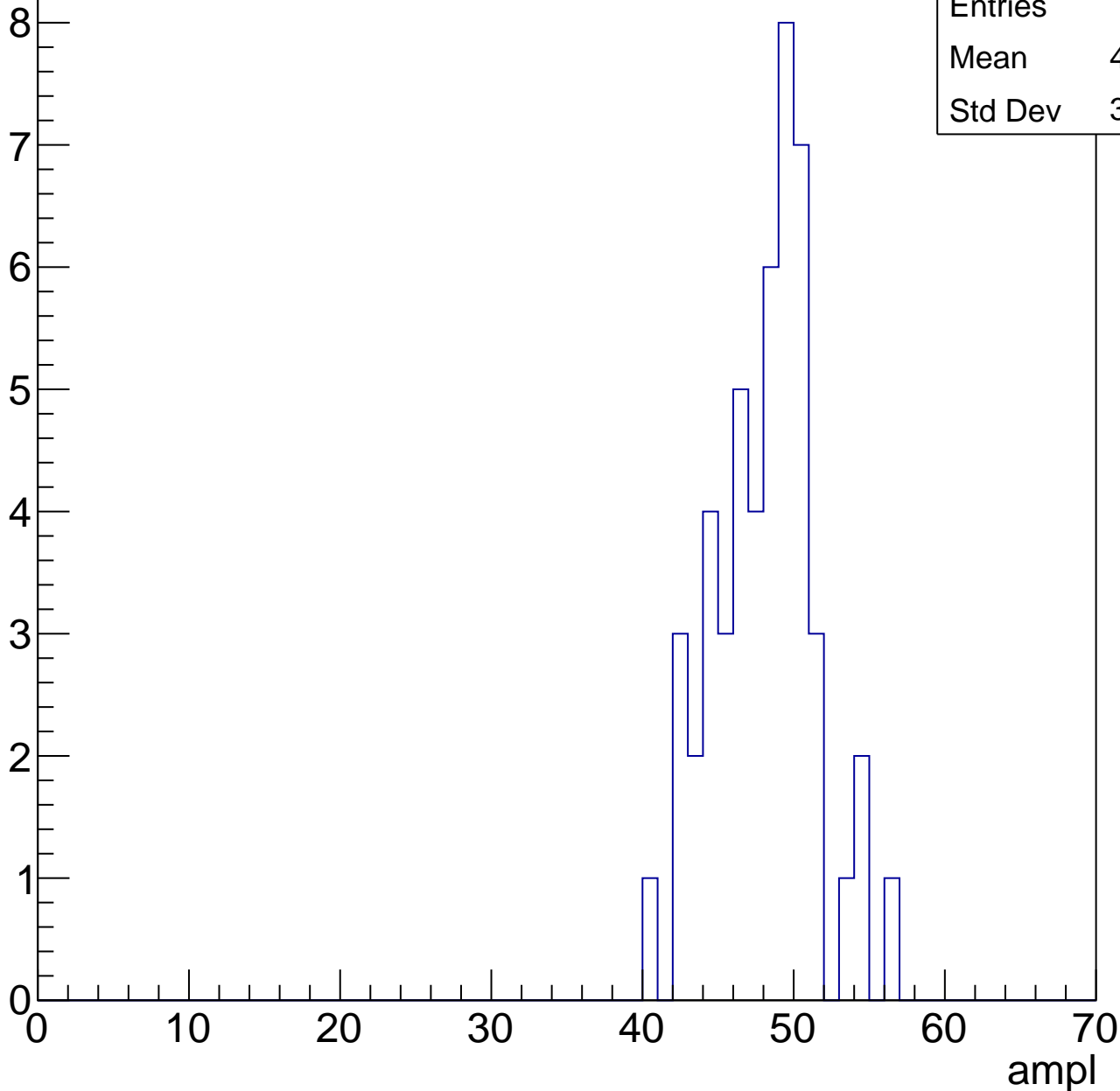


# B1L000S, U23-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

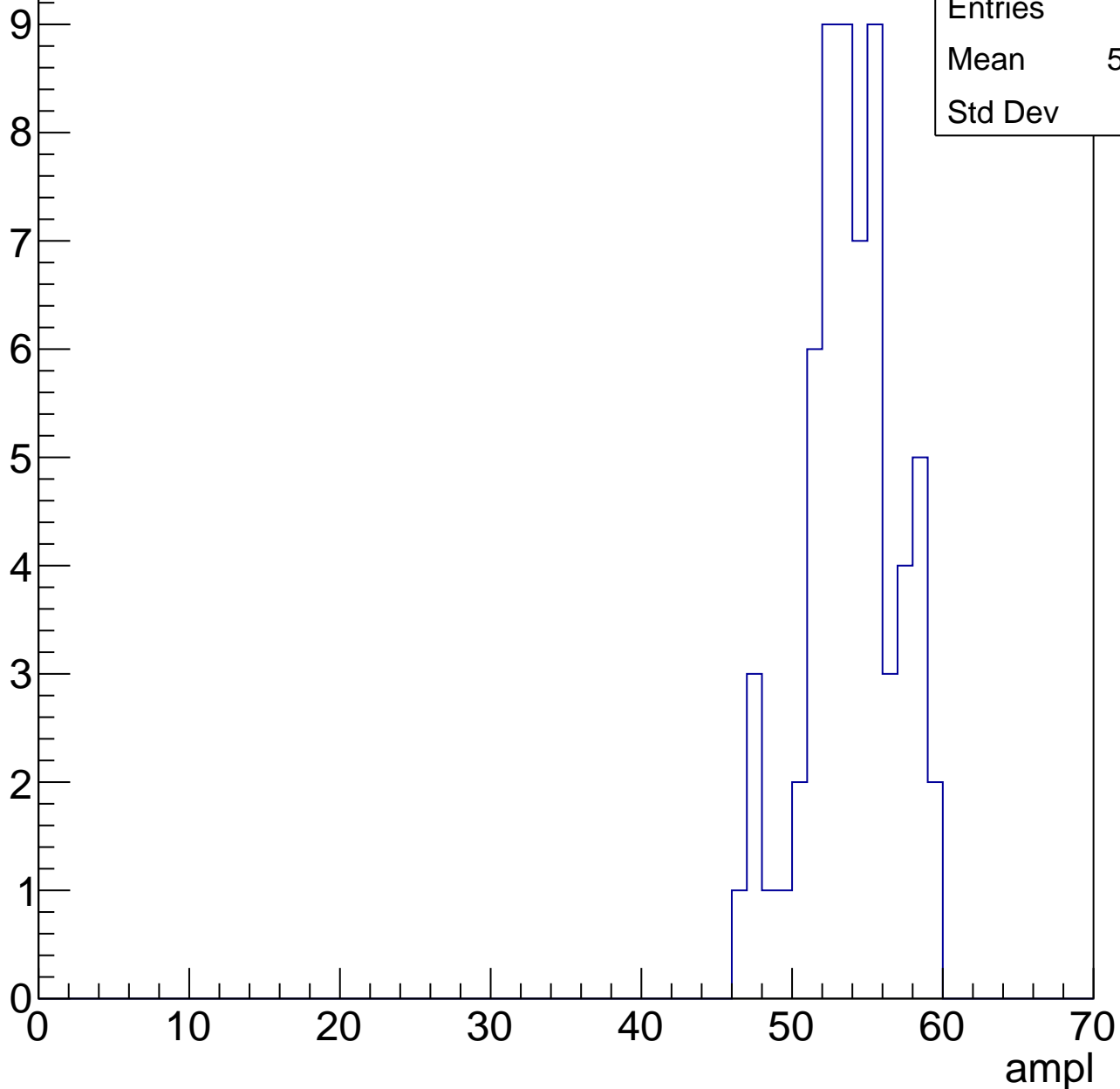
Entries	50
Mean	47.62
Std Dev	3.328



# B1L000S, U23-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	62
Mean	53.42
Std Dev	3.03

# B1L000S, U23-ch121, adc5

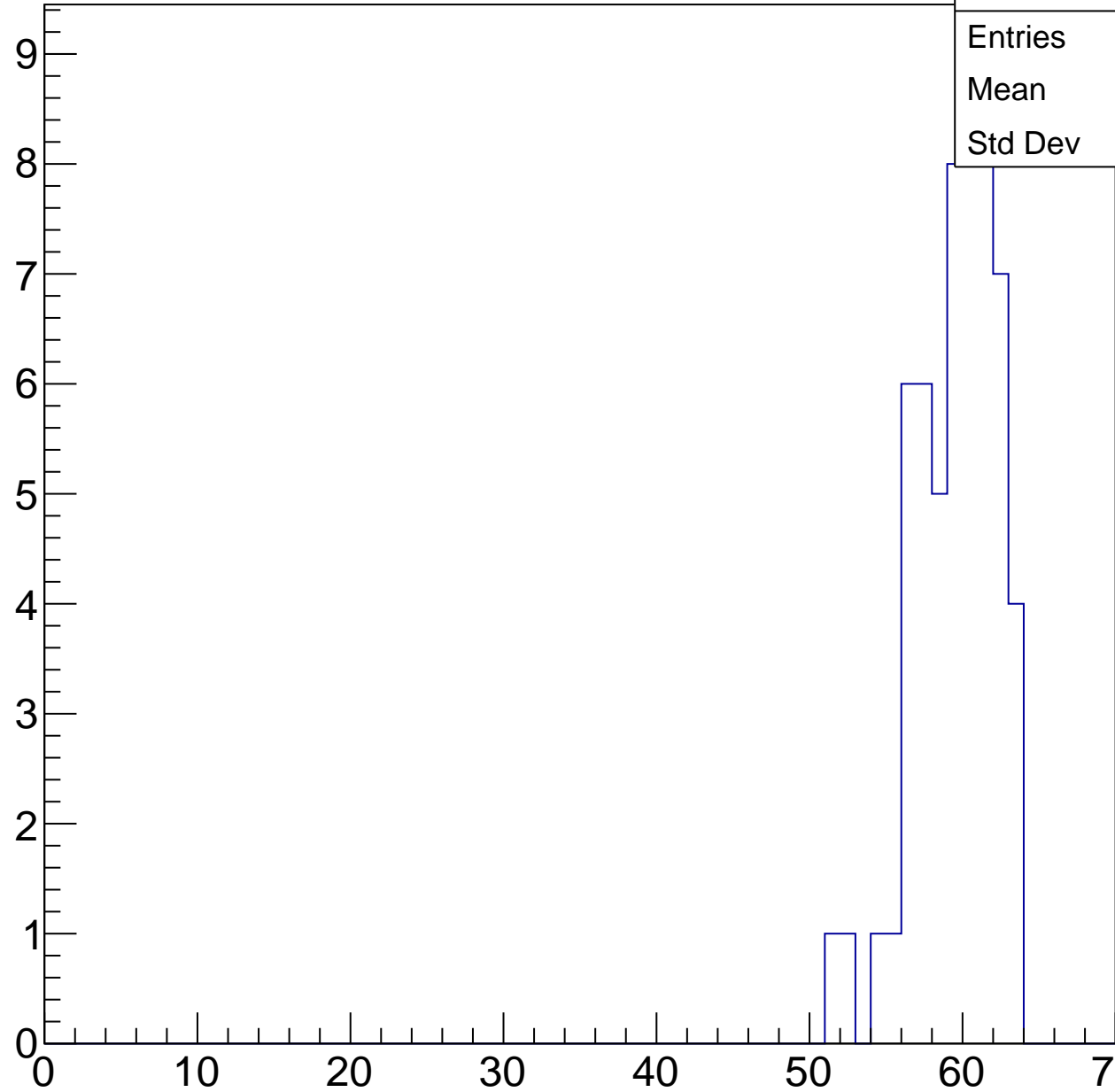
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	59.07
Std Dev	2.675

ampl

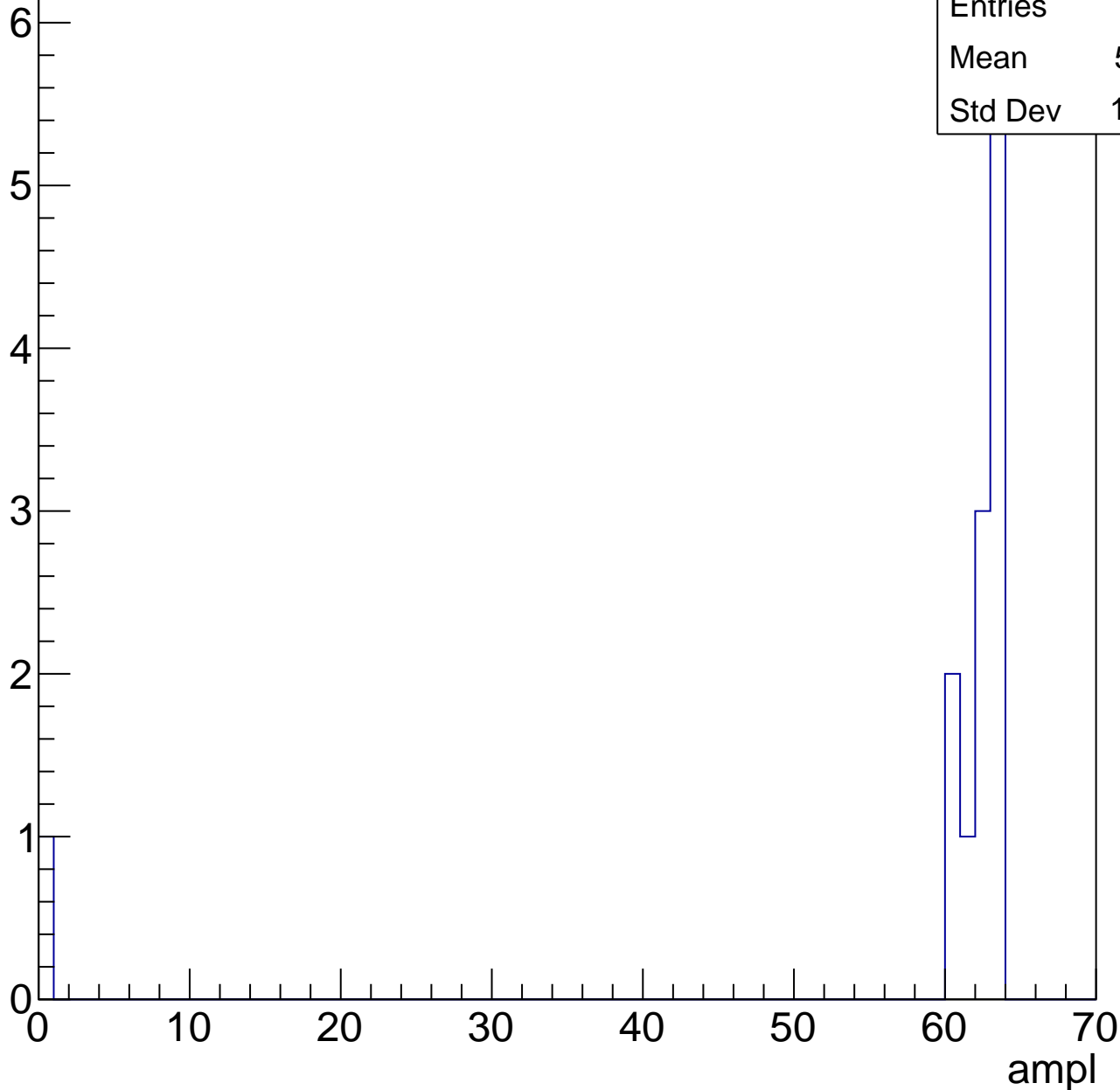


# B1L000S, U23-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.31
Std Dev	16.58





# B1L000S, U23-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch122, adc0

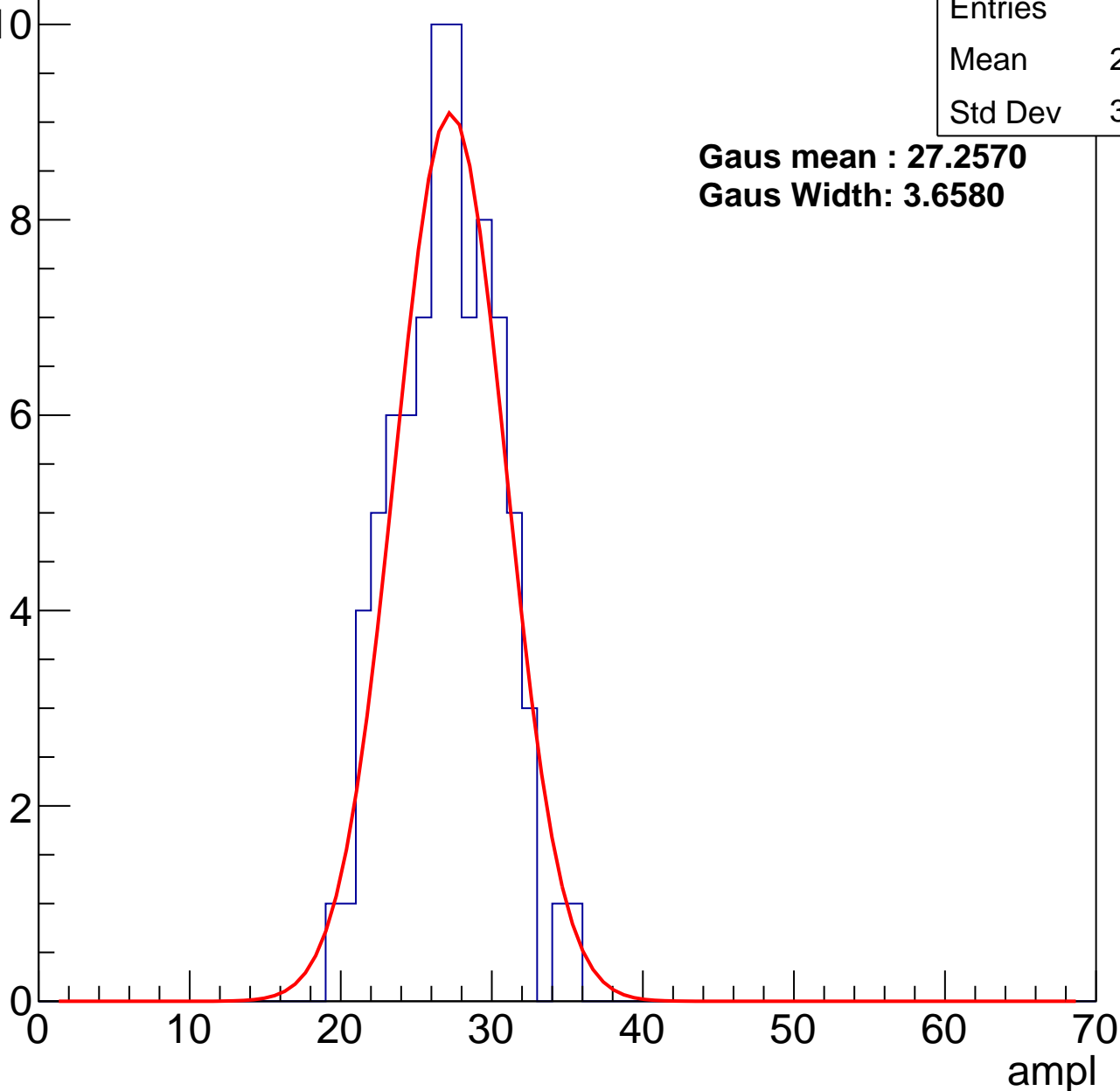
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	26.56
Std Dev	3.357

**Gaus mean : 27.2570**

**Gaus Width: 3.6580**



# B1L000S, U23-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	34.4
Std Dev	2.947

**Gaus mean : 34.8311**

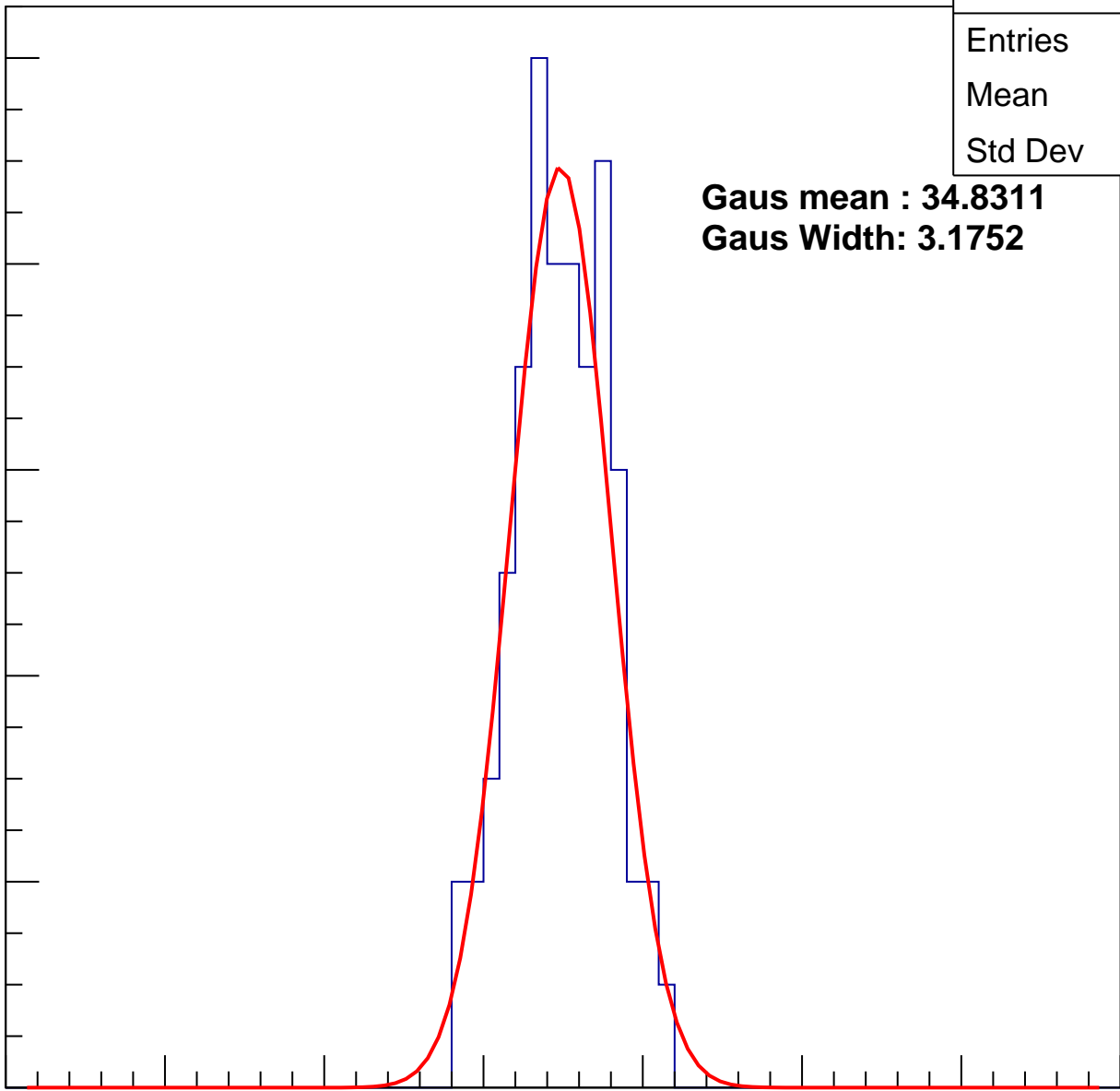
**Gaus Width: 3.1752**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch122, adc2

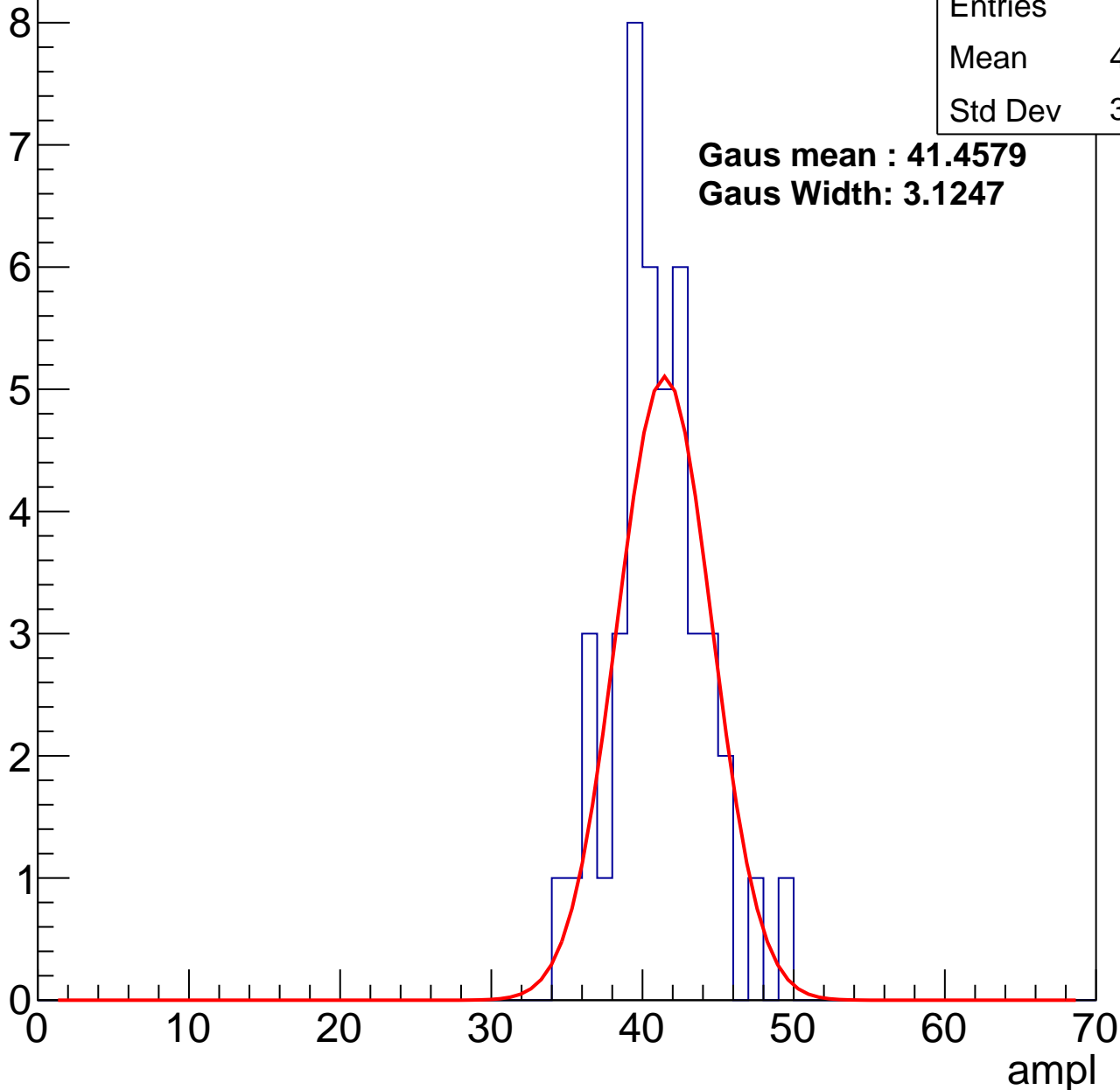
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	40.55
Std Dev	3.049

**Gaus mean : 41.4579**

**Gaus Width: 3.1247**



# B1L000S, U23-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

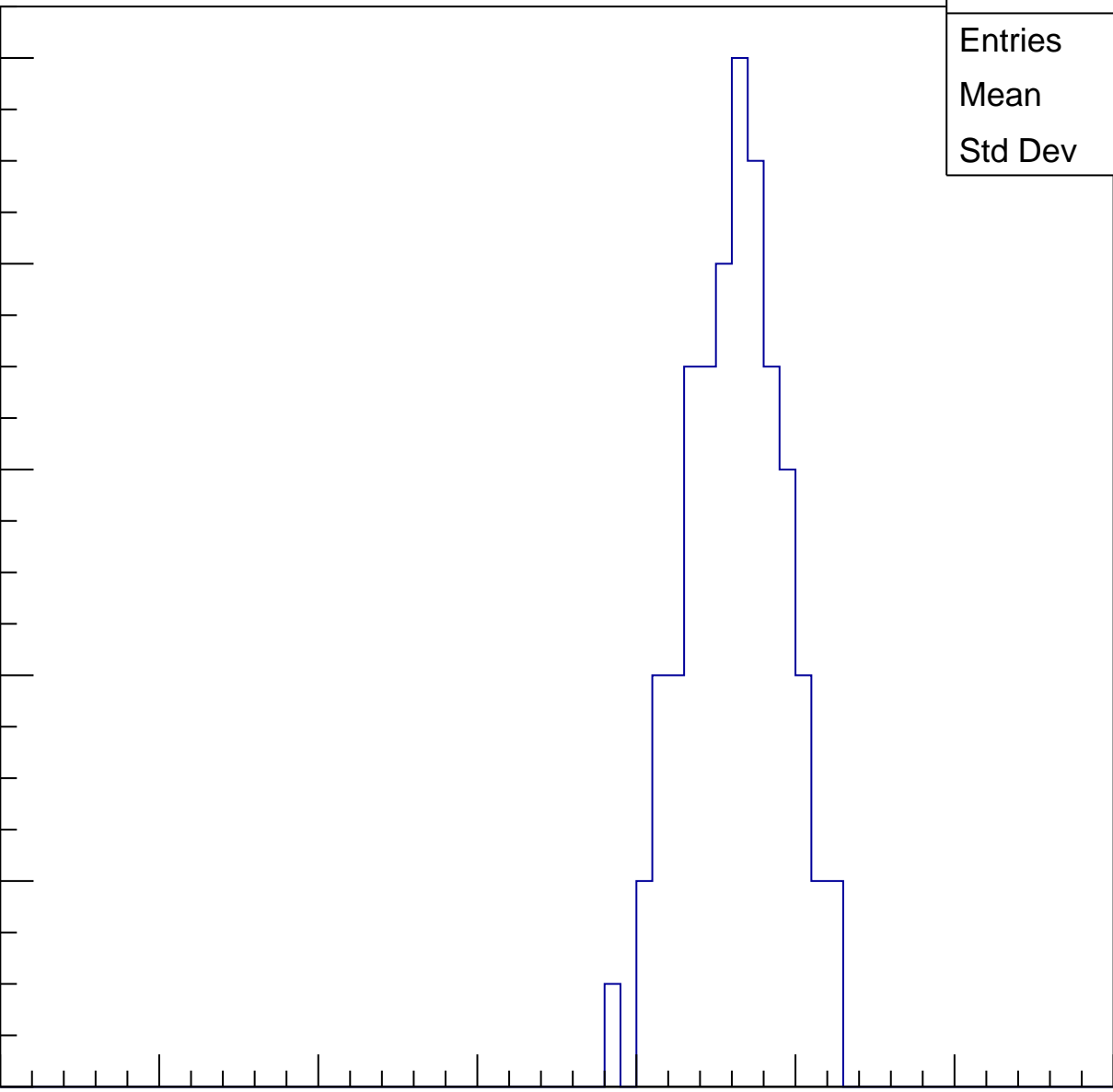
Entries	73
Mean	45.73
Std Dev	3.031

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

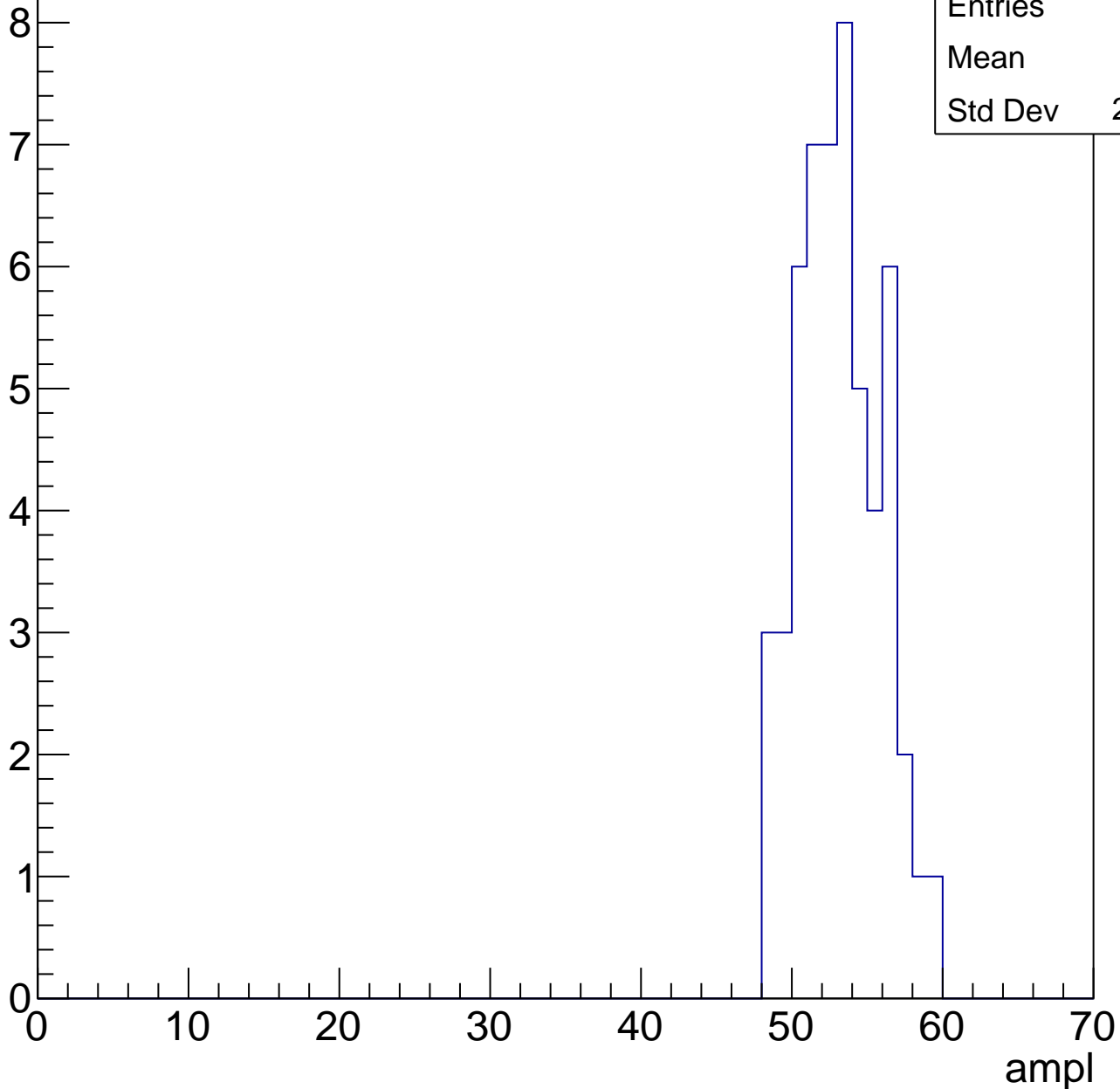


# B1L000S, U23-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	52.7
Std Dev	2.661

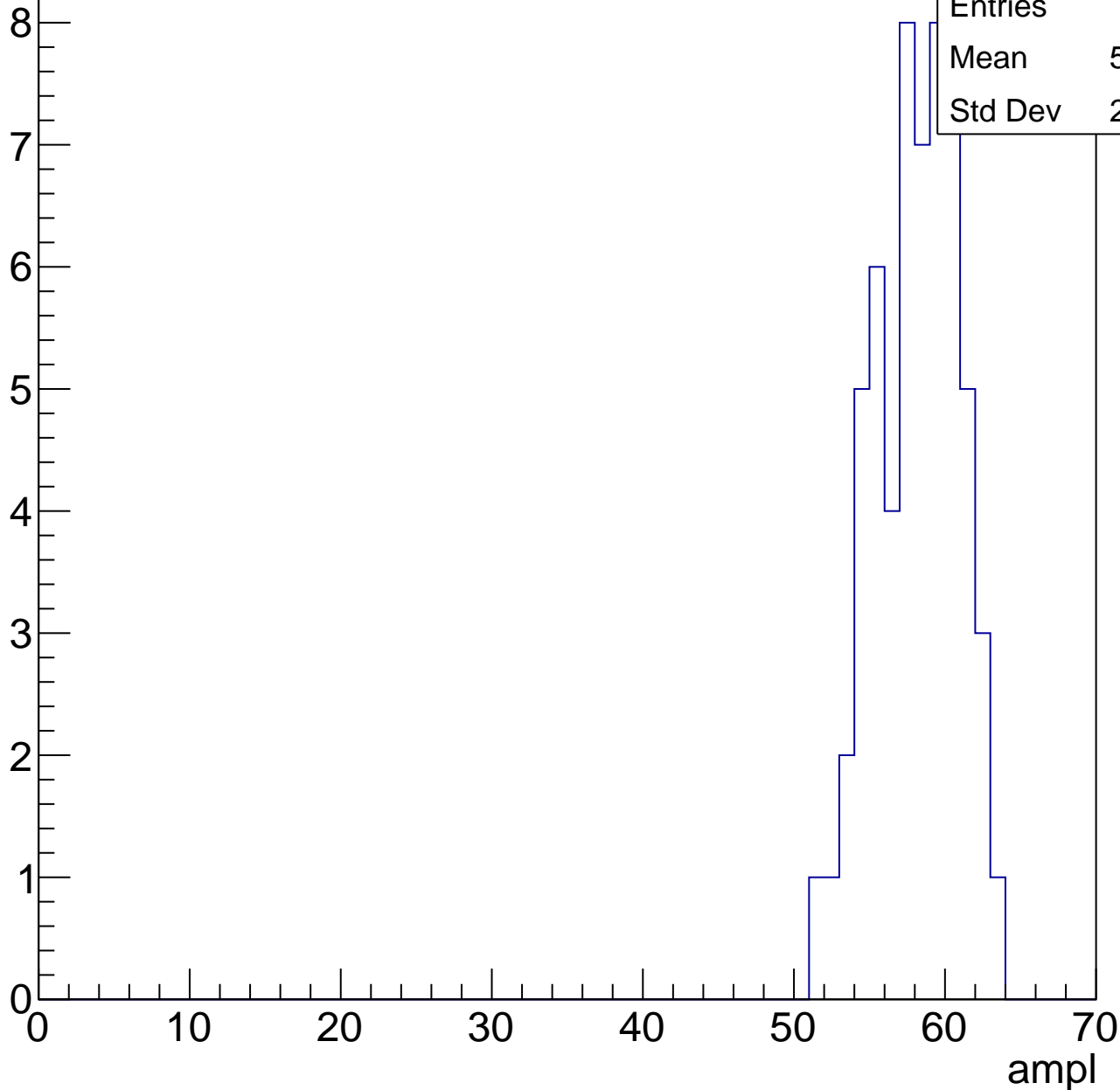


# B1L000S, U23-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.64
Std Dev	2.742

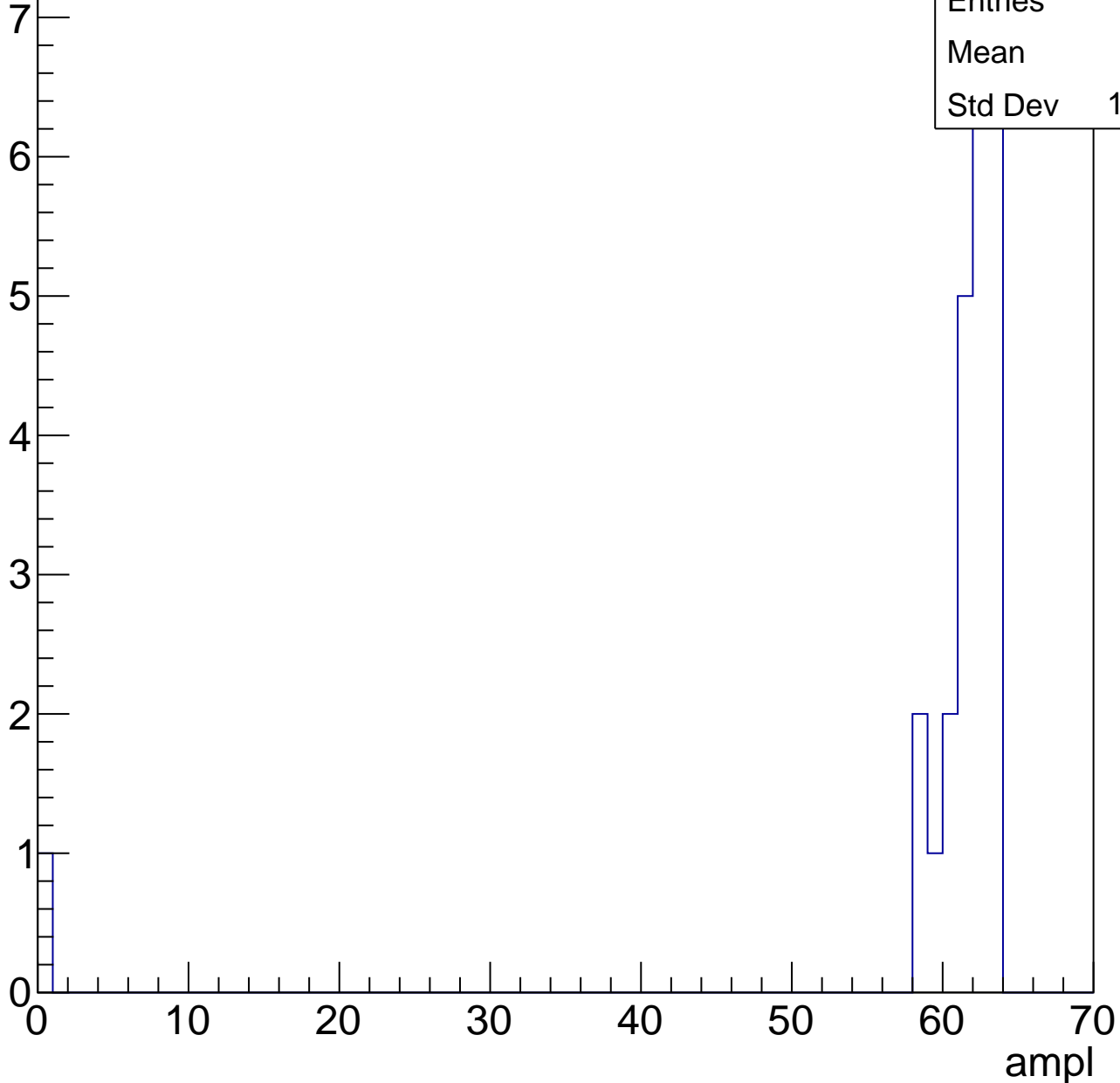


# B1L000S, U23-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	59
Std Dev	12.13





# B1L000S, U23-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch123, adc0

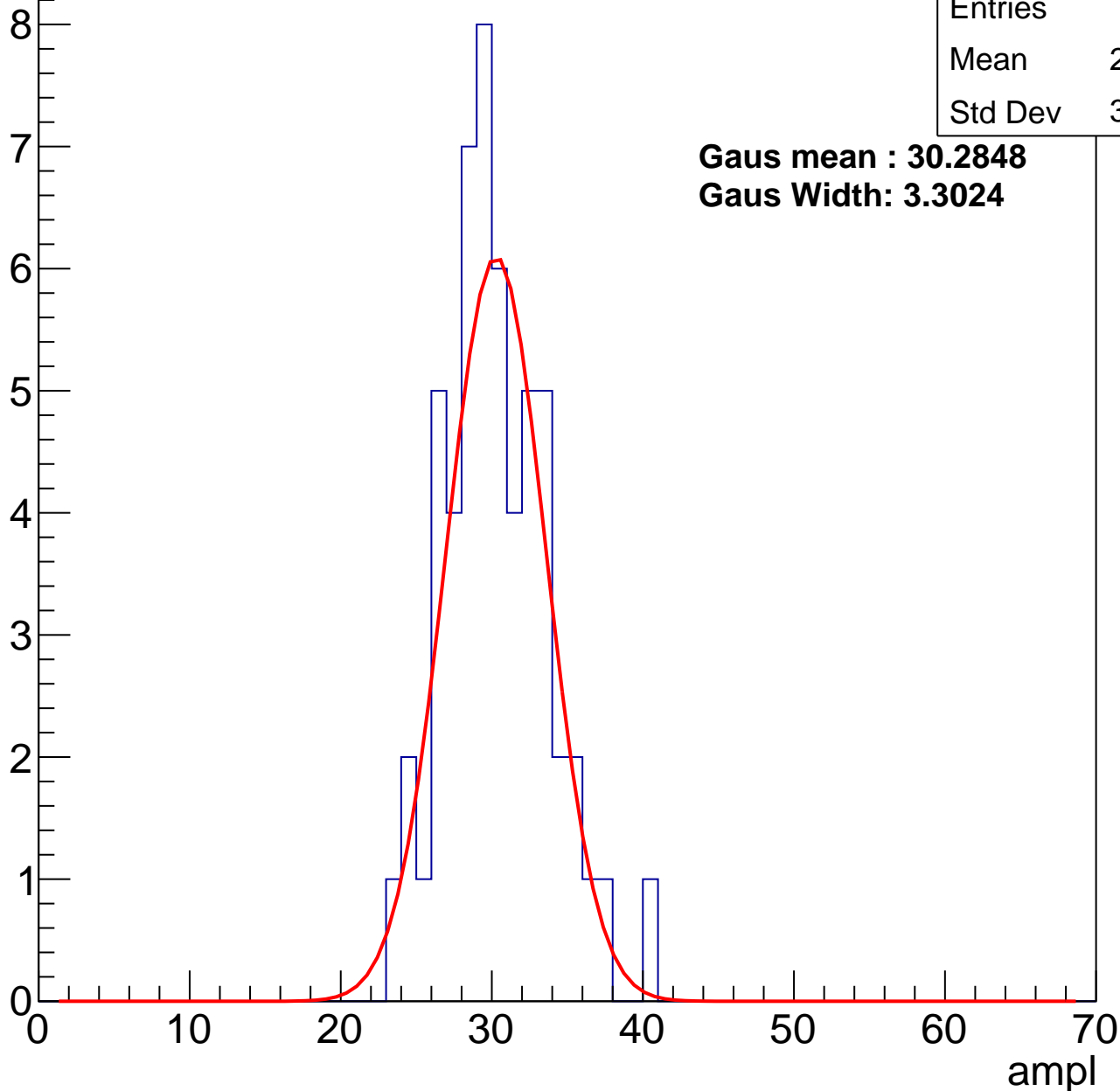
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	29.85
Std Dev	3.392

**Gaus mean : 30.2848**

**Gaus Width: 3.3024**



# B1L000S, U23-ch123, adc1

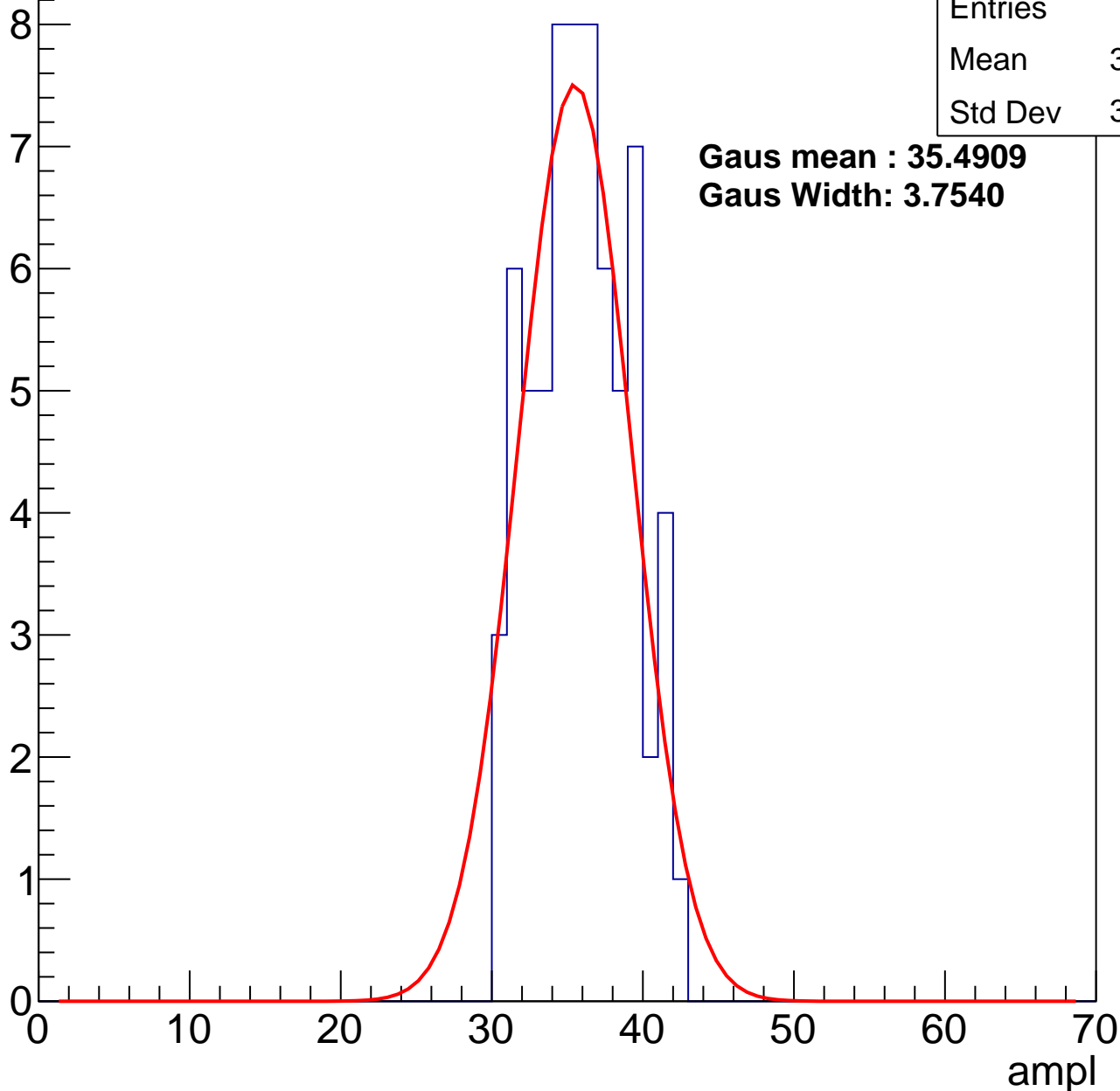
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.47
Std Dev	3.118

**Gaus mean : 35.4909**

**Gaus Width: 3.7540**



# B1L000S, U23-ch123, adc2

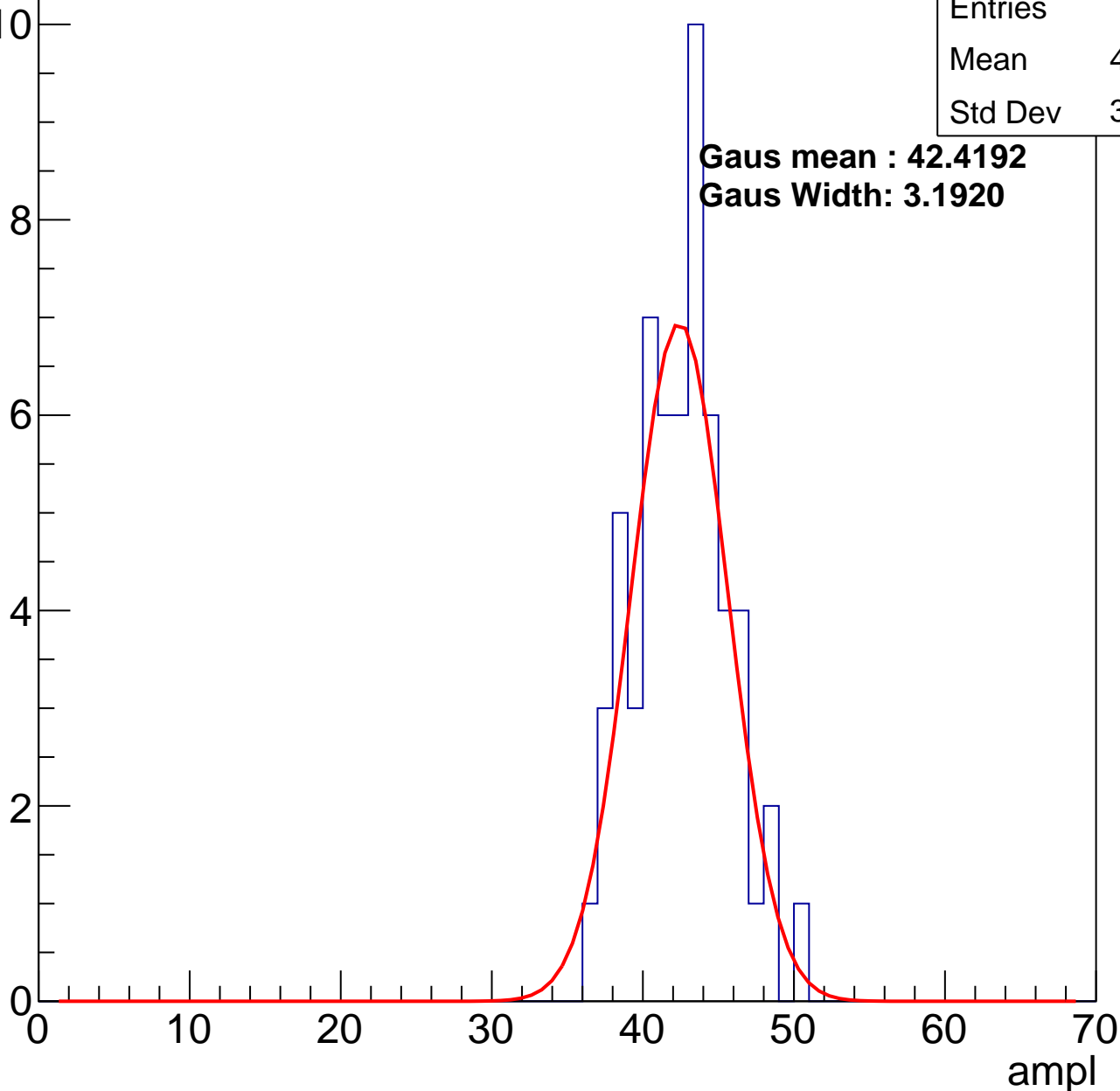
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.08
Std Dev	3.044

**Gaus mean : 42.4192**

**Gaus Width: 3.1920**

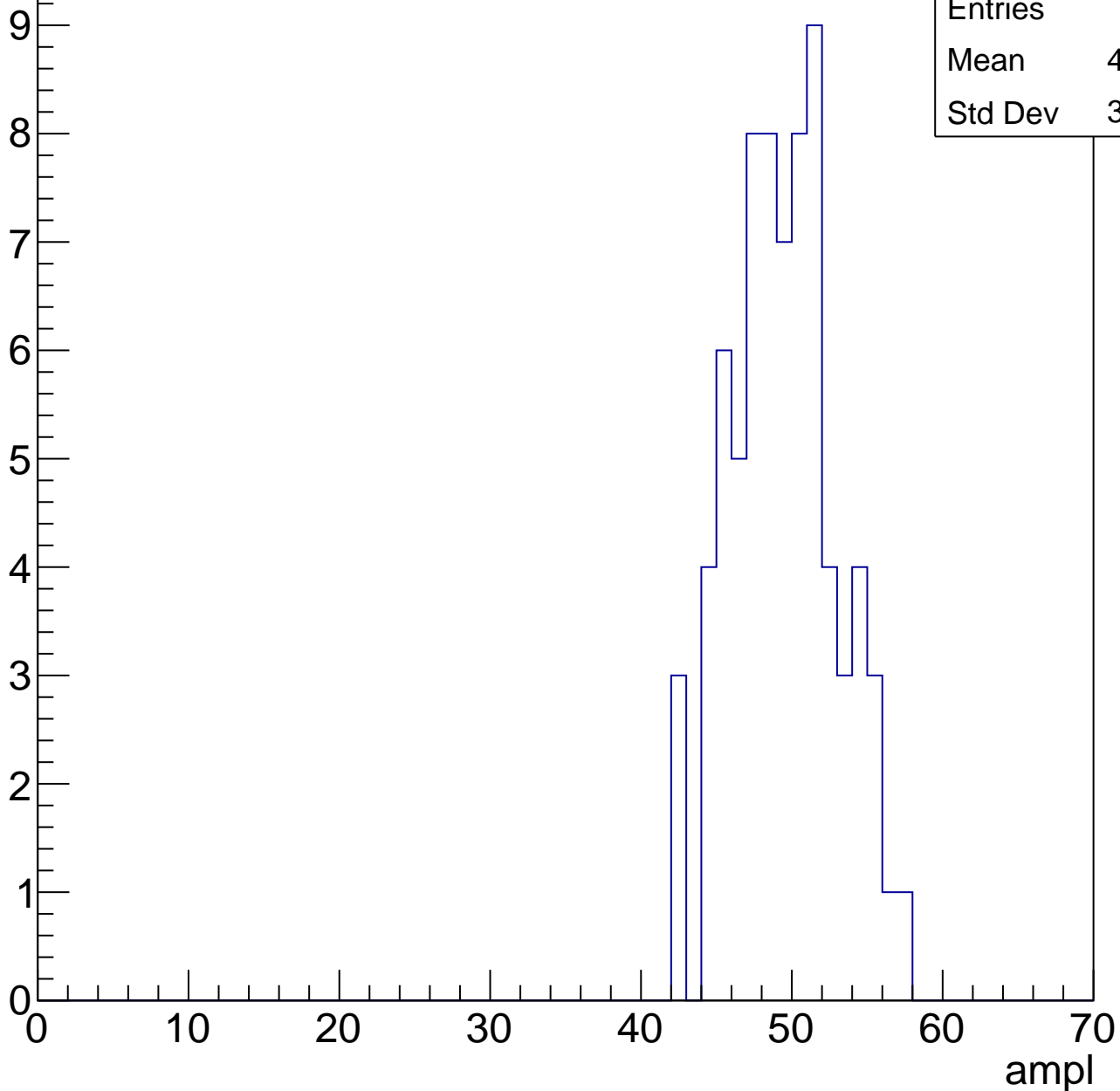


# B1L000S, U23-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	48.99
Std Dev	3.443

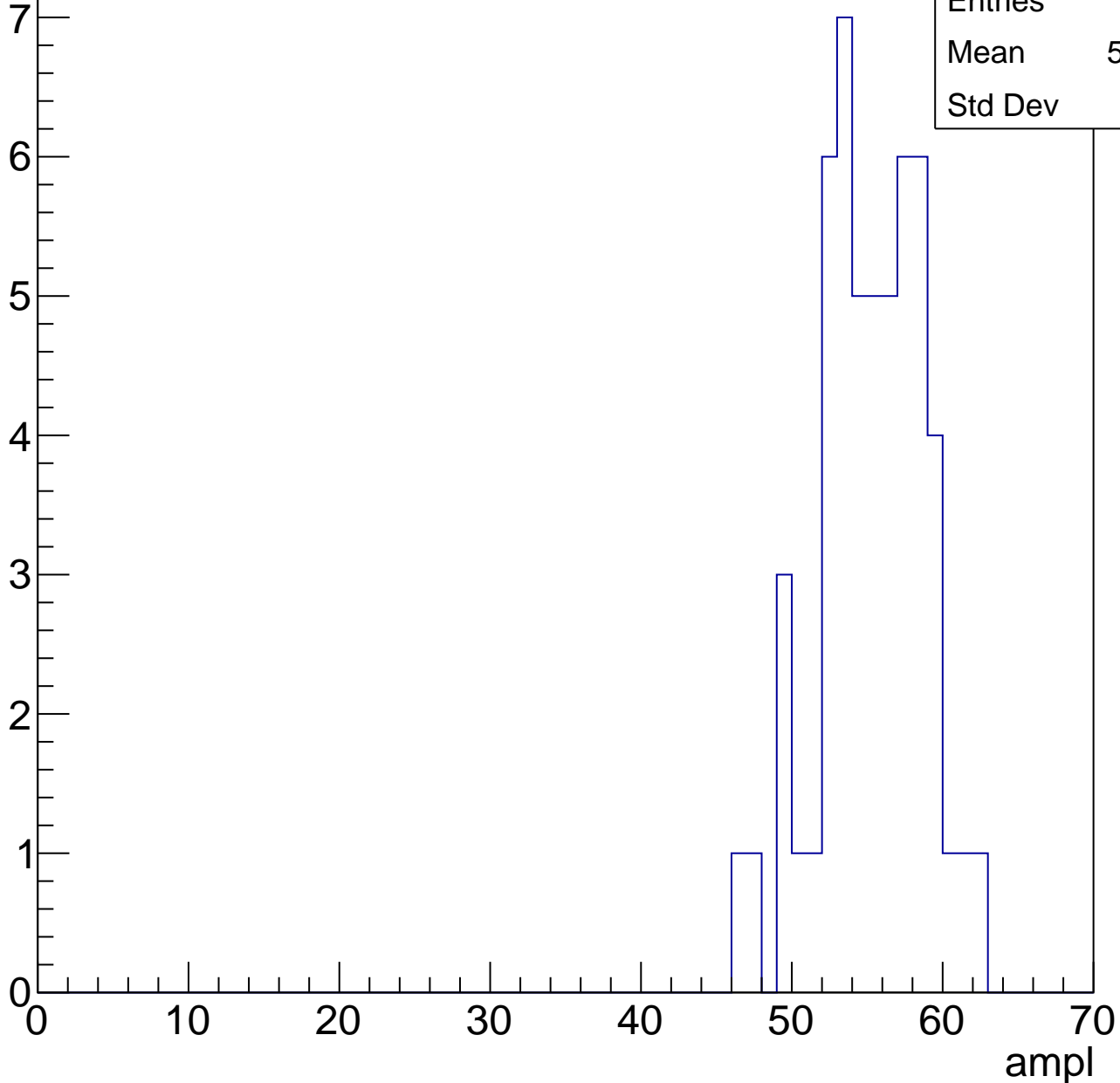


# B1L000S, U23-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

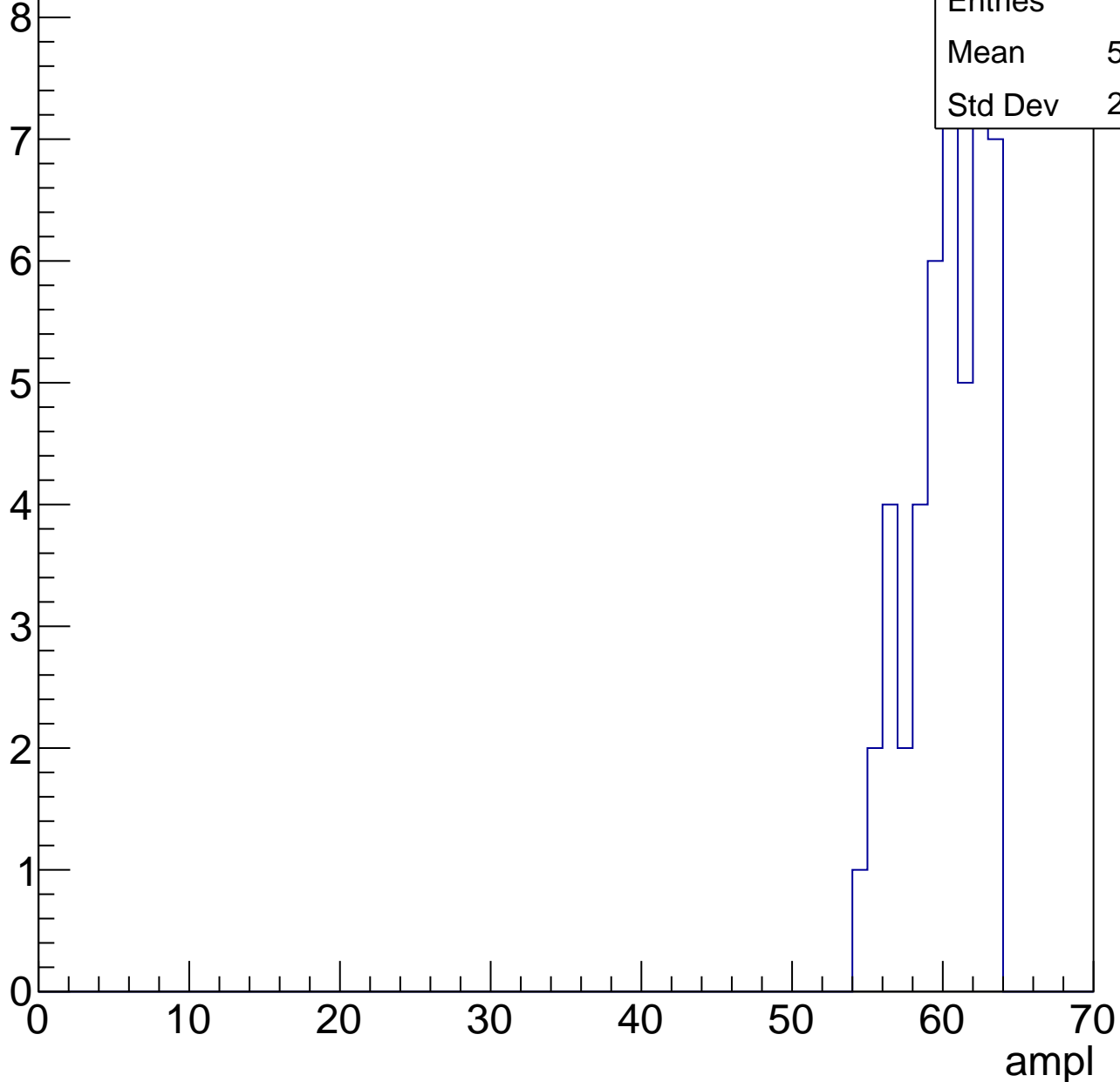
Entries	54
Mean	54.78
Std Dev	3.43



# B1L000S, U23-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

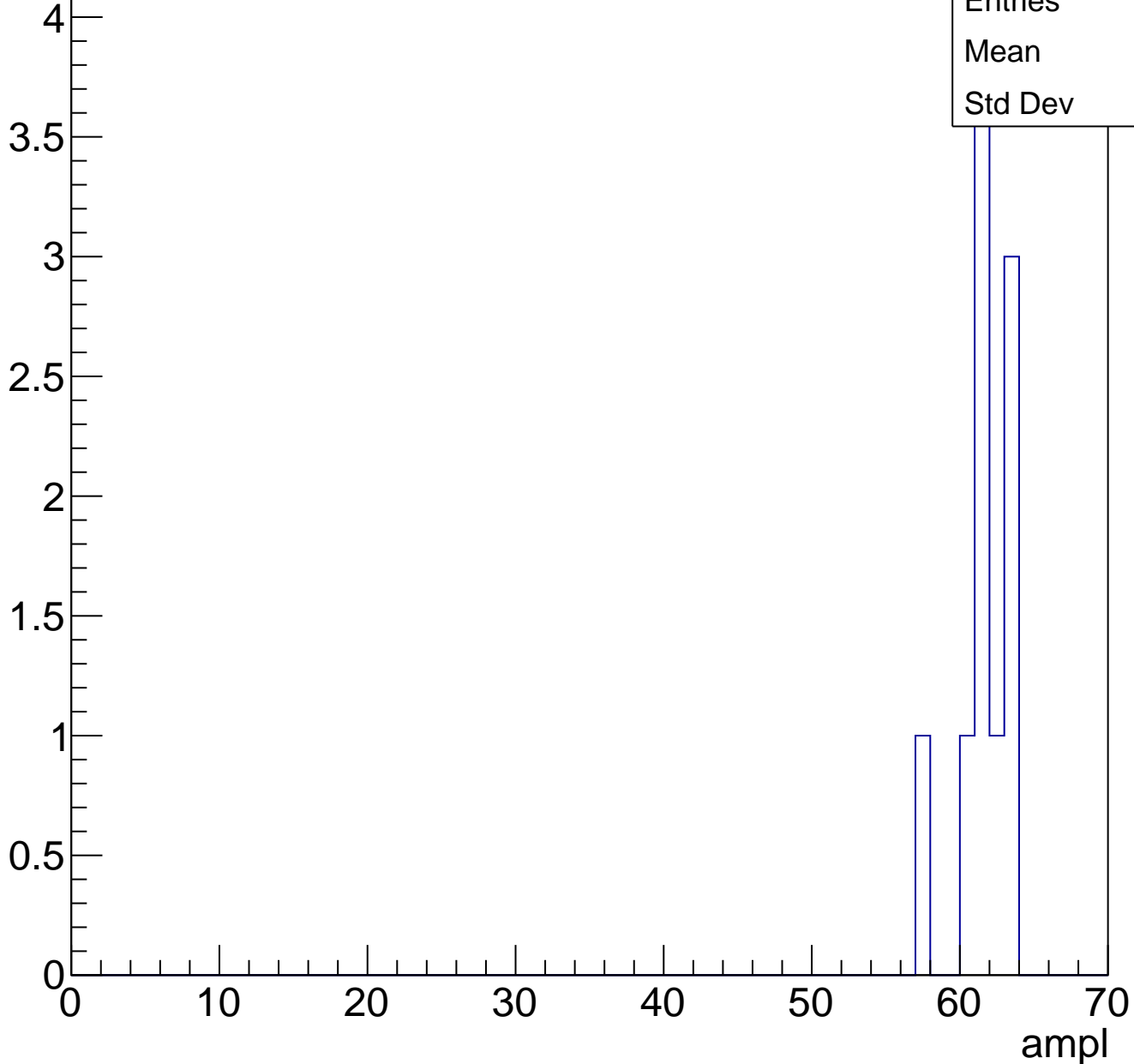


Entries	47
Mean	59.79
Std Dev	2.475

# B1L000S, U23-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

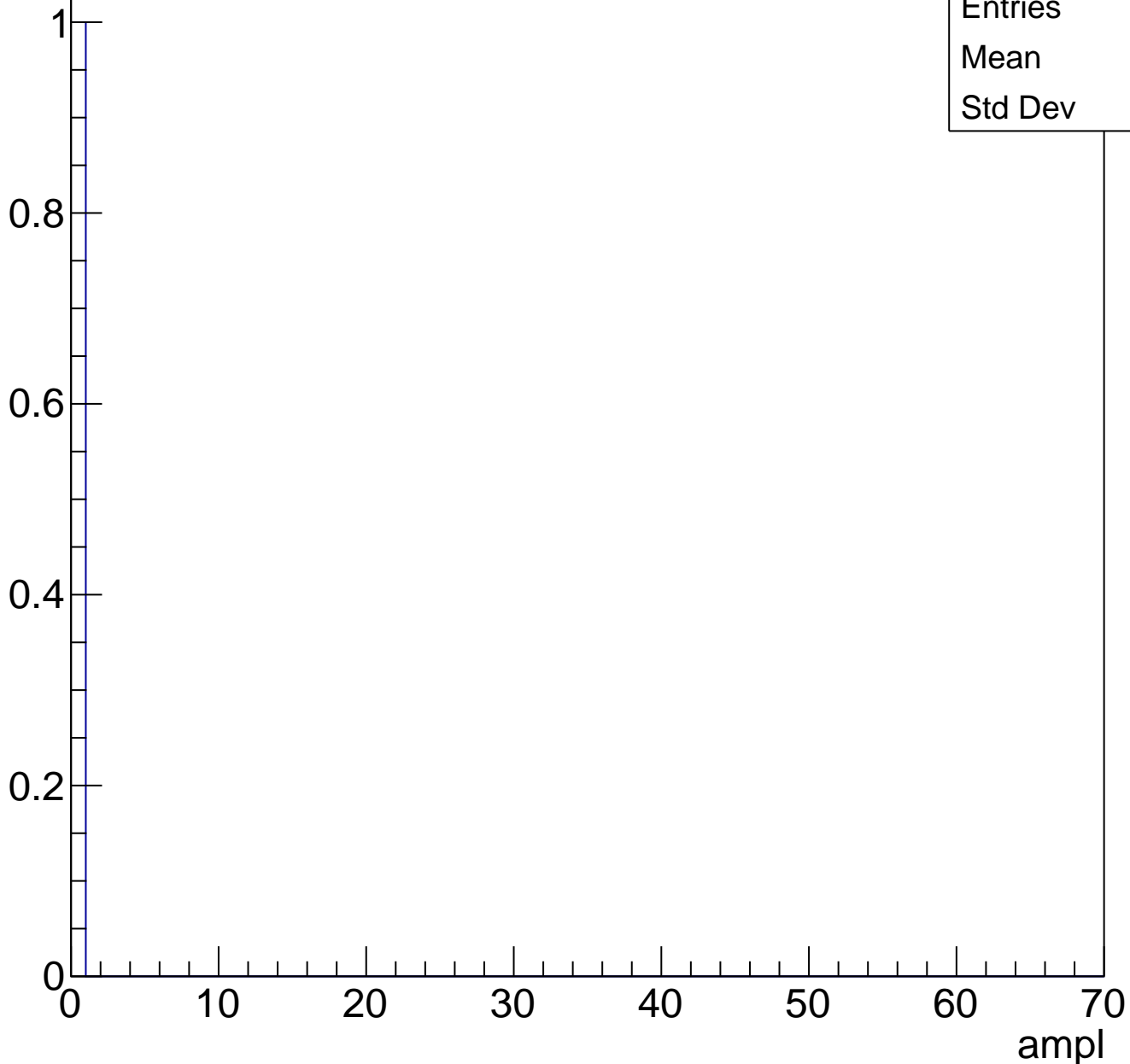




# B1L000S, U23-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	29.86
Std Dev	4.57

**Gaus mean : 30.3771**

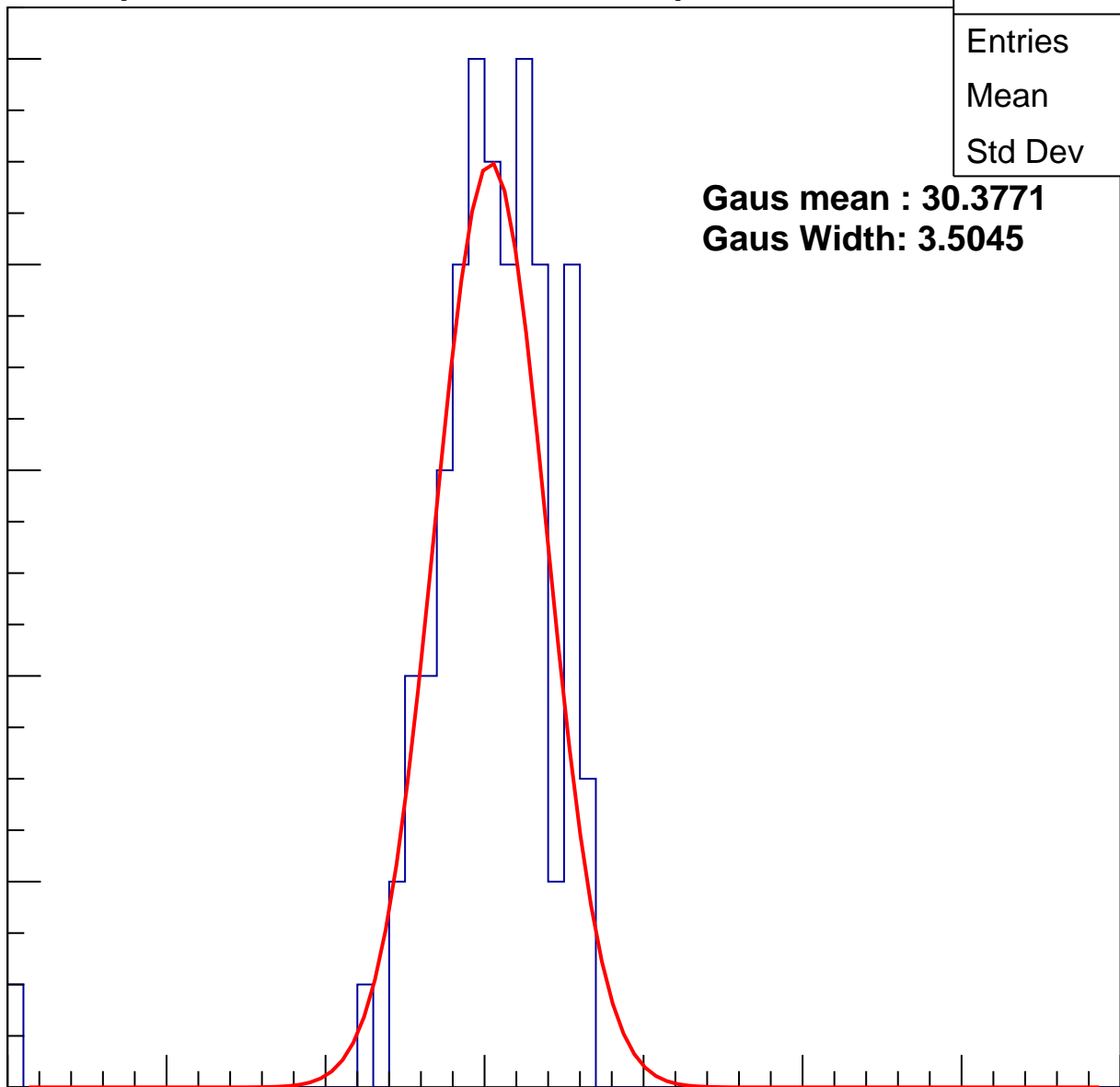
**Gaus Width: 3.5045**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U23-ch124, adc1

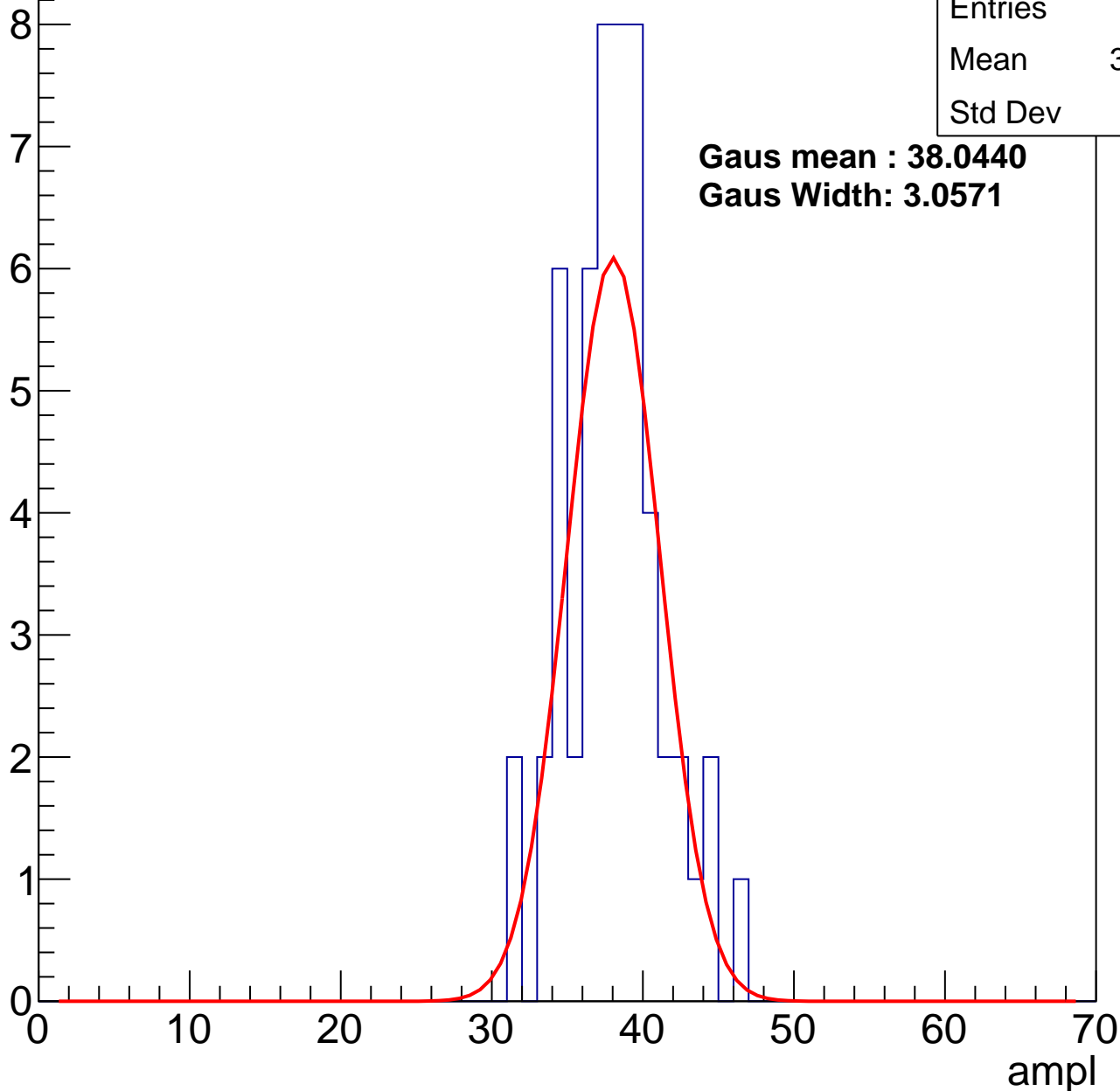
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	37.65
Std Dev	3.11

**Gaus mean : 38.0440**

**Gaus Width: 3.0571**



# B1L000S, U23-ch124, adc2

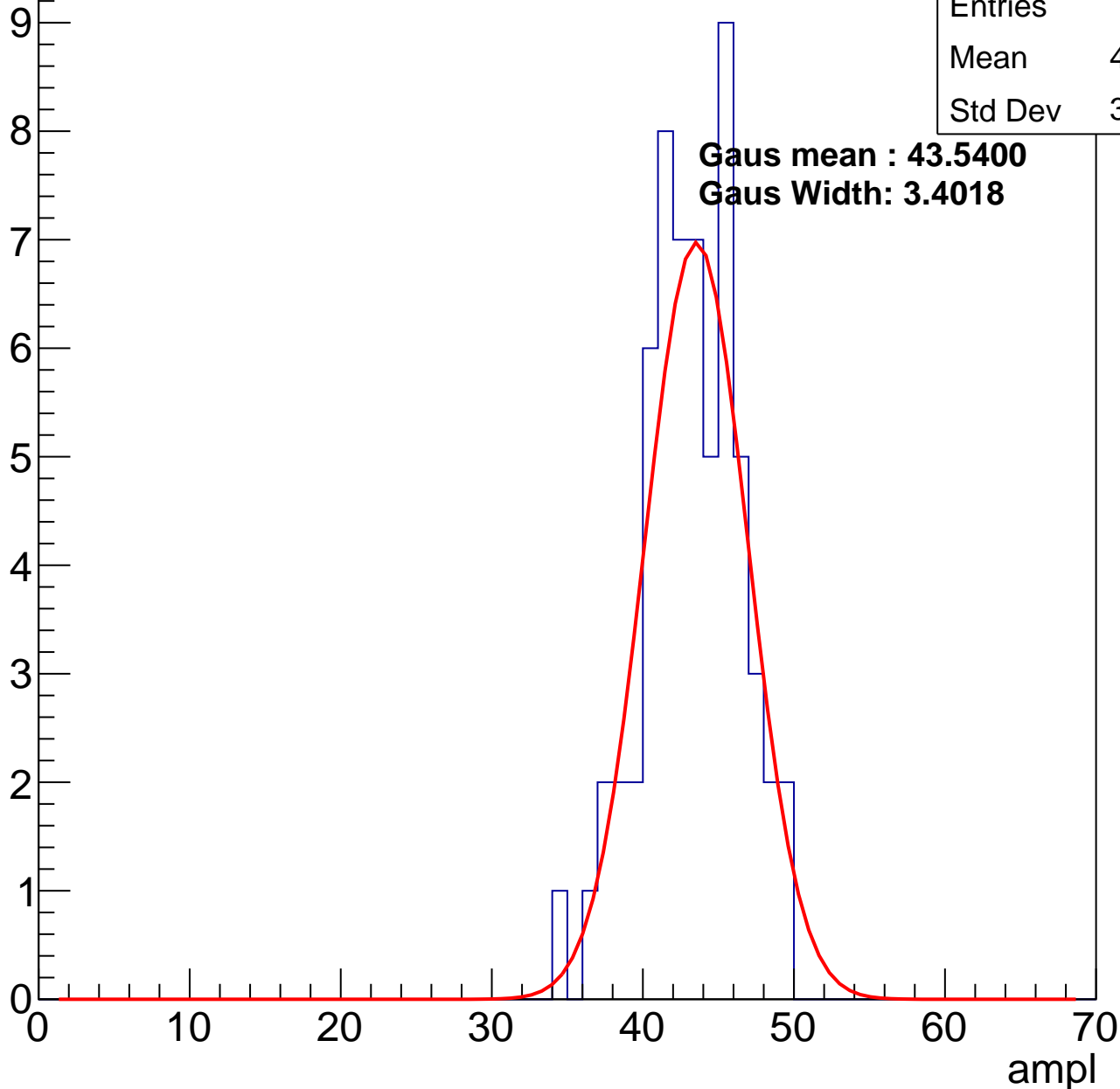
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.76
Std Dev	3.186

**Gaus mean : 43.5400**

**Gaus Width: 3.4018**

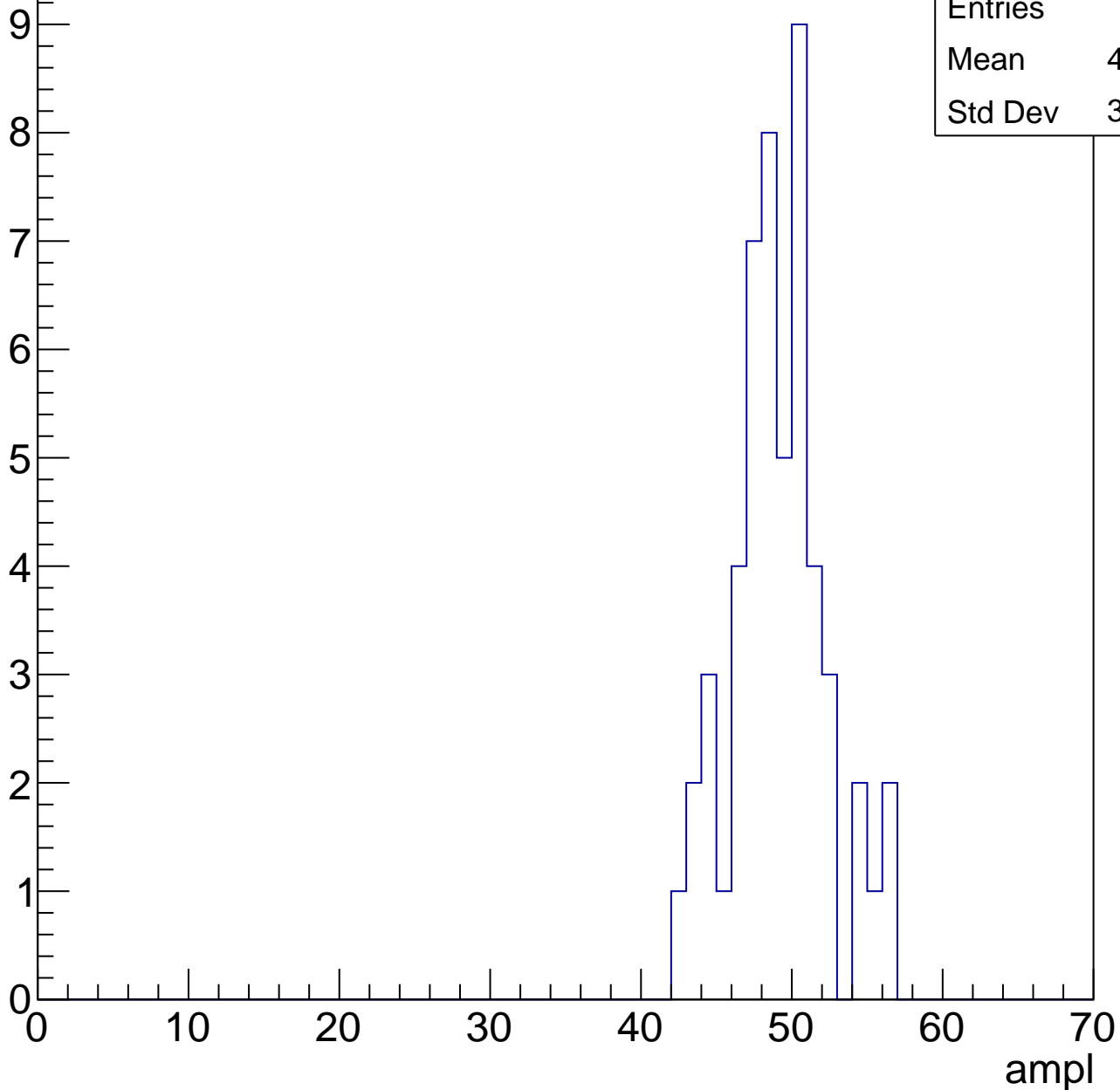


# B1L000S, U23-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	48.69
Std Dev	3.147

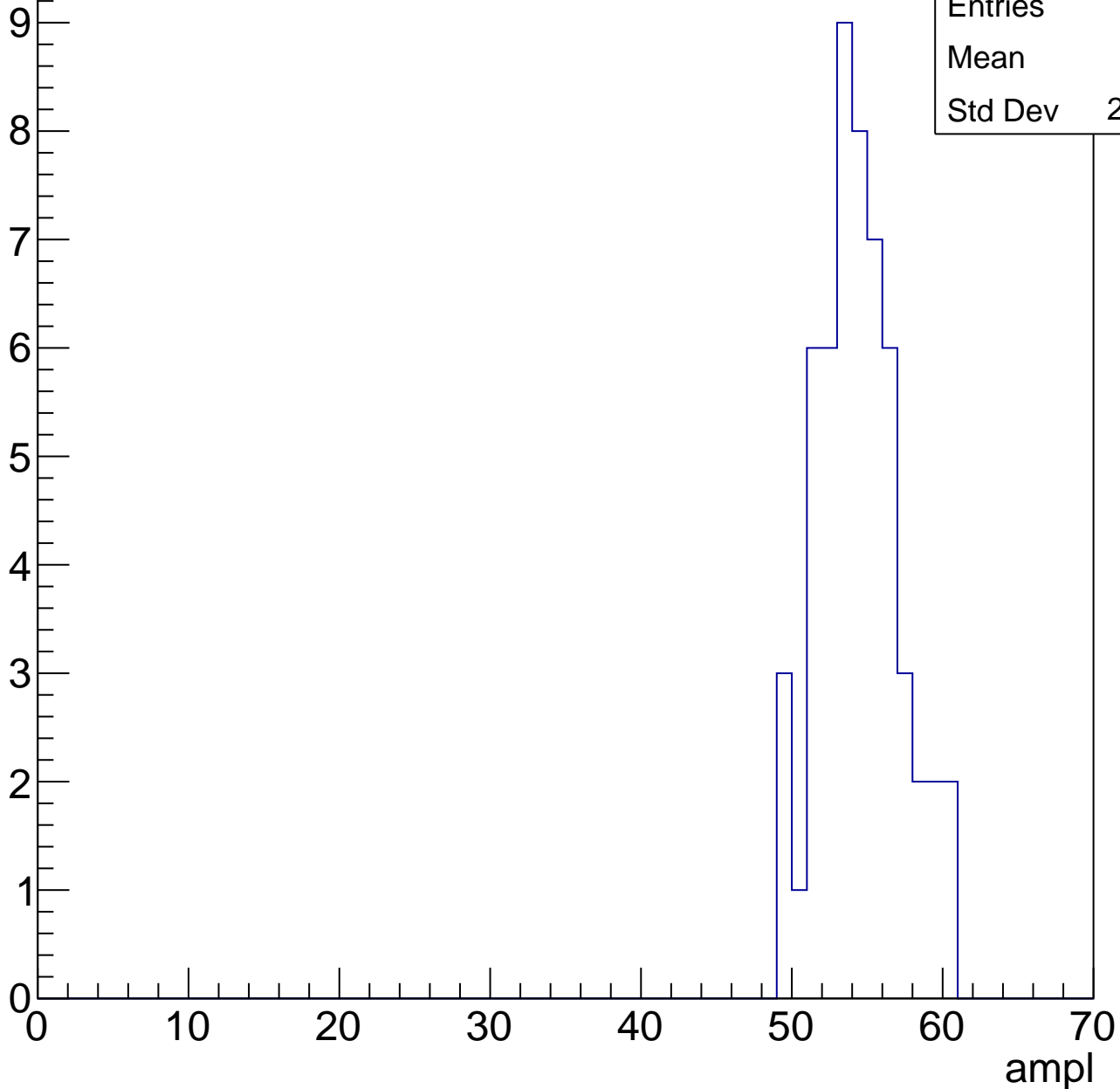


# B1L000S, U23-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	54
Std Dev	2.663

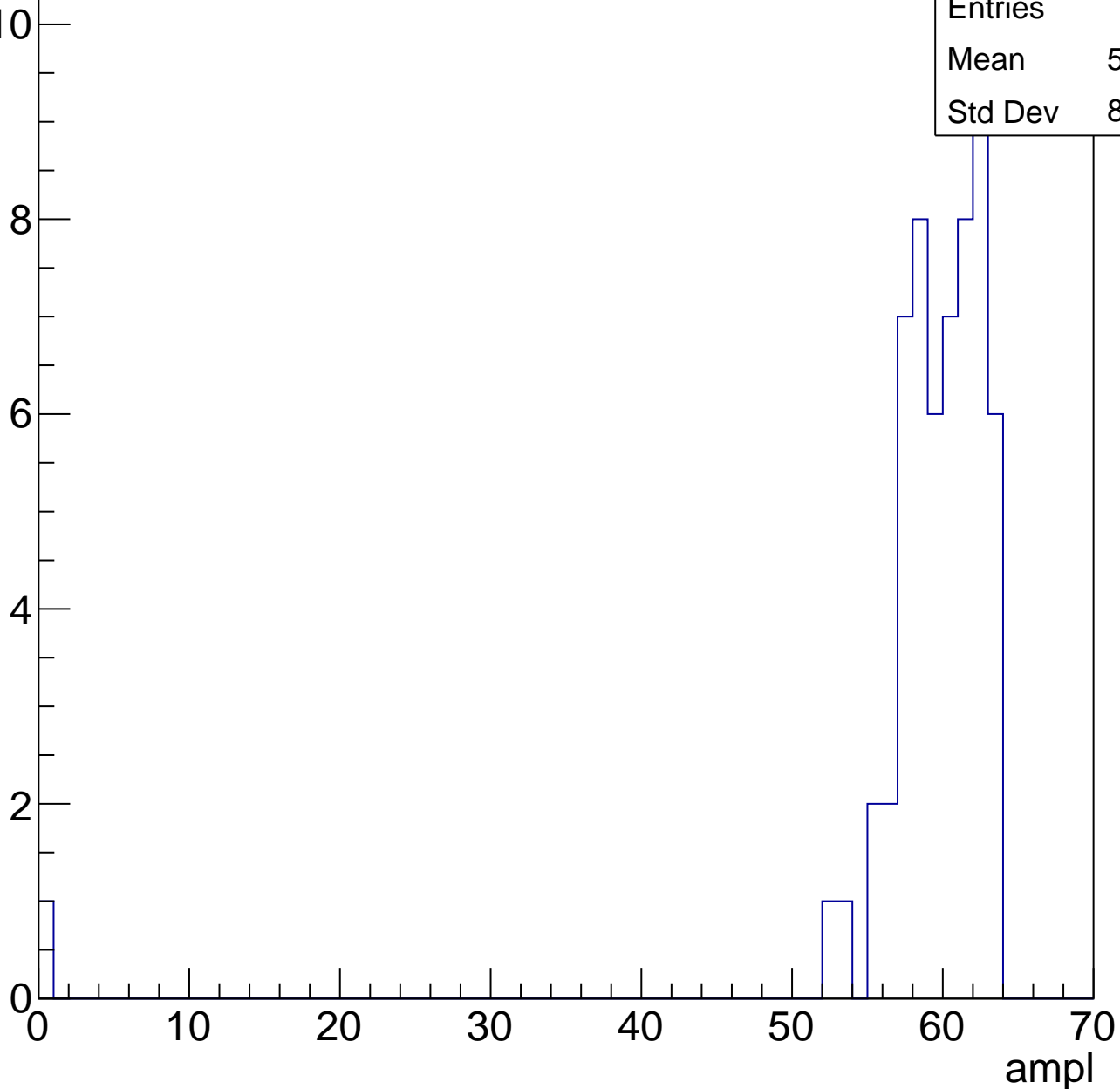


# B1L000S, U23-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

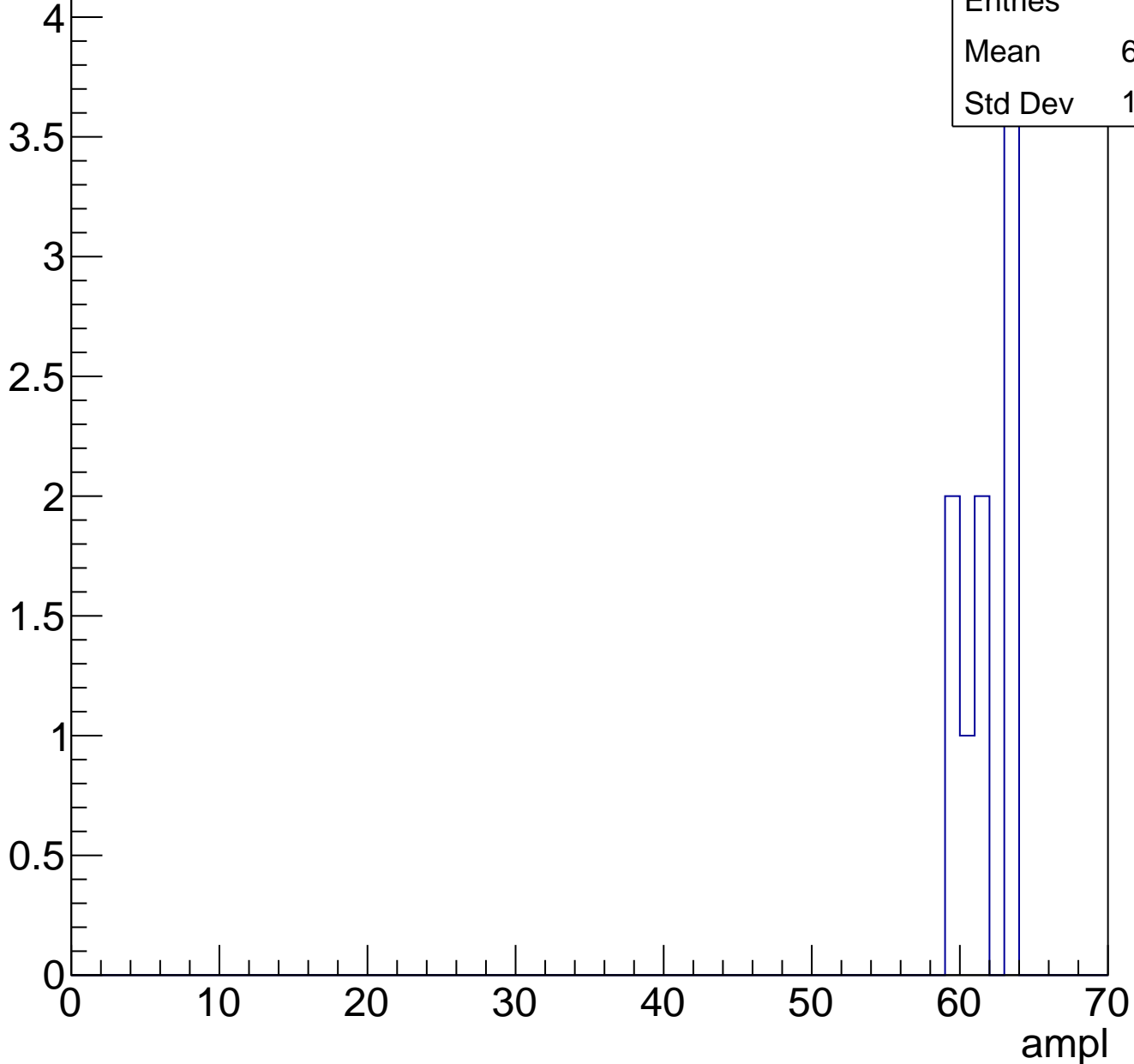
Entries	59
Mean	58.47
Std Dev	8.089



# B1L000S, U23-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U23-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch125, adc0

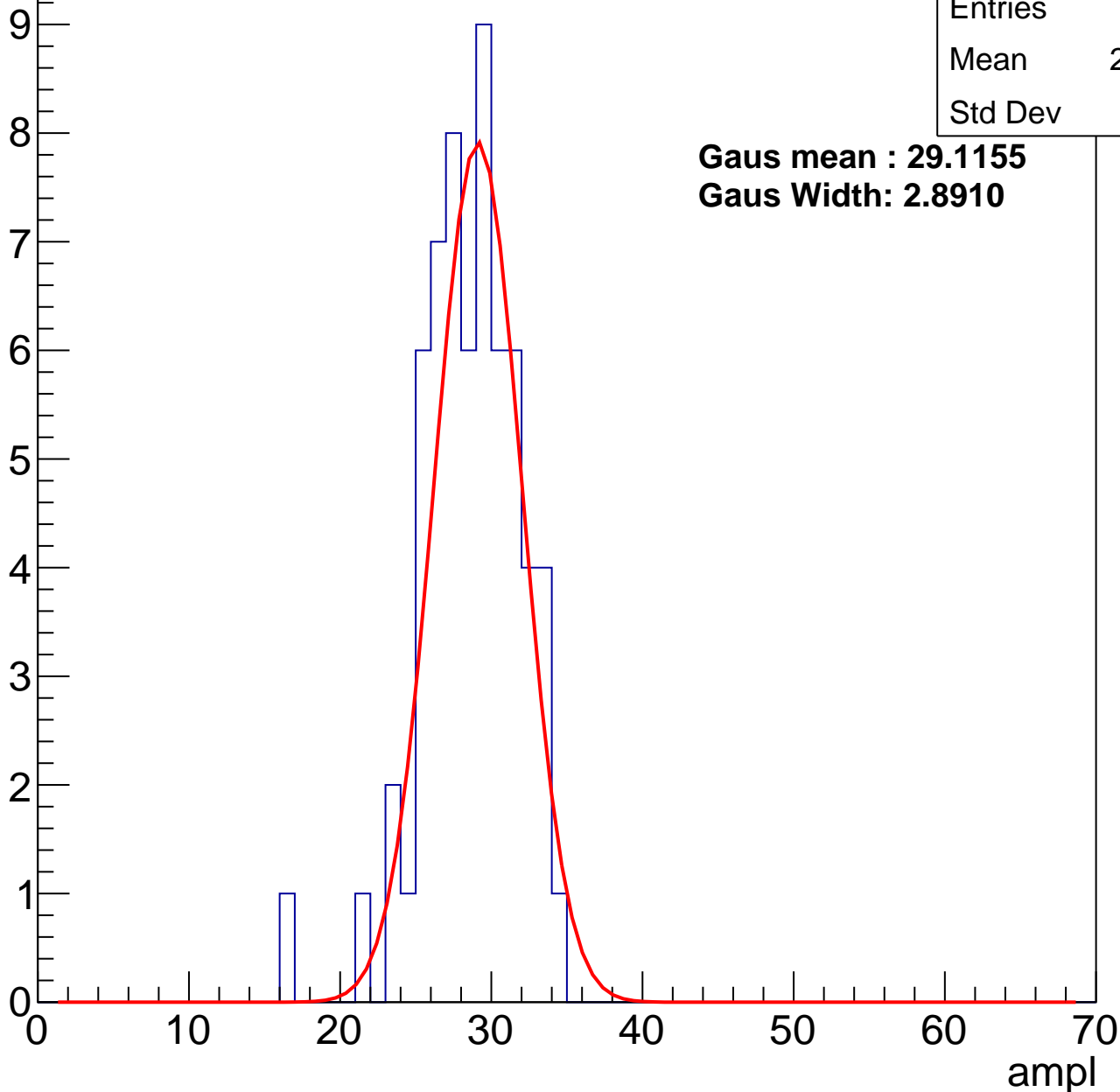
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.13
Std Dev	3.2

**Gaus mean : 29.1155**

**Gaus Width: 2.8910**



# B1L000S, U23-ch125, adc1

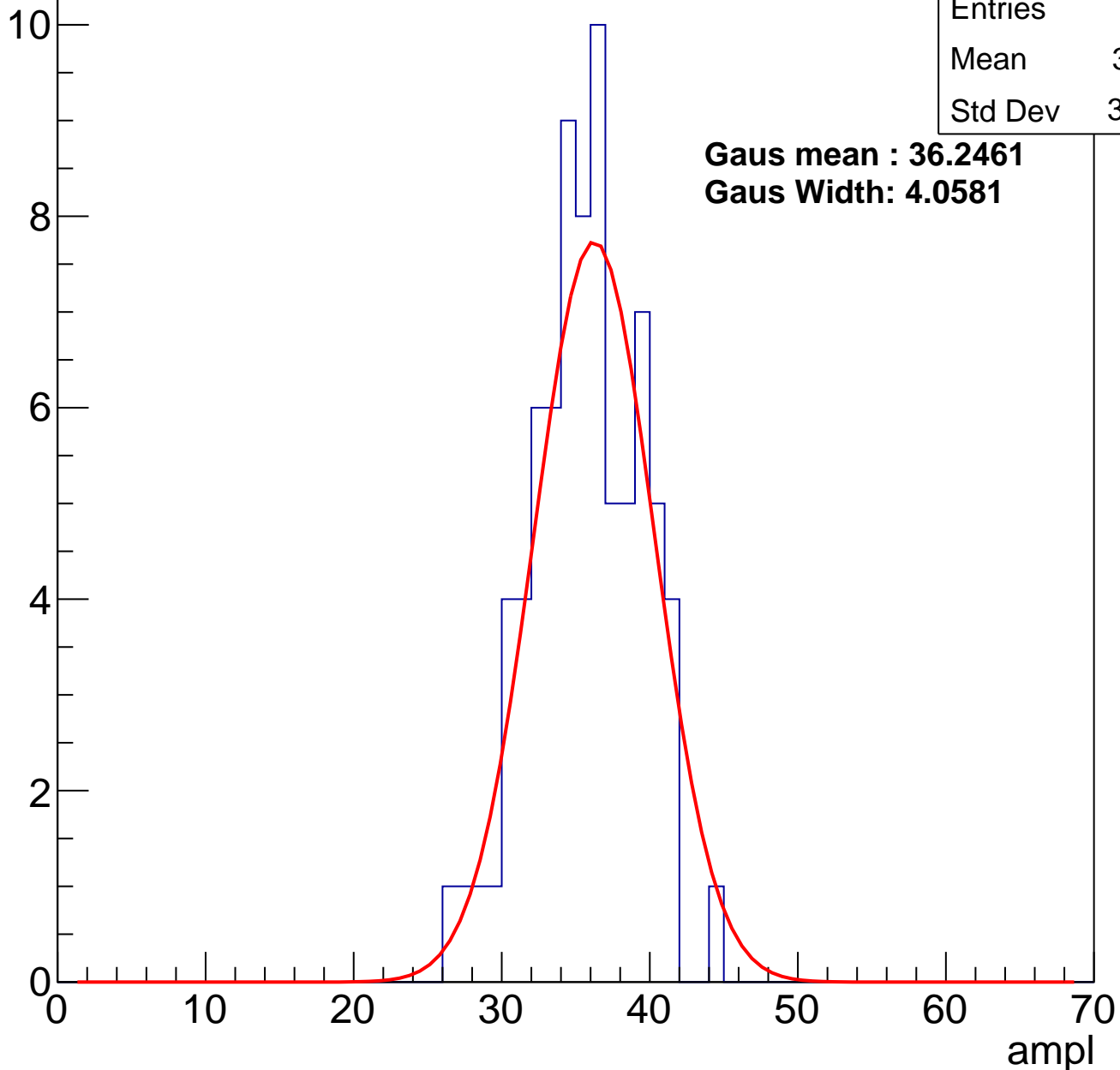
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	35.21
Std Dev	3.607

**Gaus mean : 36.2461**

**Gaus Width: 4.0581**

Entry



# B1L000S, U23-ch125, adc2

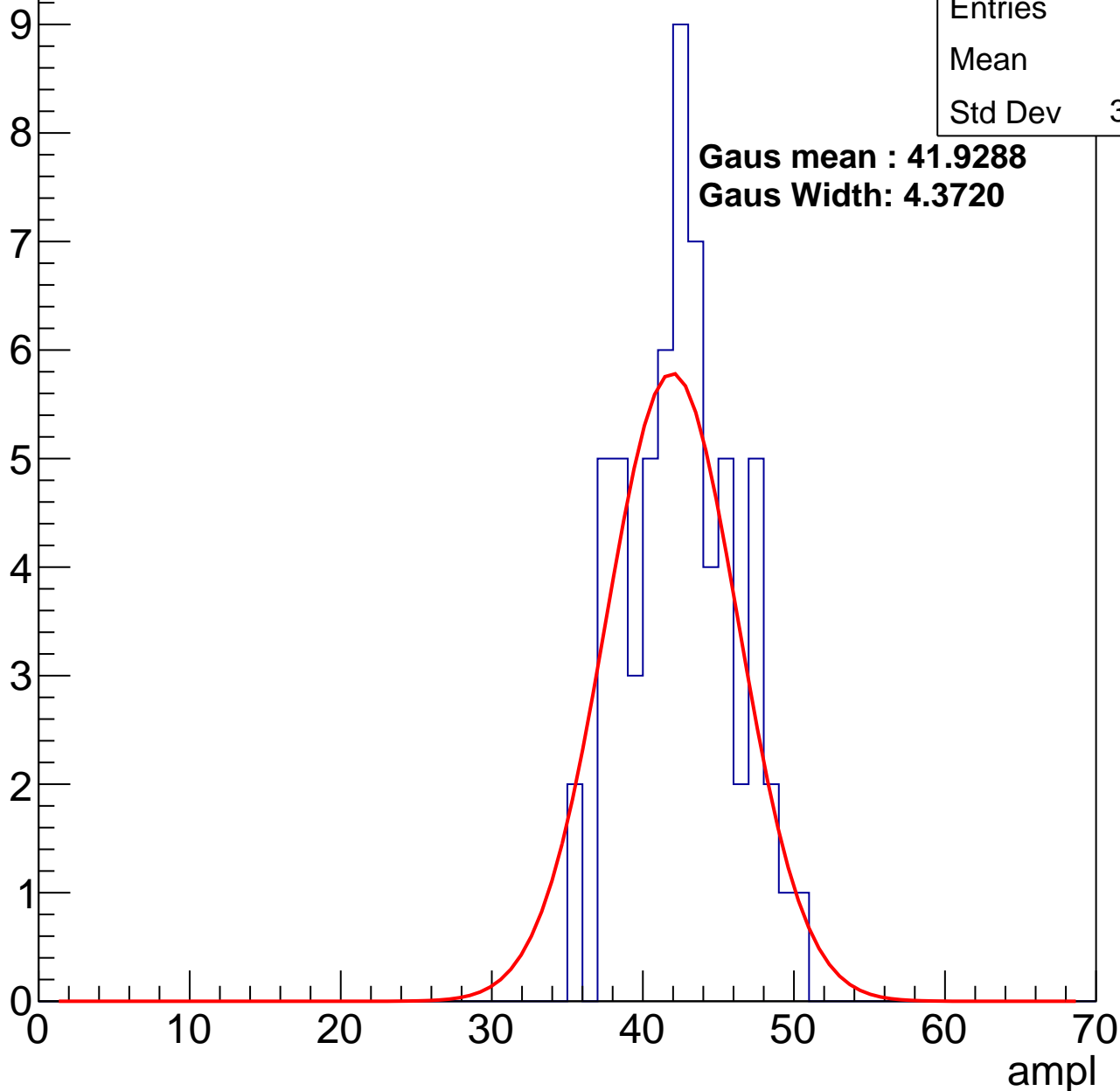
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.1
Std Dev	3.514

**Gaus mean : 41.9288**

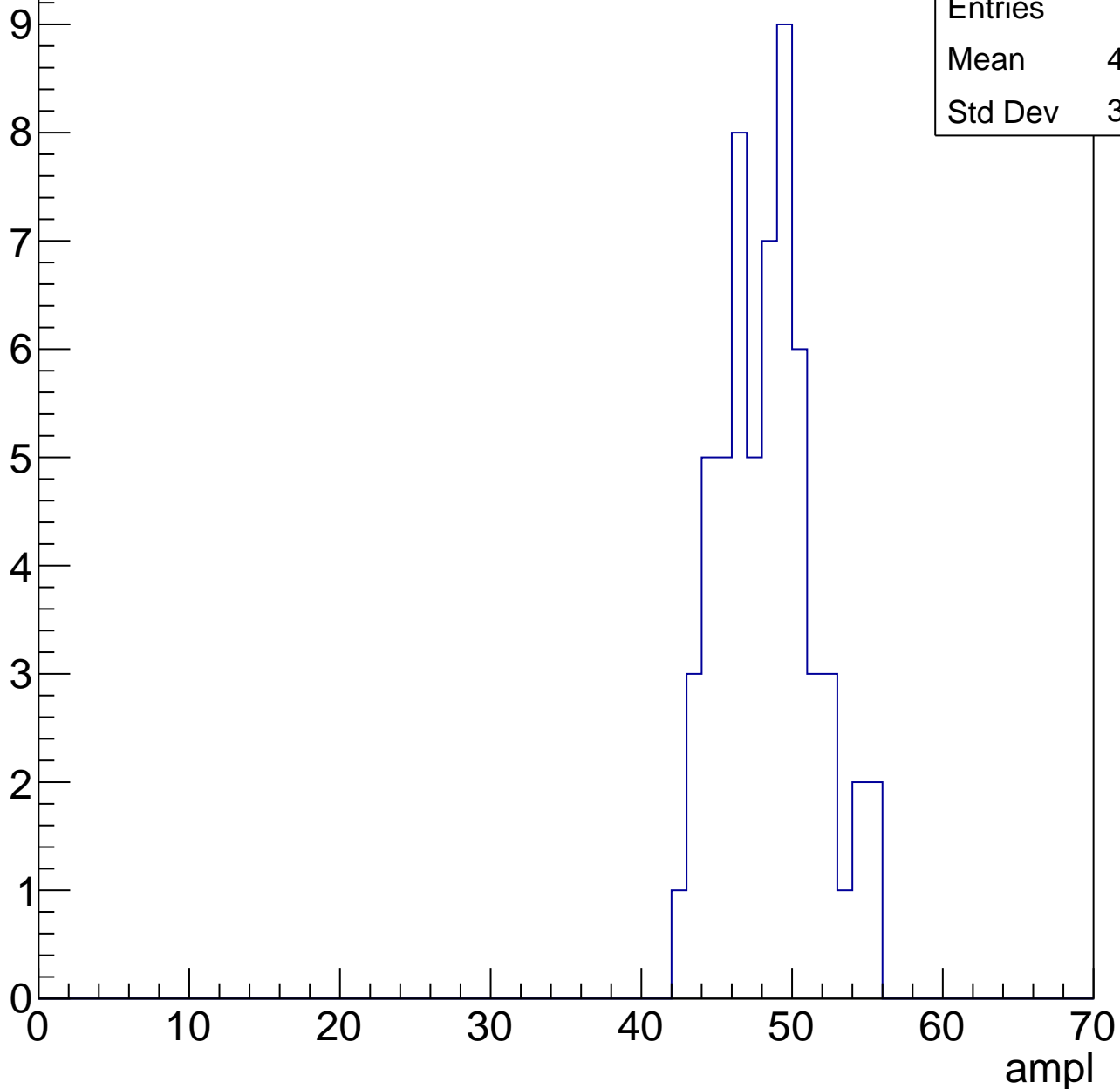
**Gaus Width: 4.3720**



# B1L000S, U23-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	60
Mean	47.93
Std Dev	3.098

# B1L000S, U23-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

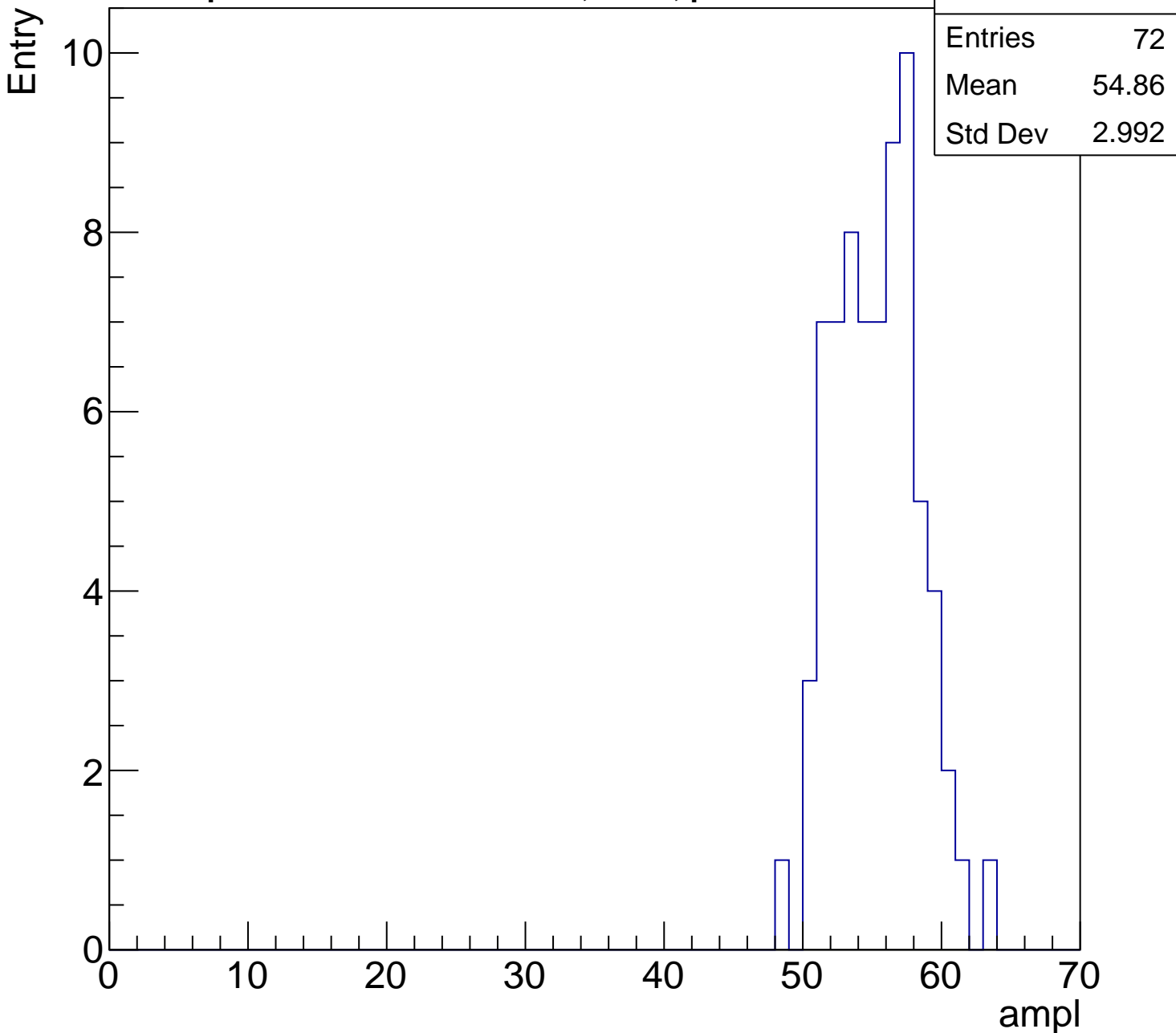
Entries	72
Mean	54.86
Std Dev	2.992

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

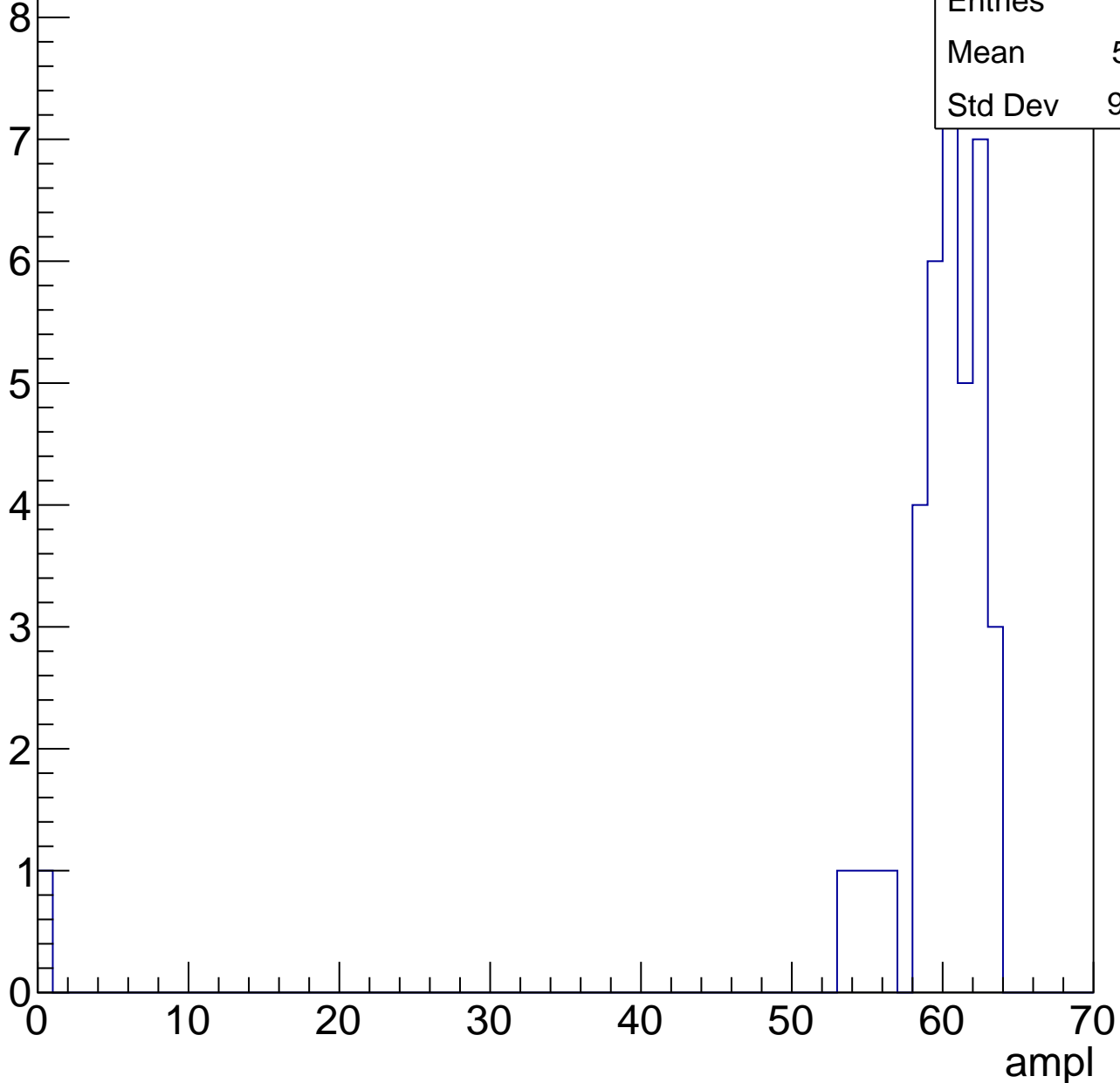


# B1L000S, U23-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	58.21
Std Dev	9.849

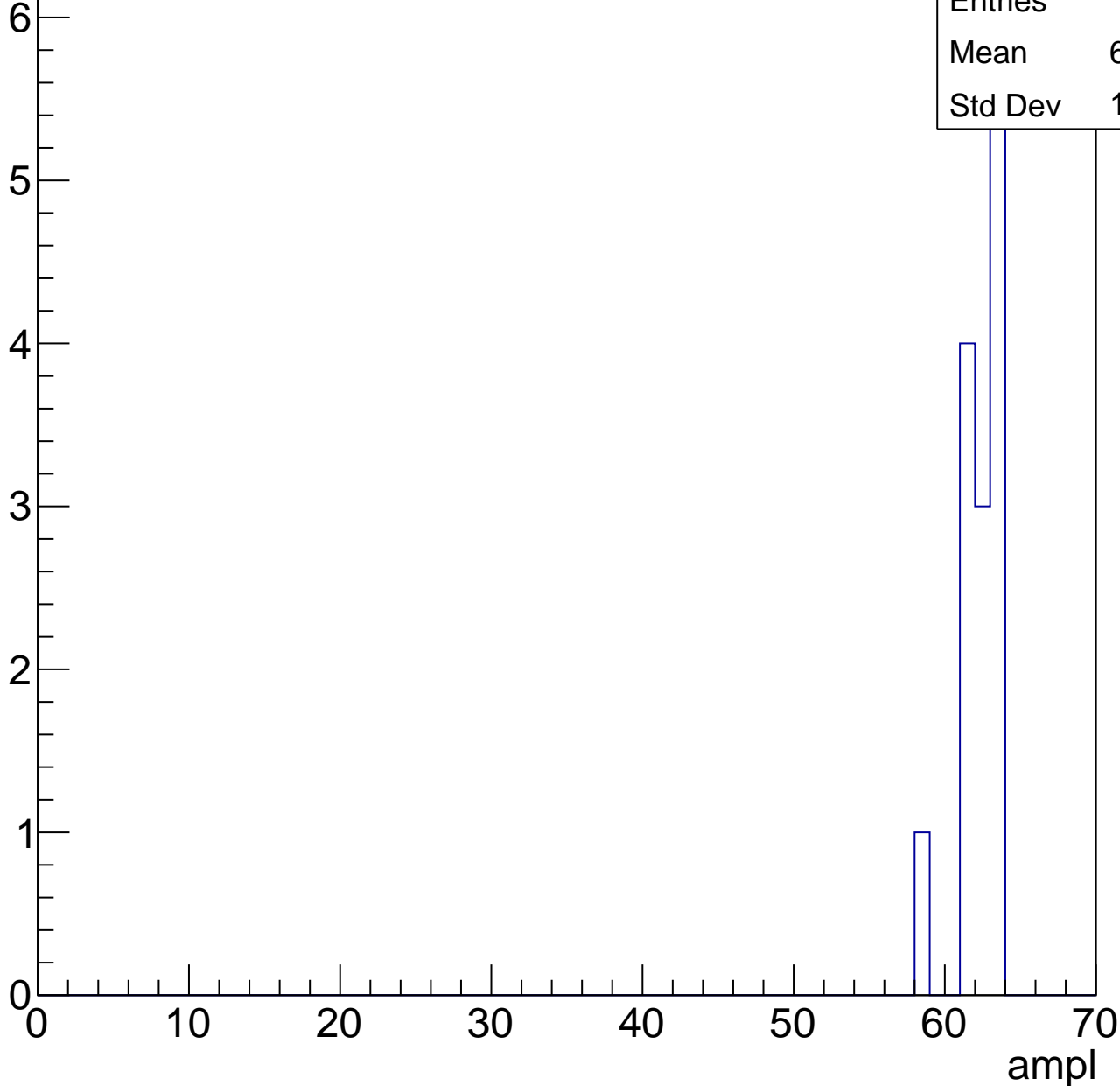


# B1L000S, U23-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.355





# B1L000S, U23-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U23-ch126, adc0

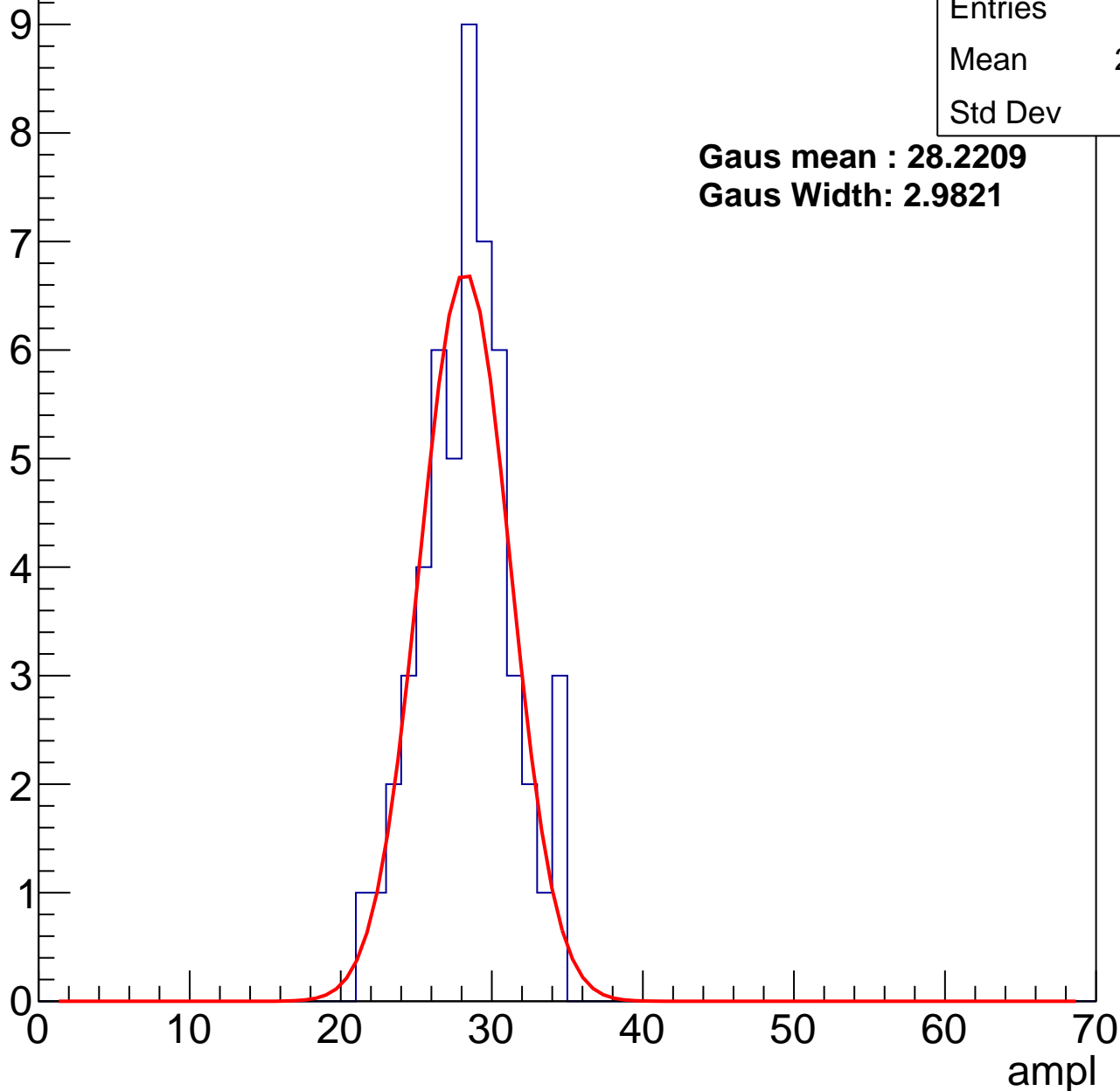
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	27.91
Std Dev	2.98

**Gaus mean : 28.2209**

**Gaus Width: 2.9821**



# B1L000S, U23-ch126, adc1

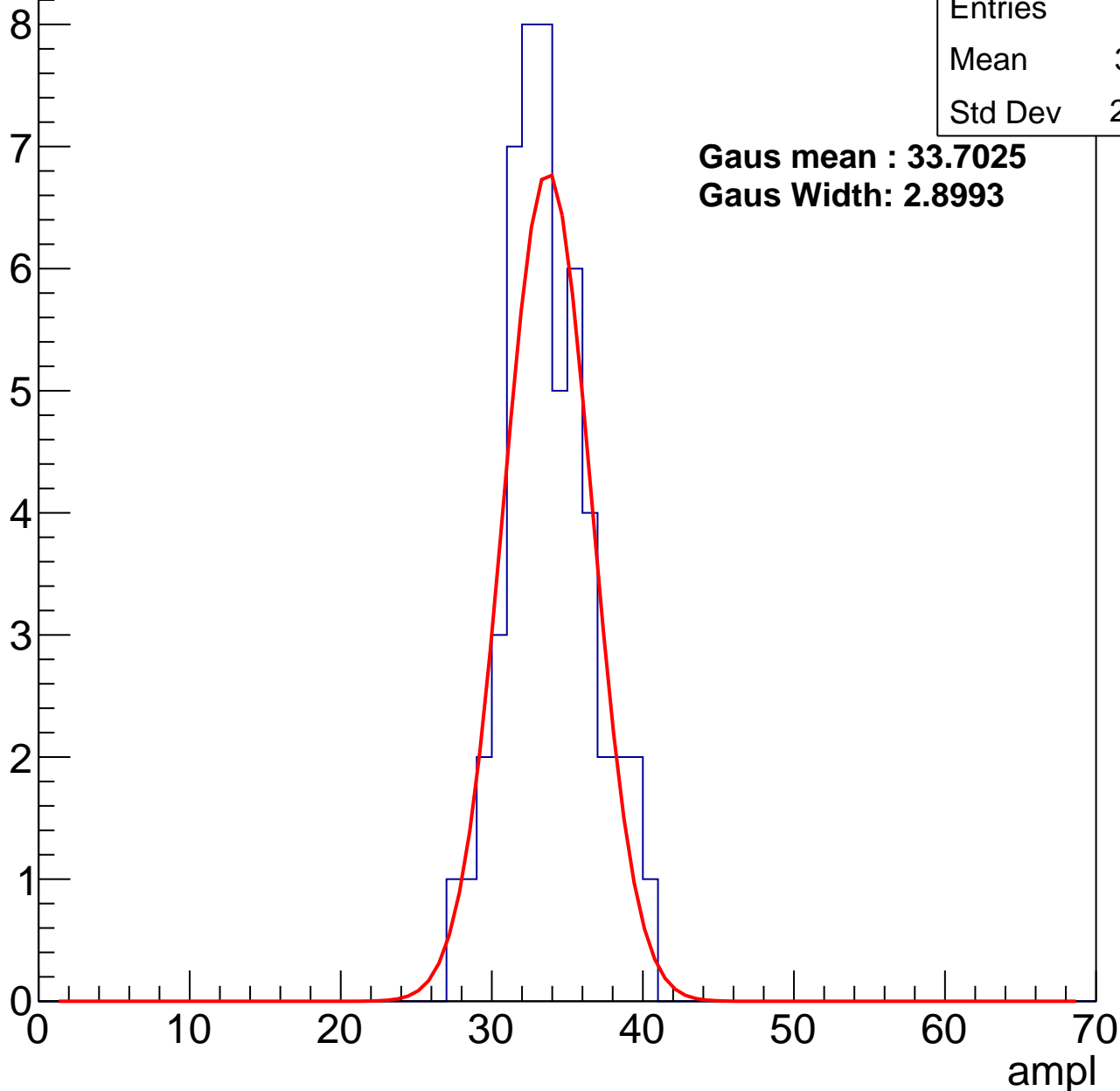
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	33.31
Std Dev	2.839

**Gaus mean : 33.7025**

**Gaus Width: 2.8993**



# B1L000S, U23-ch126, adc2

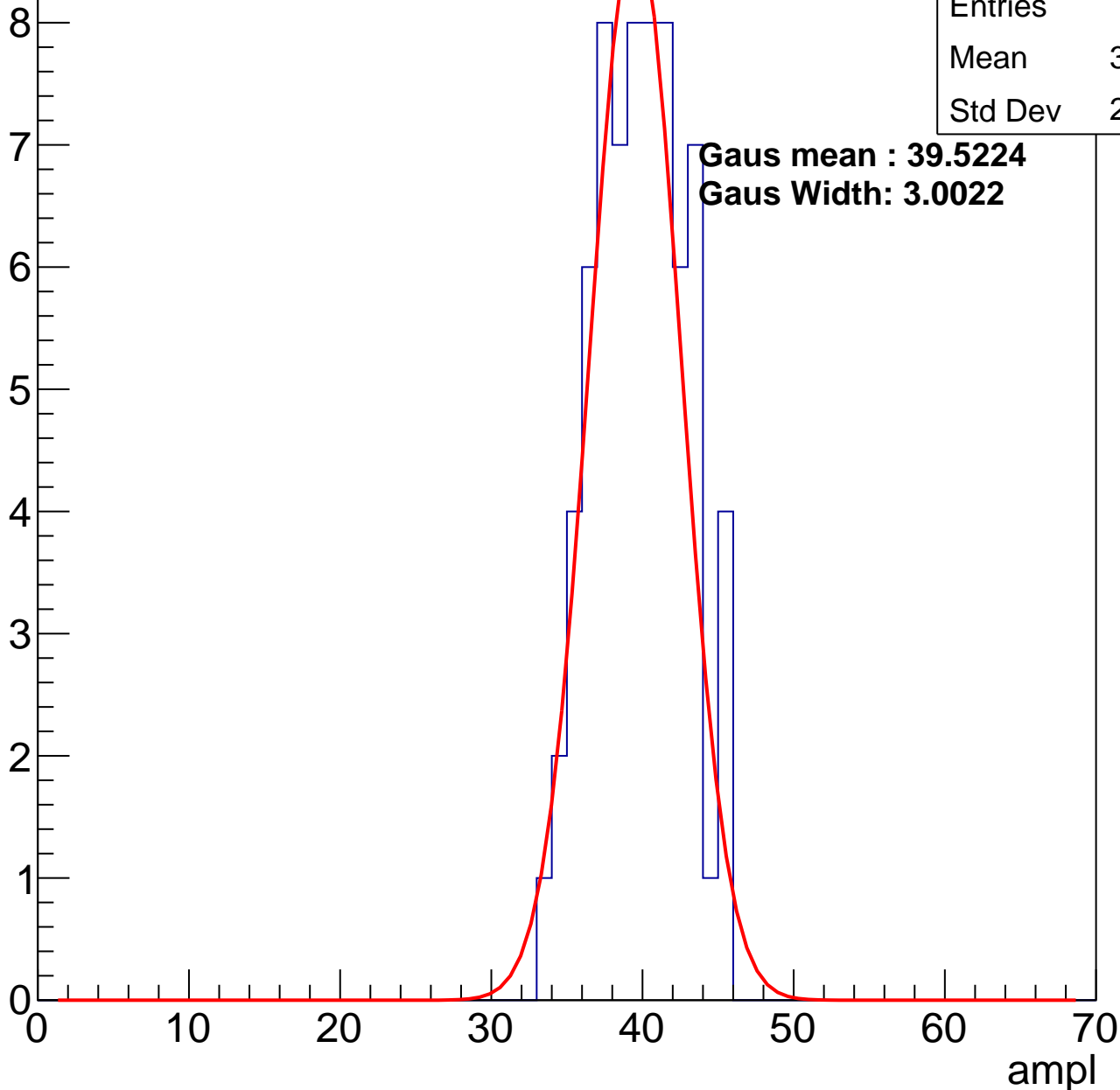
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	39.37
Std Dev	2.948

**Gaus mean : 39.5224**

**Gaus Width: 3.0022**

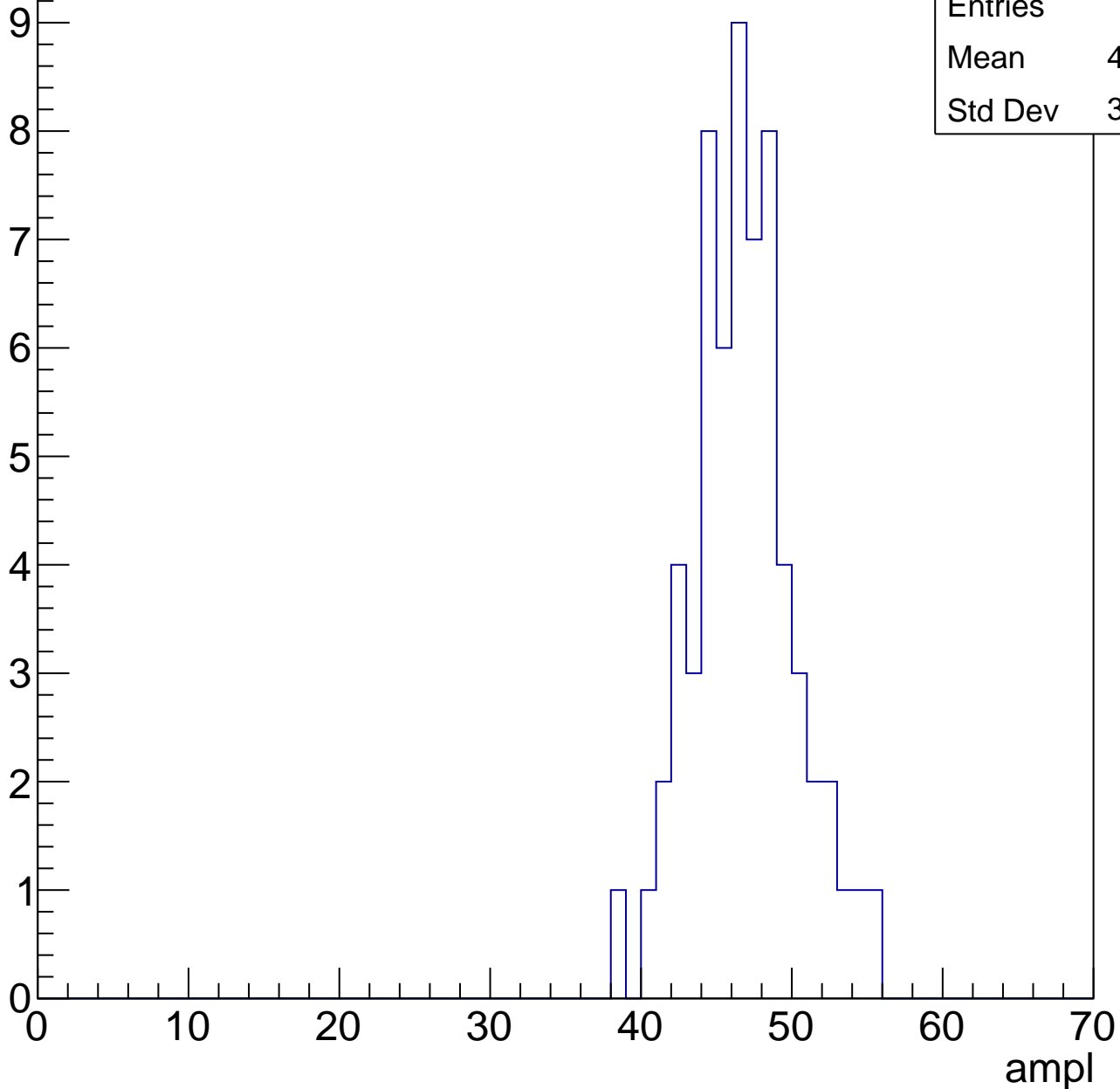


# B1L000S, U23-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	46.35
Std Dev	3.358

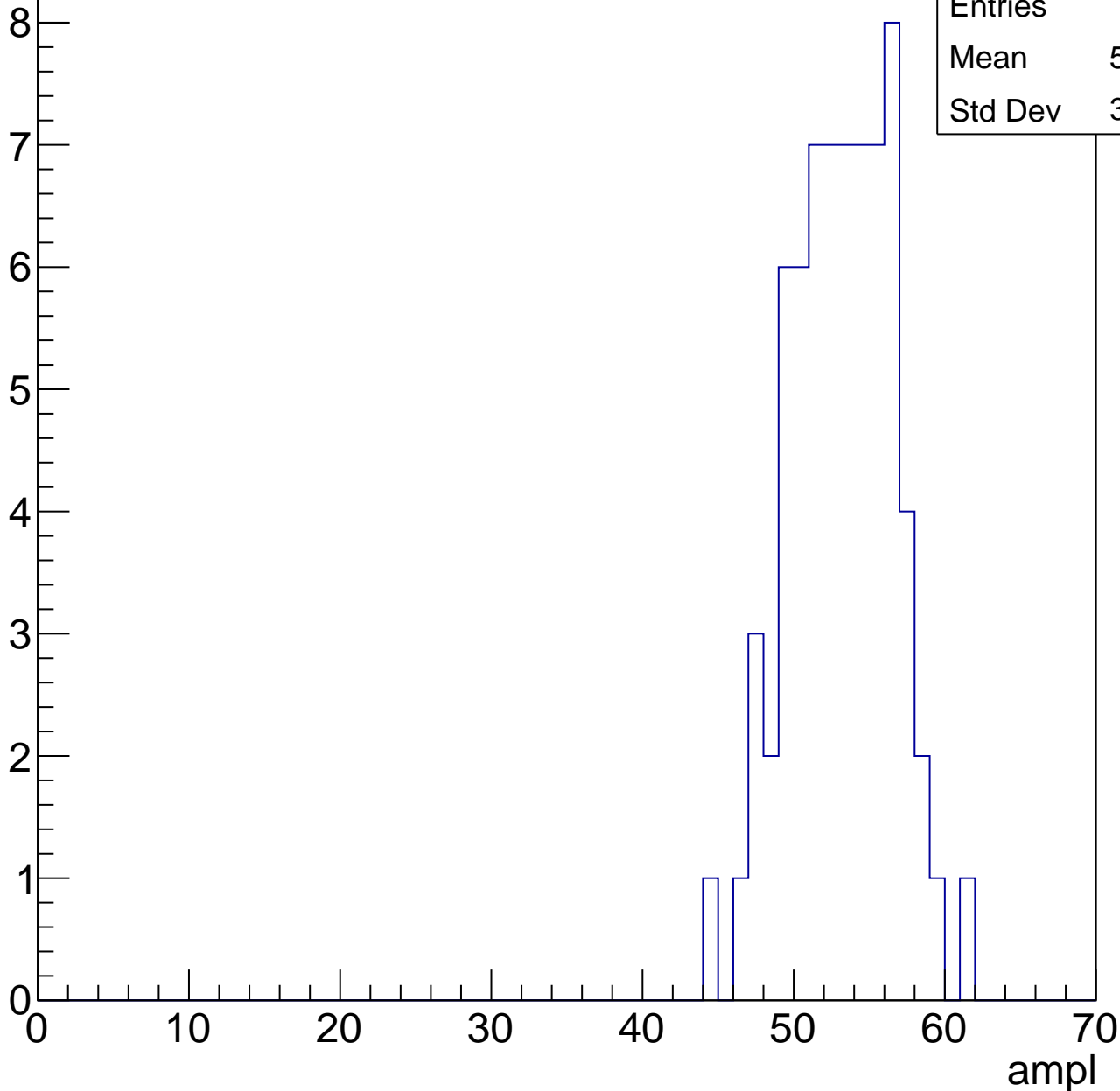


# B1L000S, U23-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

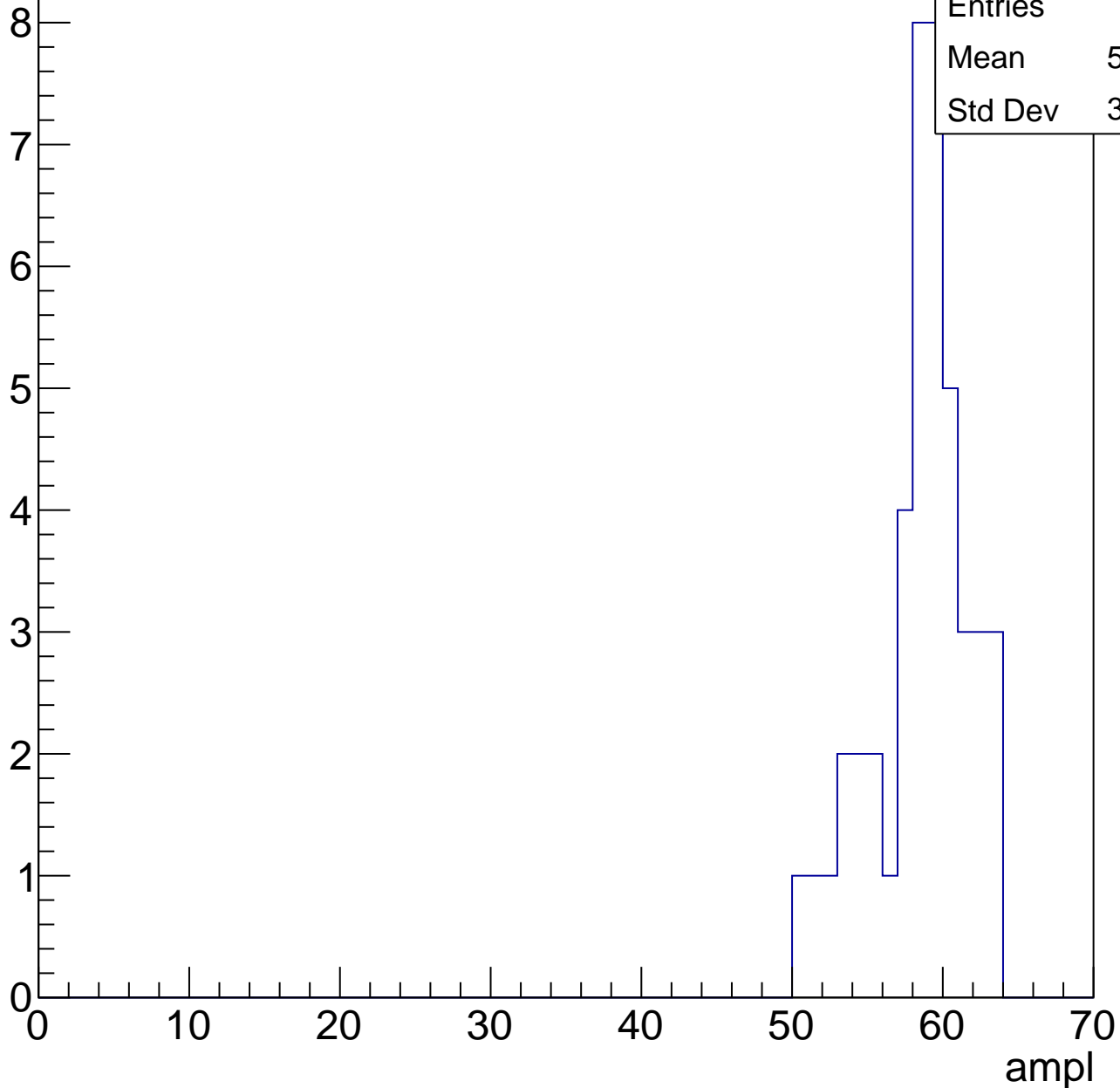
Entries	70
Mean	52.69
Std Dev	3.353



# B1L000S, U23-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

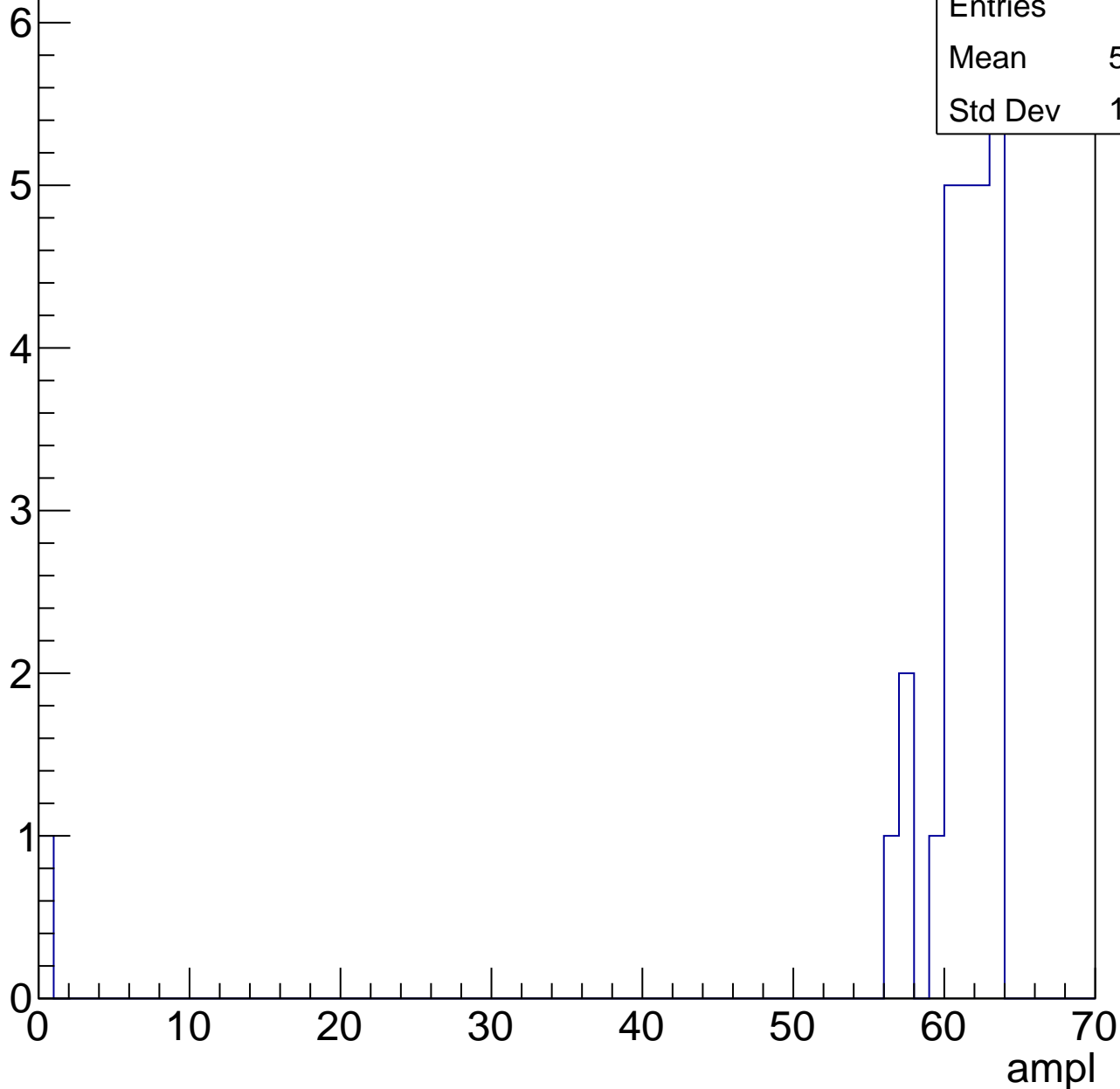


# B1L000S, U23-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.54
Std Dev	11.86

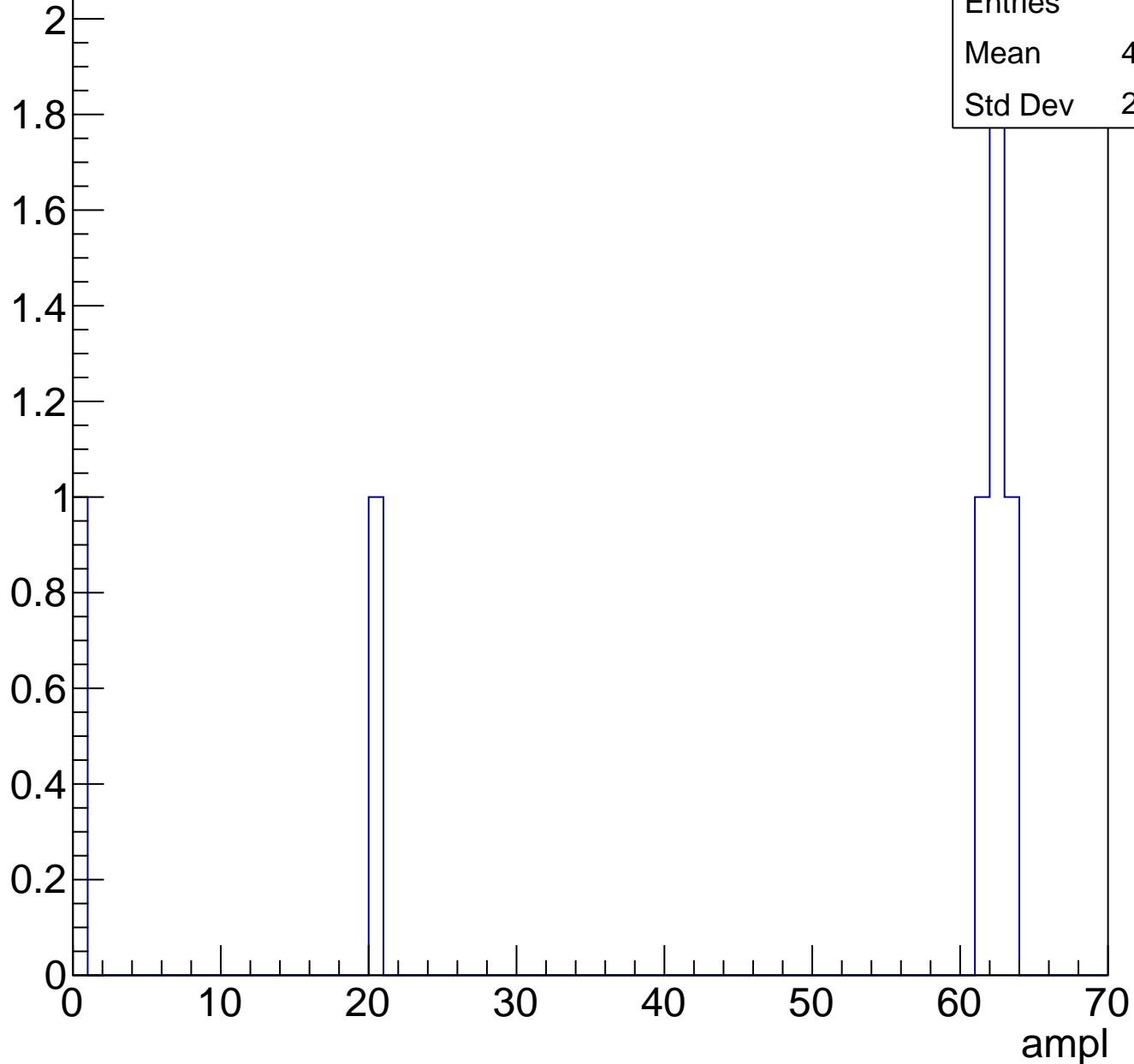




# B1L000S, U23-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U23-ch127, adc0

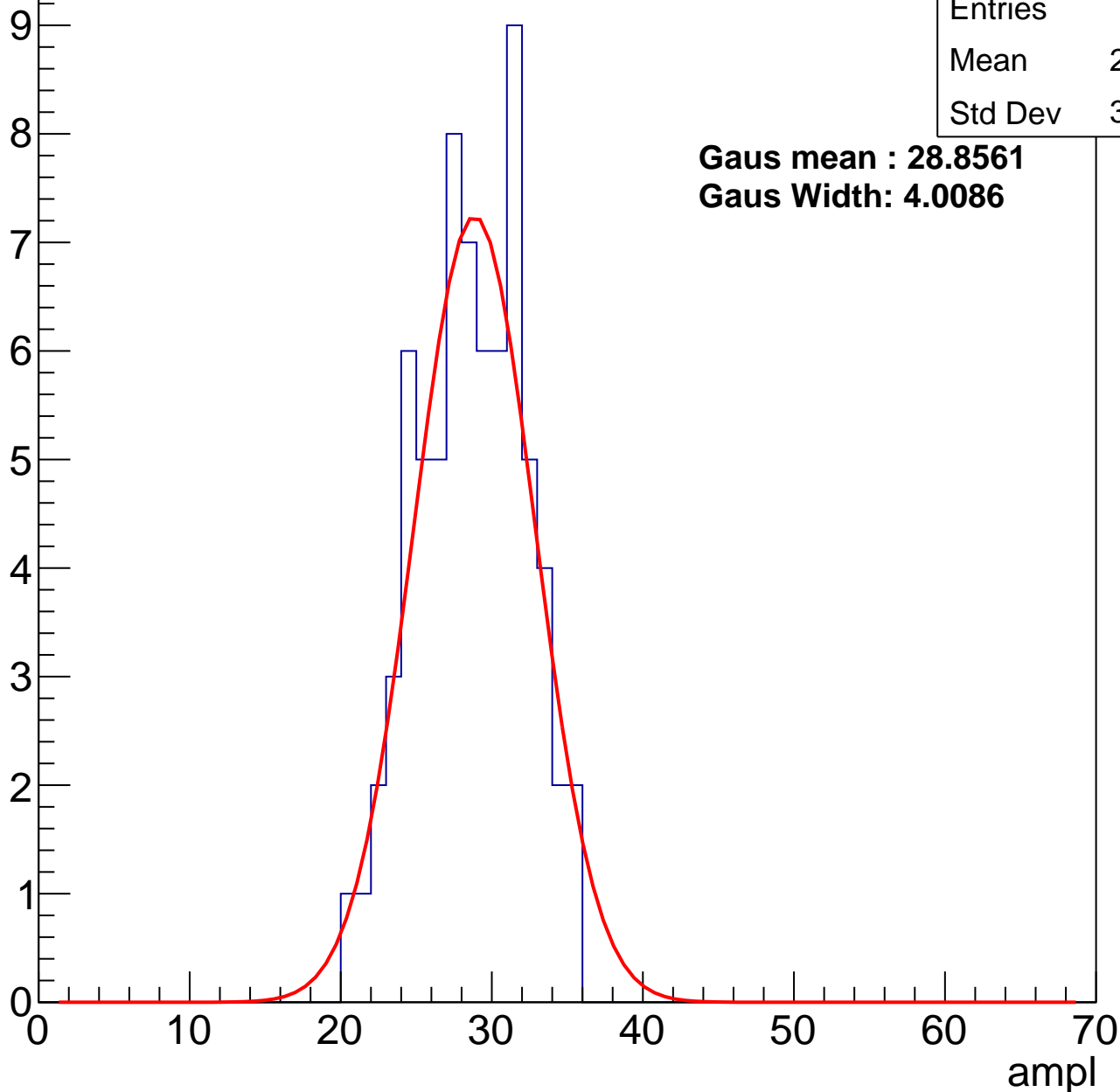
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	28.17
Std Dev	3.512

**Gaus mean : 28.8561**

**Gaus Width: 4.0086**



# B1L000S, U23-ch127, adc1

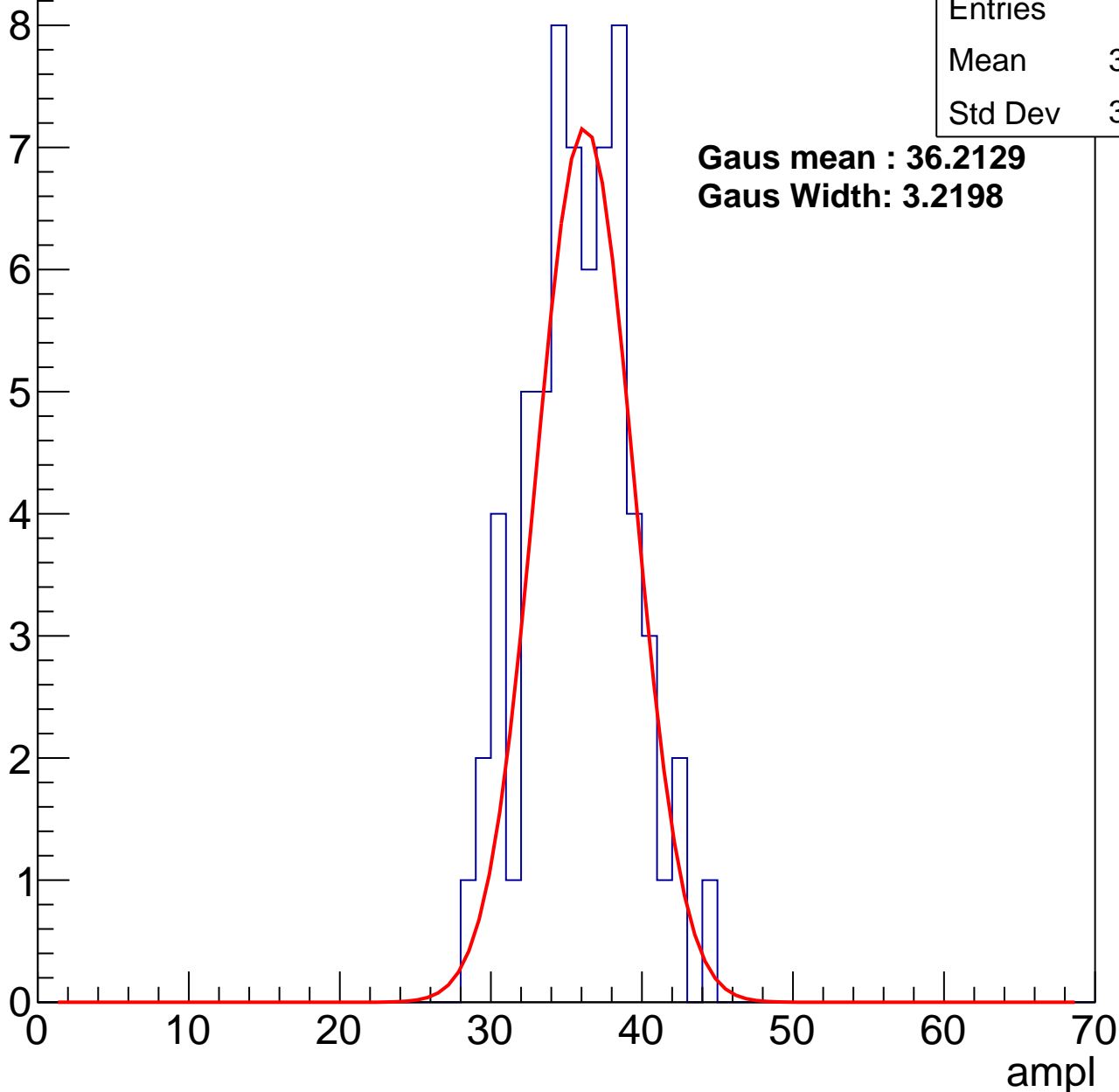
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.43
Std Dev	3.415

**Gaus mean : 36.2129**

**Gaus Width: 3.2198**



# B1L000S, U23-ch127, adc2

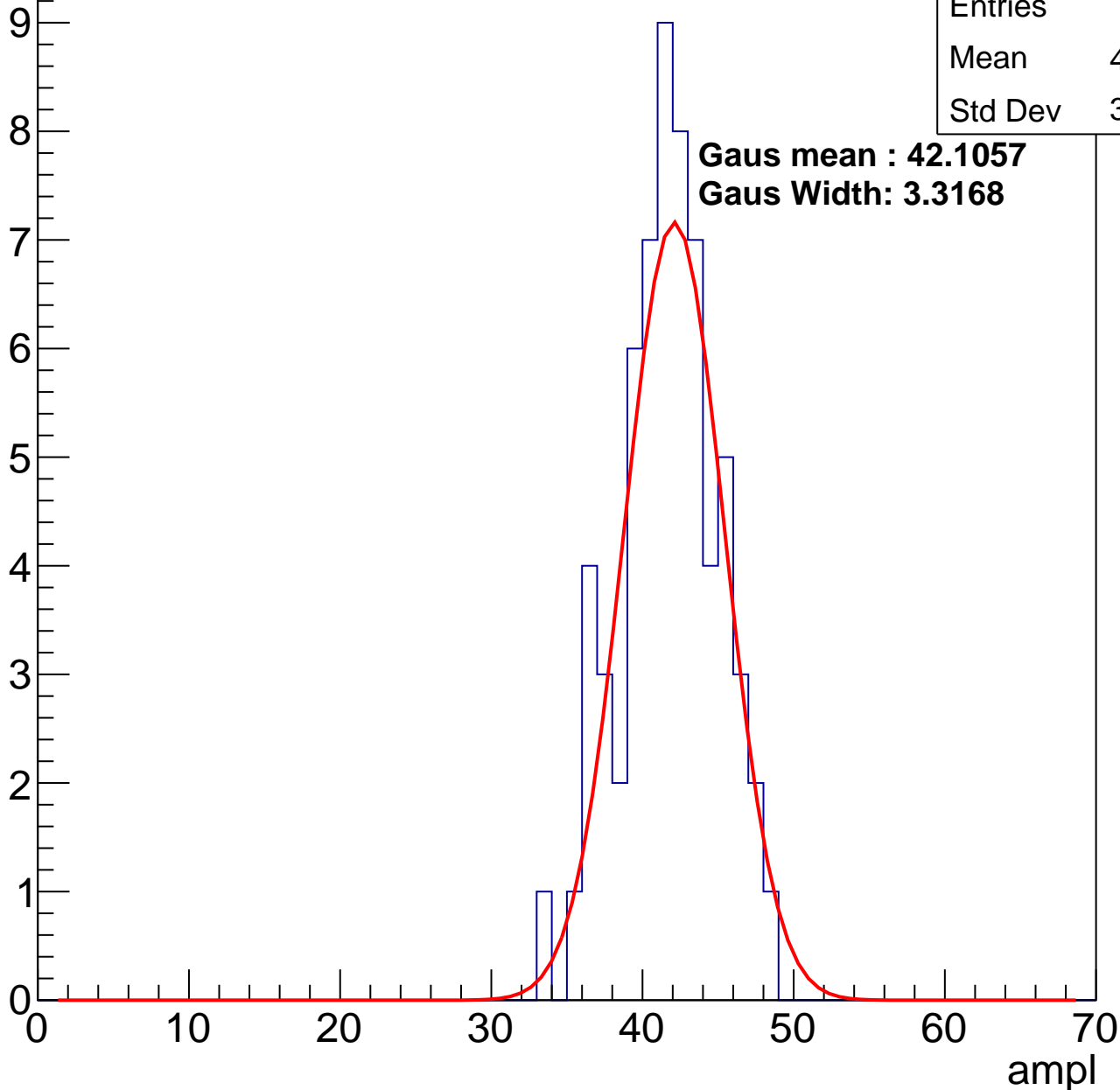
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.27
Std Dev	3.188

**Gaus mean : 42.1057**

**Gaus Width: 3.3168**

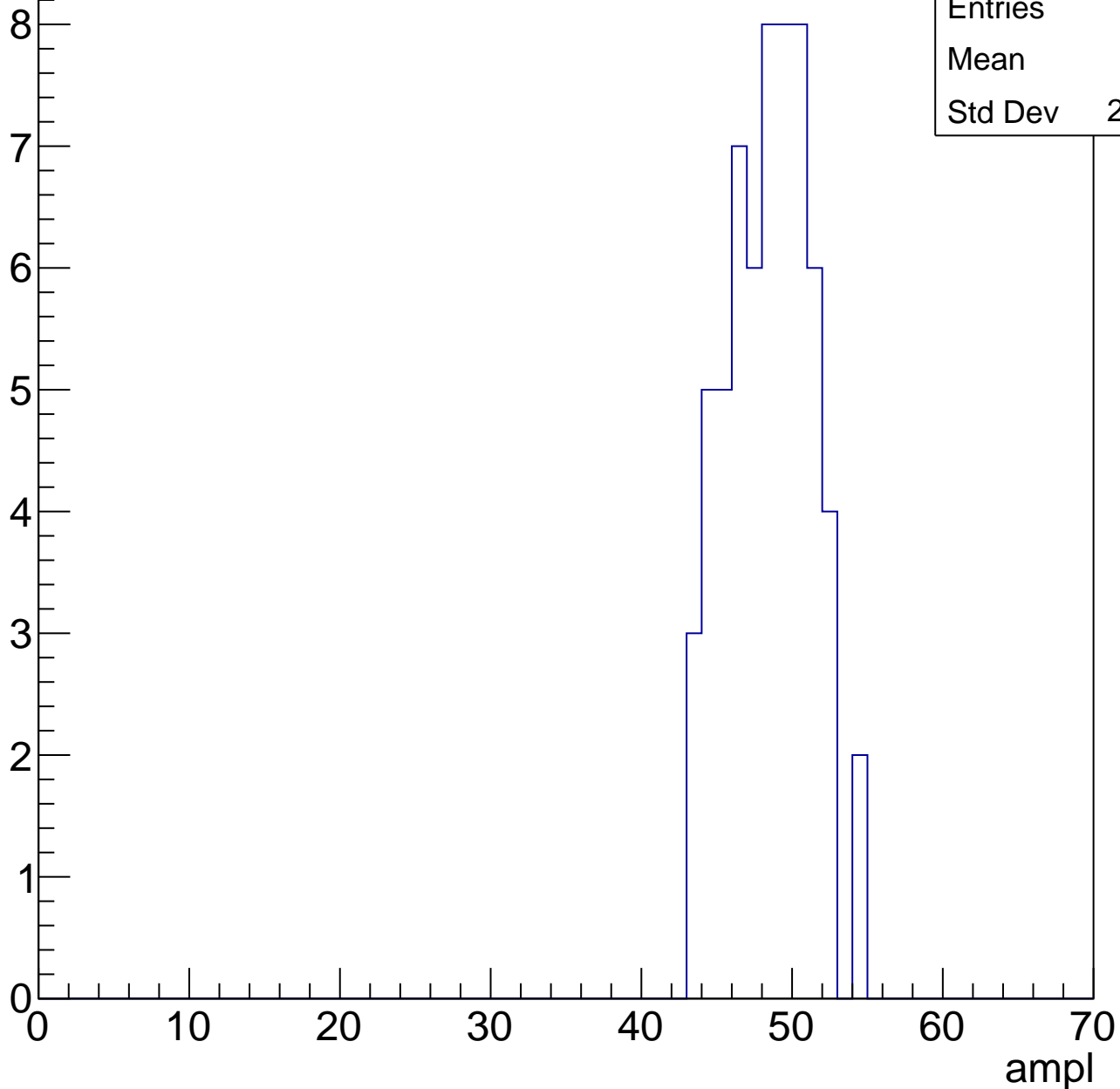


# B1L000S, U23-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	48
Std Dev	2.736

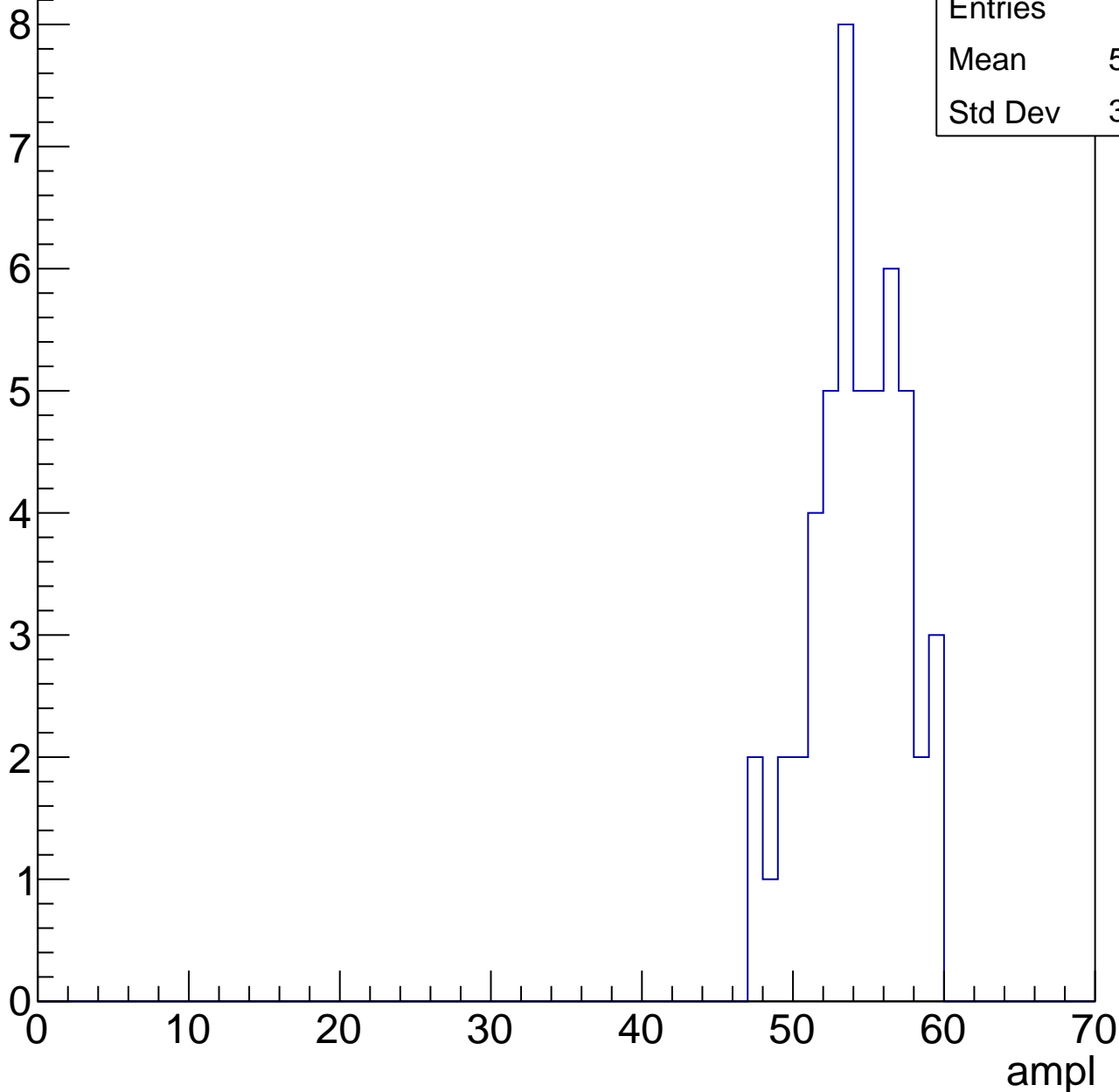


# B1L000S, U23-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.74
Std Dev	3.025

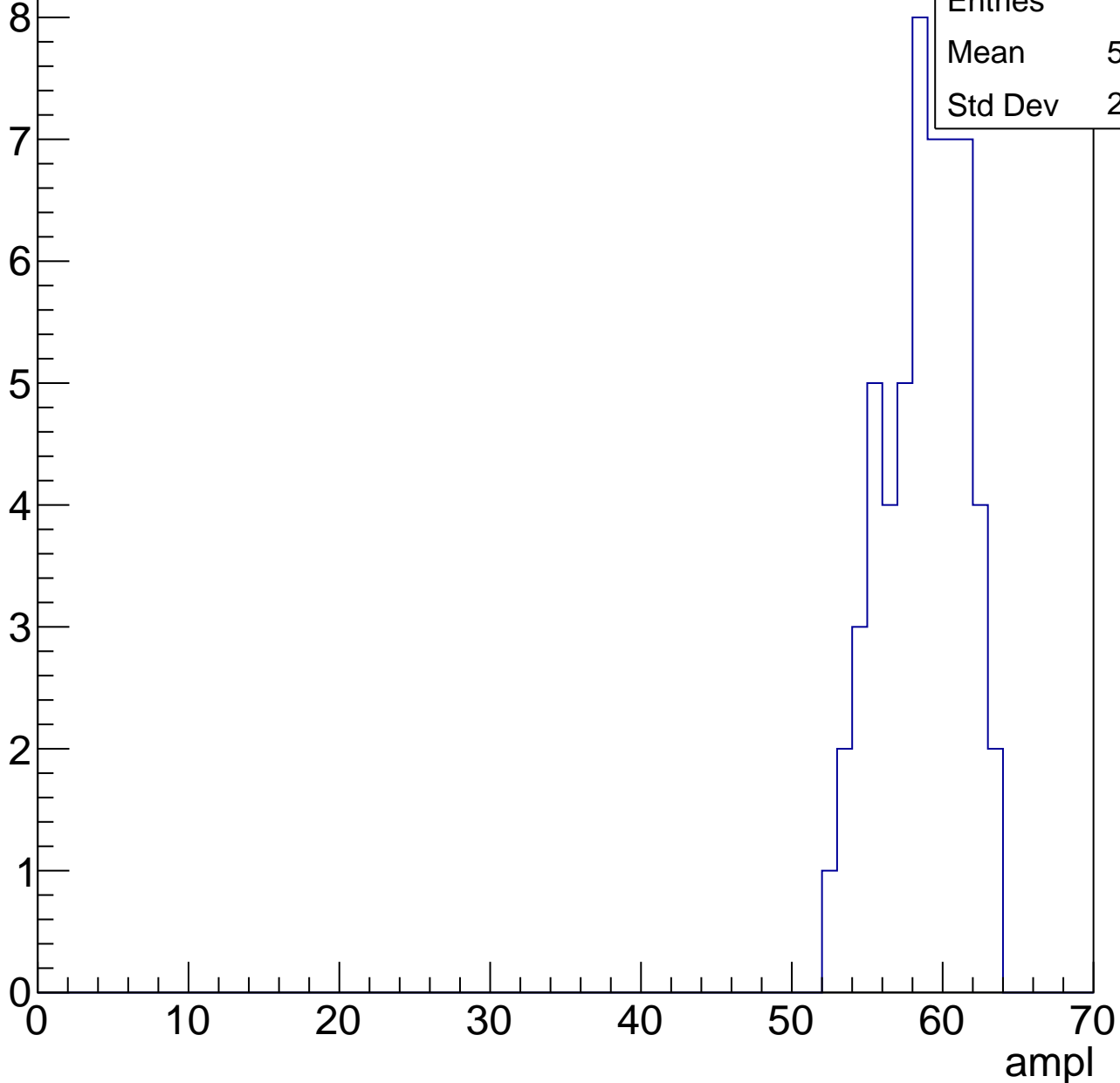


# B1L000S, U23-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.22
Std Dev	2.728

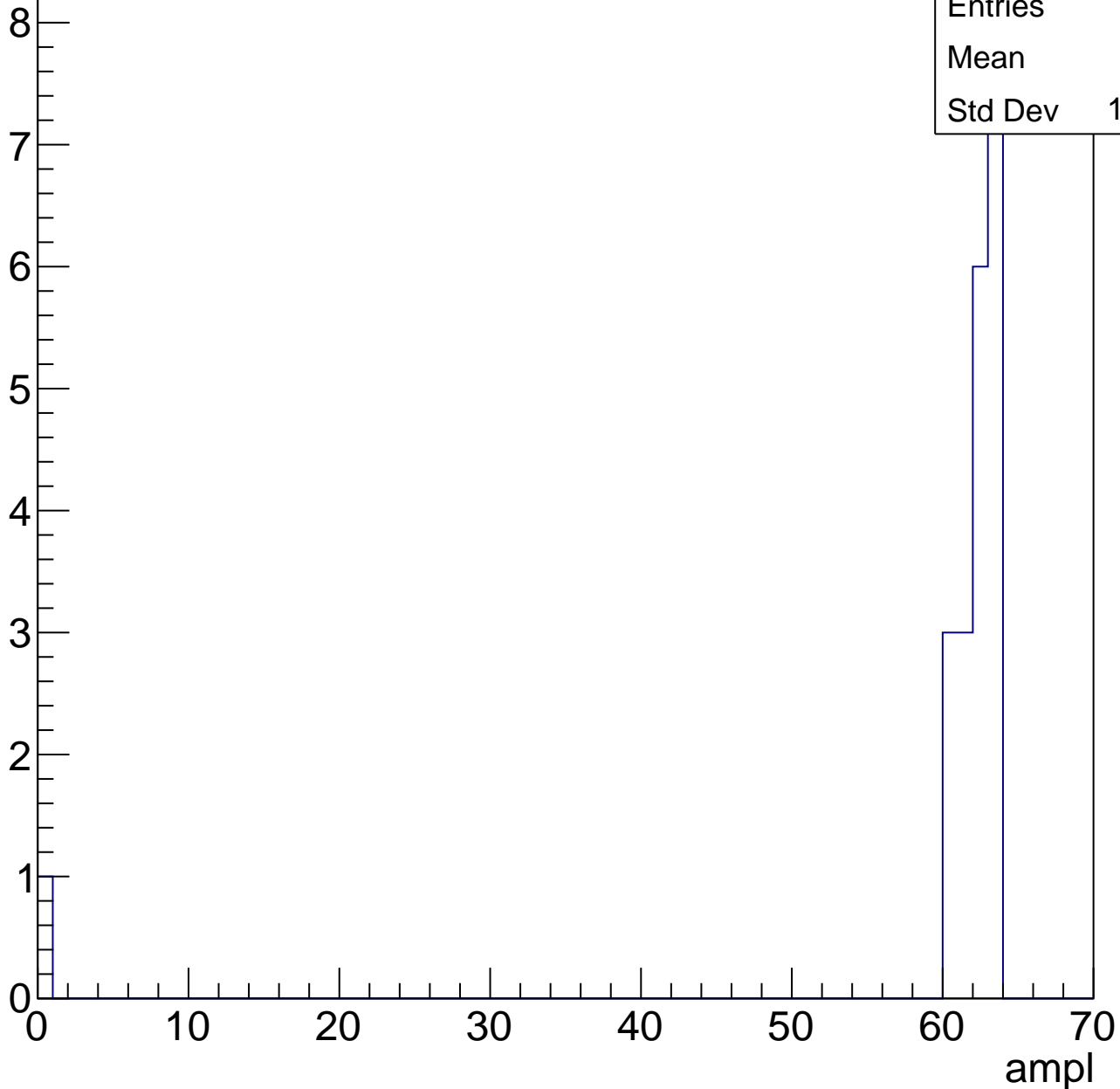


# B1L000S, U23-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	59
Std Dev	13.23





# B1L000S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

