



# B1L000S, U24-ch0, adc0

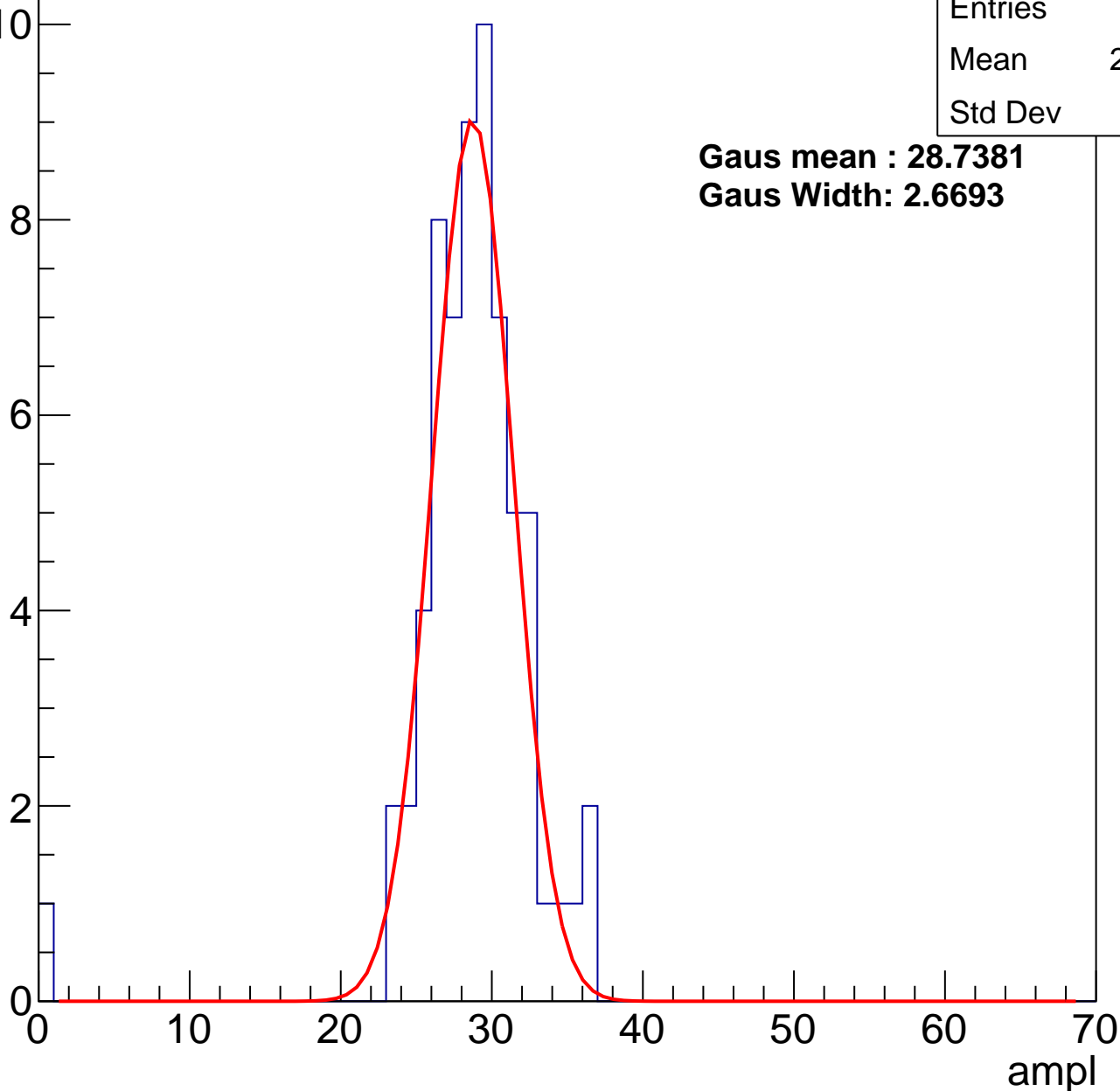
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.18
Std Dev	4.54

**Gaus mean : 28.7381**

**Gaus Width: 2.6693**



# B1L000S, U24-ch0, adc1

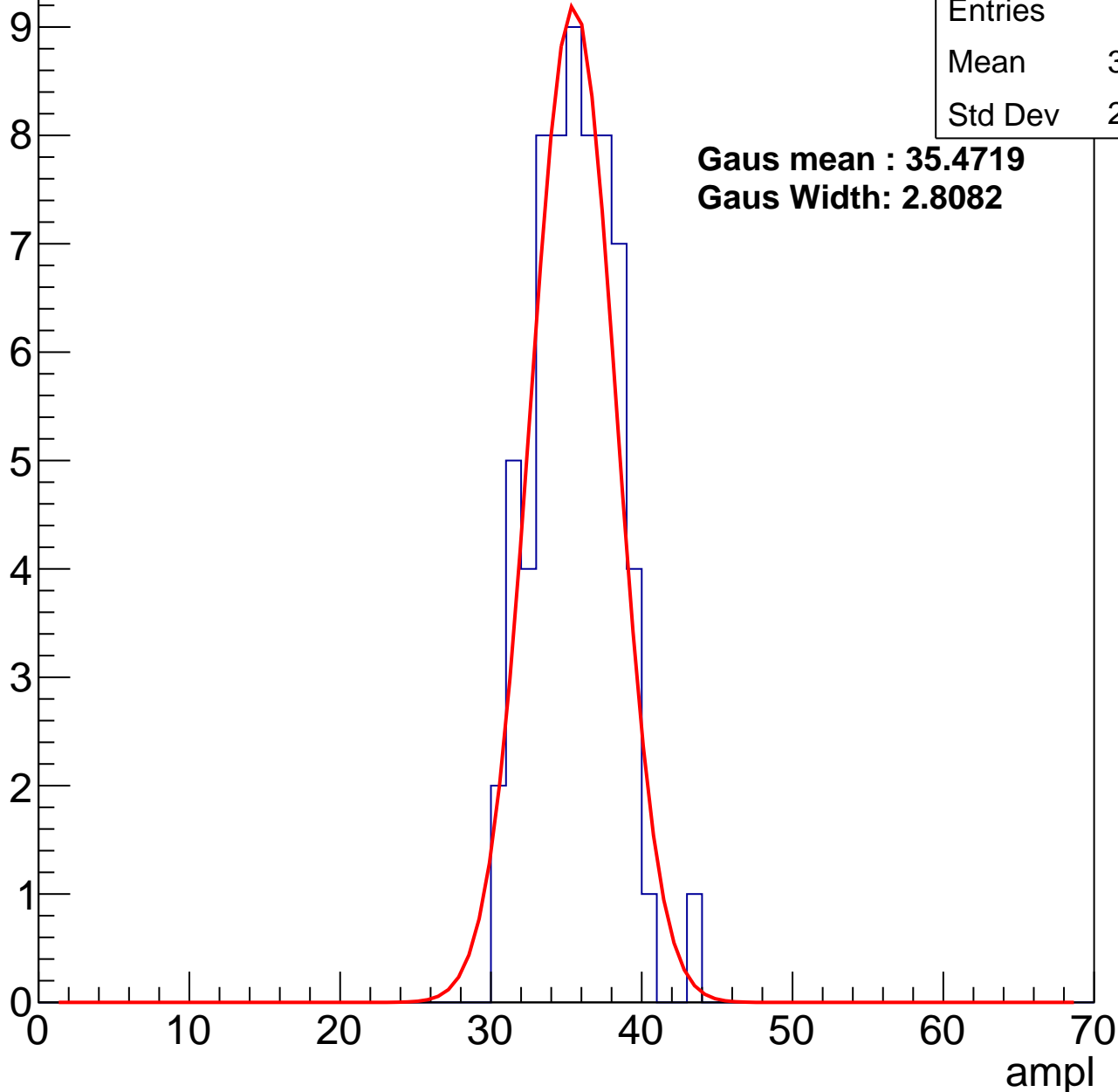
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.12
Std Dev	2.663

**Gaus mean : 35.4719**

**Gaus Width: 2.8082**



# B1L000S, U24-ch0, adc2

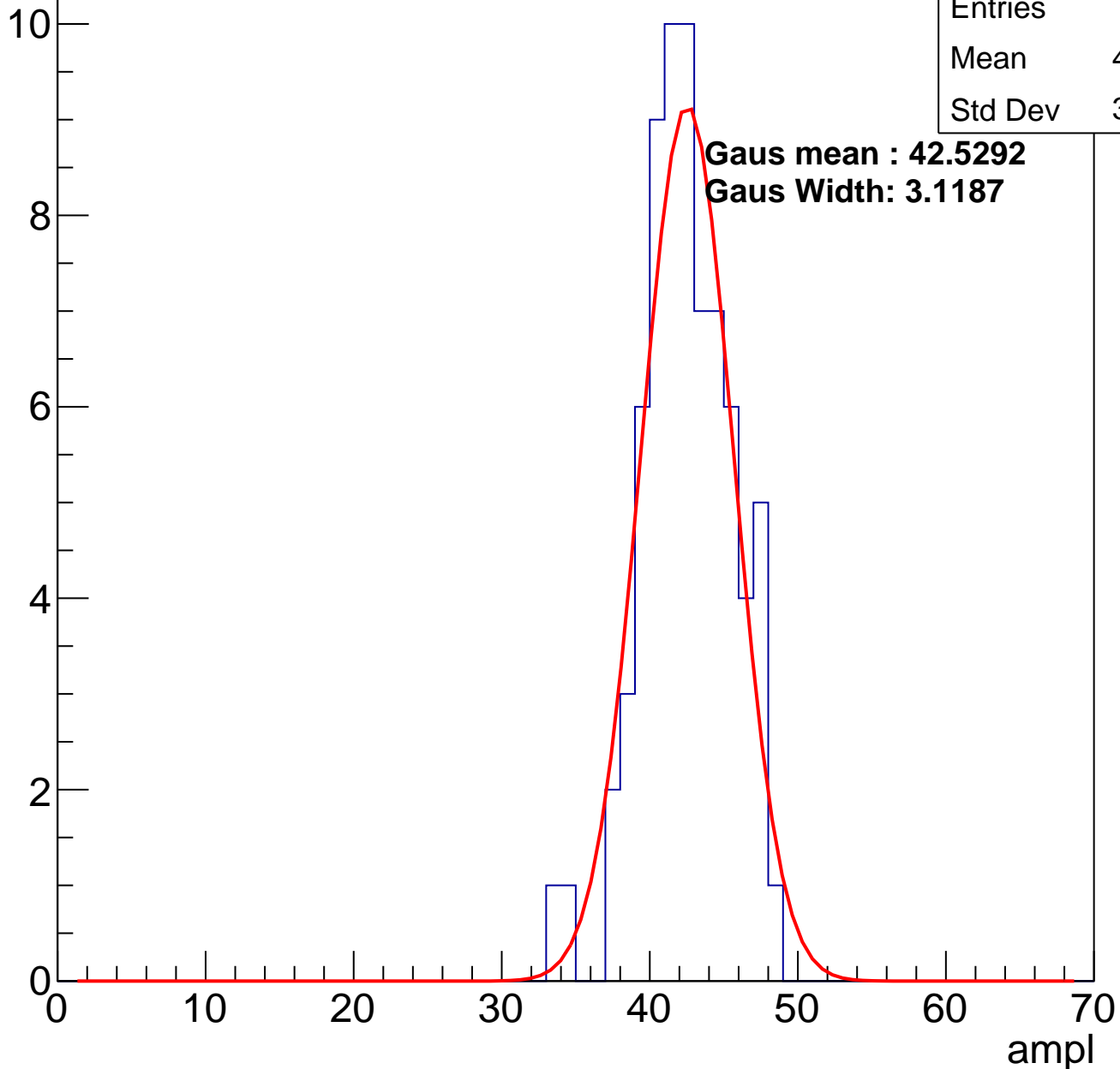
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	42.01
Std Dev	3.021

**Gaus mean : 42.5292**

**Gaus Width: 3.1187**

Entry

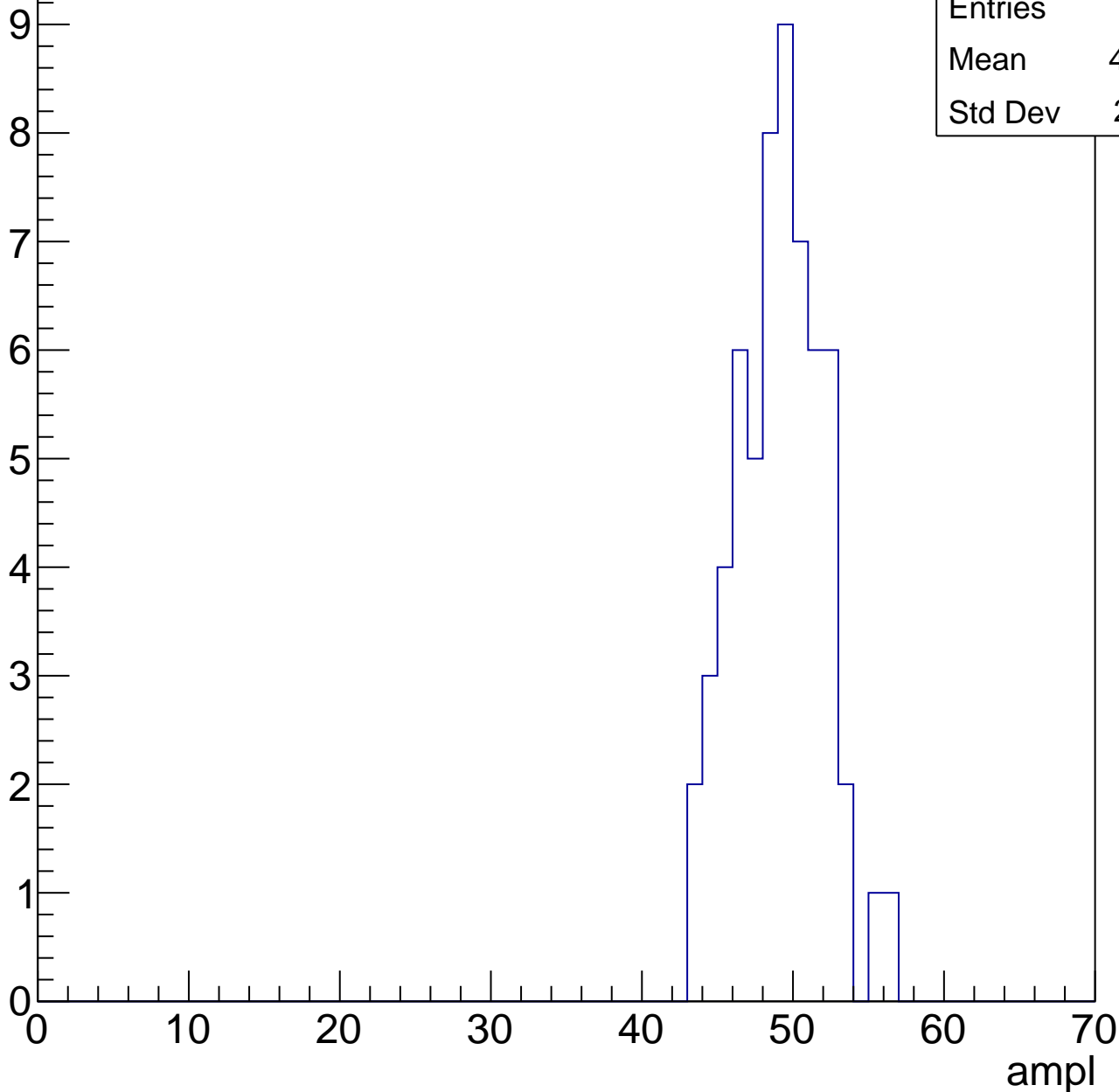


# B1L000S, U24-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

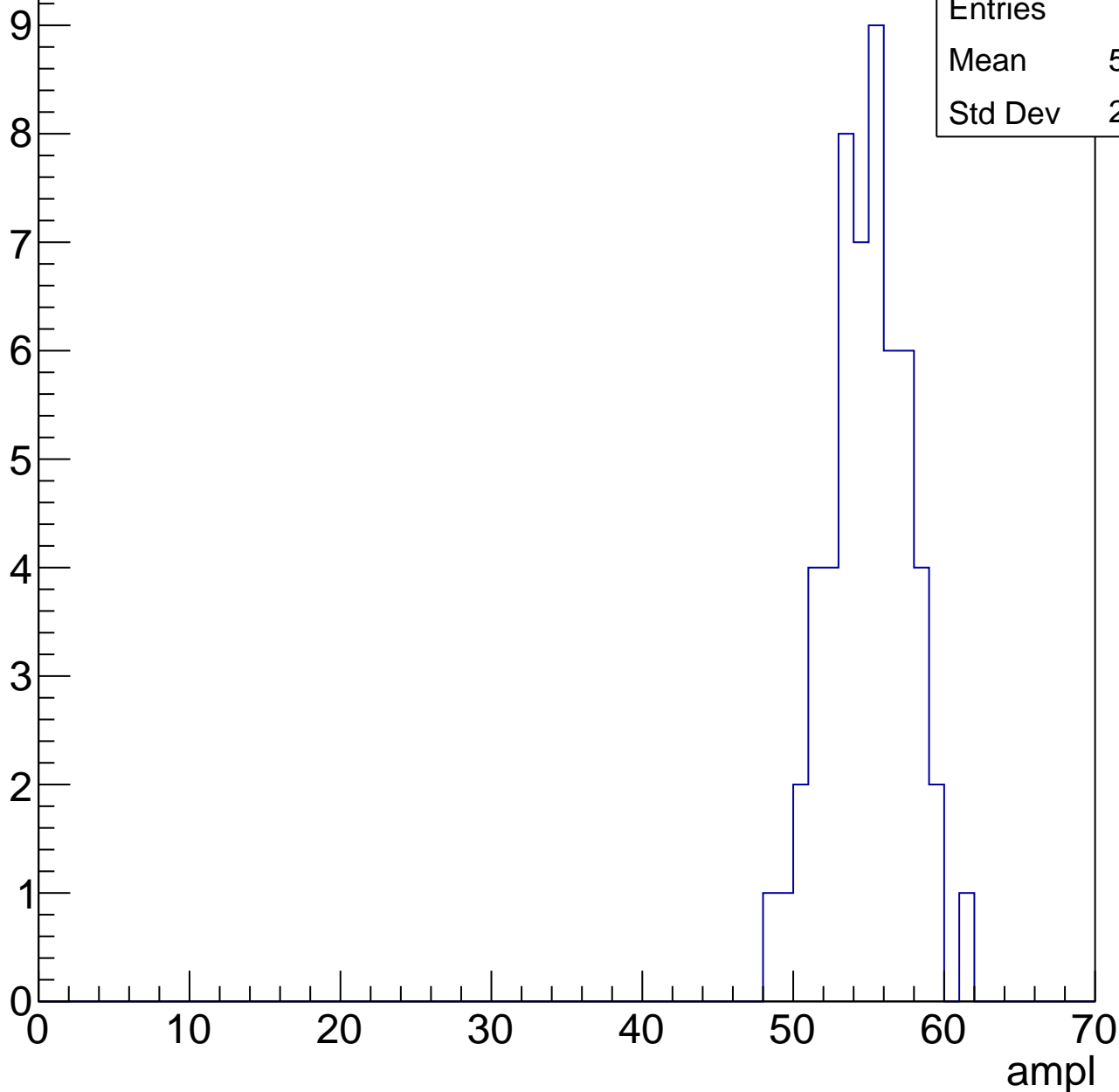
Entries	60
Mean	48.65
Std Dev	2.851



# B1L000S, U24-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

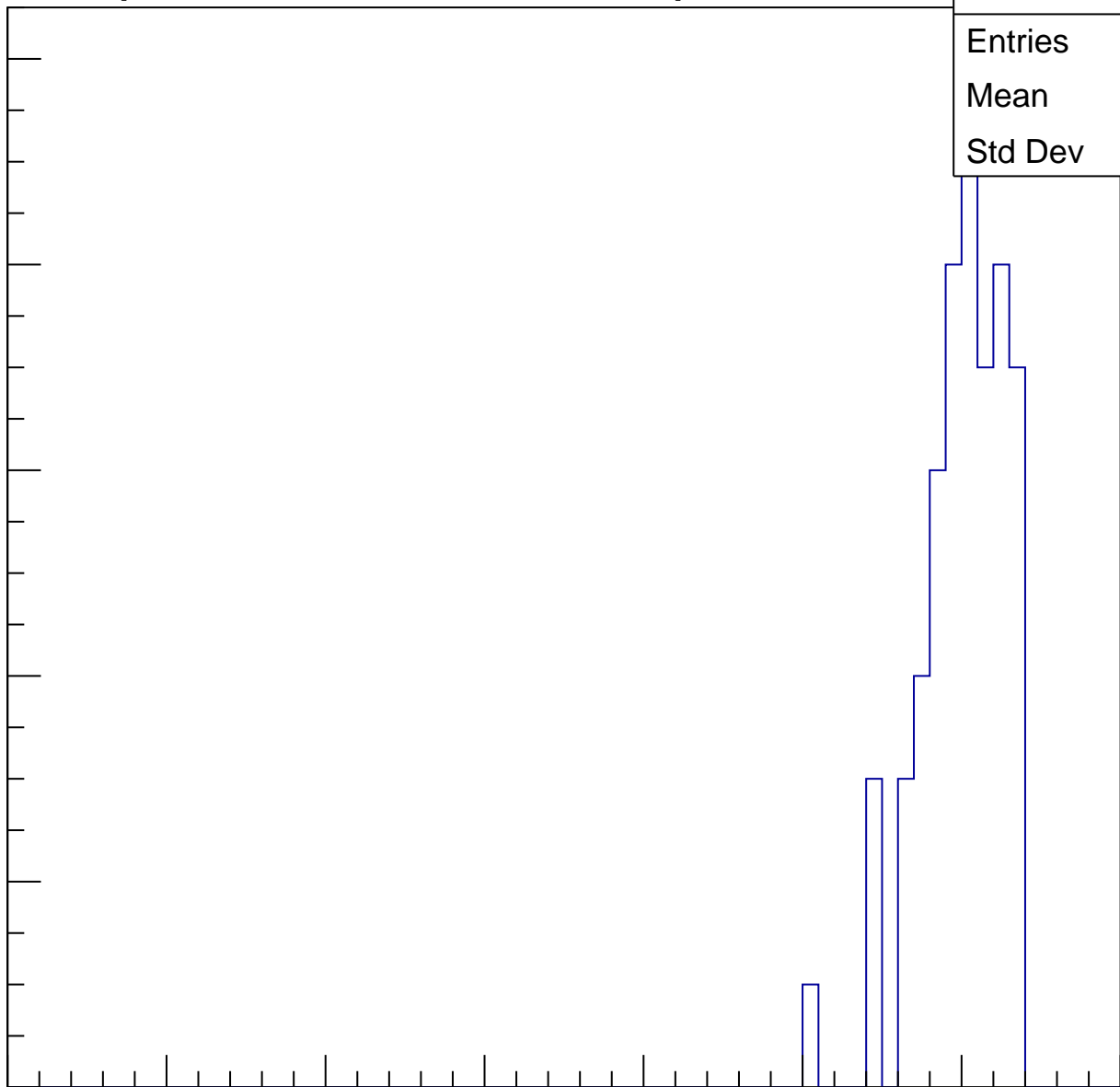
40

50

60

ampl

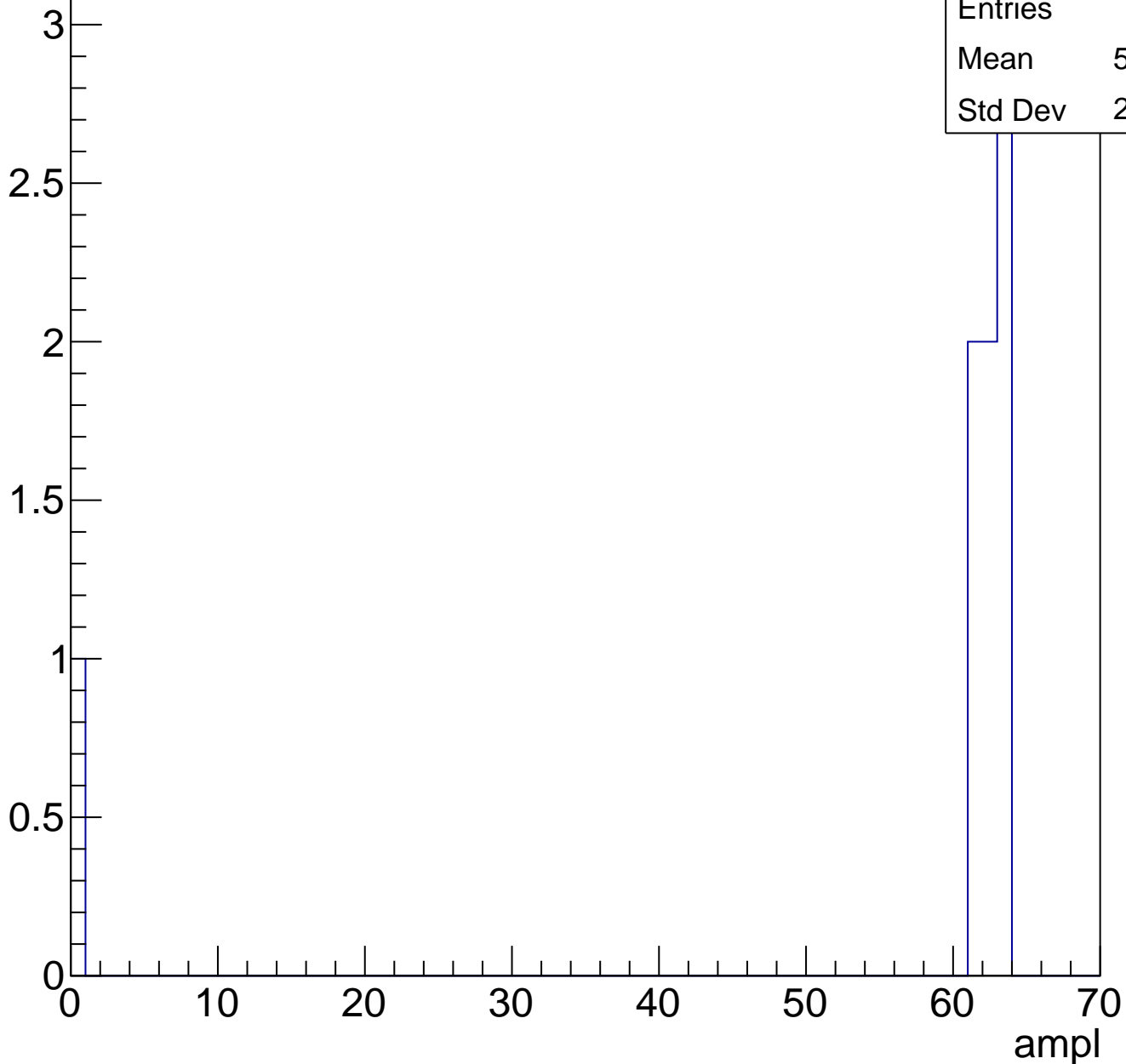
Entries	57
Mean	59.51
Std Dev	2.689



# B1L000S, U24-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

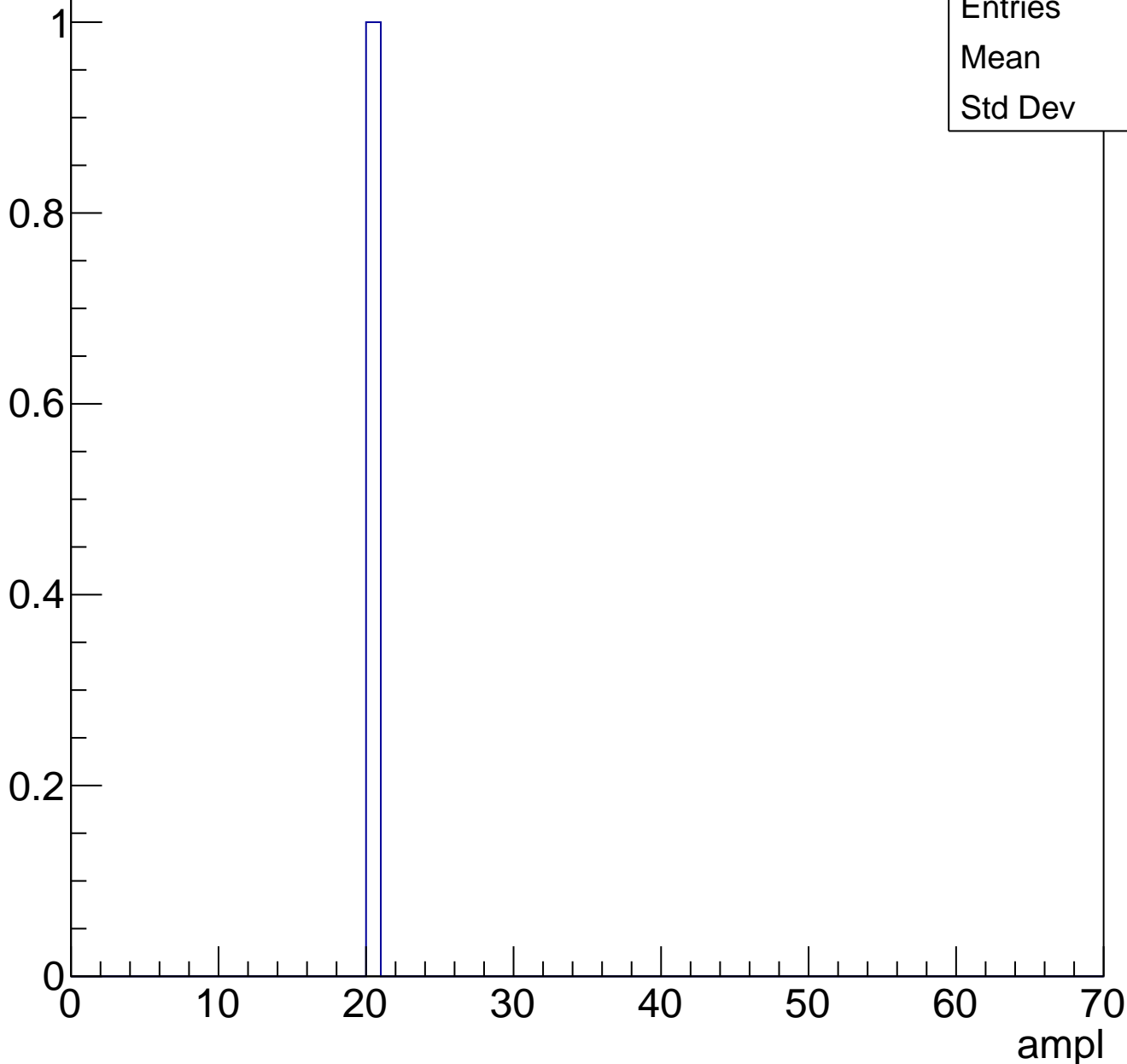




# B1L000S, U24-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch1, adc0

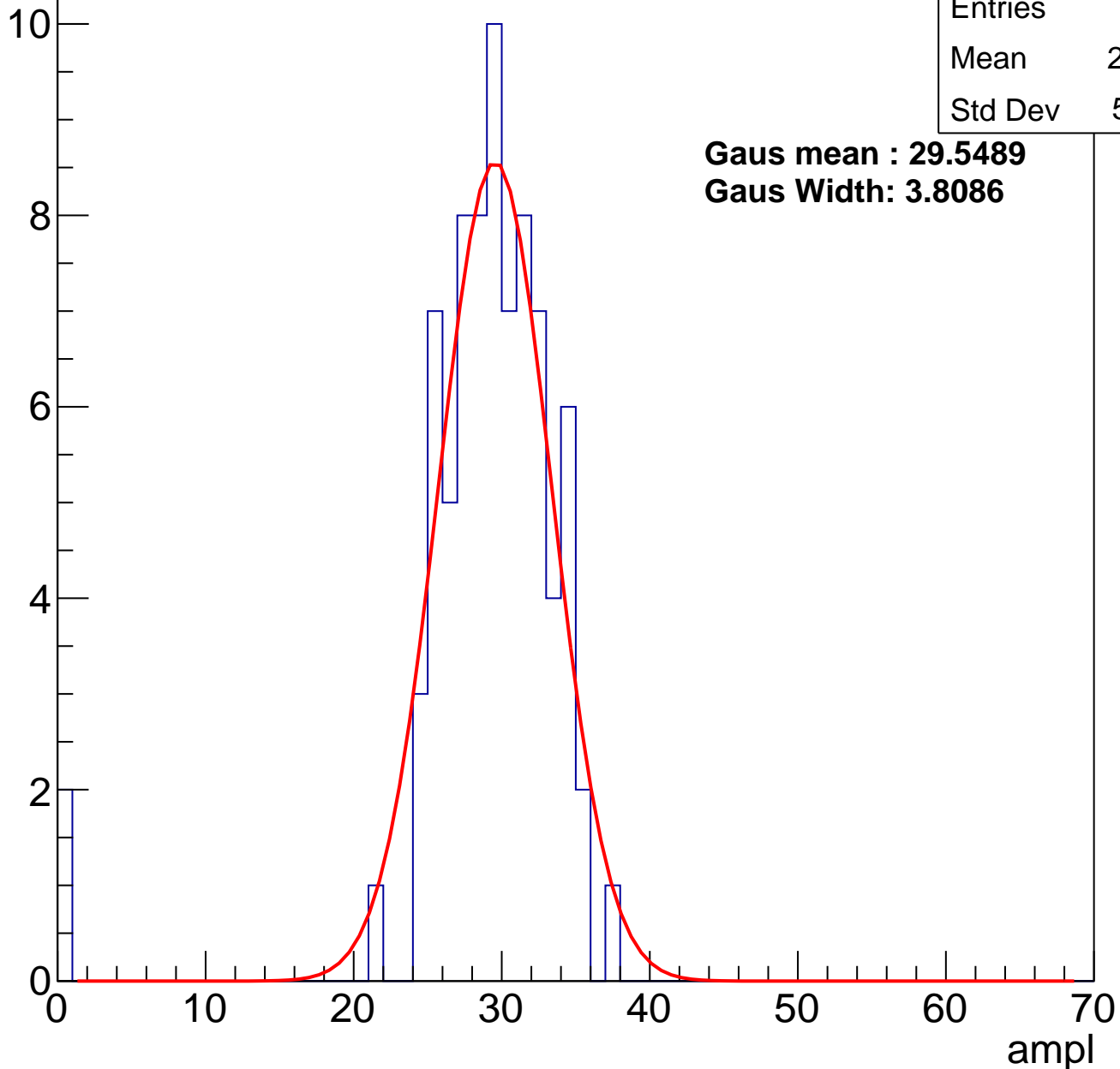
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	28.52
Std Dev	5.571

**Gaus mean : 29.5489**

**Gaus Width: 3.8086**

Entry



# B1L000S, U24-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	36.93
Std Dev	3.578

**Gaus mean : 37.3983**

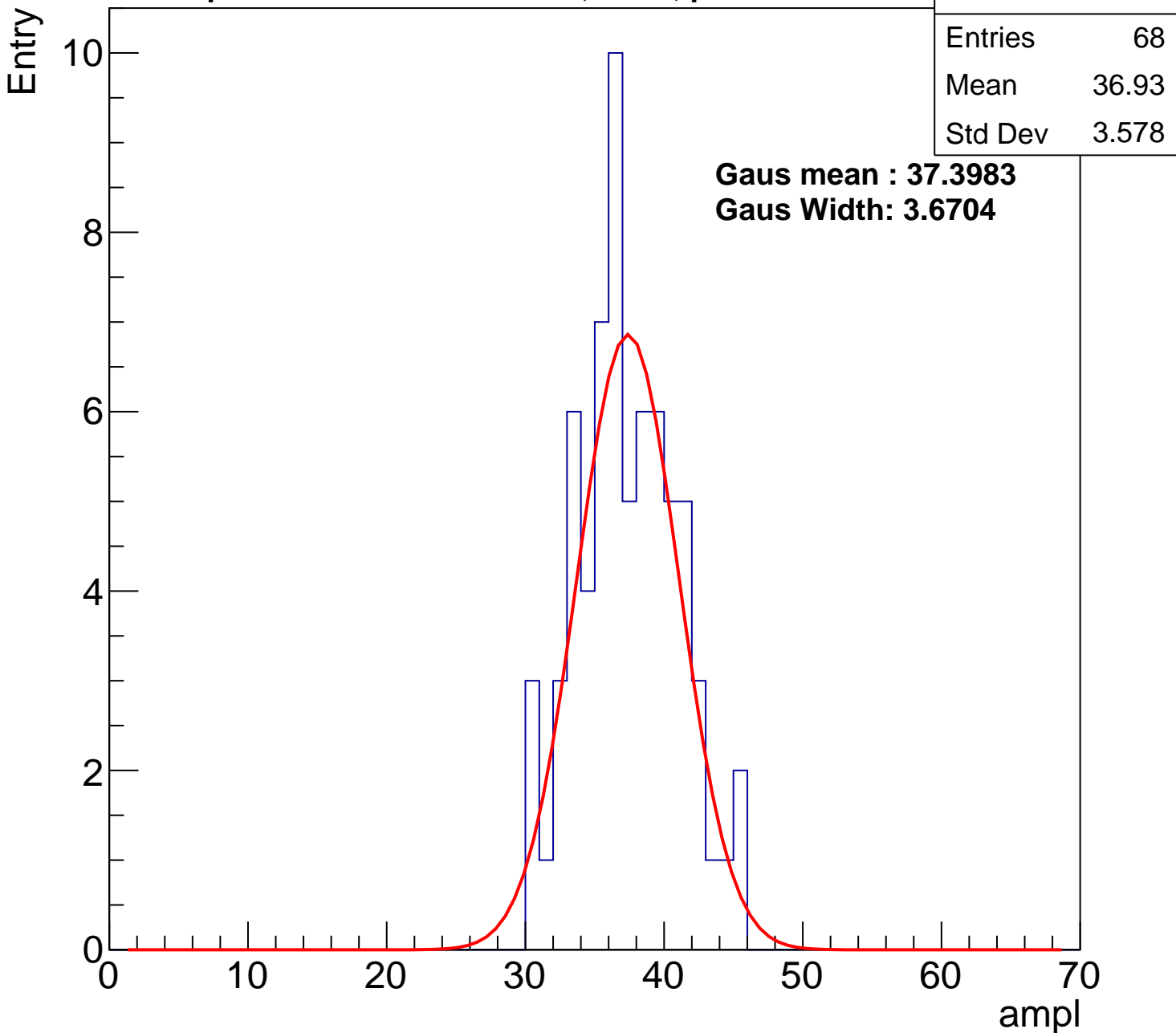
**Gaus Width: 3.6704**

Entry

10  
8  
6  
4  
2  
0

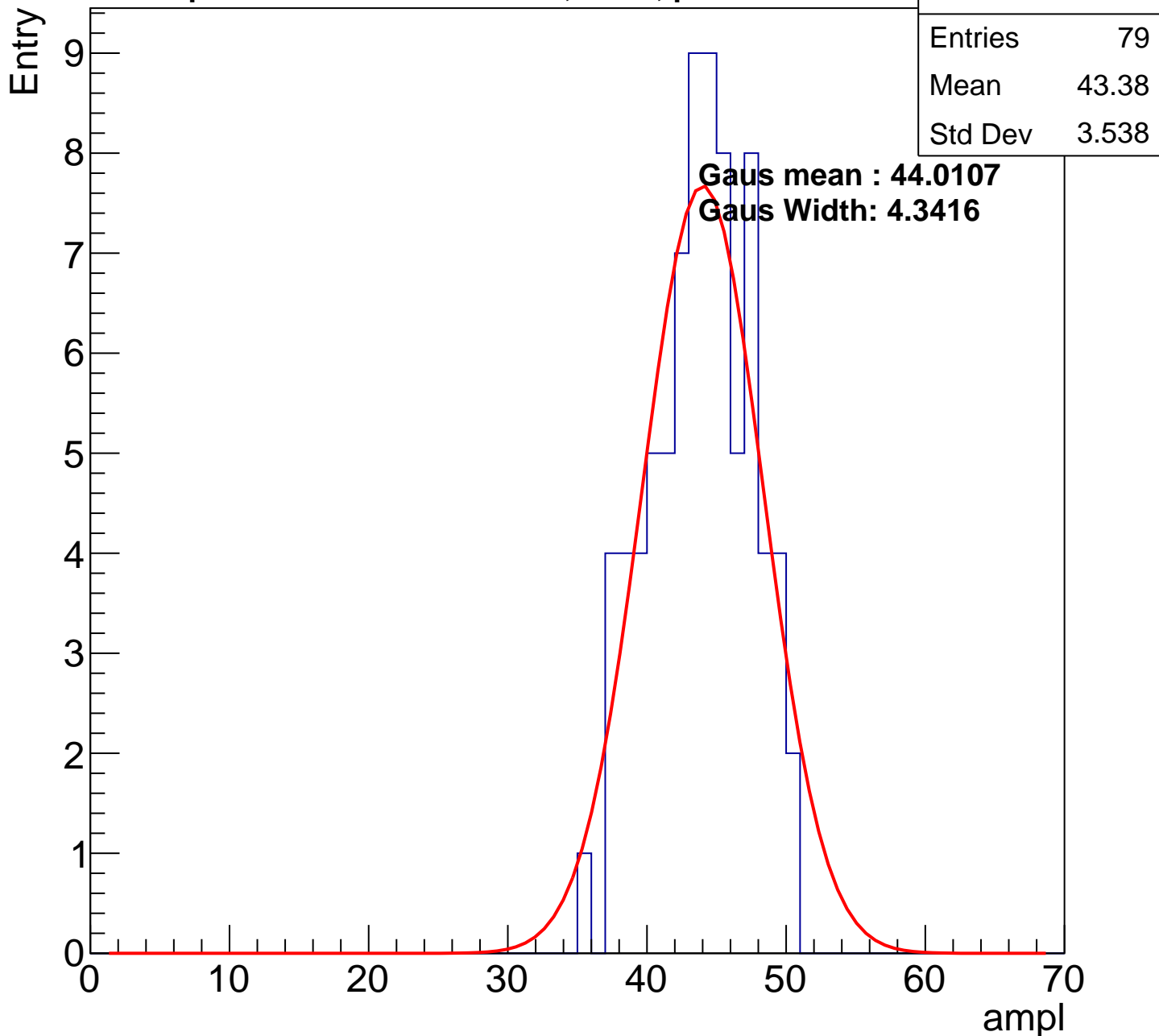
0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

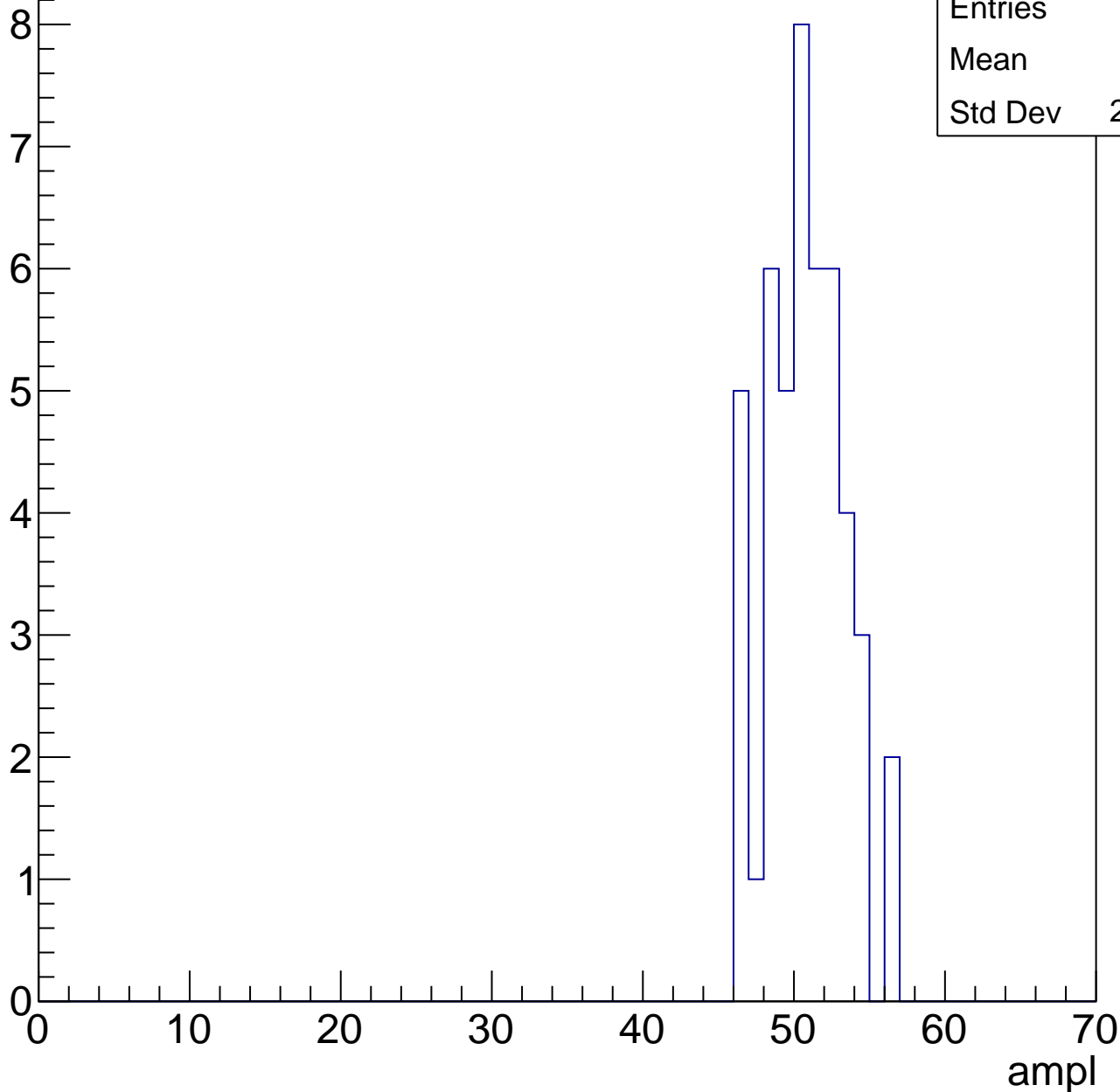


# B1L000S, U24-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

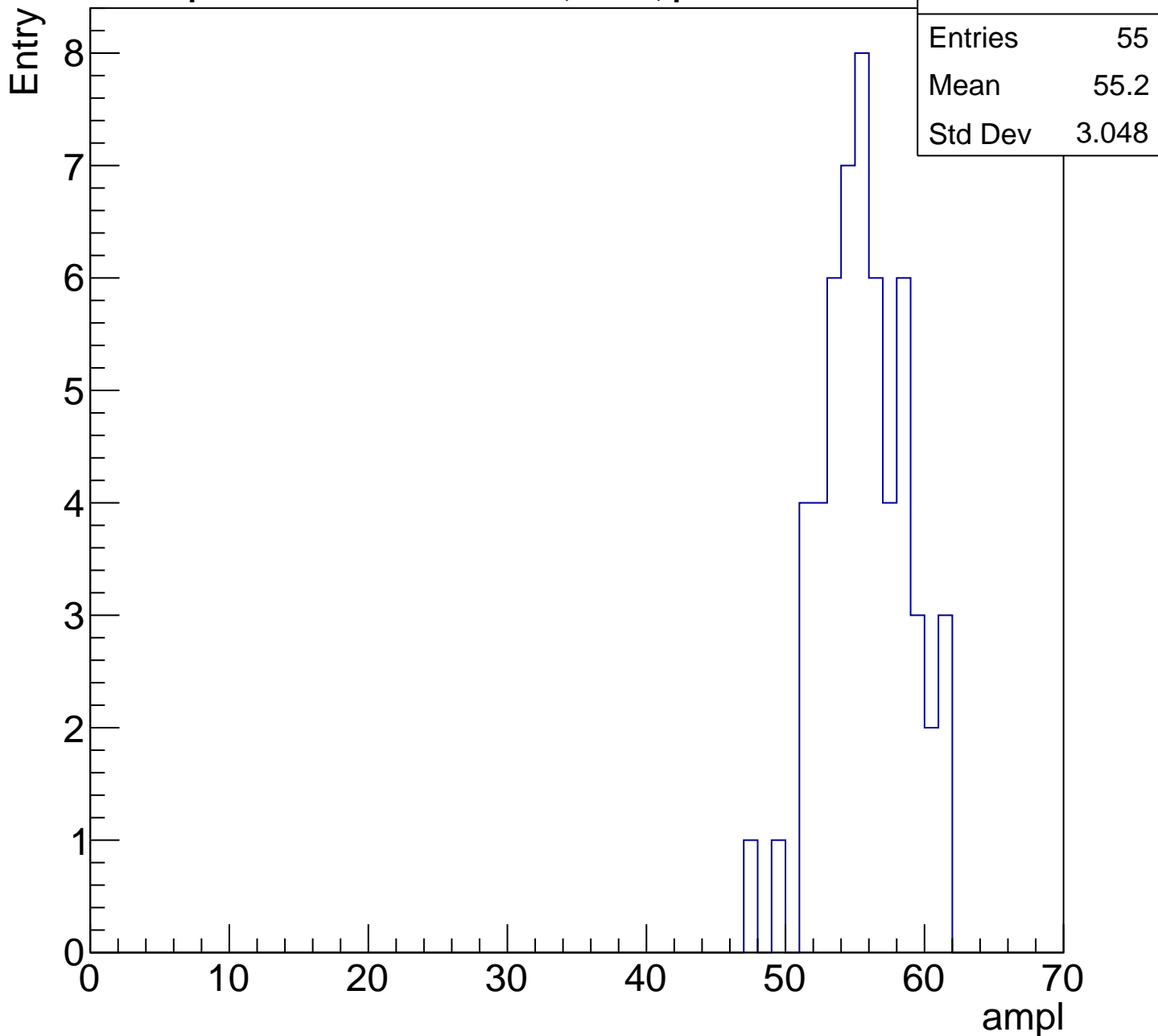
Entry

Entries	46
Mean	50.3
Std Dev	2.553



# B1L000S, U24-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

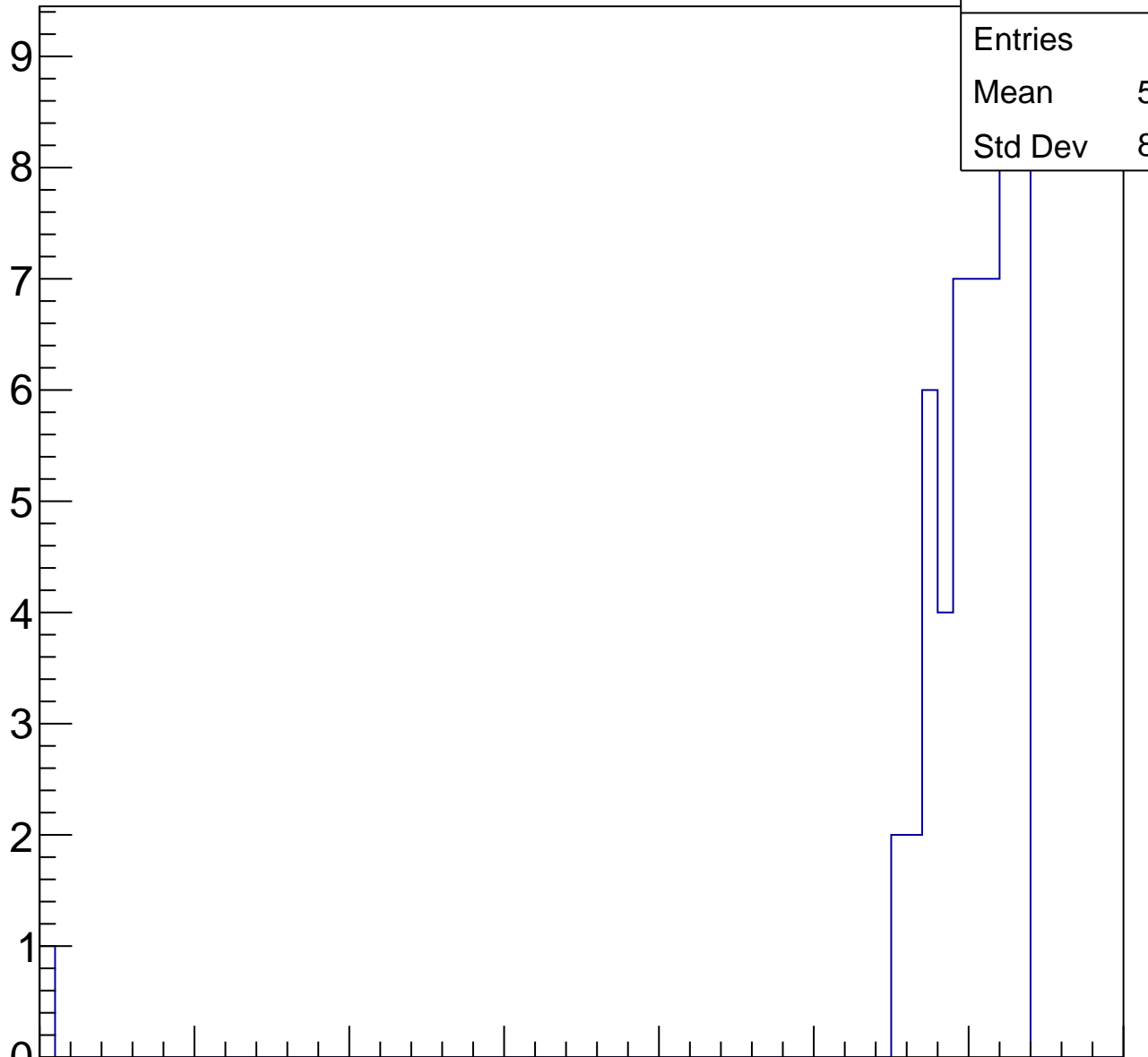
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.83
Std Dev	8.469

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch2, adc0

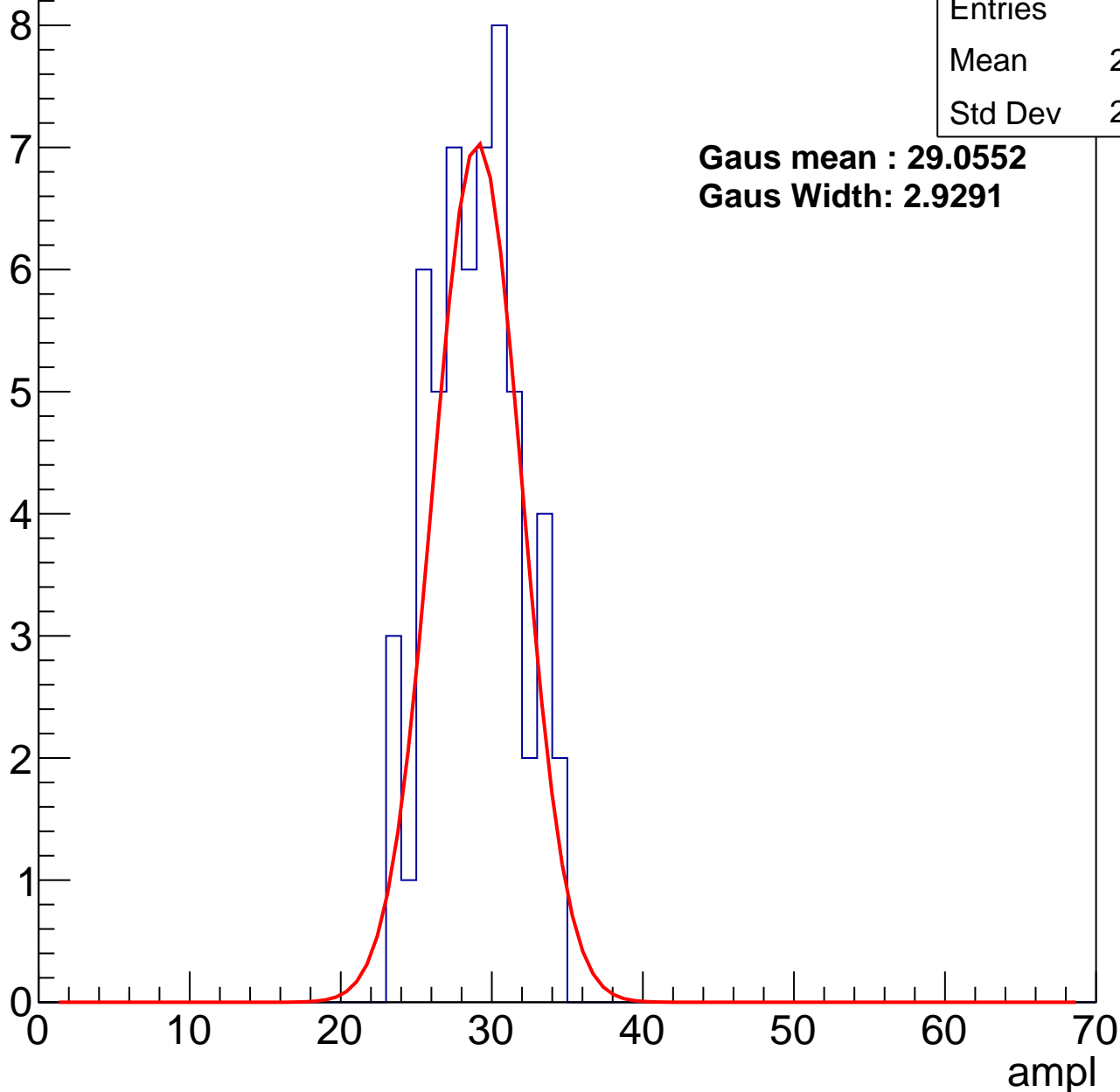
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	28.43
Std Dev	2.834

**Gaus mean : 29.0552**

**Gaus Width: 2.9291**



# B1L000S, U24-ch2, adc1

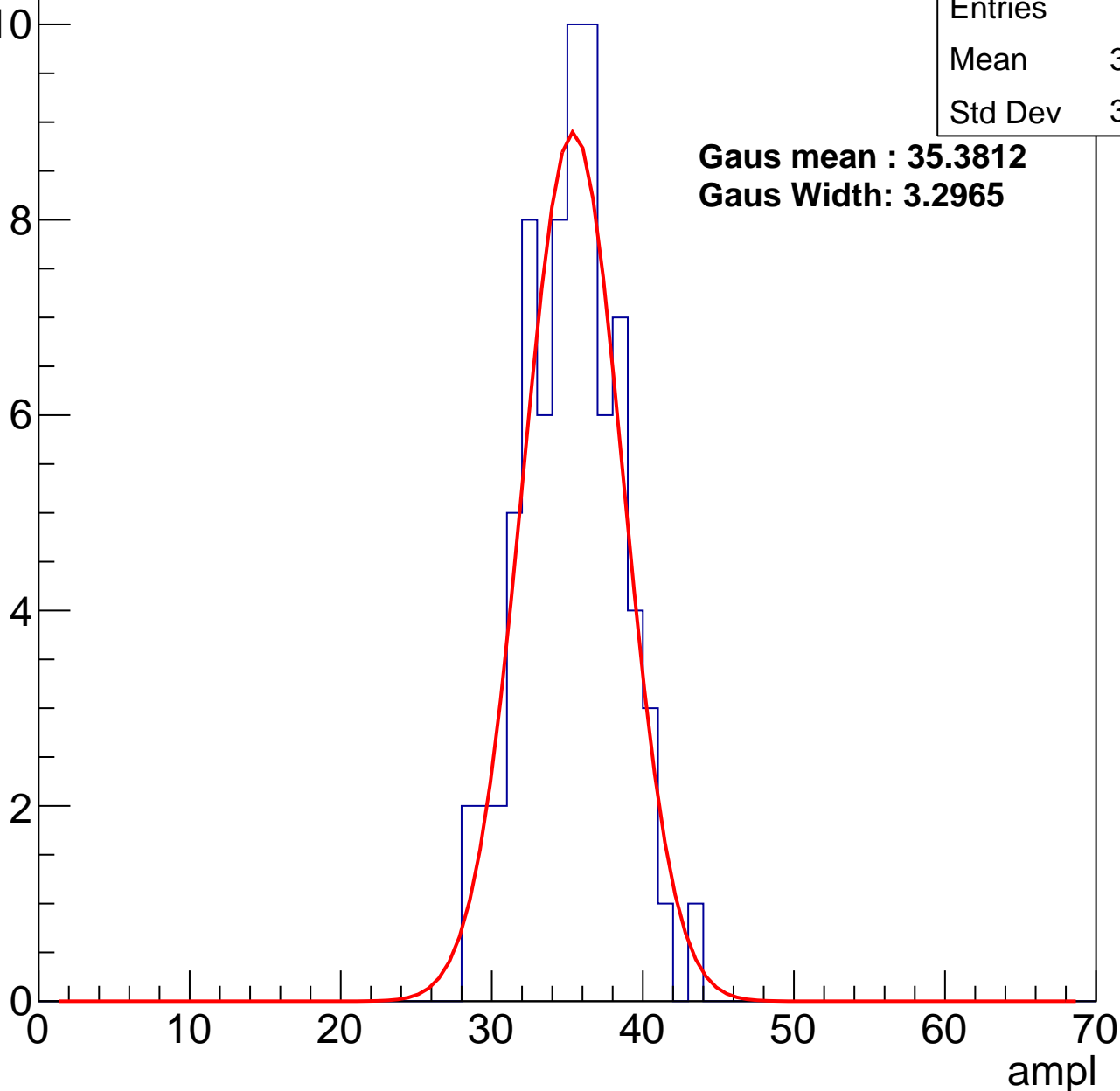
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	34.84
Std Dev	3.137

**Gaus mean : 35.3812**

**Gaus Width: 3.2965**



# B1L000S, U24-ch2, adc2

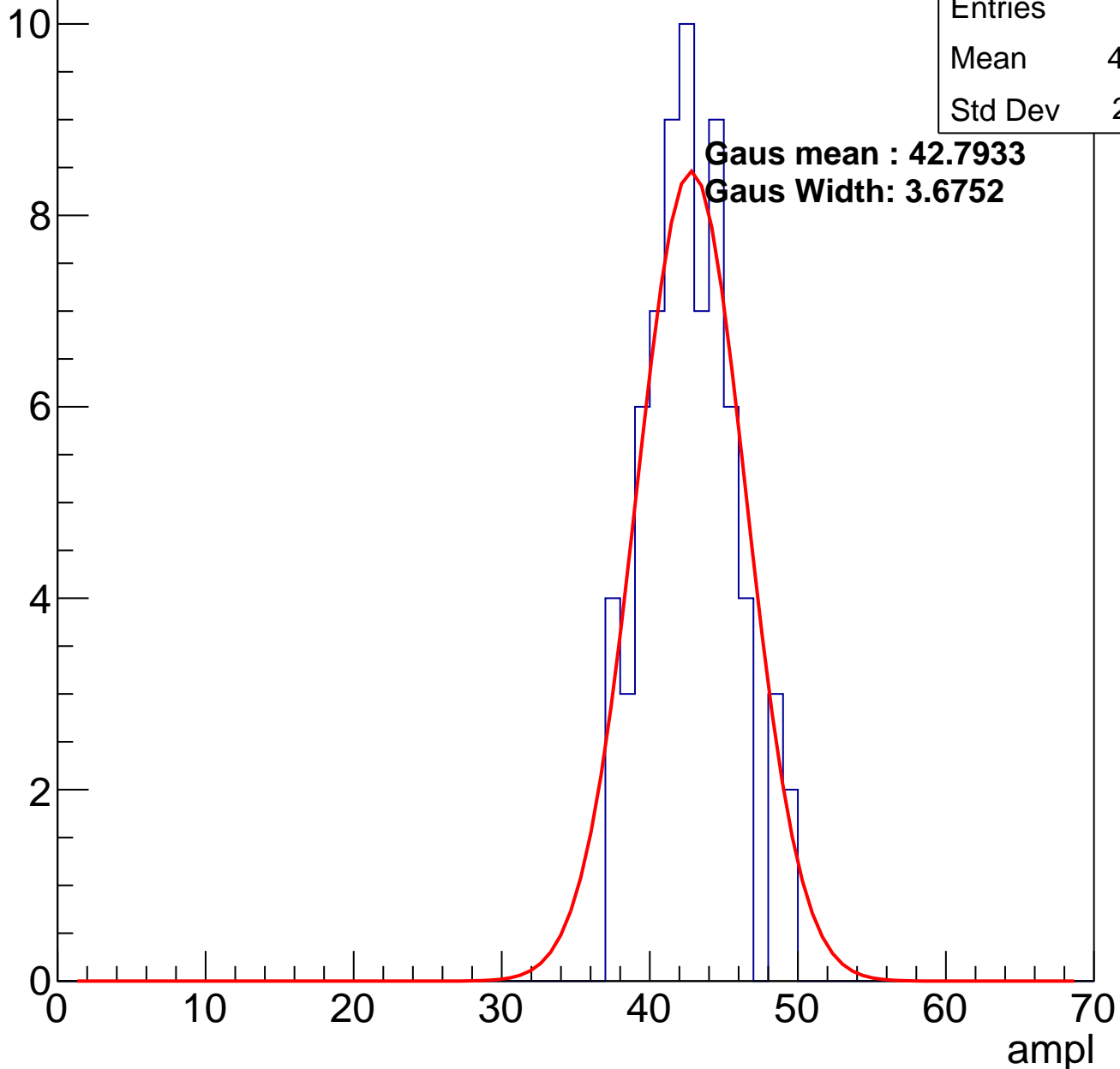
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	42.26
Std Dev	2.931

**Gaus mean : 42.7933**

**Gaus Width: 3.6752**

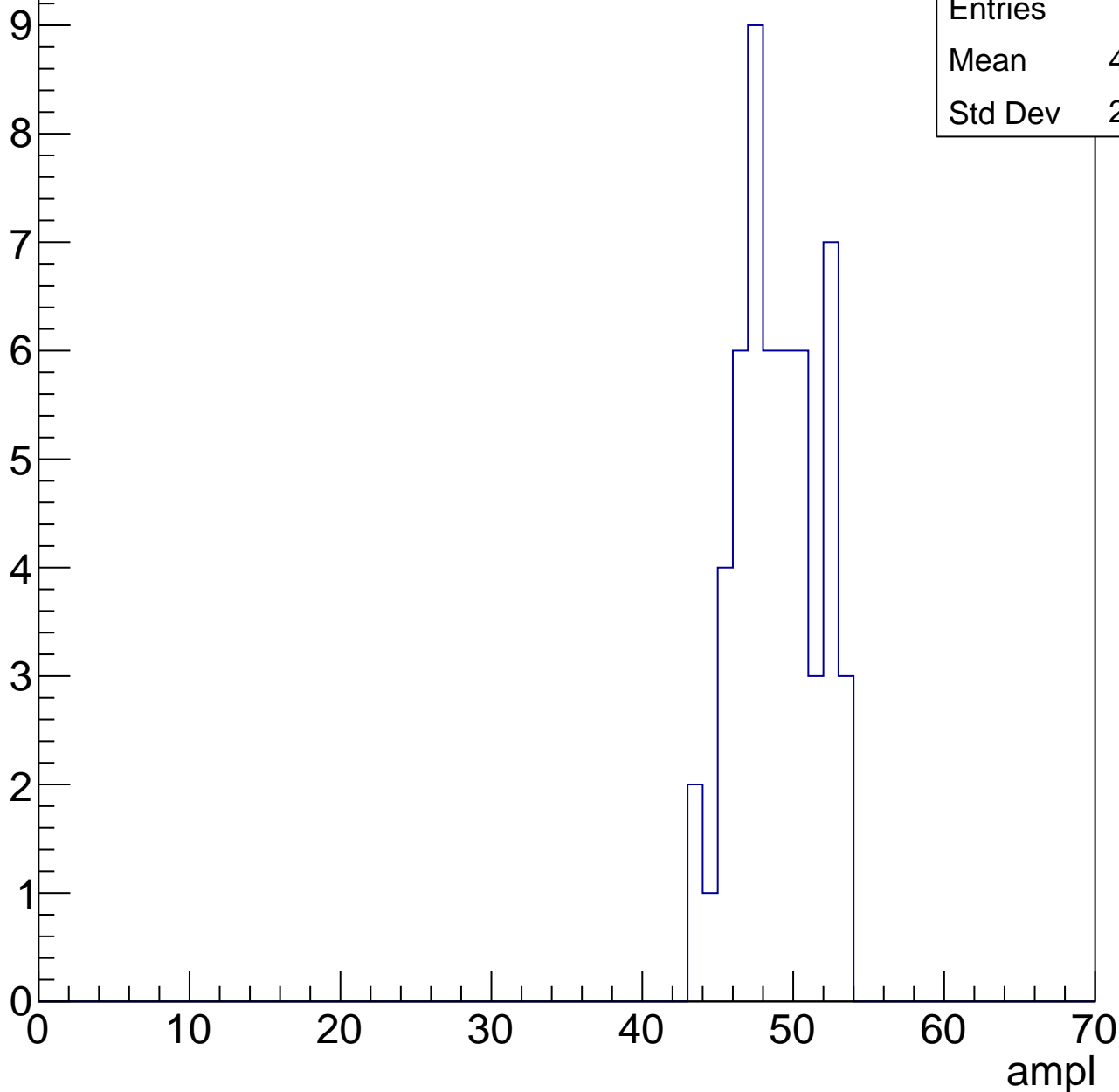
Entry



# B1L000S, U24-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



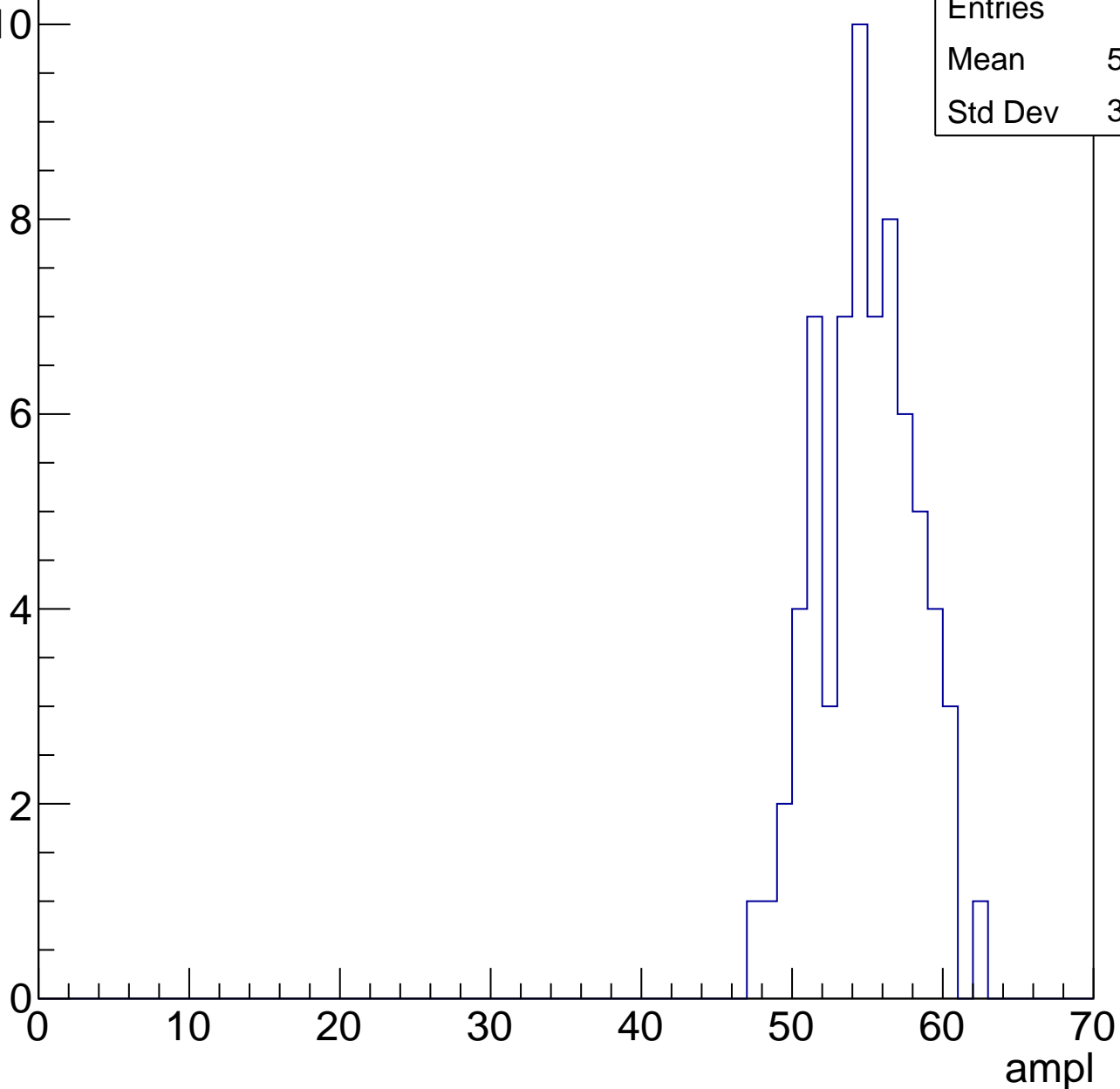
Entries	53
Mean	48.43
Std Dev	2.639

# B1L000S, U24-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	54.49
Std Dev	3.206

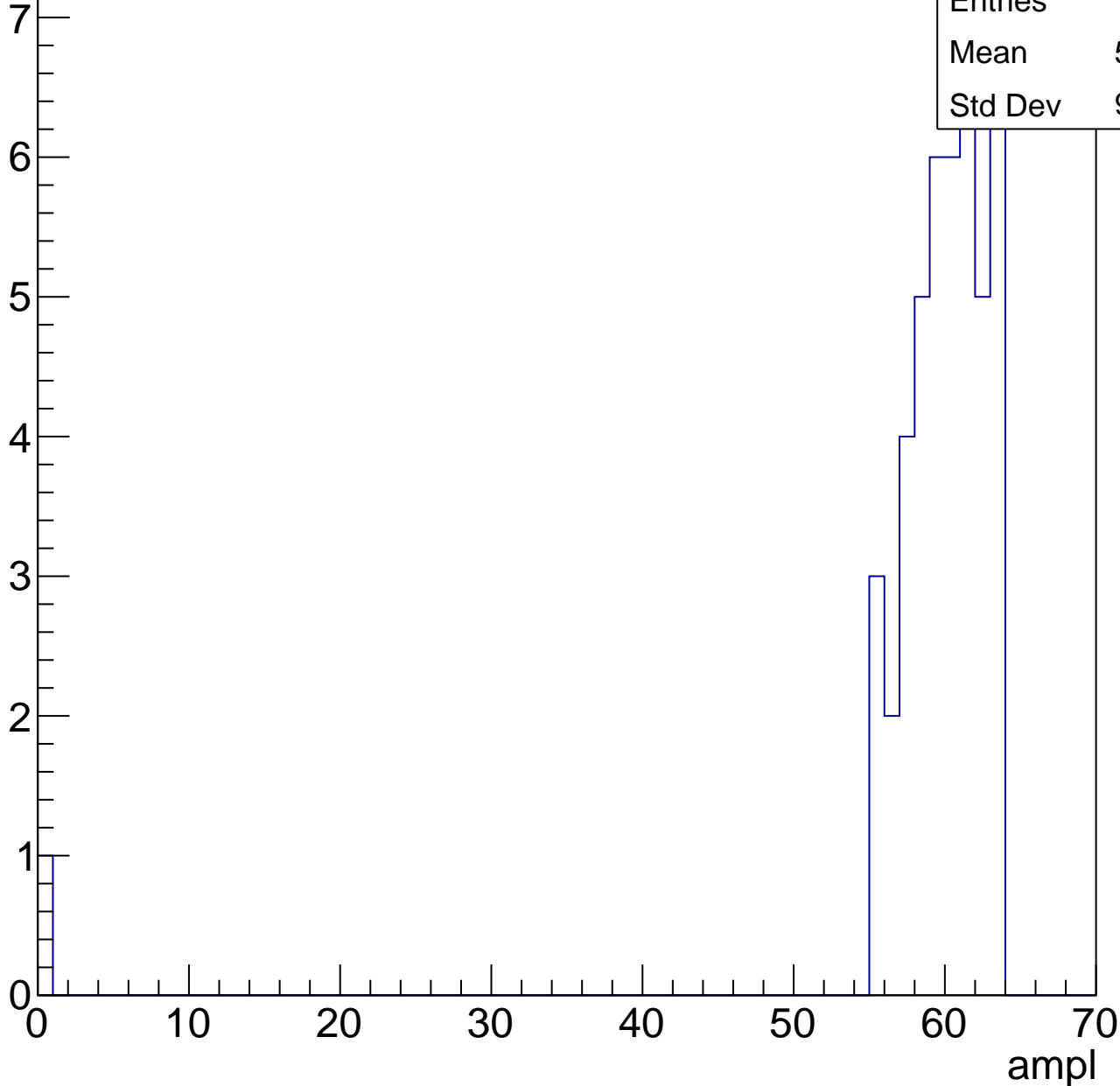


# B1L000S, U24-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

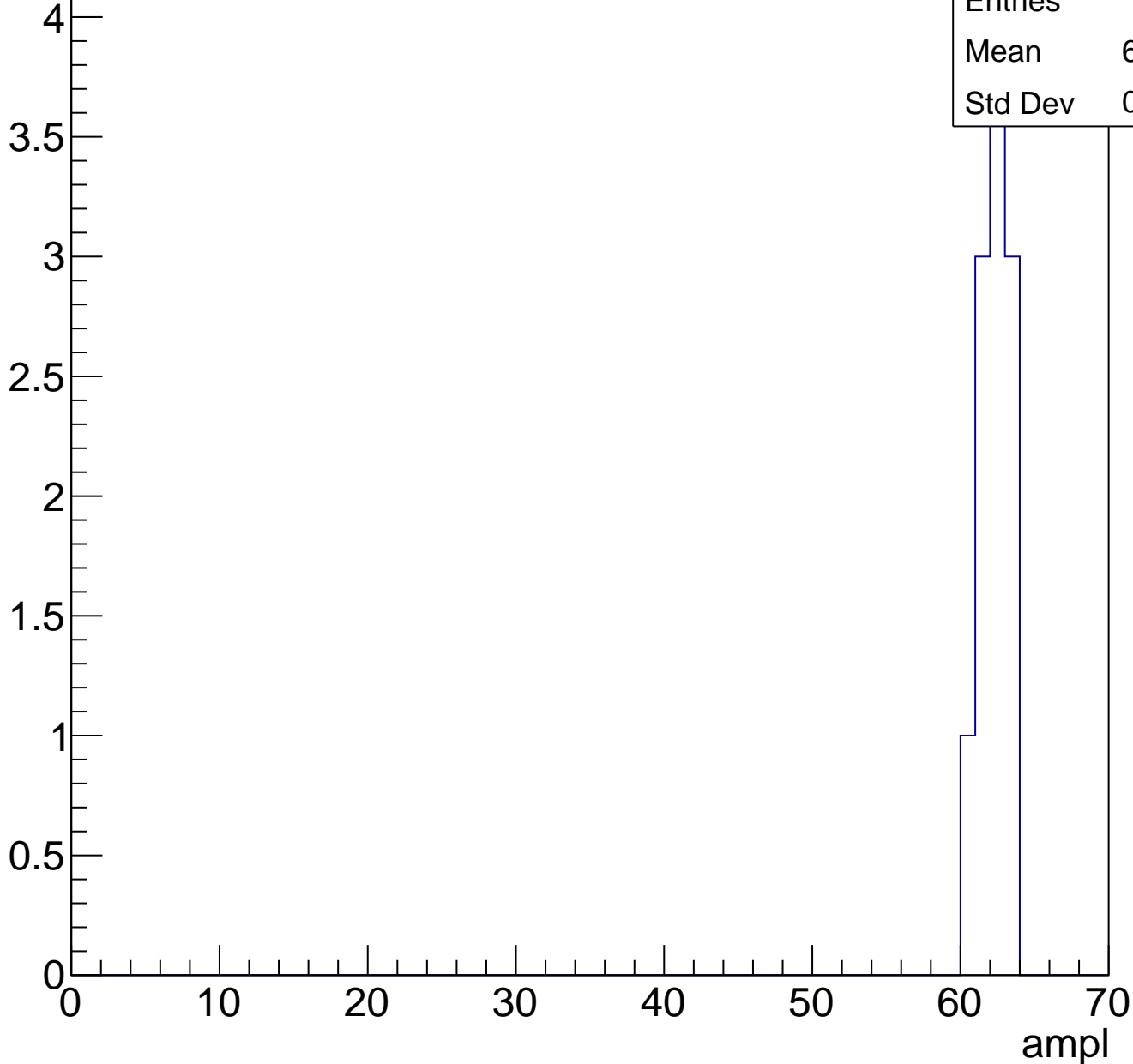
Entries	46
Mean	58.41
Std Dev	9.021



# B1L000S, U24-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U24-ch3, adc0

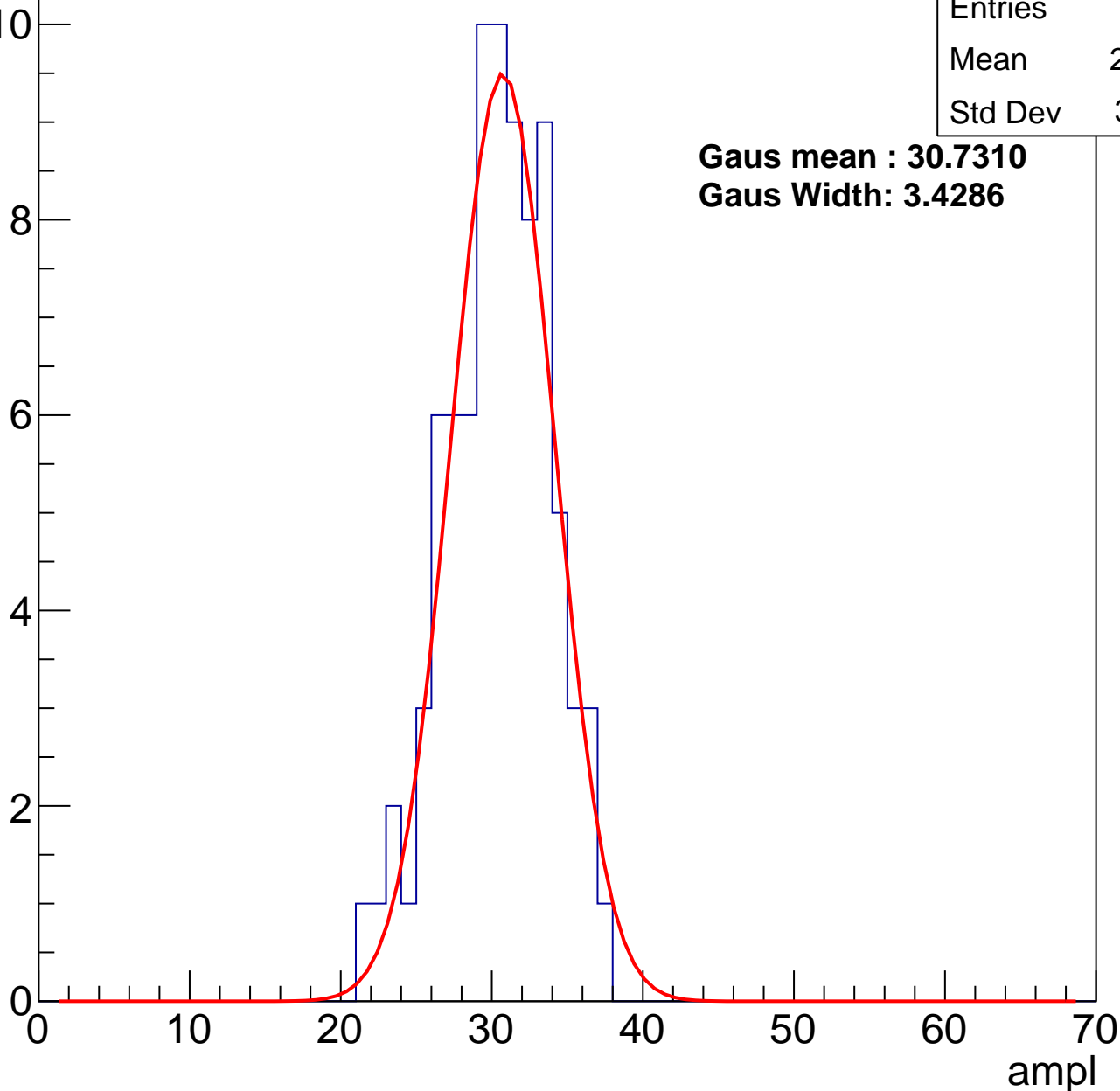
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	84
Mean	29.95
Std Dev	3.401

**Gaus mean : 30.7310**

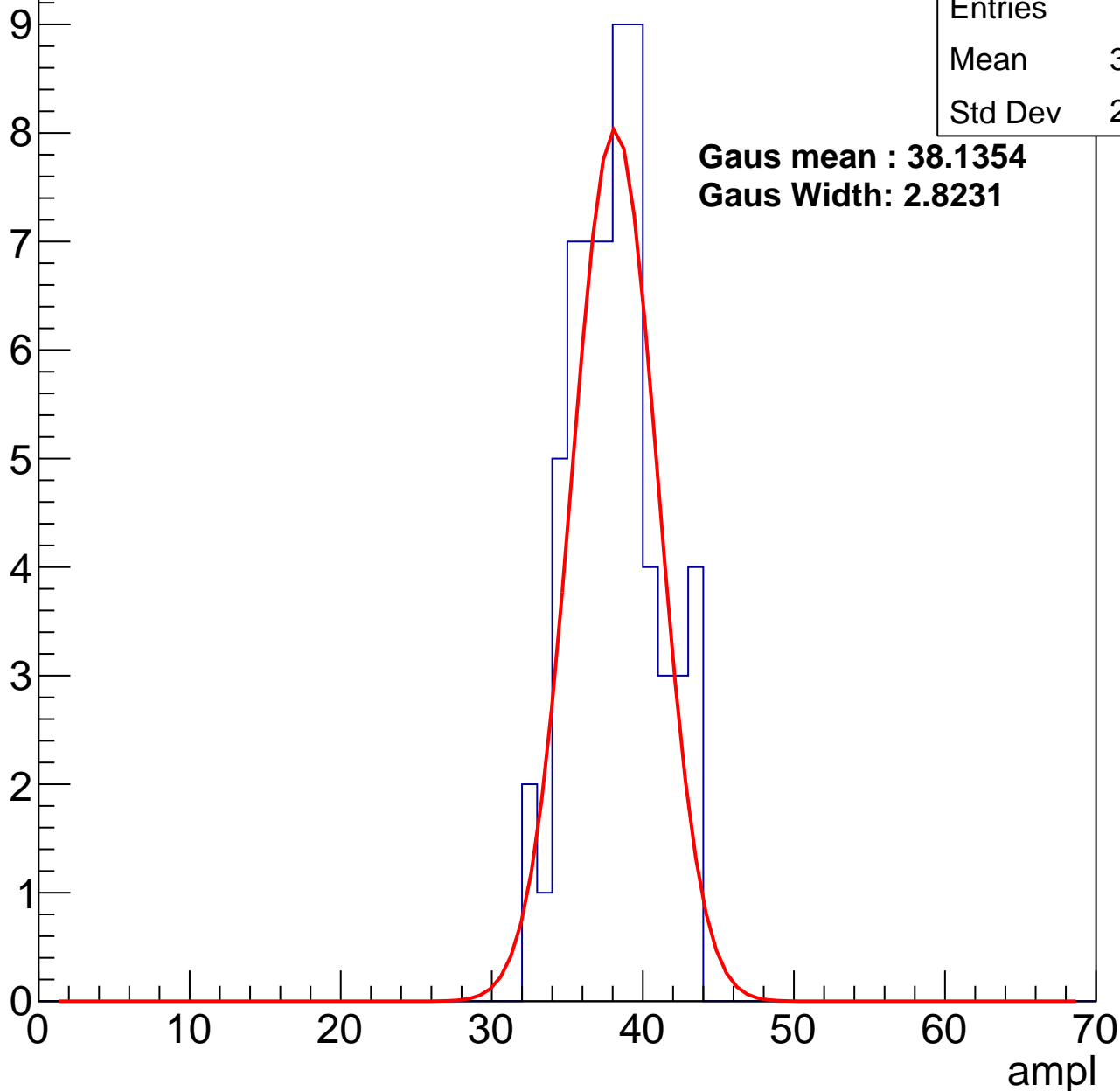
**Gaus Width: 3.4286**



# B1L000S, U24-ch3, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch3, adc2

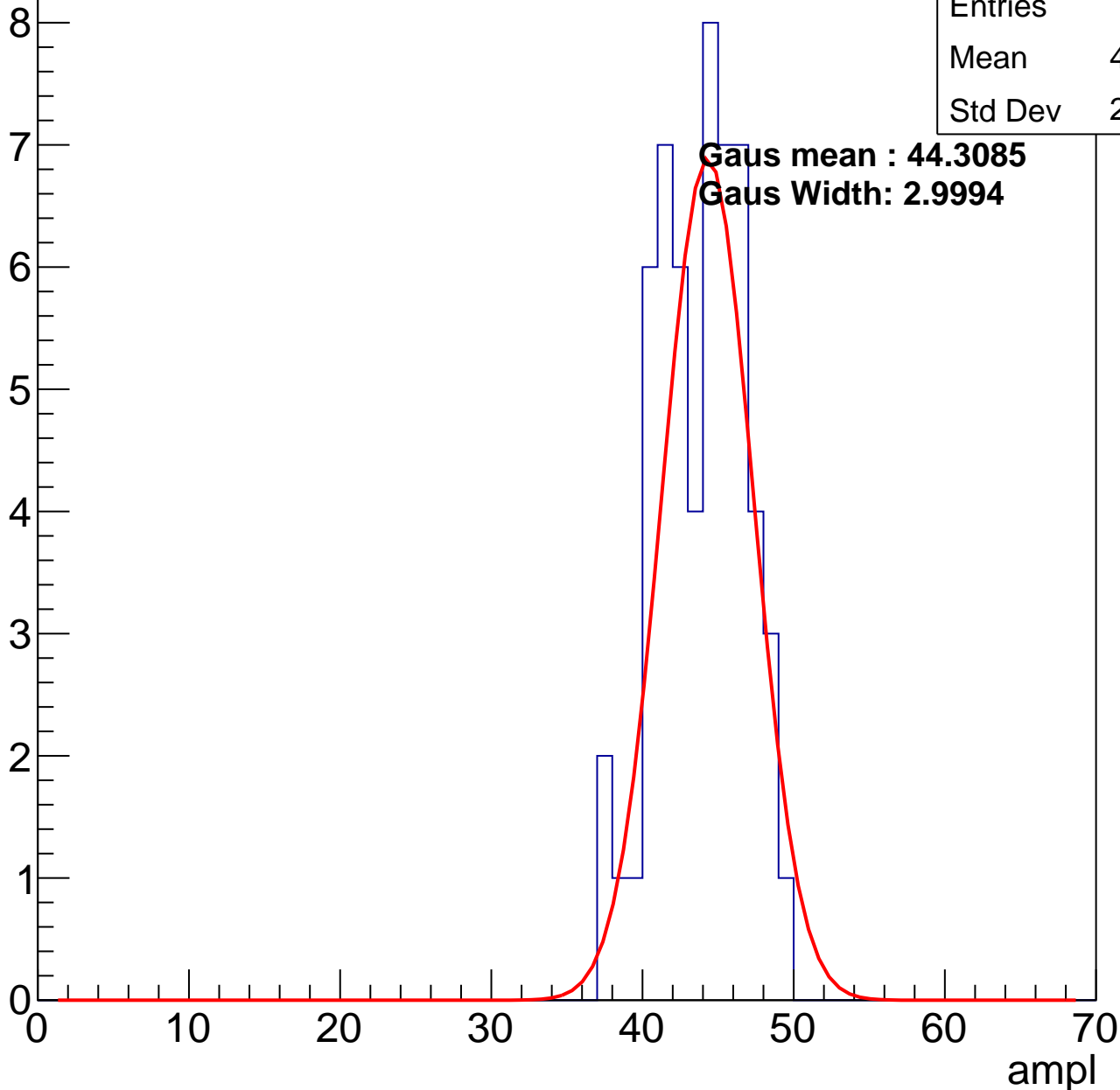
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	43.37
Std Dev	2.863

**Gaus mean : 44.3085**

**Gaus Width: 2.9994**

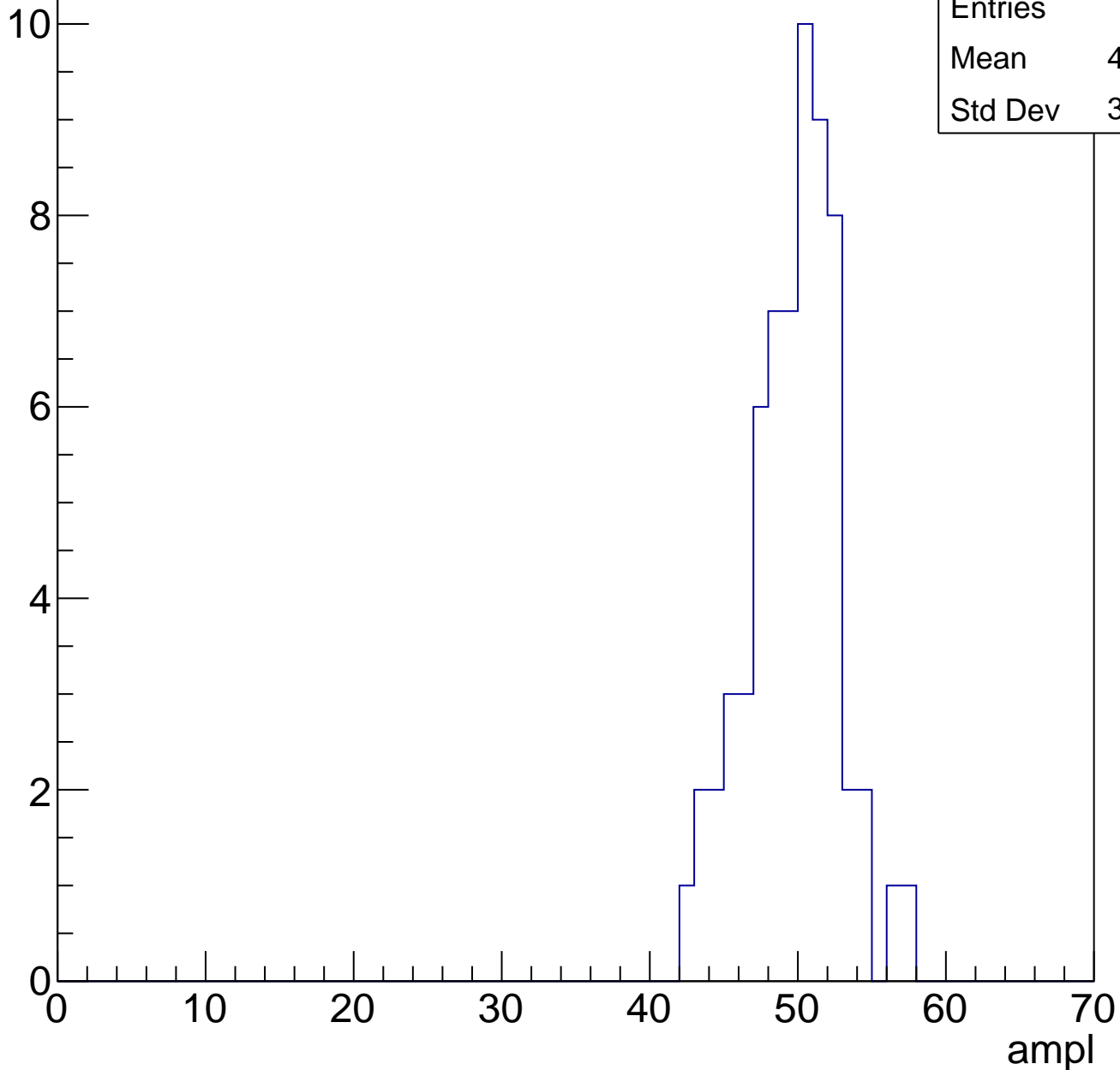


# B1L000S, U24-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	49.25
Std Dev	3.026

Entry



# B1L000S, U24-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

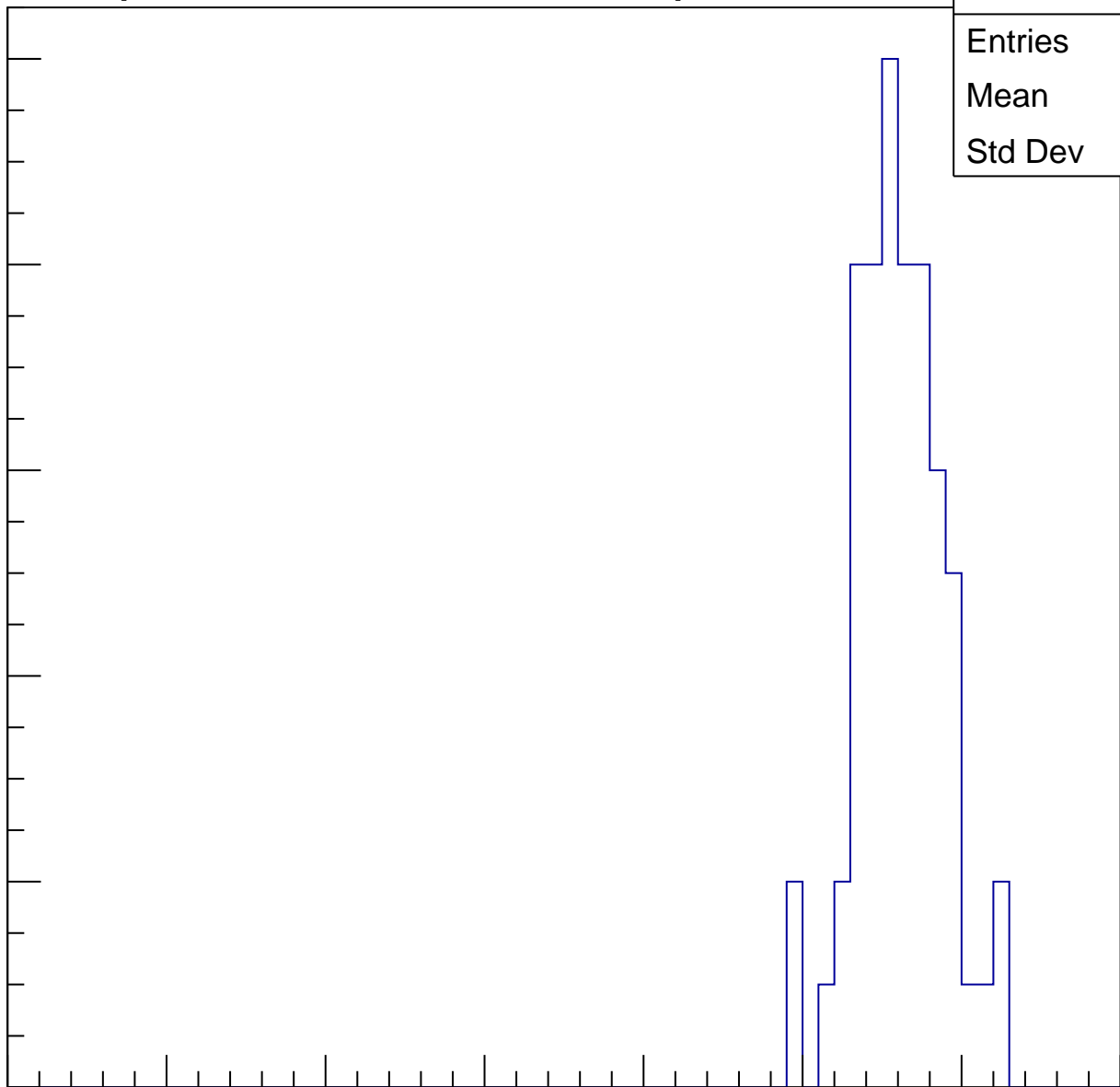
Entries	62
Mean	55.66
Std Dev	2.7

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 46

Mean 59.37

Std Dev 9.03

8

6

4

2

0

0

10

20

30

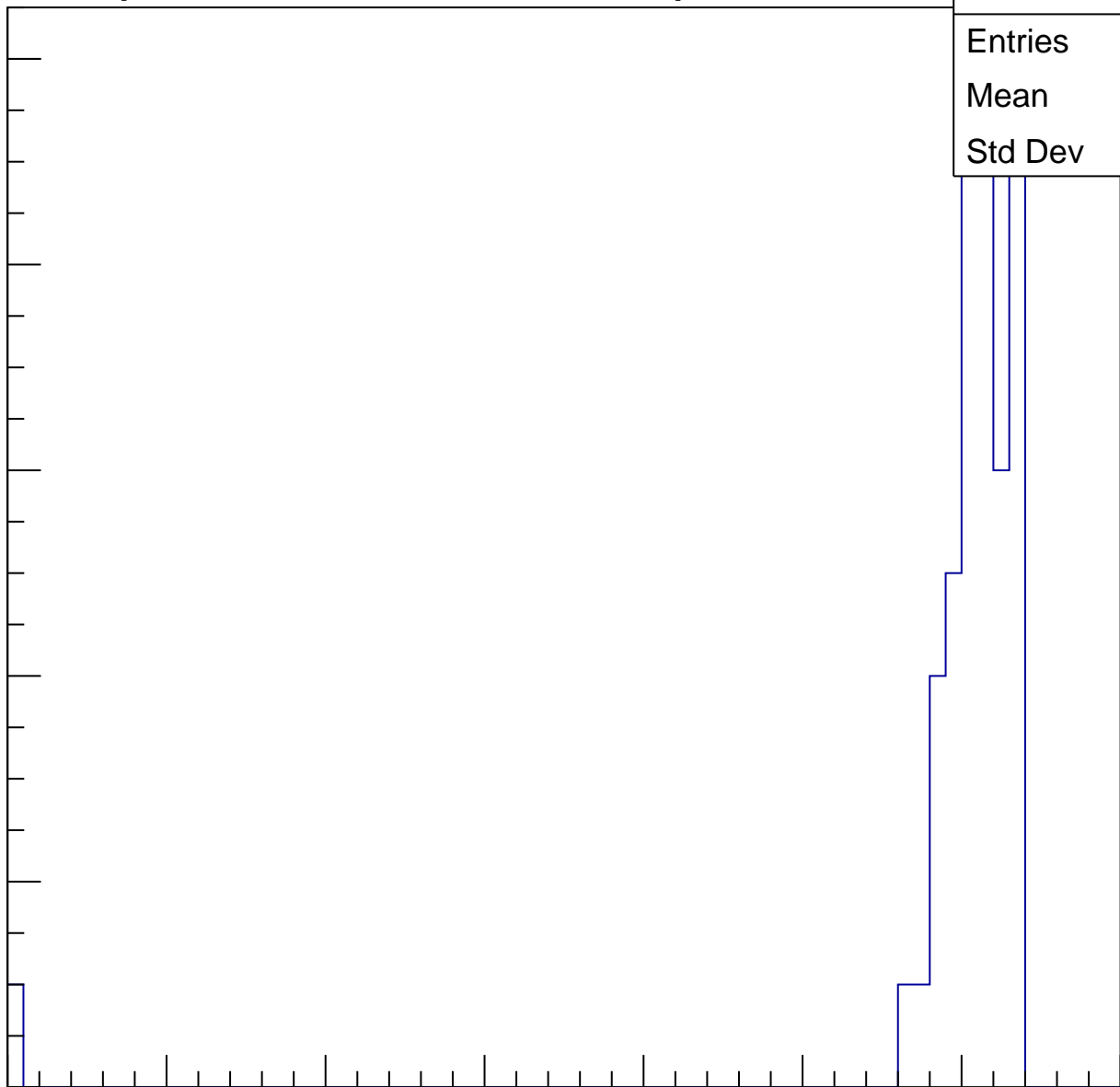
40

50

60

70

ampl



# B1L000S, U24-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch4, adc0

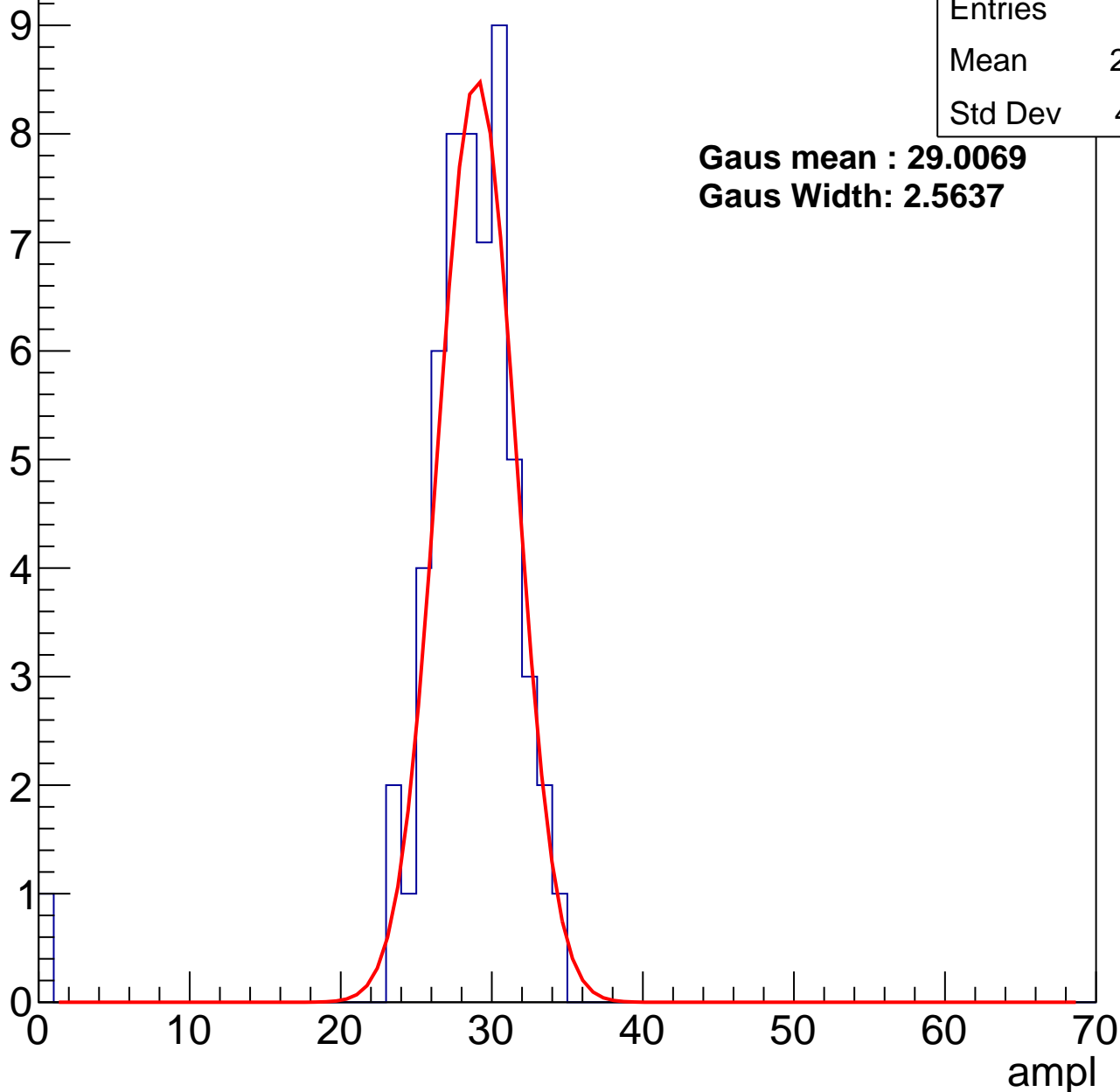
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	27.89
Std Dev	4.471

**Gaus mean : 29.0069**

**Gaus Width: 2.5637**



# B1L000S, U24-ch4, adc1

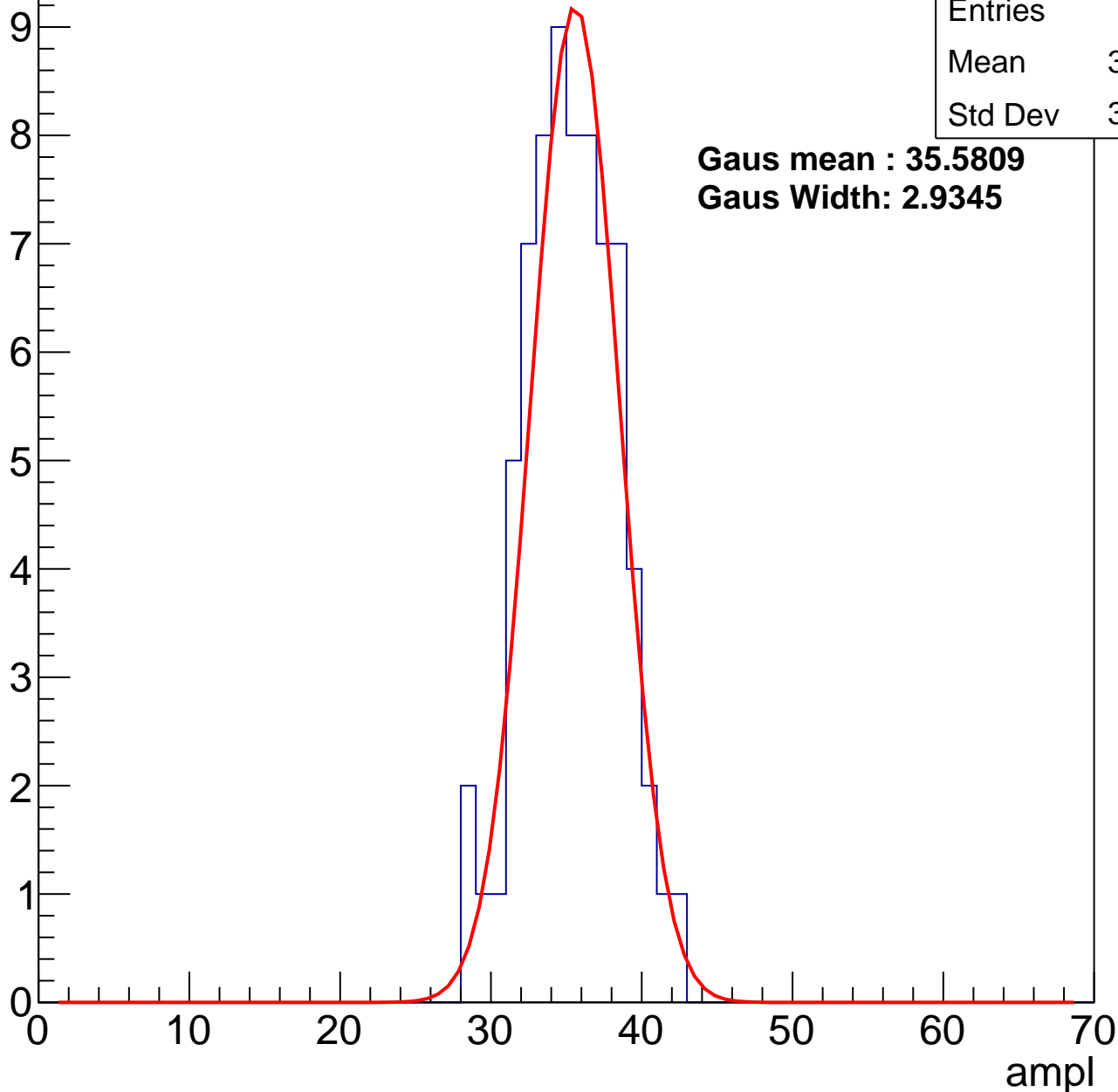
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.87
Std Dev	3.002

**Gaus mean : 35.5809**

**Gaus Width: 2.9345**



# B1L000S, U24-ch4, adc2

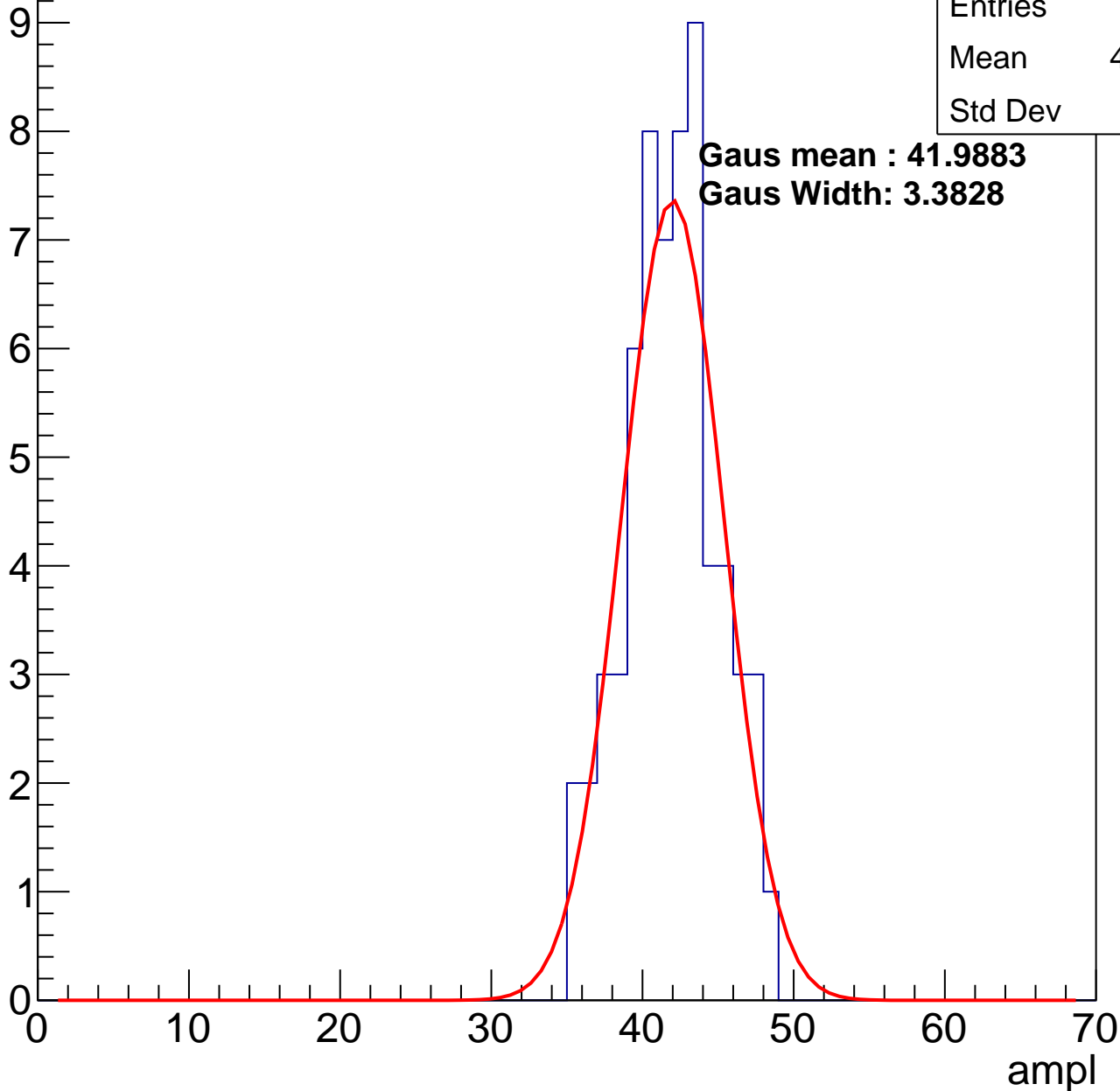
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.49
Std Dev	3.06

**Gaus mean : 41.9883**

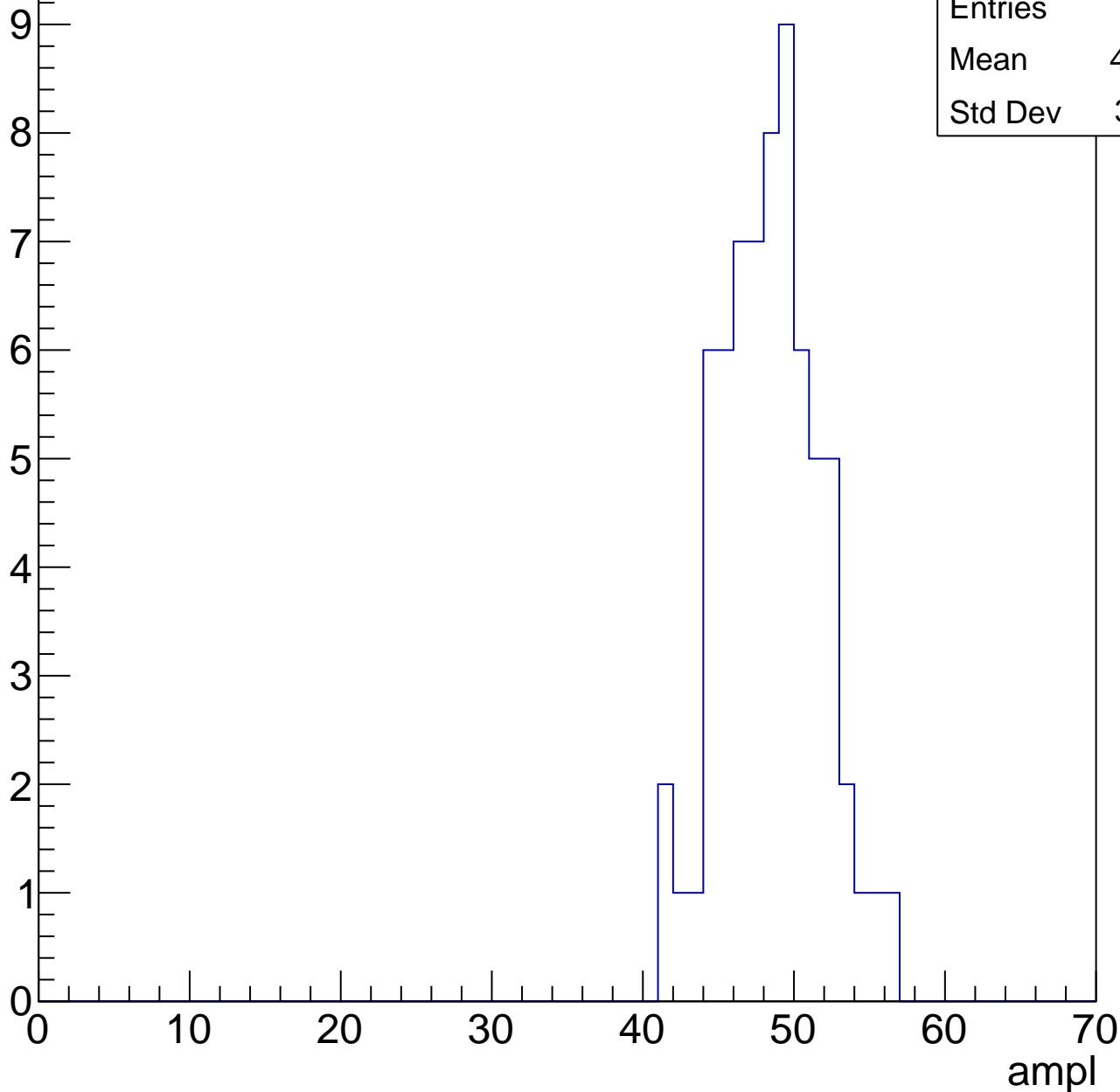
**Gaus Width: 3.3828**



# B1L000S, U24-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

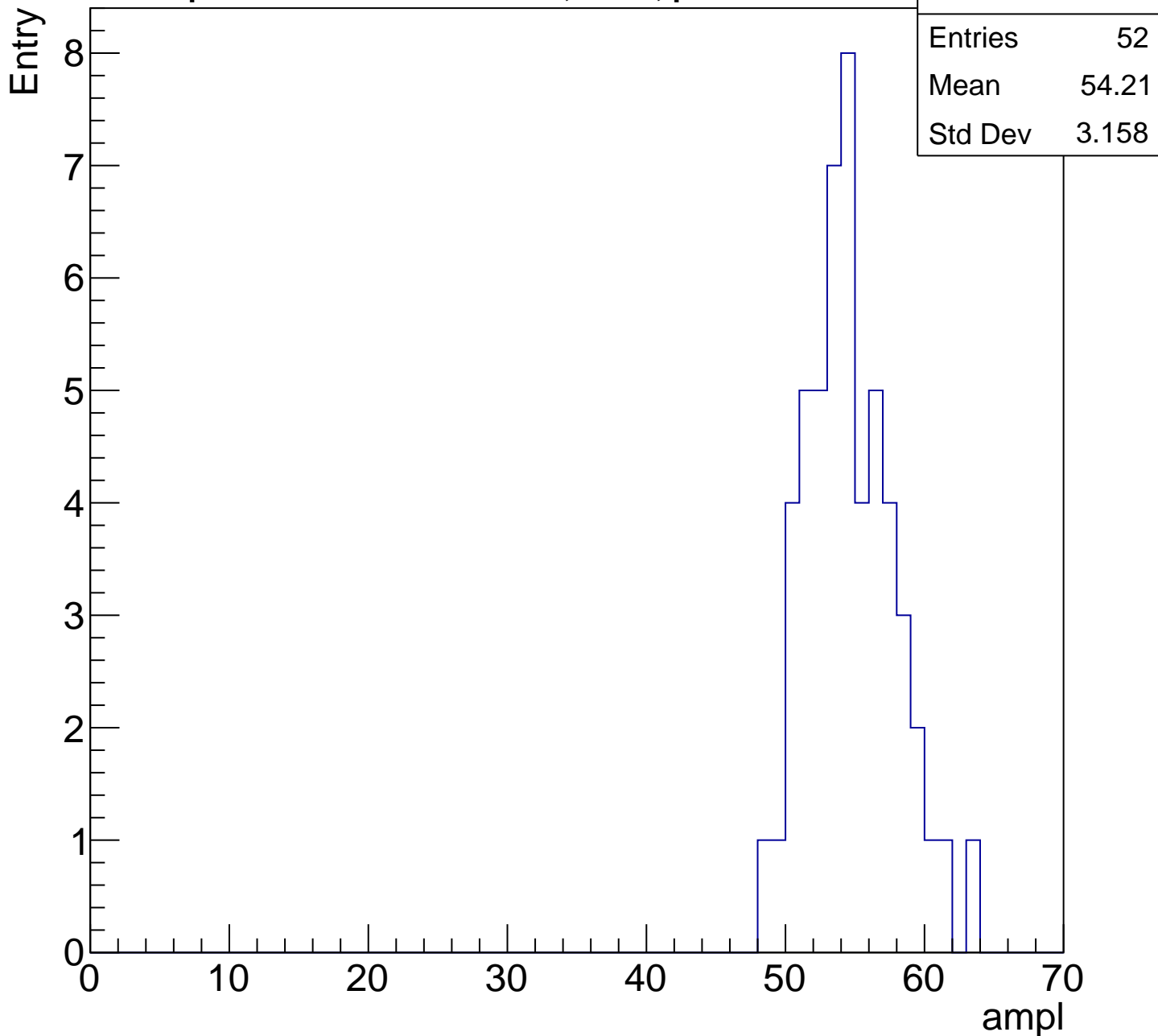
Entry



Entries	68
Mean	47.99
Std Dev	3.211

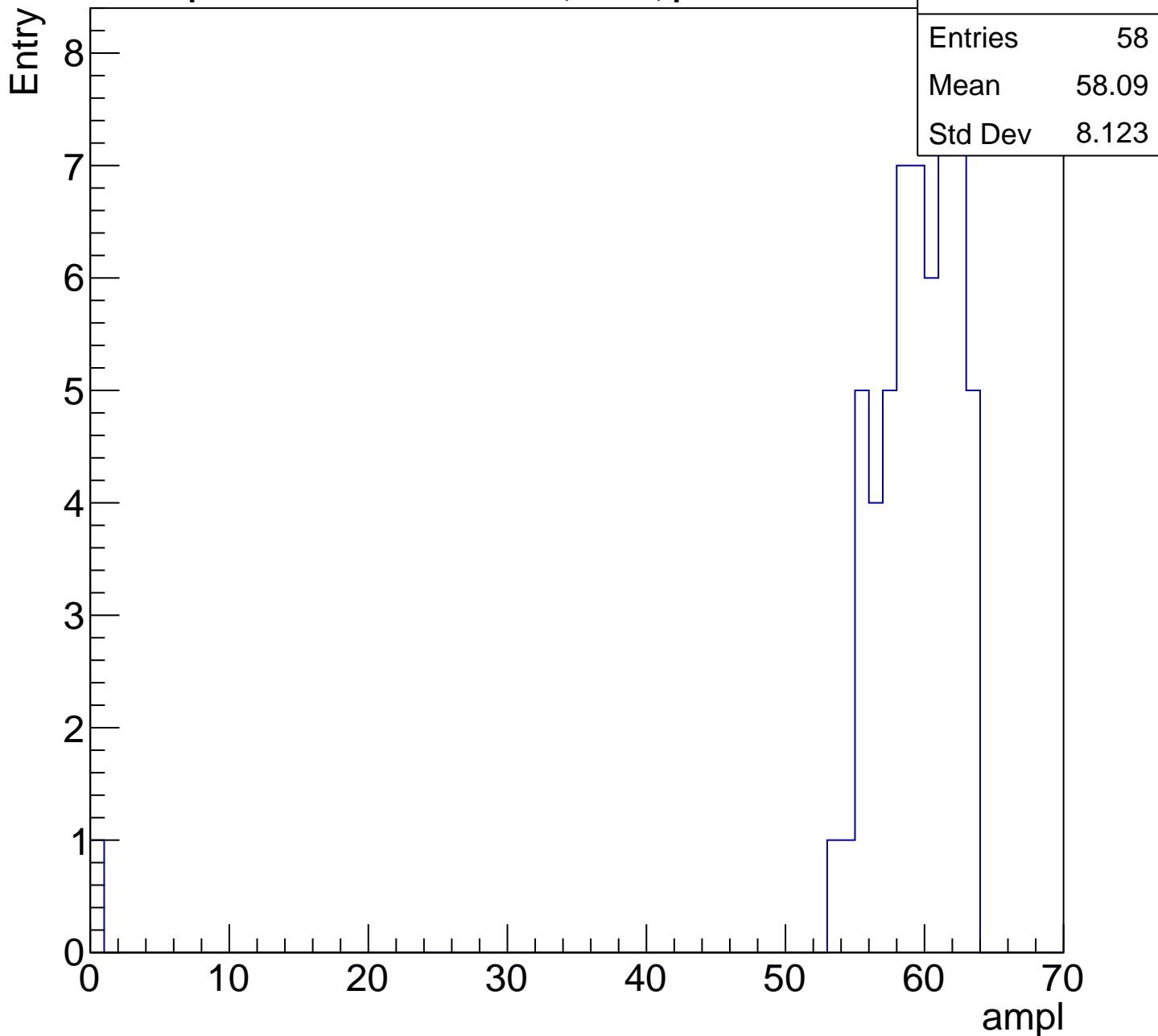
# B1L000S, U24-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch4, adc5

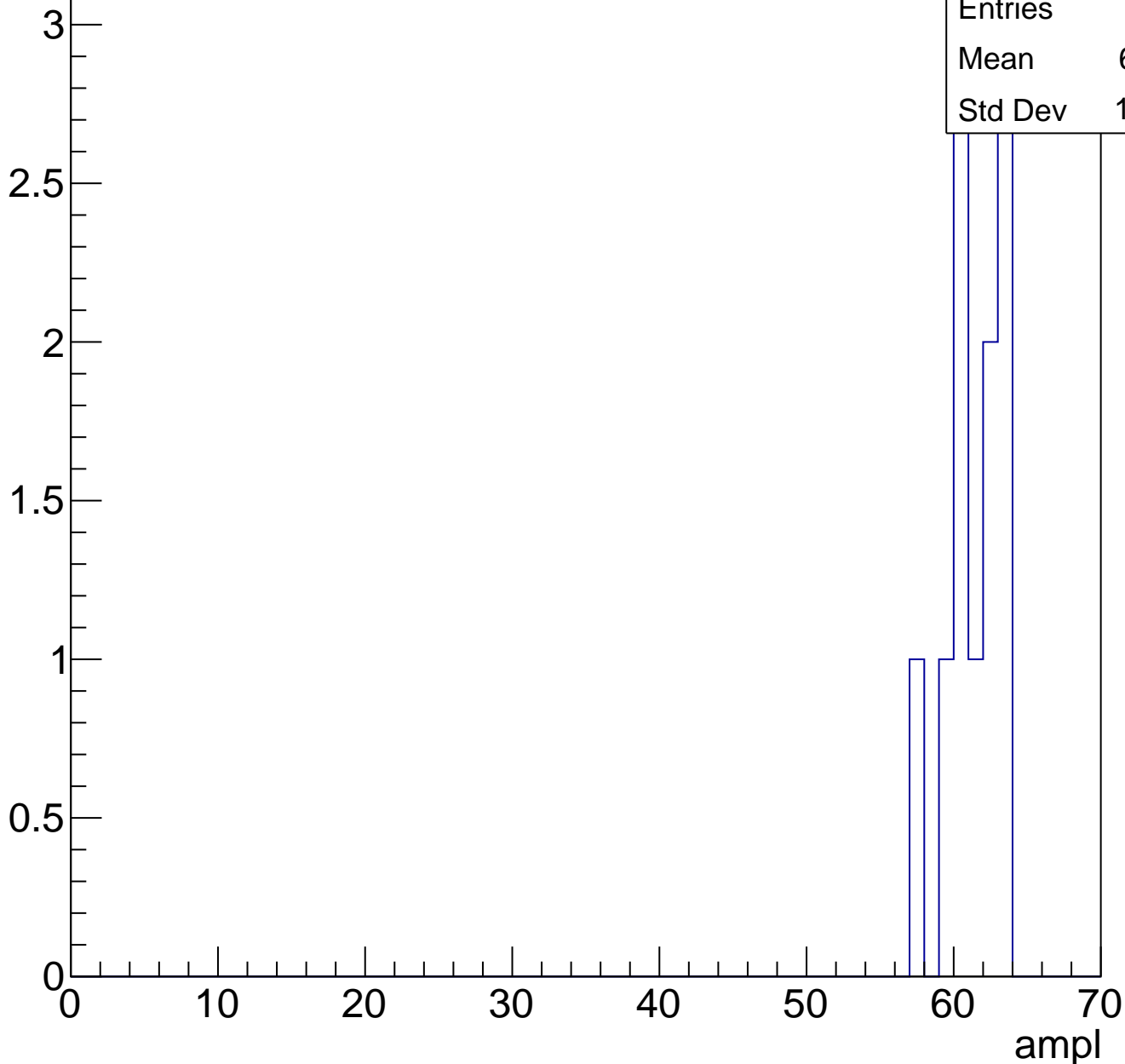
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

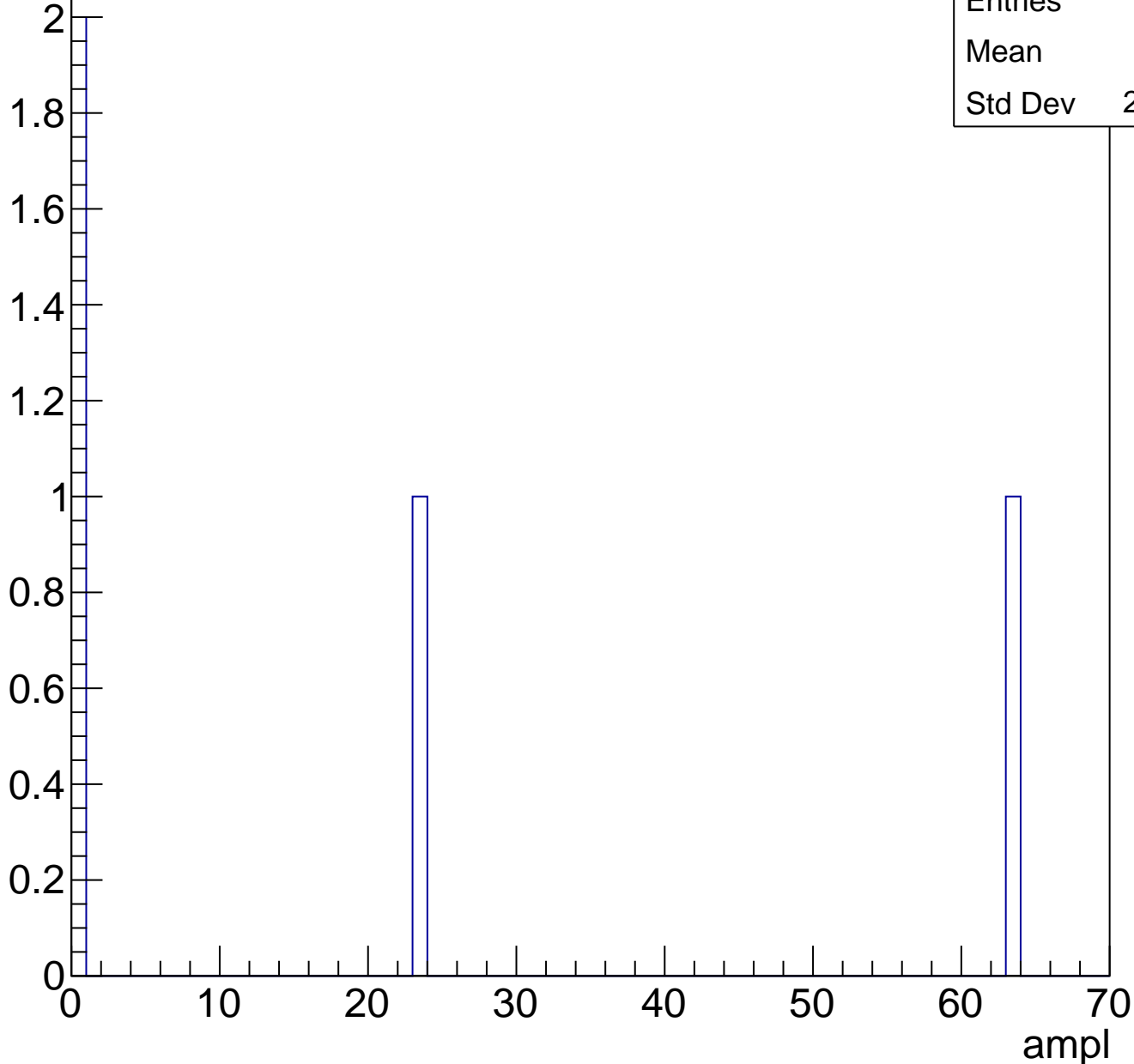




# B1L000S, U24-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21.5
Std Dev	25.73

# B1L000S, U24-ch5, adc0

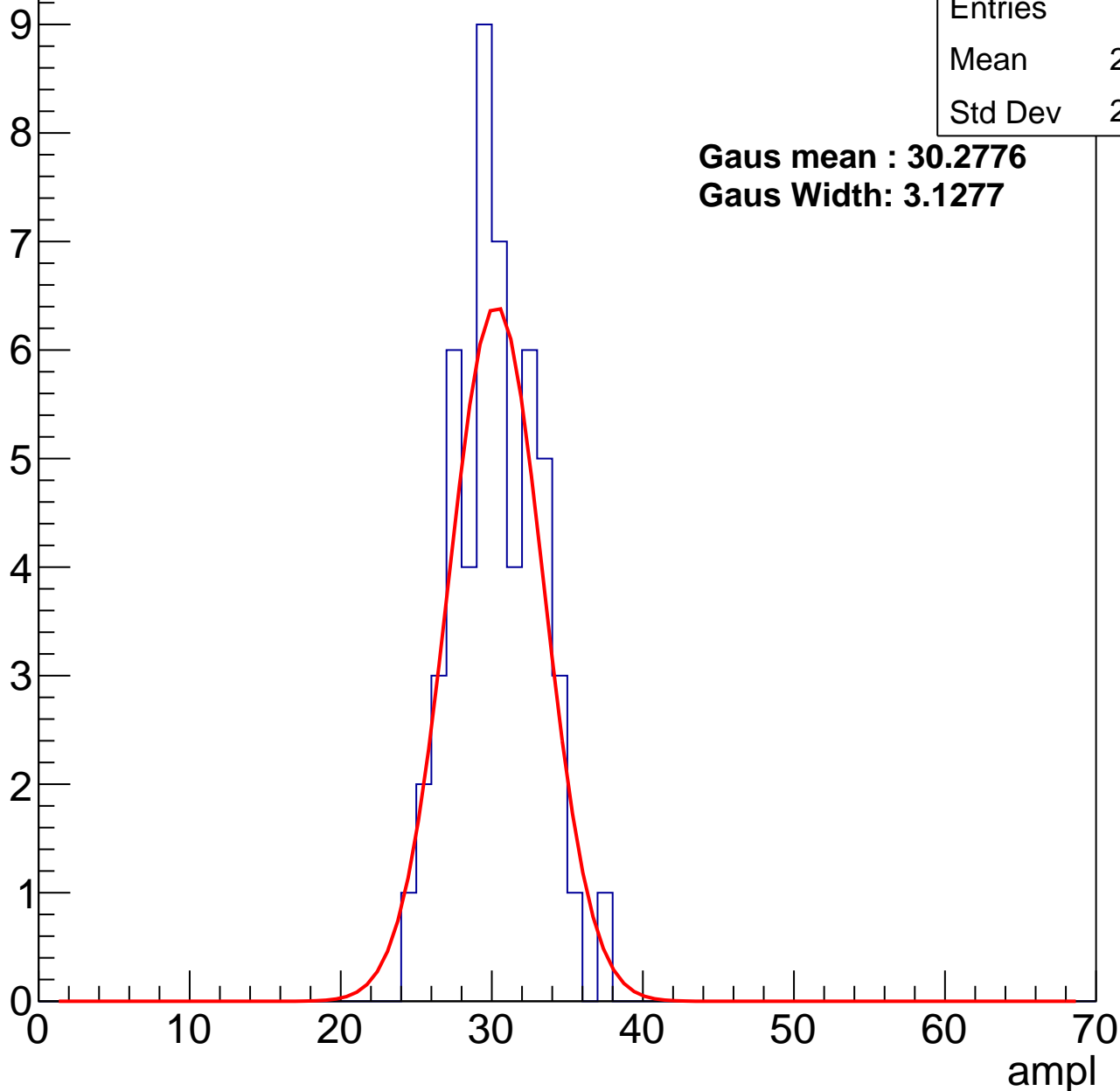
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	29.85
Std Dev	2.797

**Gaus mean : 30.2776**

**Gaus Width: 3.1277**



# B1L000S, U24-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	36.37
Std Dev	3.501

**Gaus mean : 36.3020**

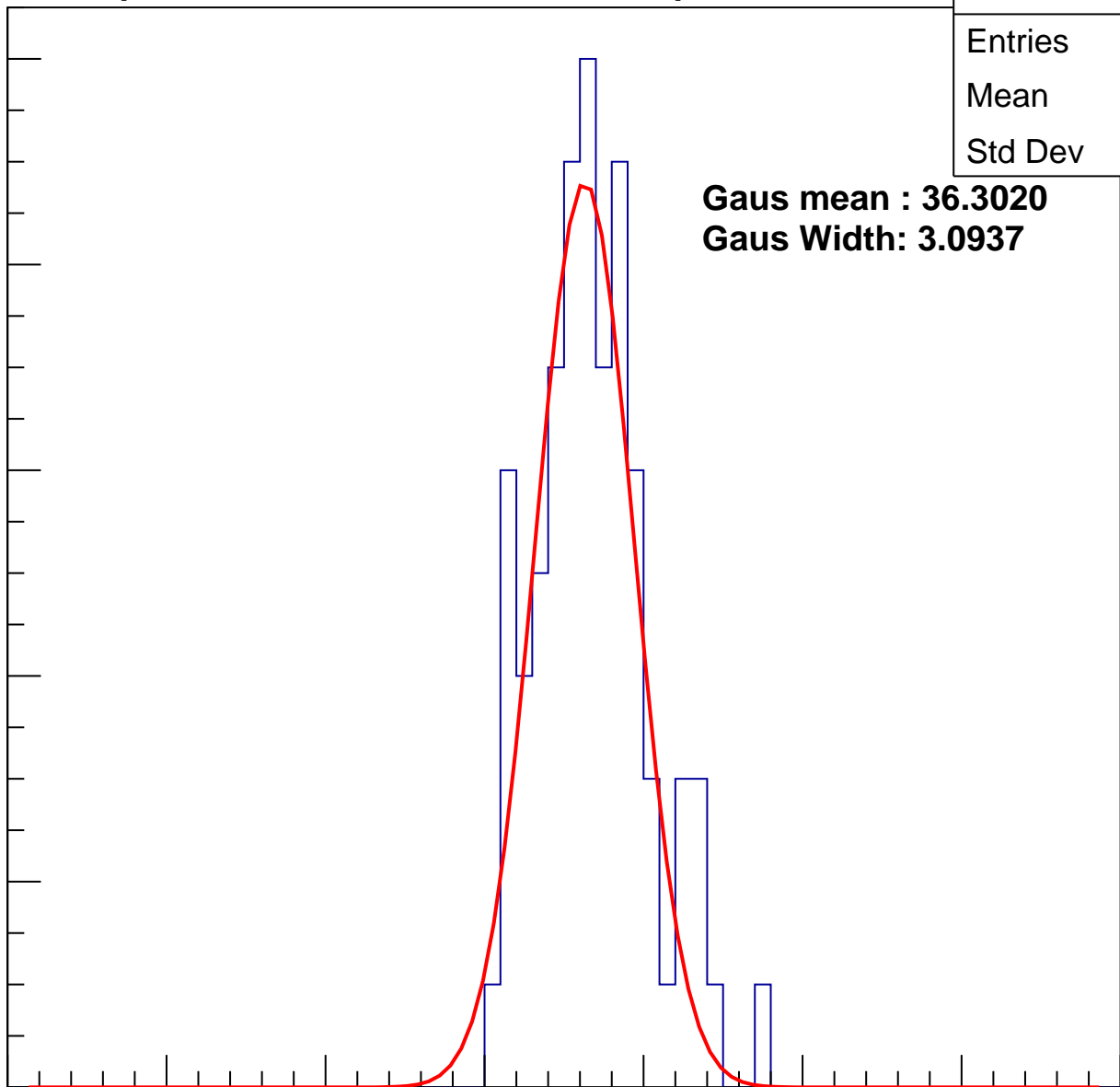
**Gaus Width: 3.0937**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch5, adc2

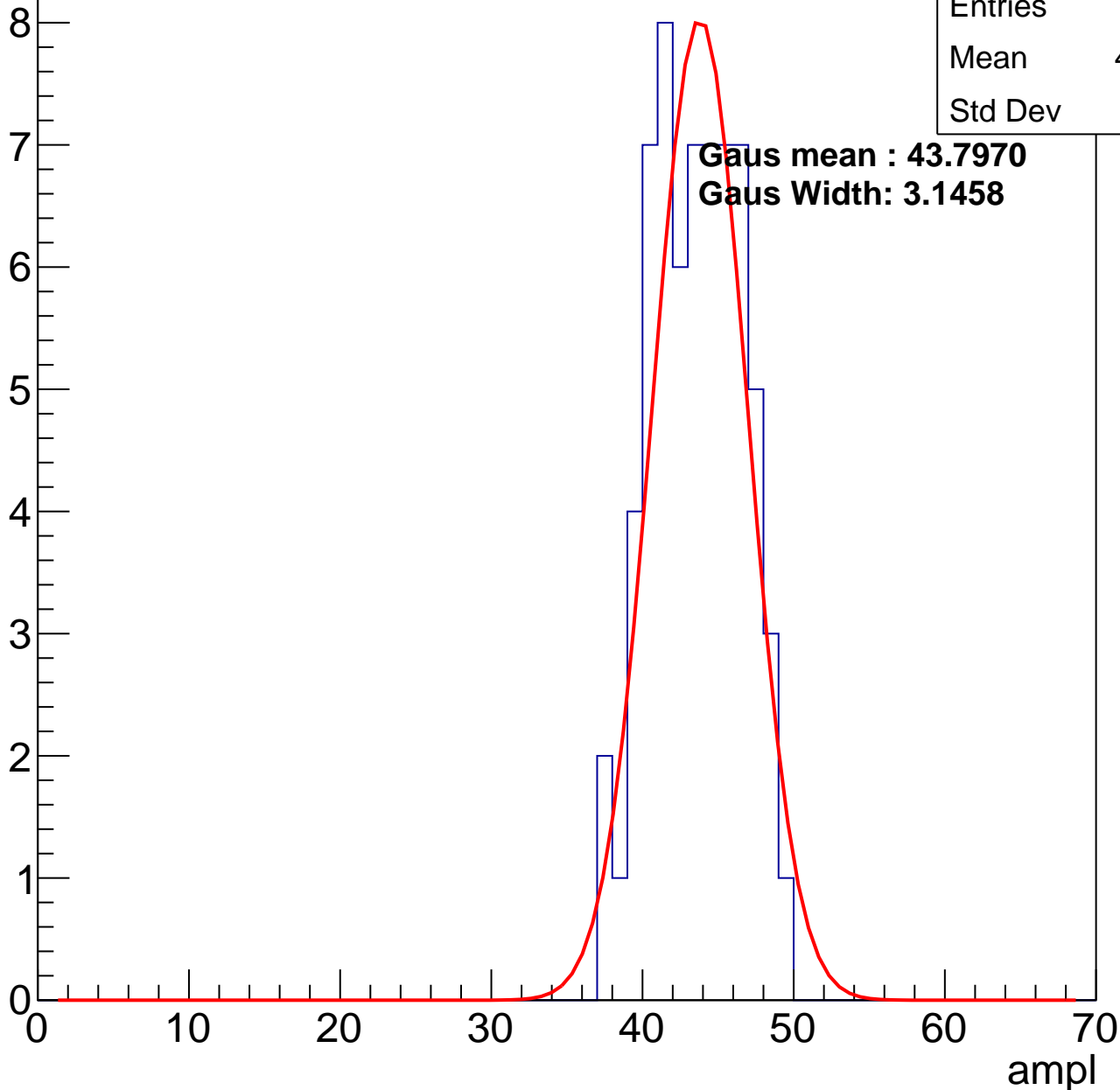
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.11
Std Dev	2.91

**Gaus mean : 43.7970**

**Gaus Width: 3.1458**

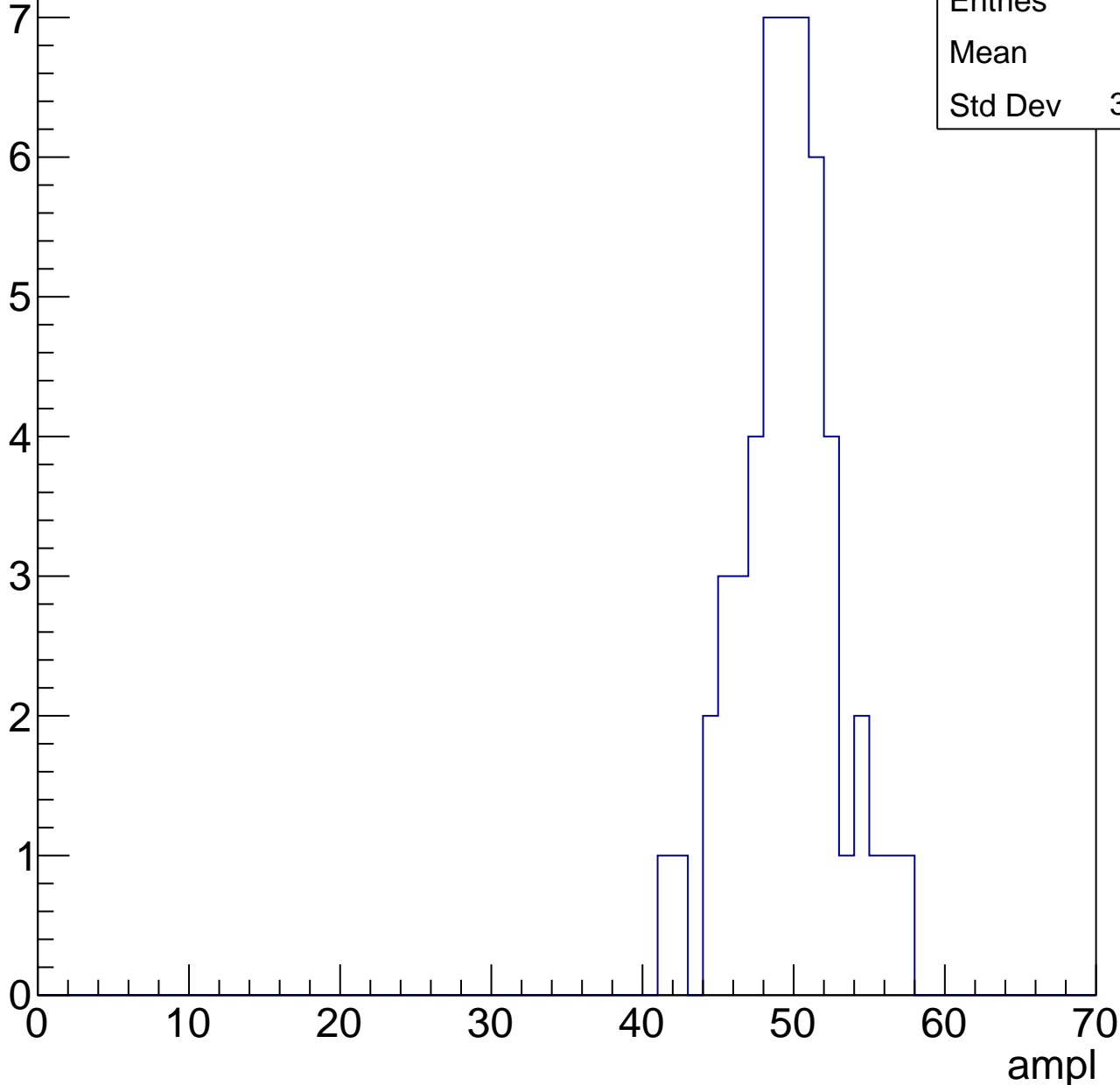


# B1L000S, U24-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

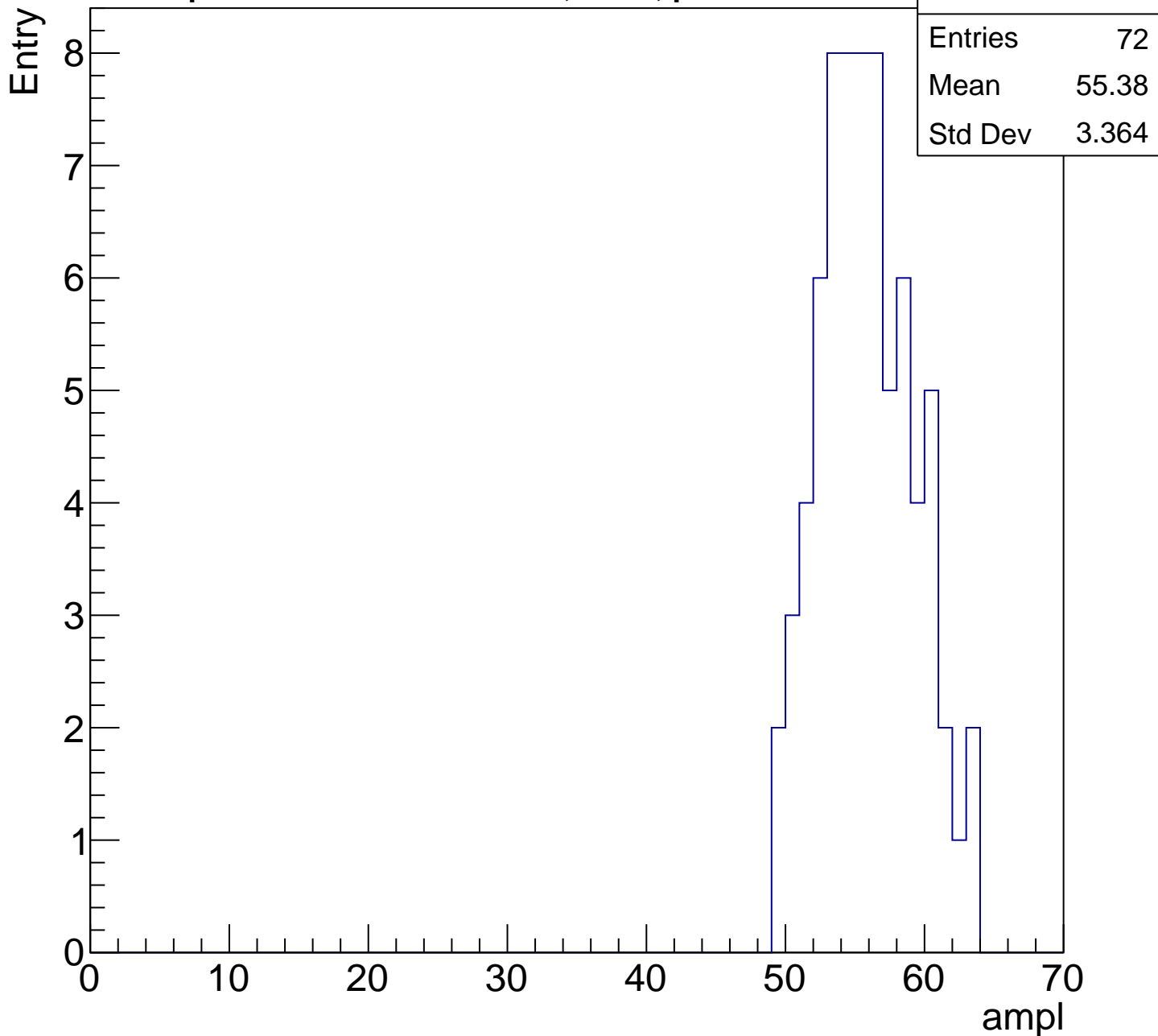
Entry

Entries	51
Mean	49.1
Std Dev	3.262



# B1L000S, U24-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

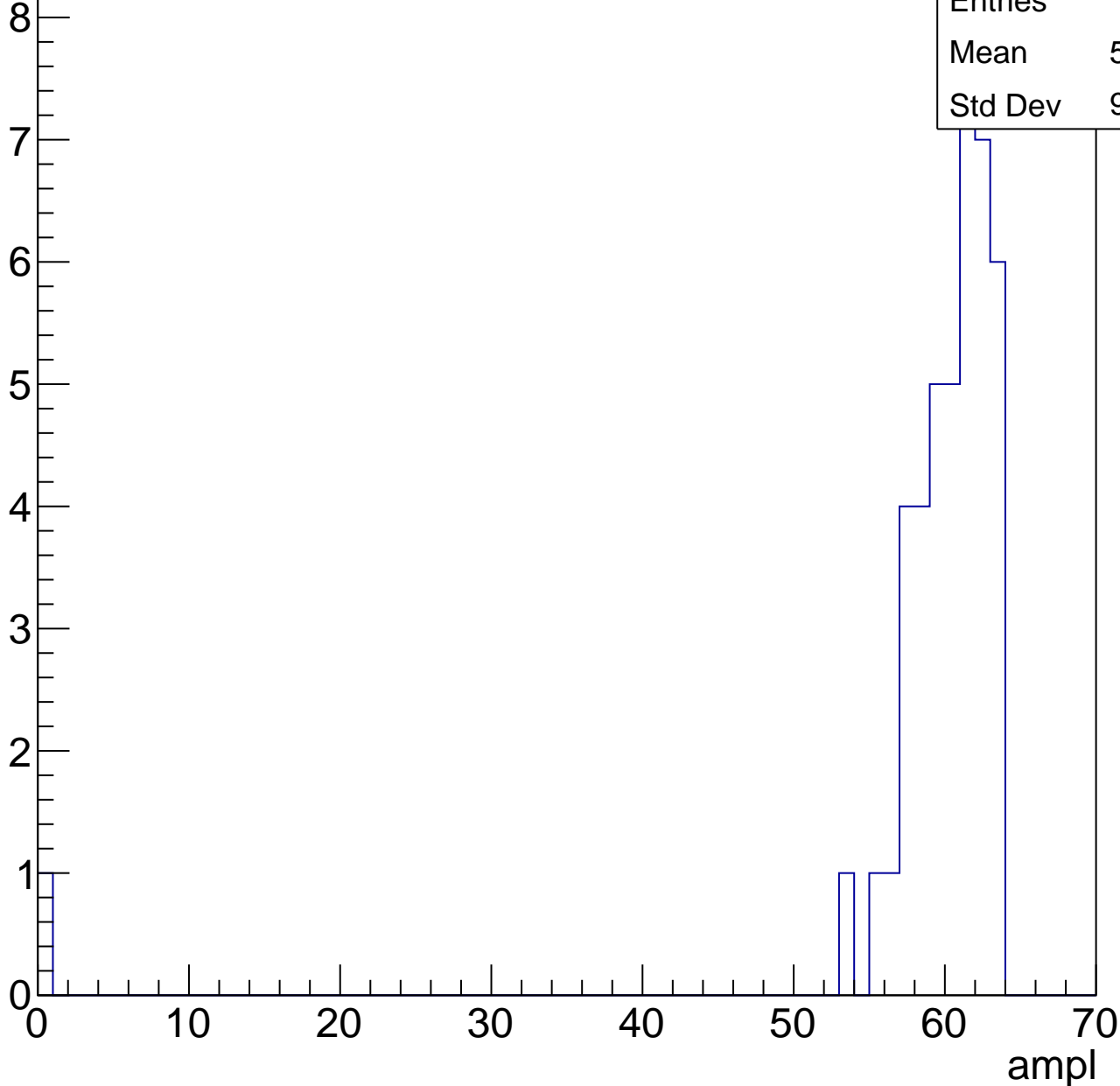


# B1L000S, U24-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

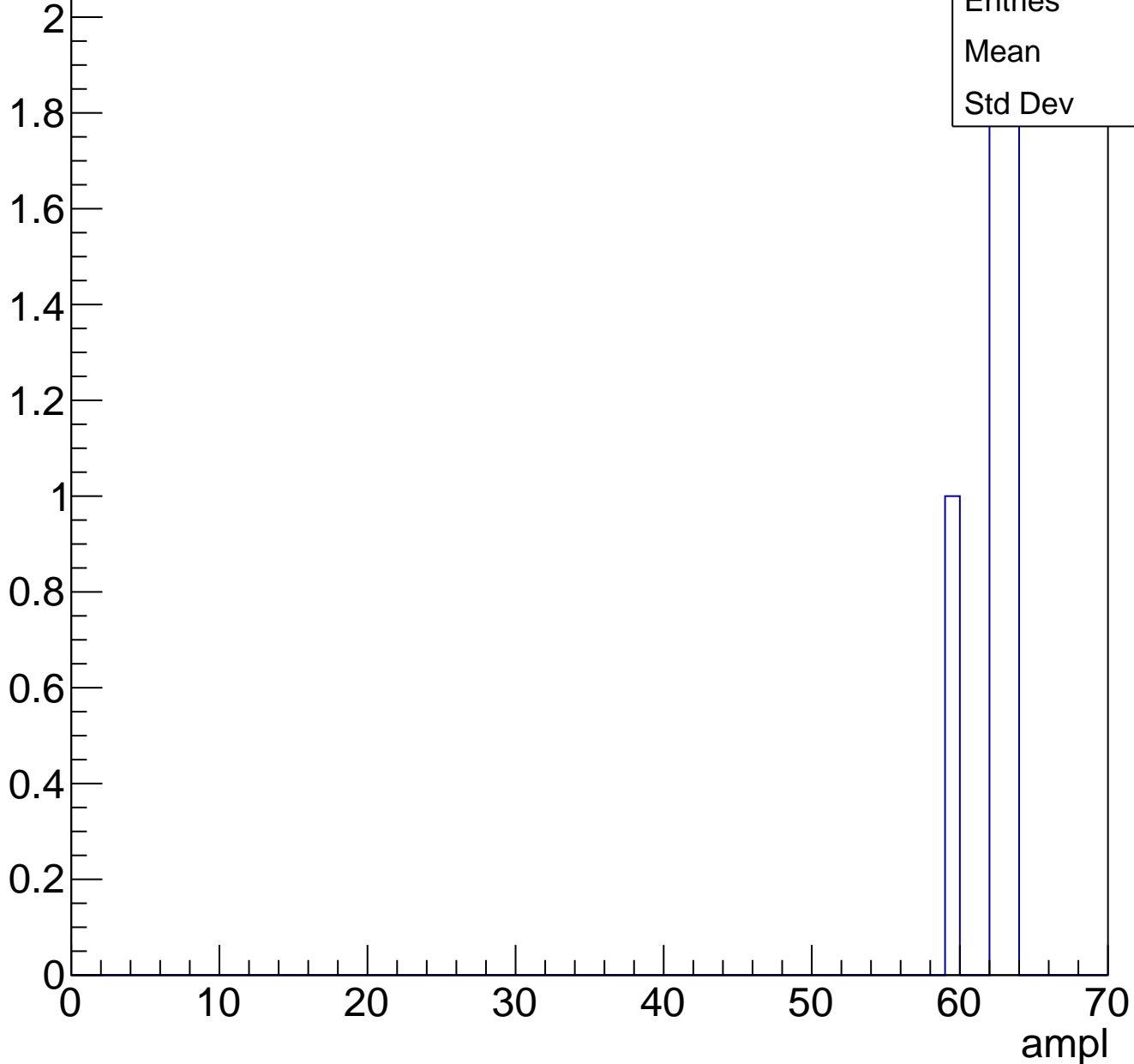
Entries	43
Mean	58.58
Std Dev	9.339



# B1L000S, U24-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch6, adc0

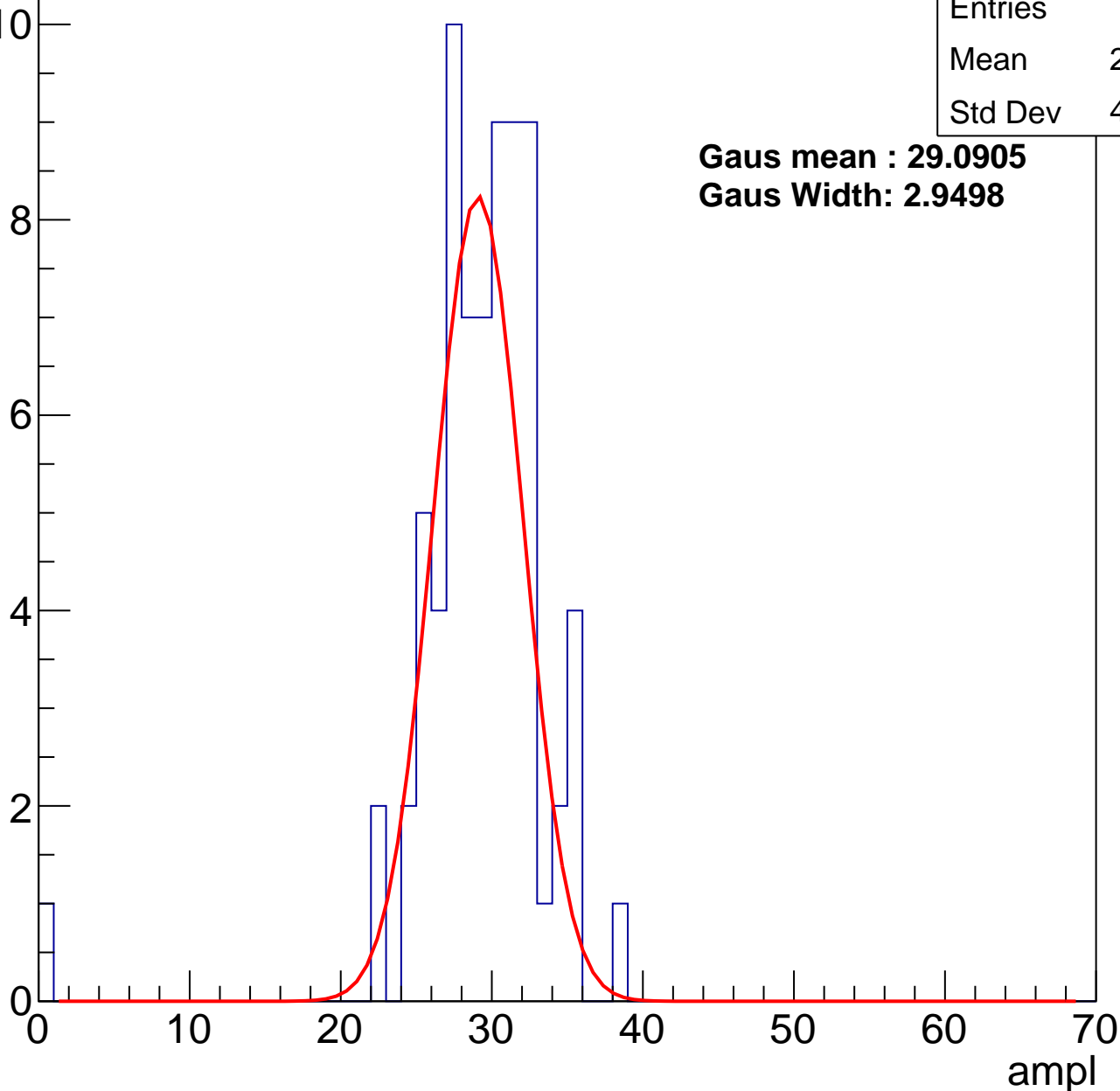
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.85
Std Dev	4.634

**Gaus mean : 29.0905**

**Gaus Width: 2.9498**



# B1L000S, U24-ch6, adc1

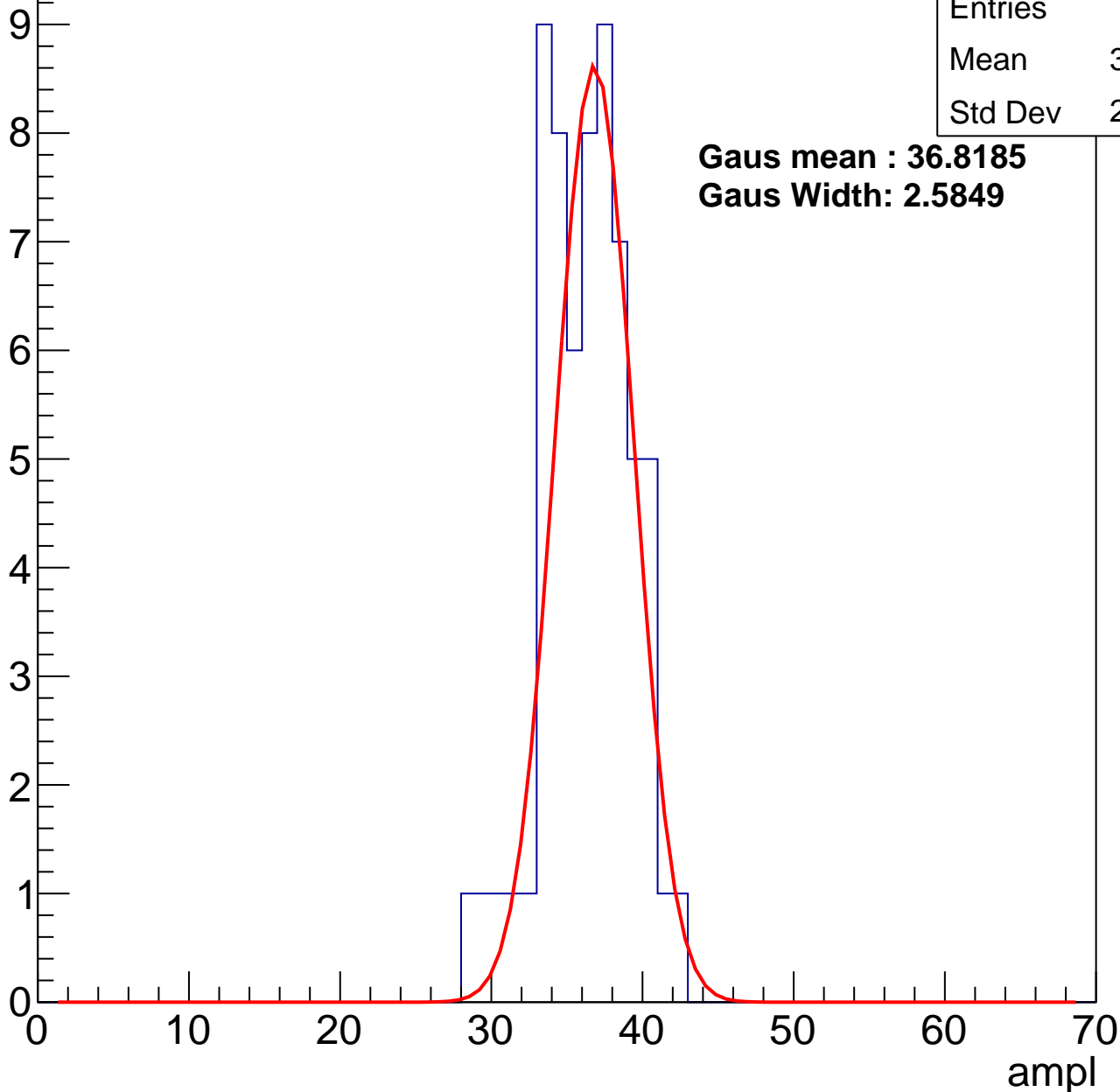
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.84
Std Dev	2.879

**Gaus mean : 36.8185**

**Gaus Width: 2.5849**



# B1L000S, U24-ch6, adc2

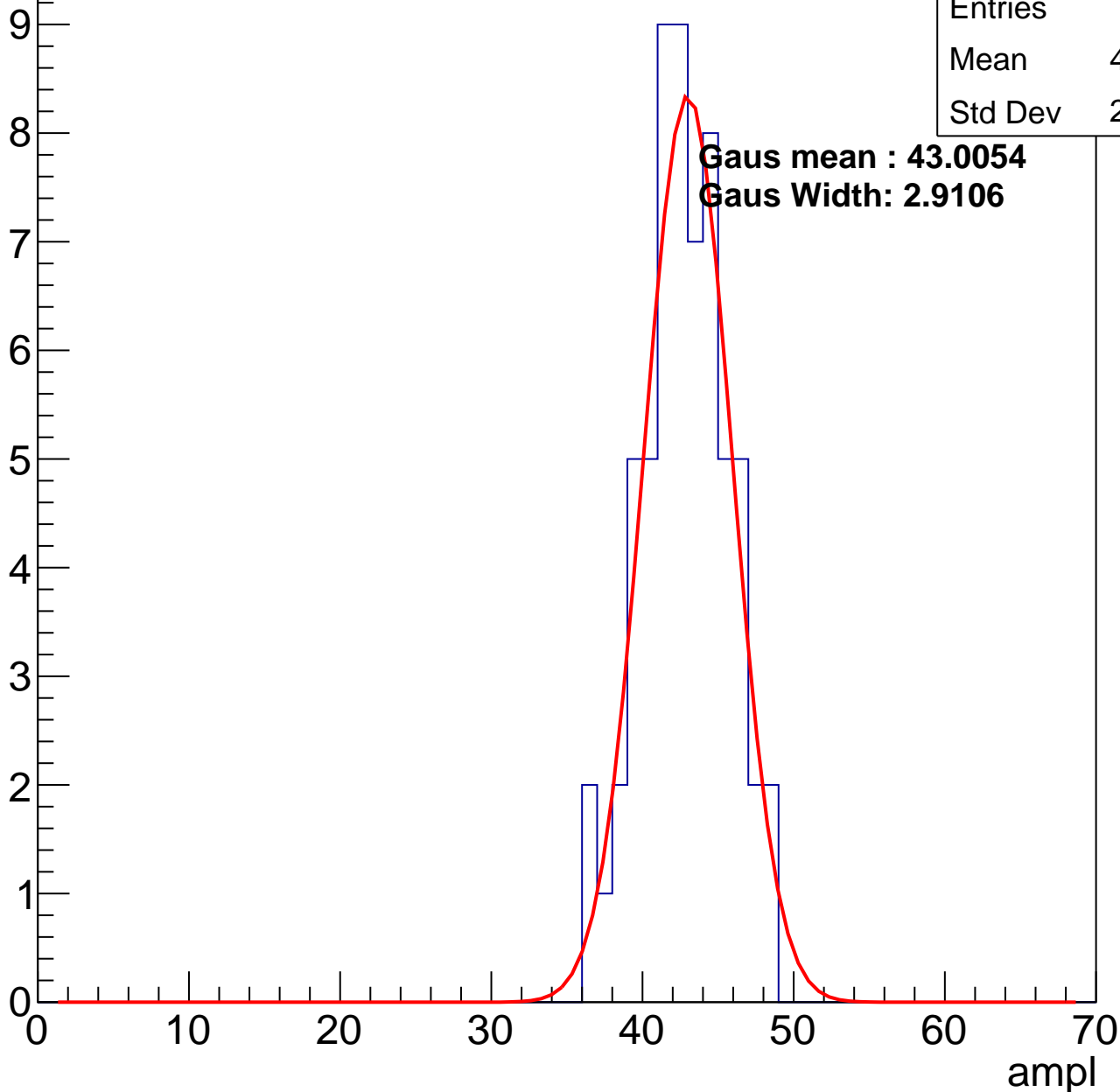
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.34
Std Dev	2.788

**Gaus mean : 43.0054**

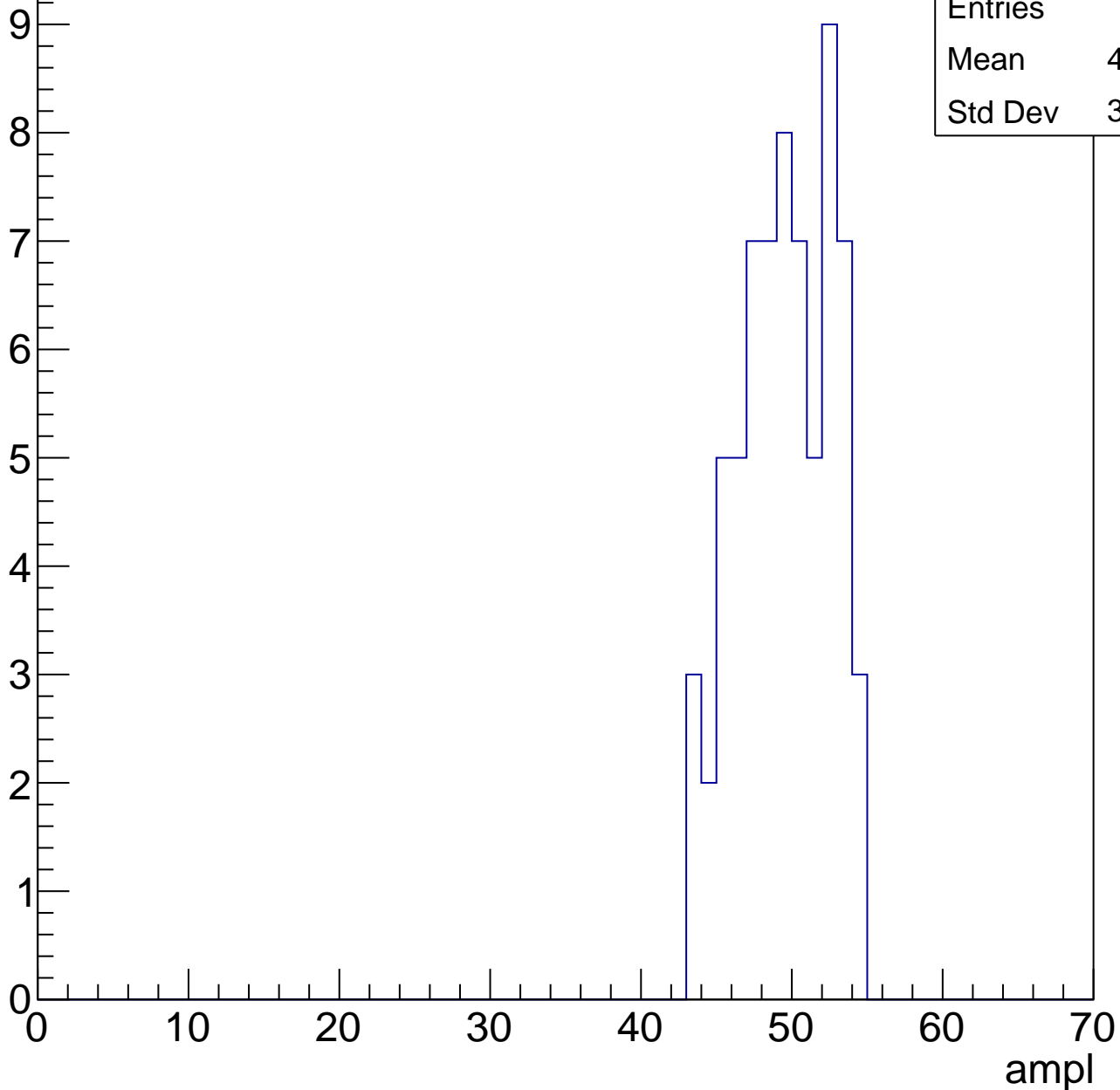
**Gaus Width: 2.9106**



# B1L000S, U24-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

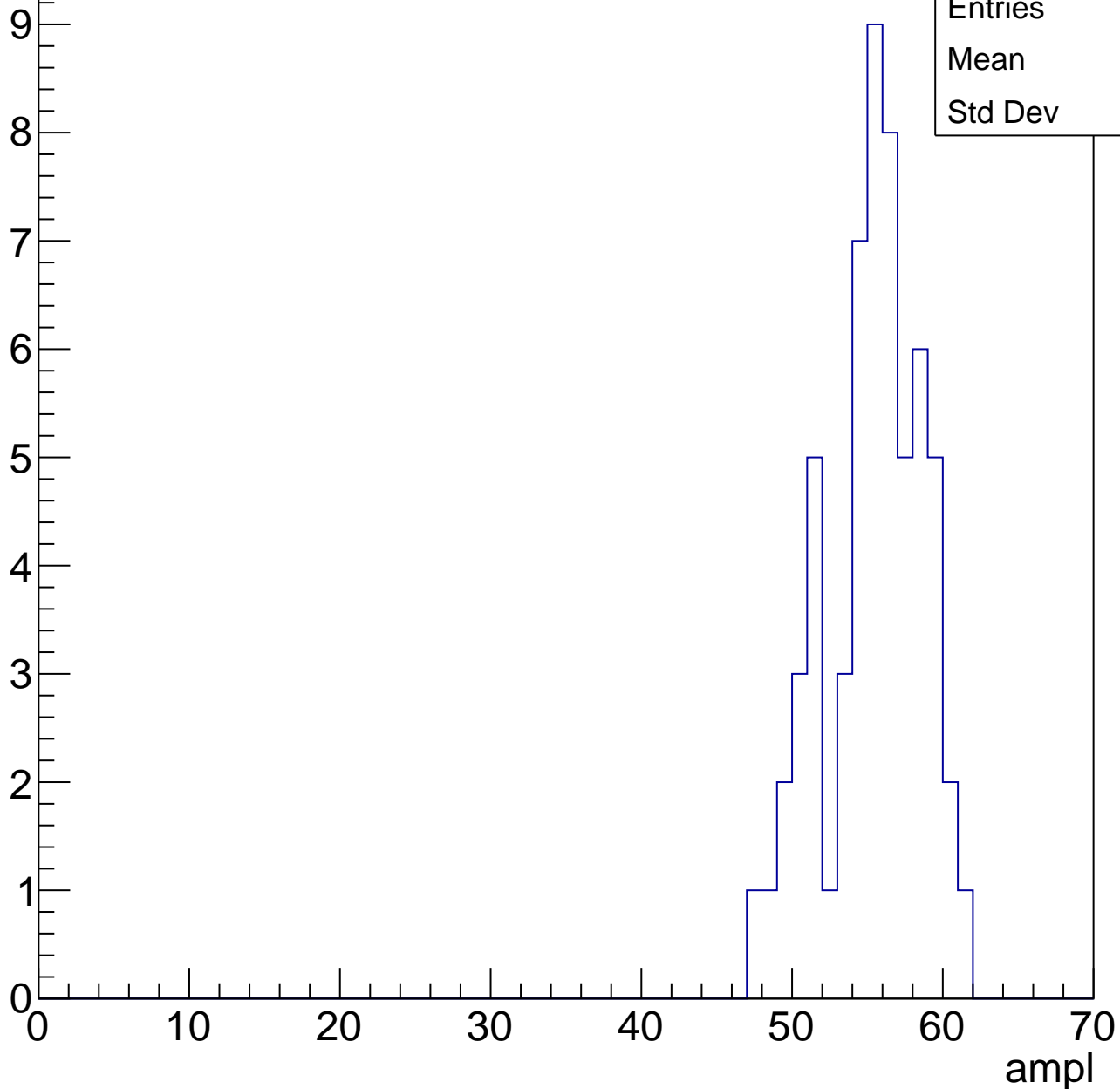


Entries	68
Mean	49.04
Std Dev	3.002

# B1L000S, U24-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

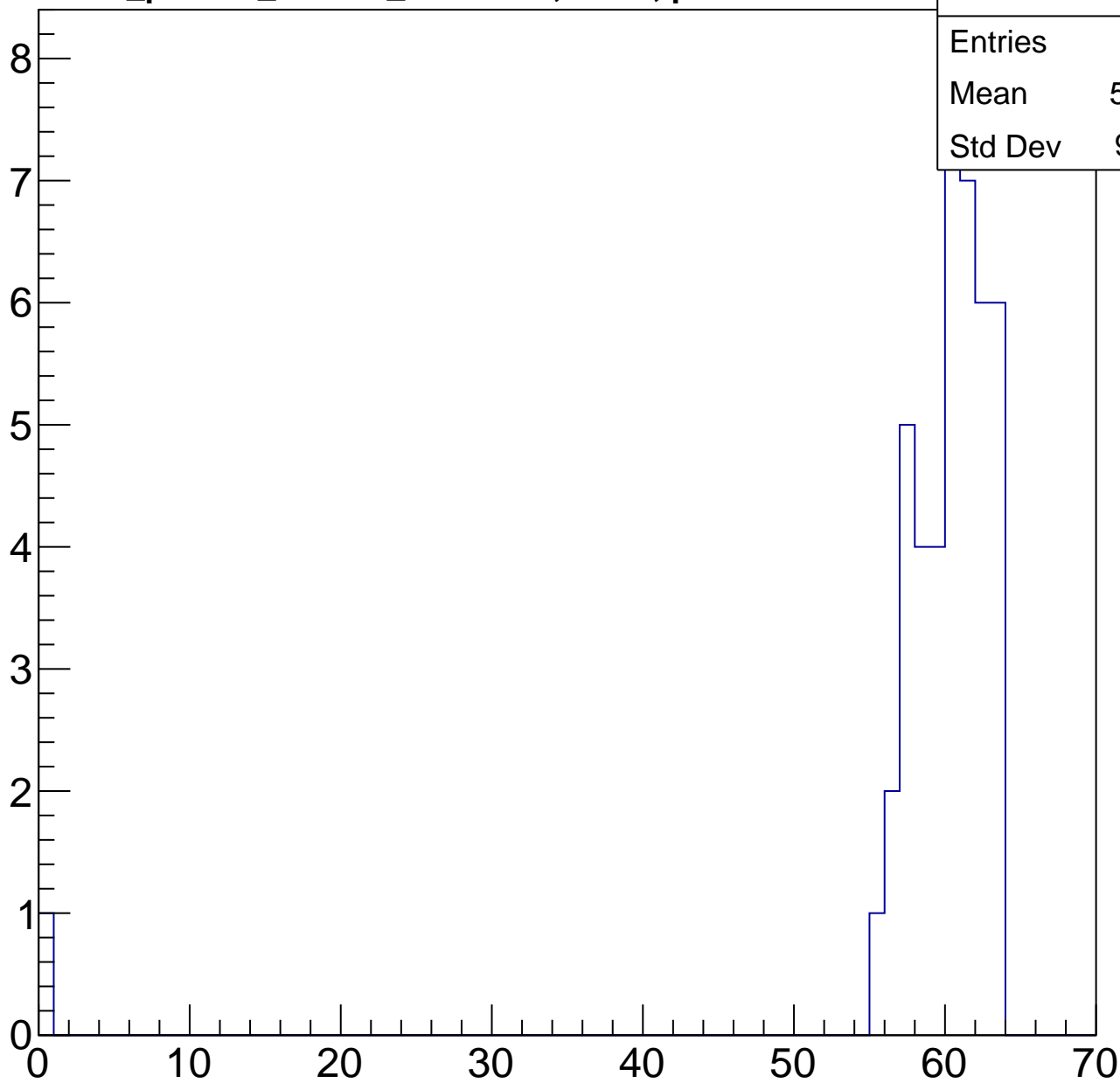
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	58.57
Std Dev	9.191

ampl

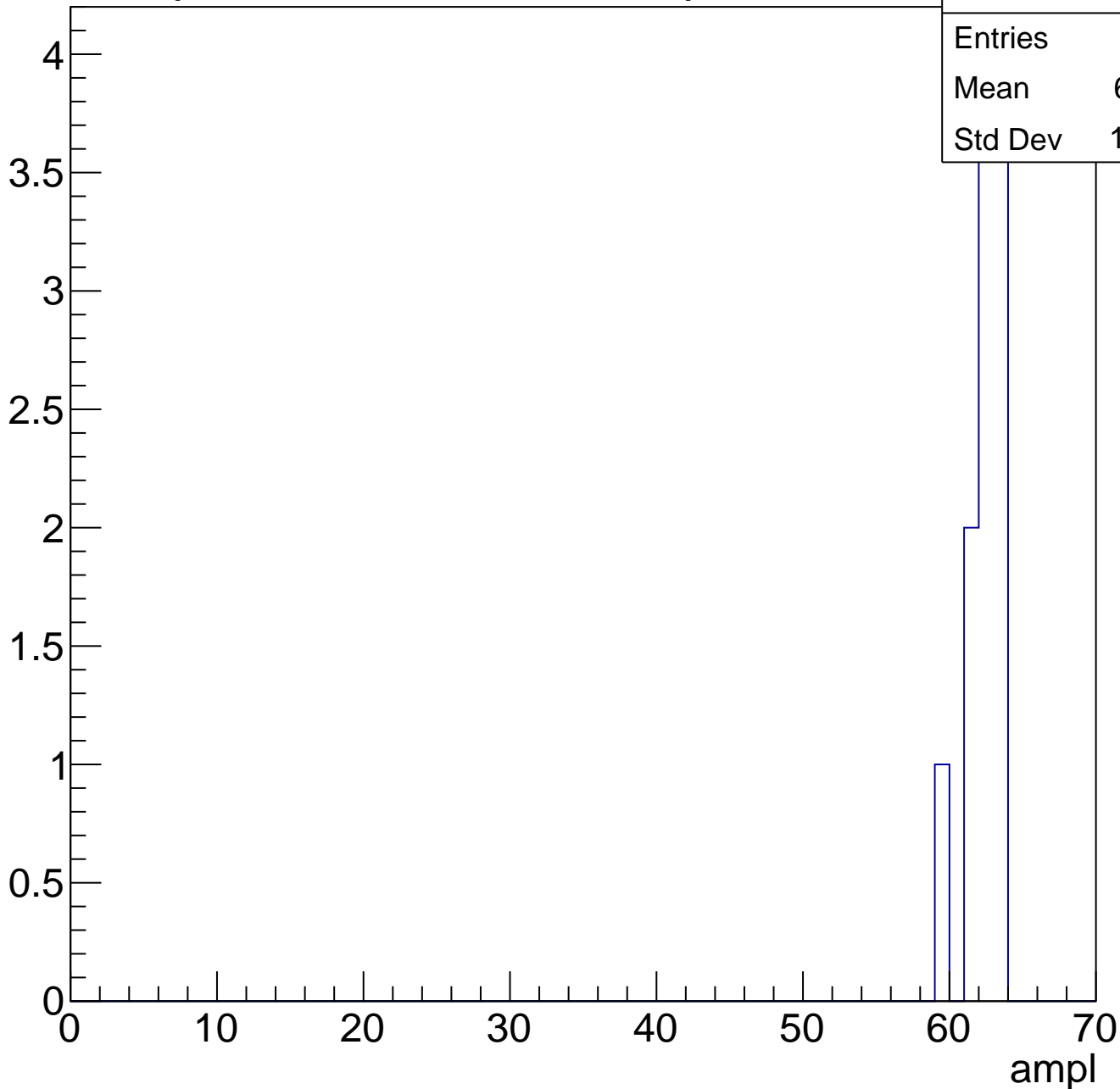
0 10 20 30 40 50 60 70



# B1L000S, U24-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch7, adc0

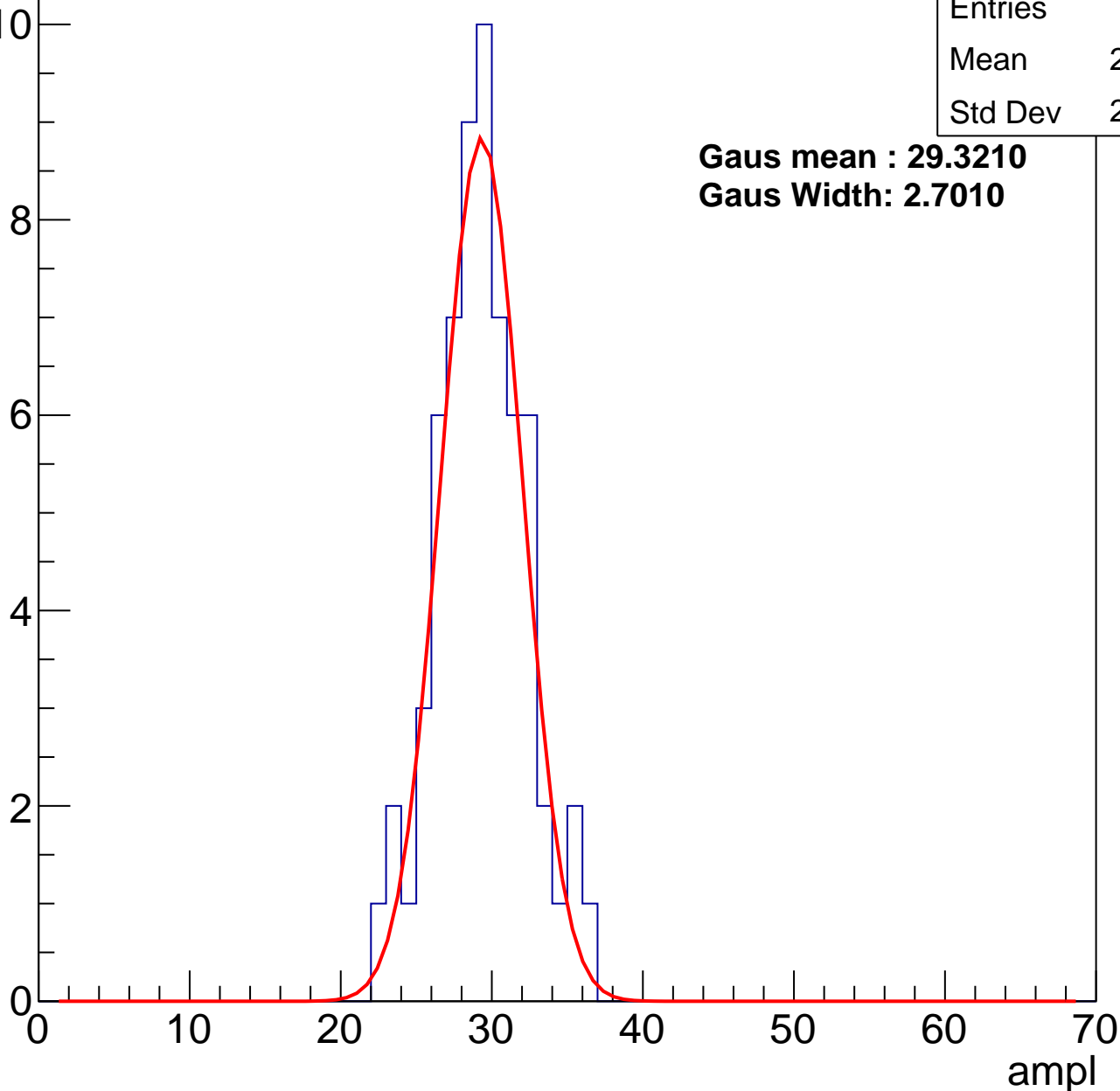
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.88
Std Dev	2.924

**Gaus mean : 29.3210**

**Gaus Width: 2.7010**



# B1L000S, U24-ch7, adc1

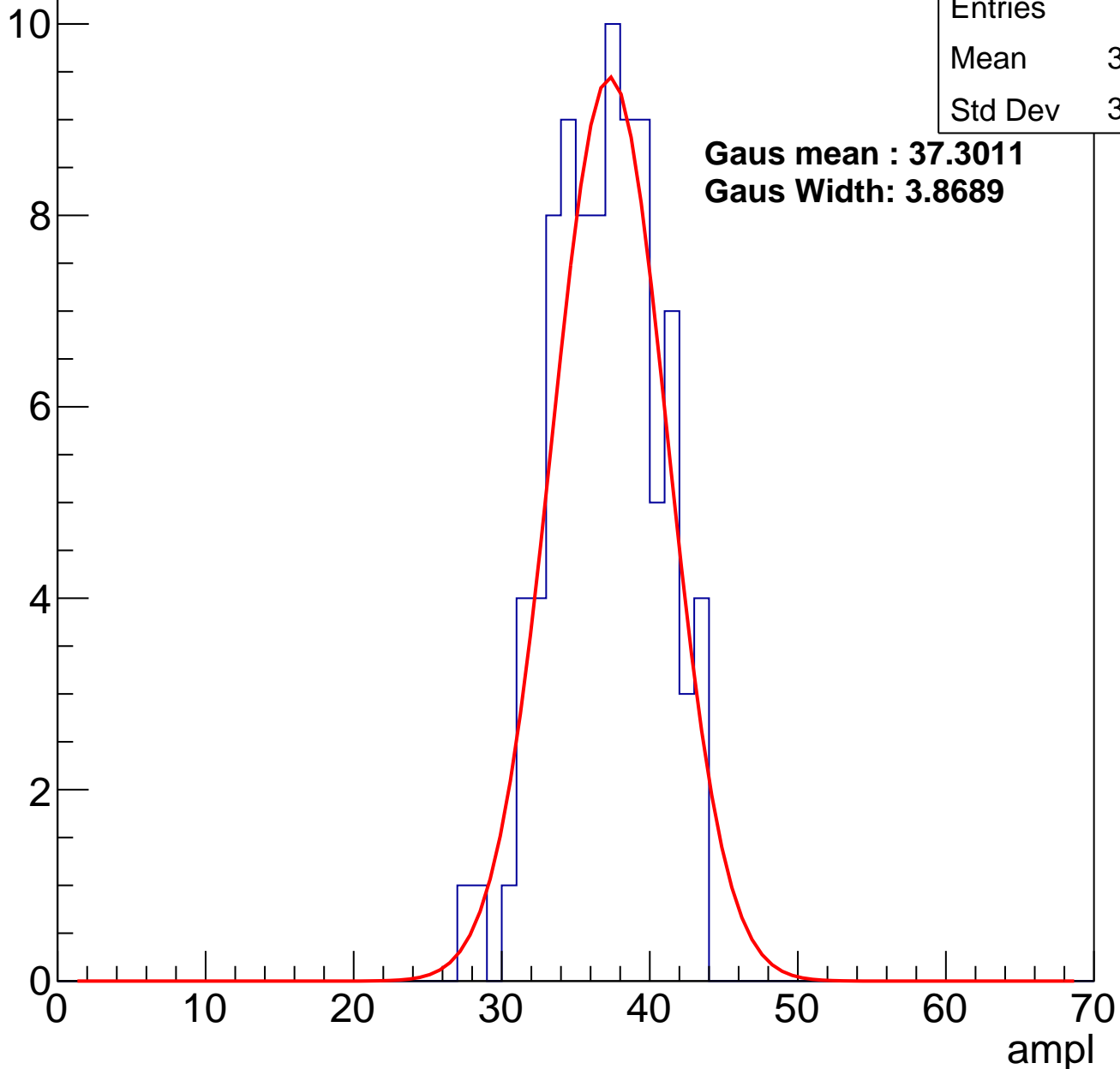
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	91
Mean	36.52
Std Dev	3.512

**Gaus mean : 37.3011**

**Gaus Width: 3.8689**

Entry



# B1L000S, U24-ch7, adc2

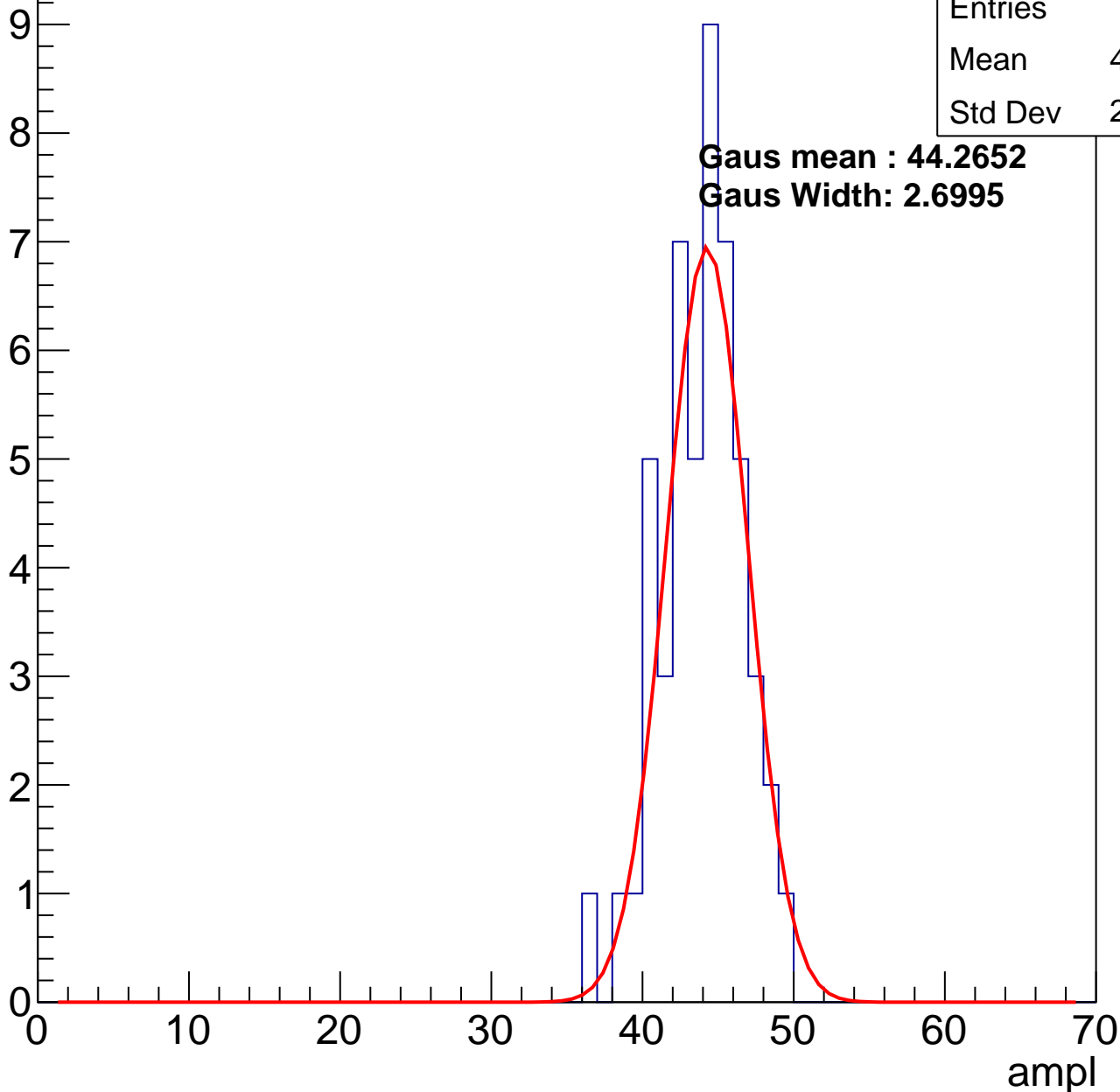
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	43.44
Std Dev	2.684

**Gaus mean : 44.2652**

**Gaus Width: 2.6995**

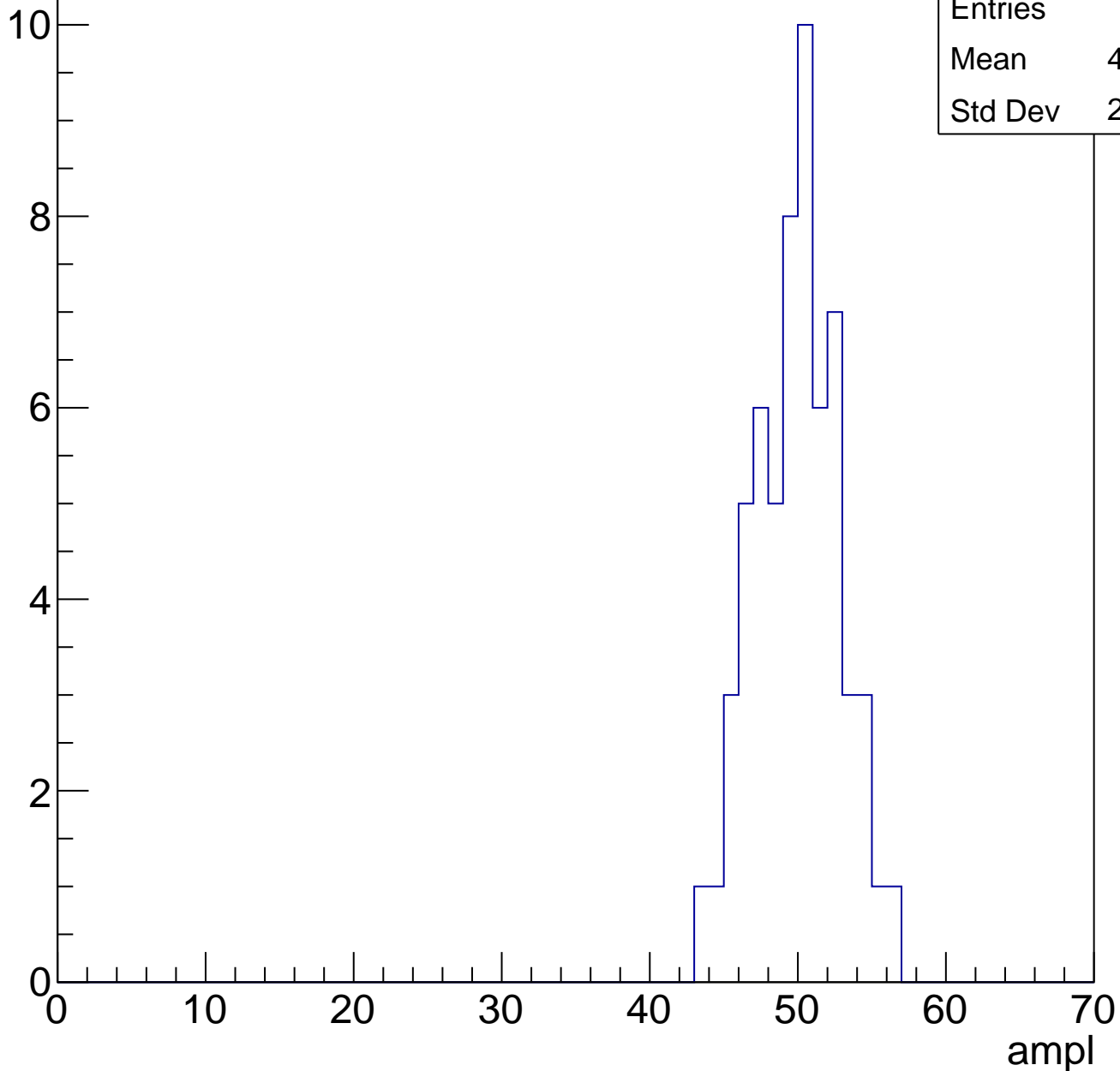


# B1L000S, U24-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	49.47
Std Dev	2.813

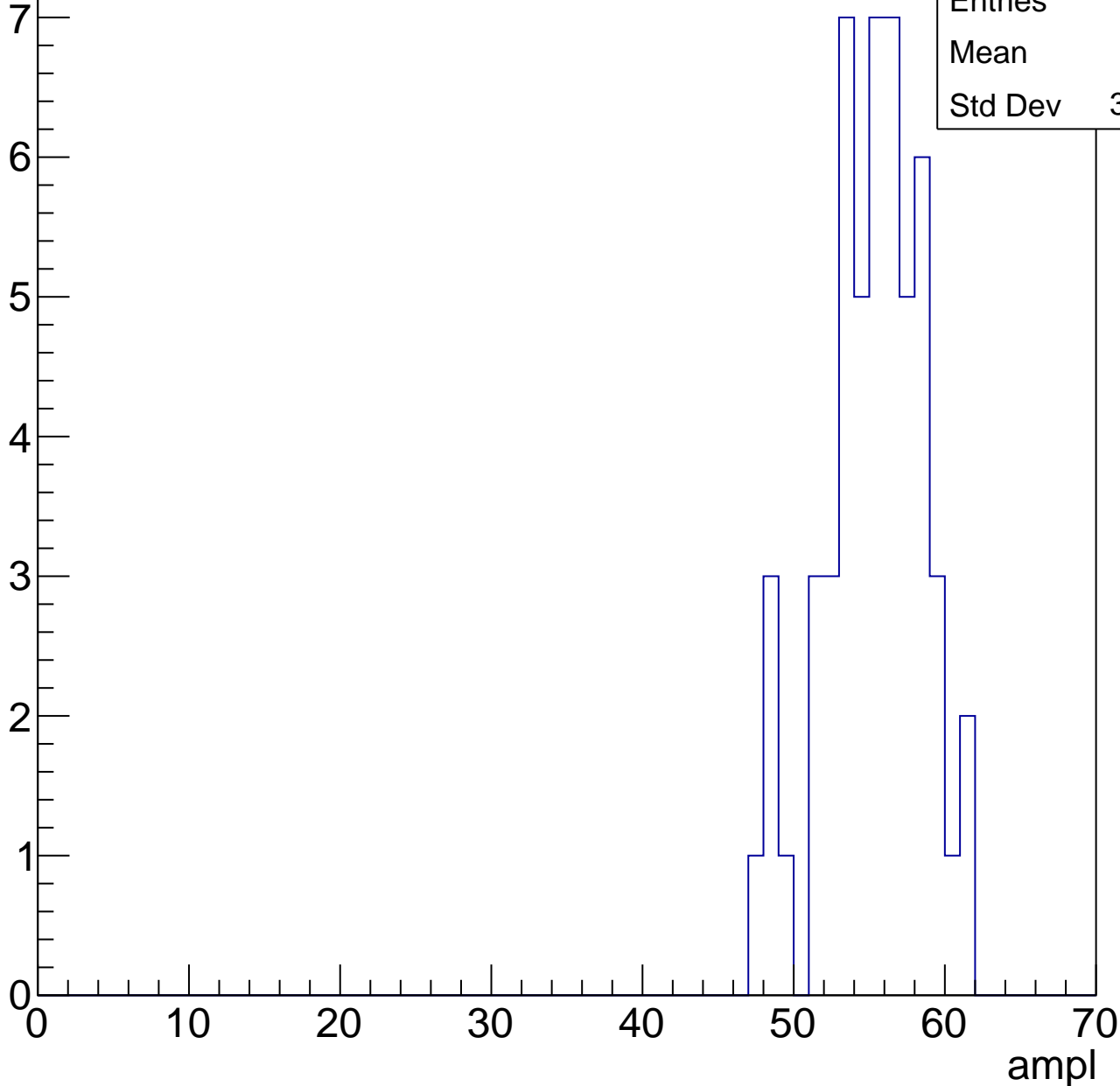


# B1L000S, U24-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.8
Std Dev	3.274



# B1L000S, U24-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

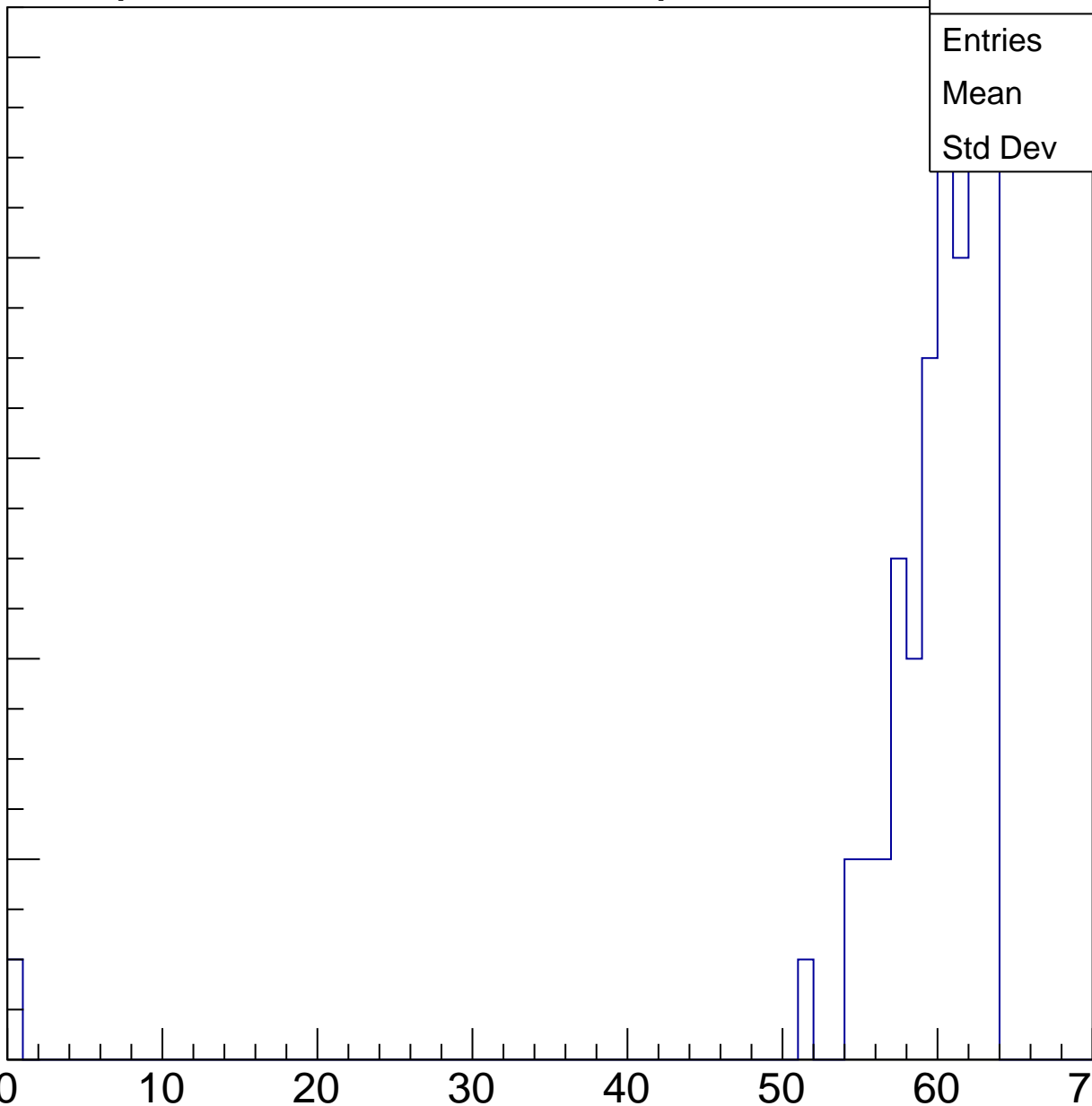
0

Entries	60
---------	----

Mean	58.78
------	-------

Std Dev	8.112
---------	-------

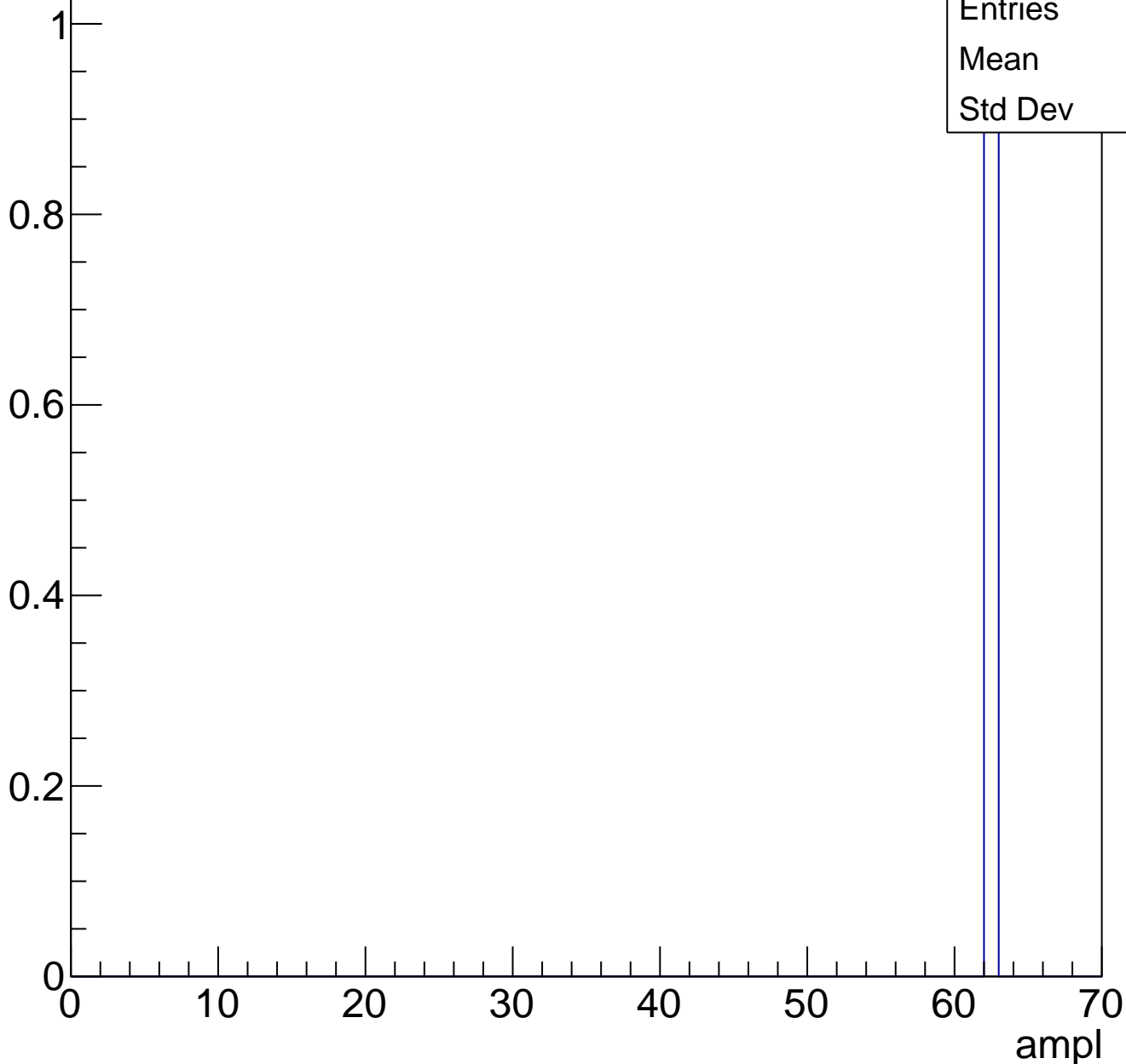
ampl



# B1L000S, U24-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch8, adc0

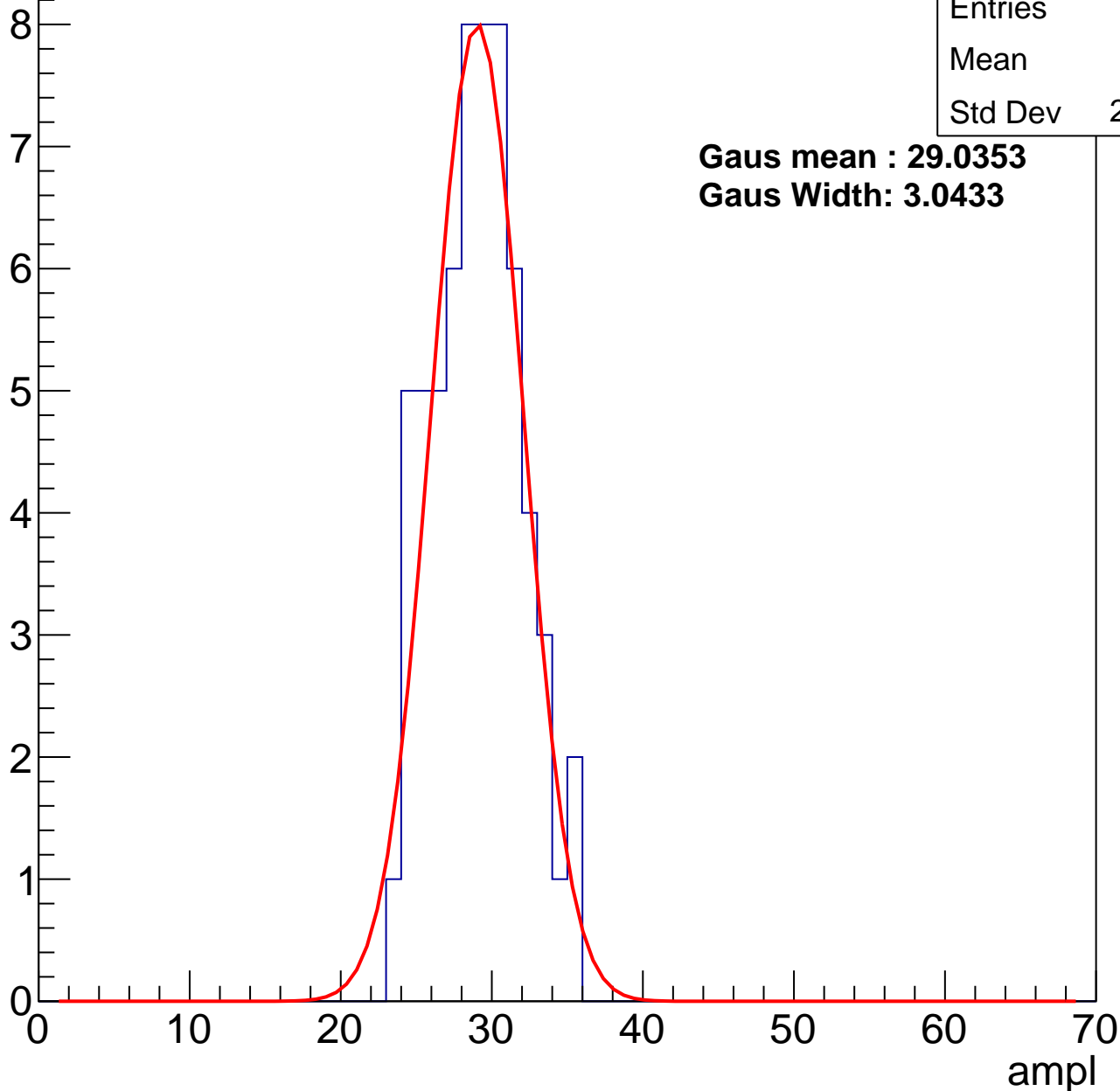
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.6
Std Dev	2.899

**Gaus mean : 29.0353**

**Gaus Width: 3.0433**



# B1L000S, U24-ch8, adc1

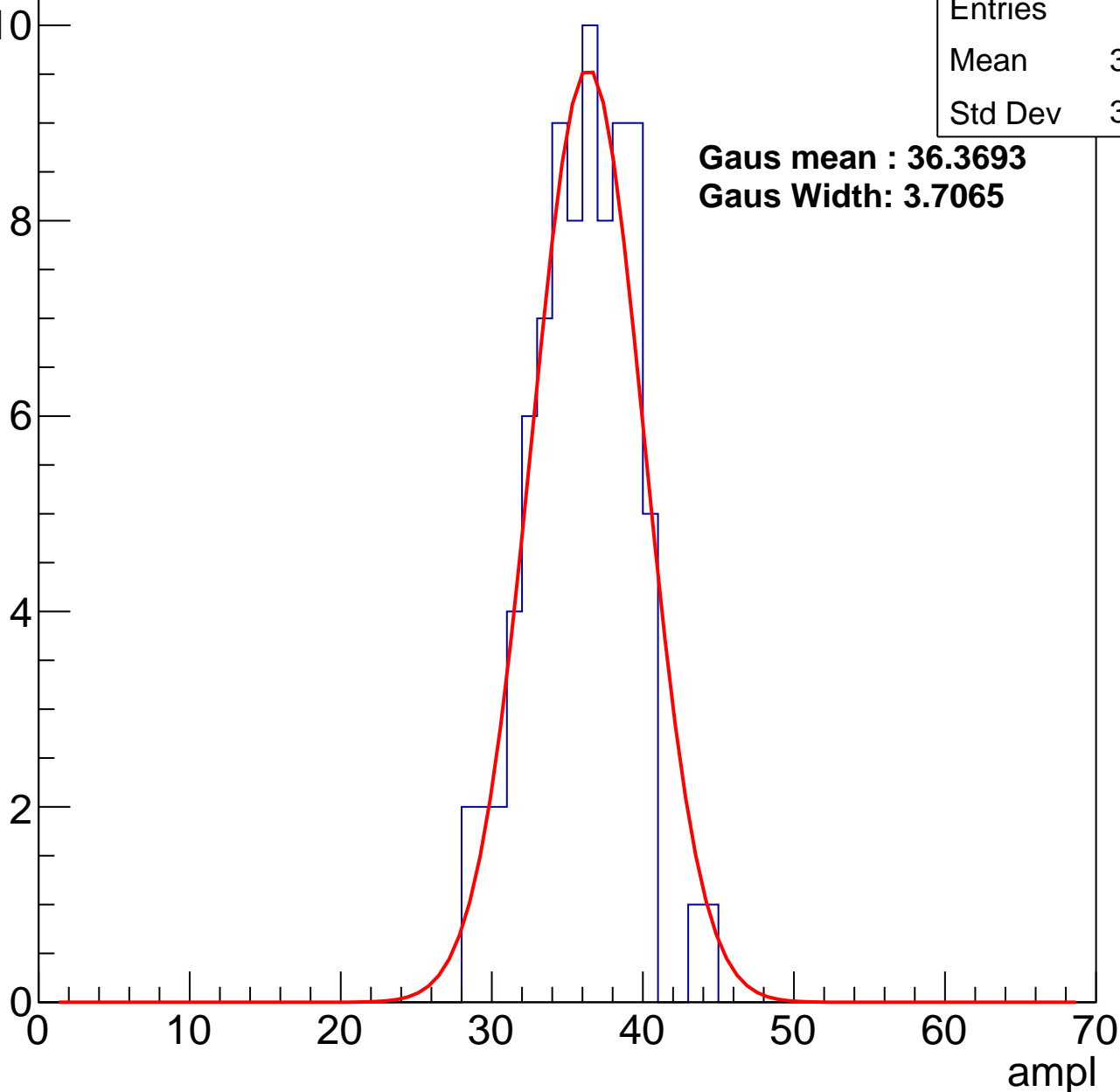
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	35.46
Std Dev	3.283

**Gaus mean : 36.3693**

**Gaus Width: 3.7065**



# B1L000S, U24-ch8, adc2

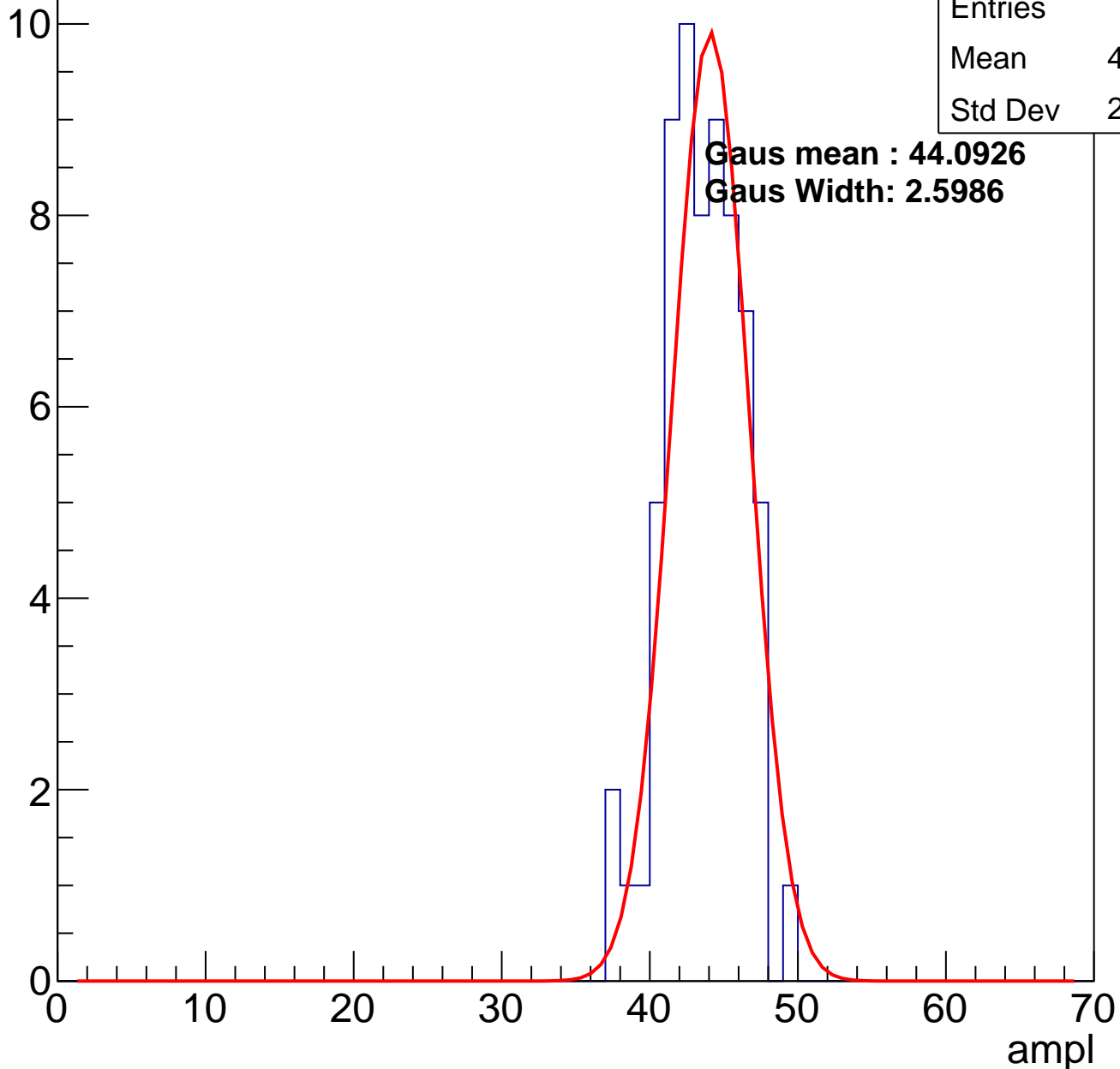
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	43.12
Std Dev	2.532

**Gaus mean : 44.0926**

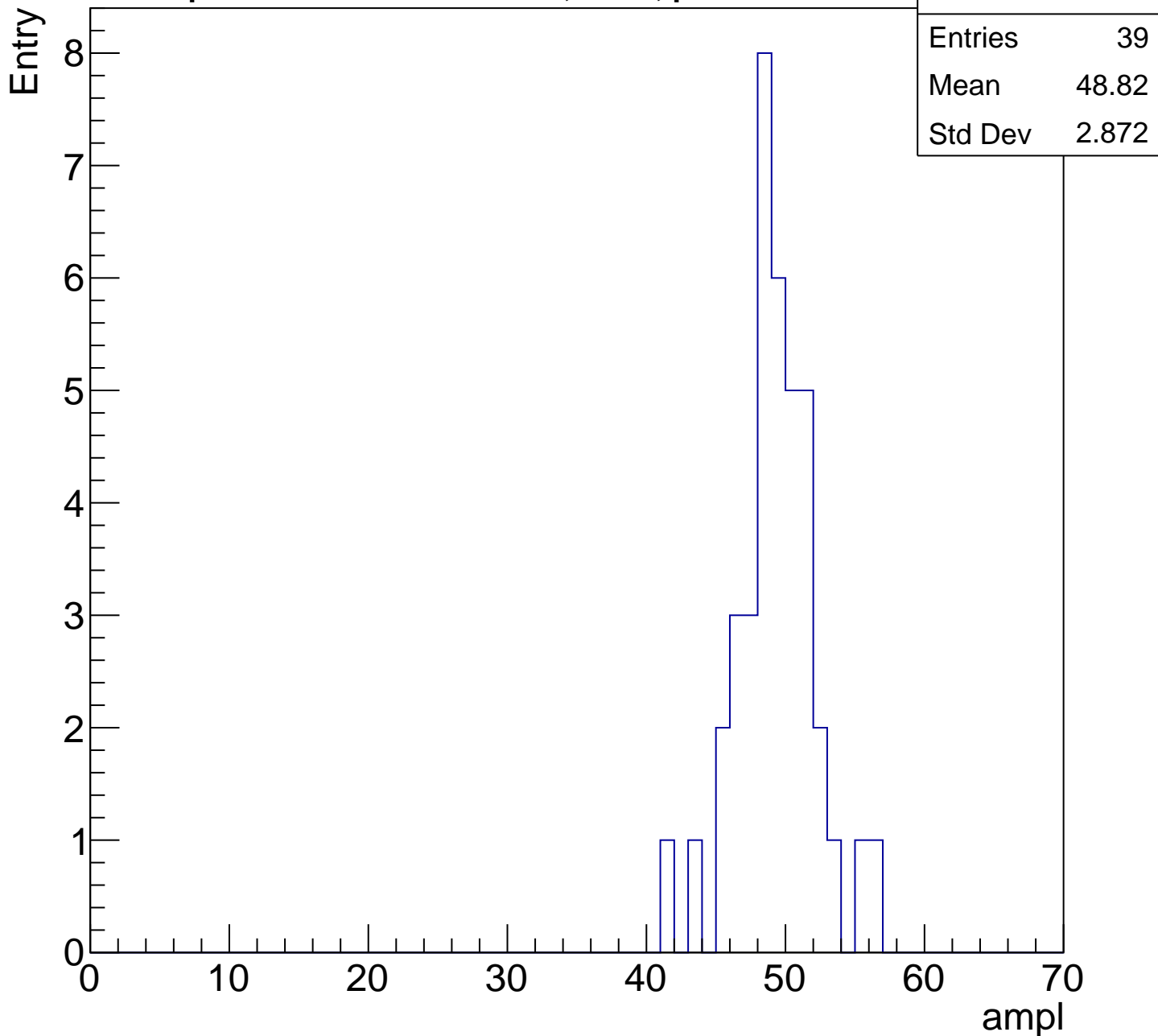
**Gaus Width: 2.5986**

Entry



# B1L000S, U24-ch8, adc3

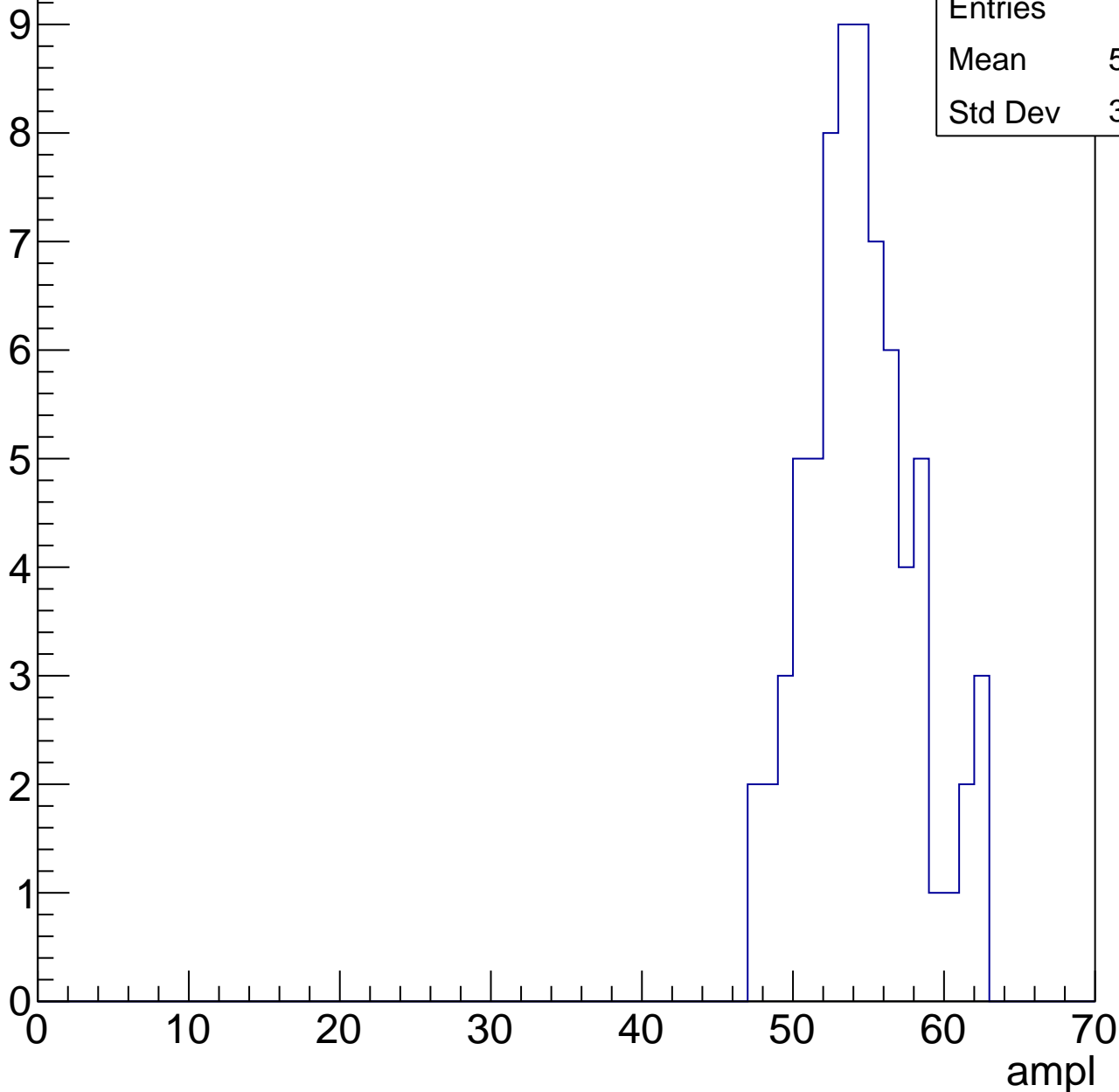
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

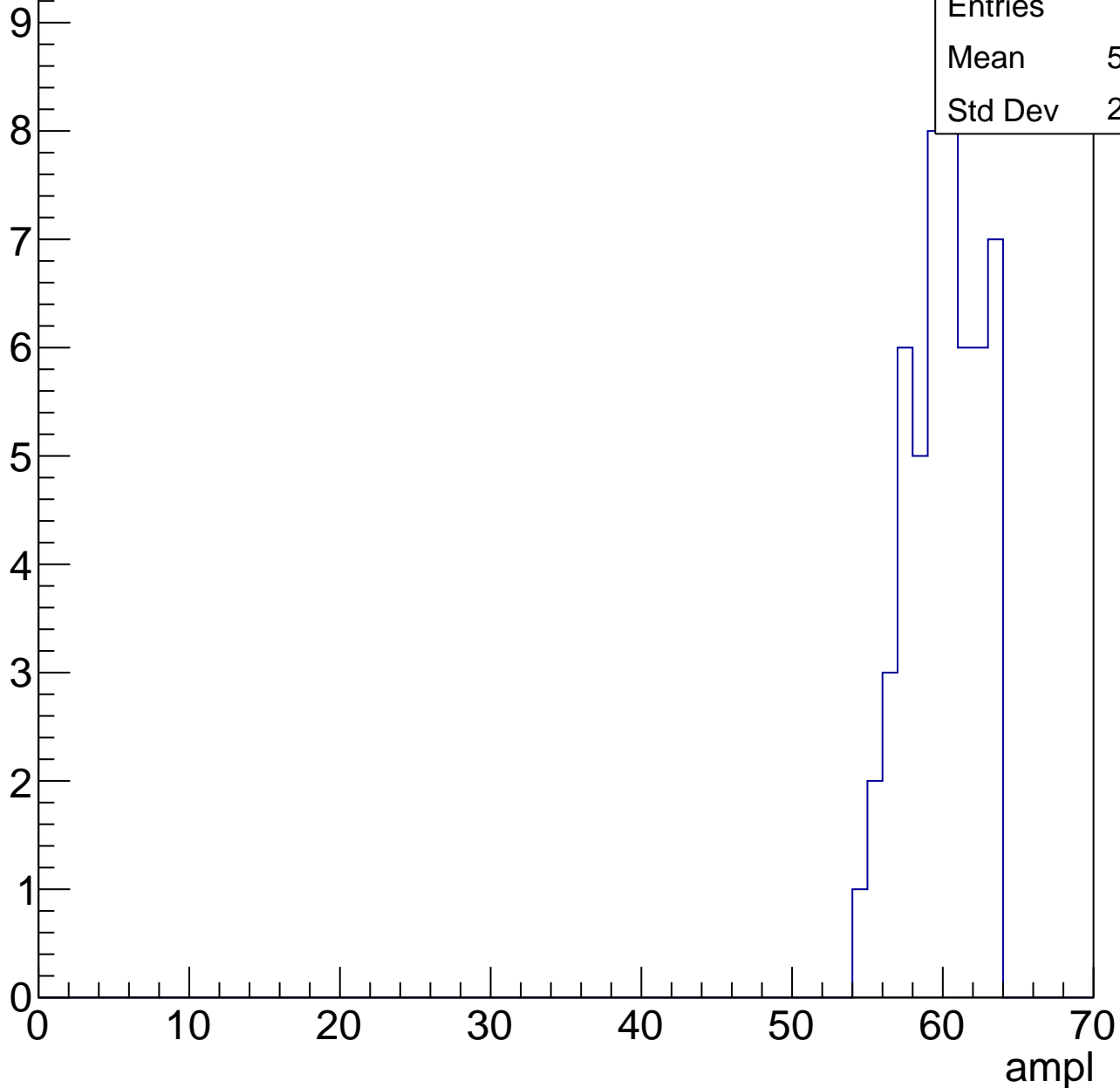
Entry



# B1L000S, U24-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

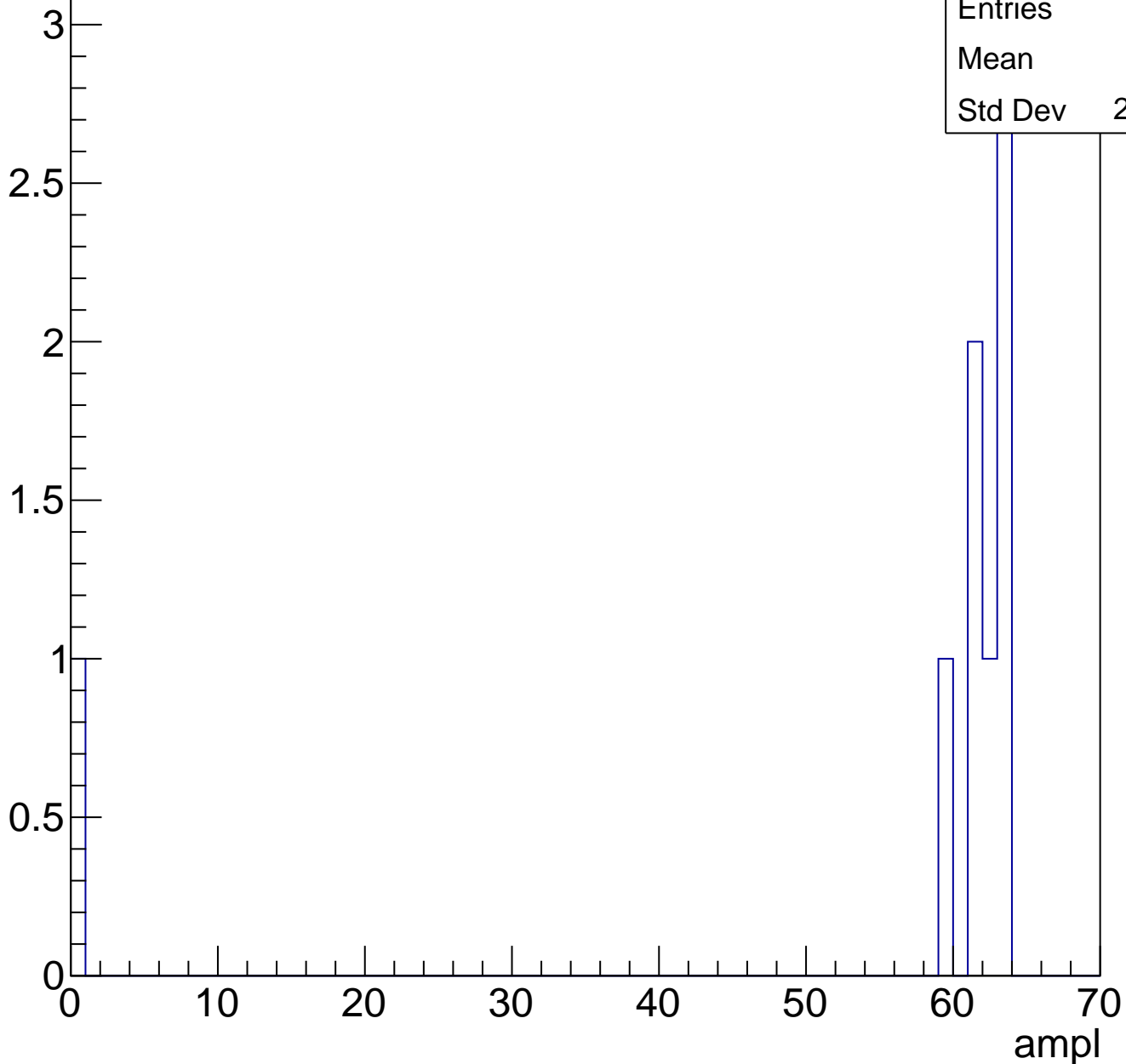
Entry



# B1L000S, U24-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch9, adc0

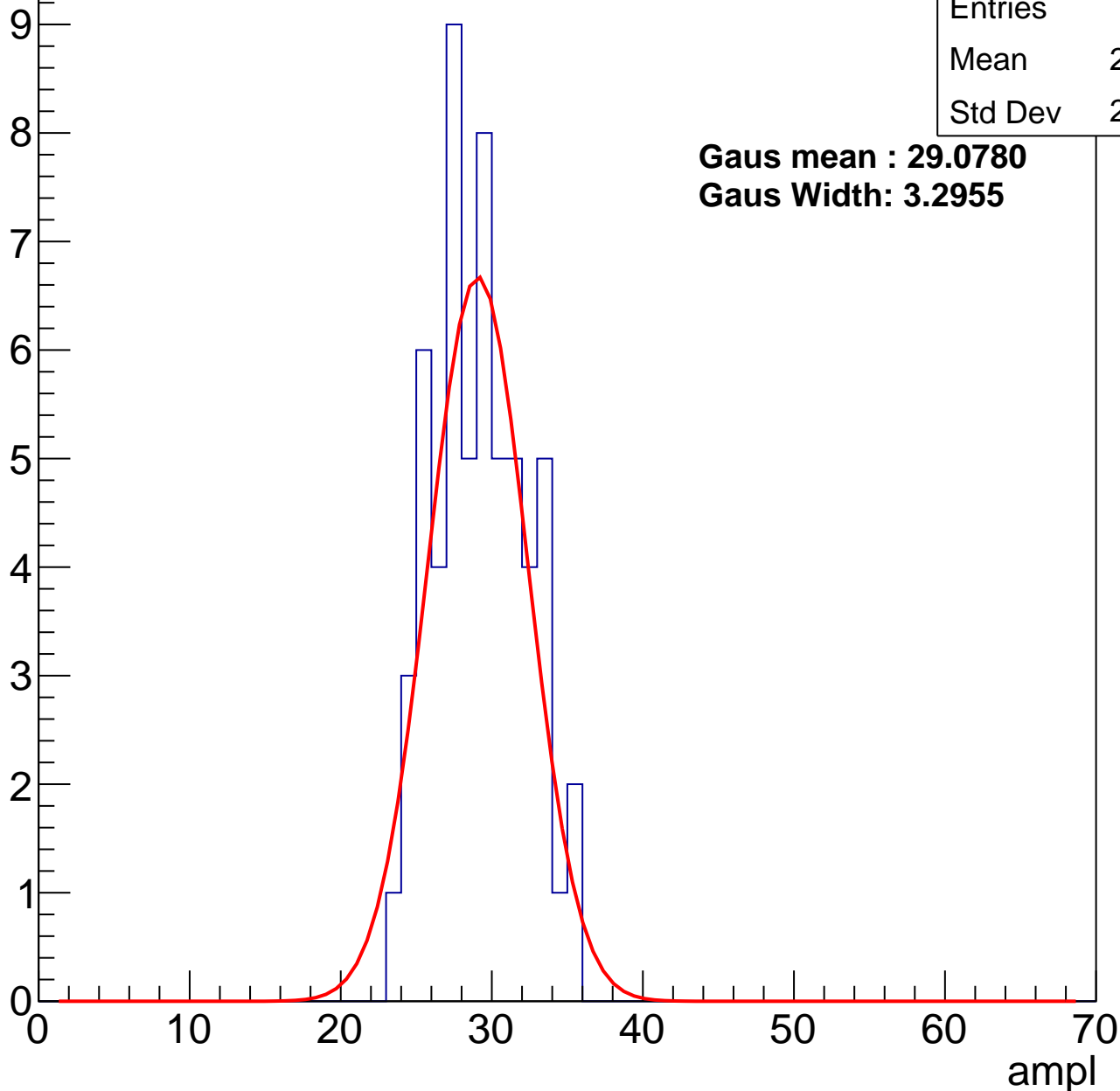
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	28.72
Std Dev	2.987

**Gaus mean : 29.0780**

**Gaus Width: 3.2955**



# B1L000S, U24-ch9, adc1

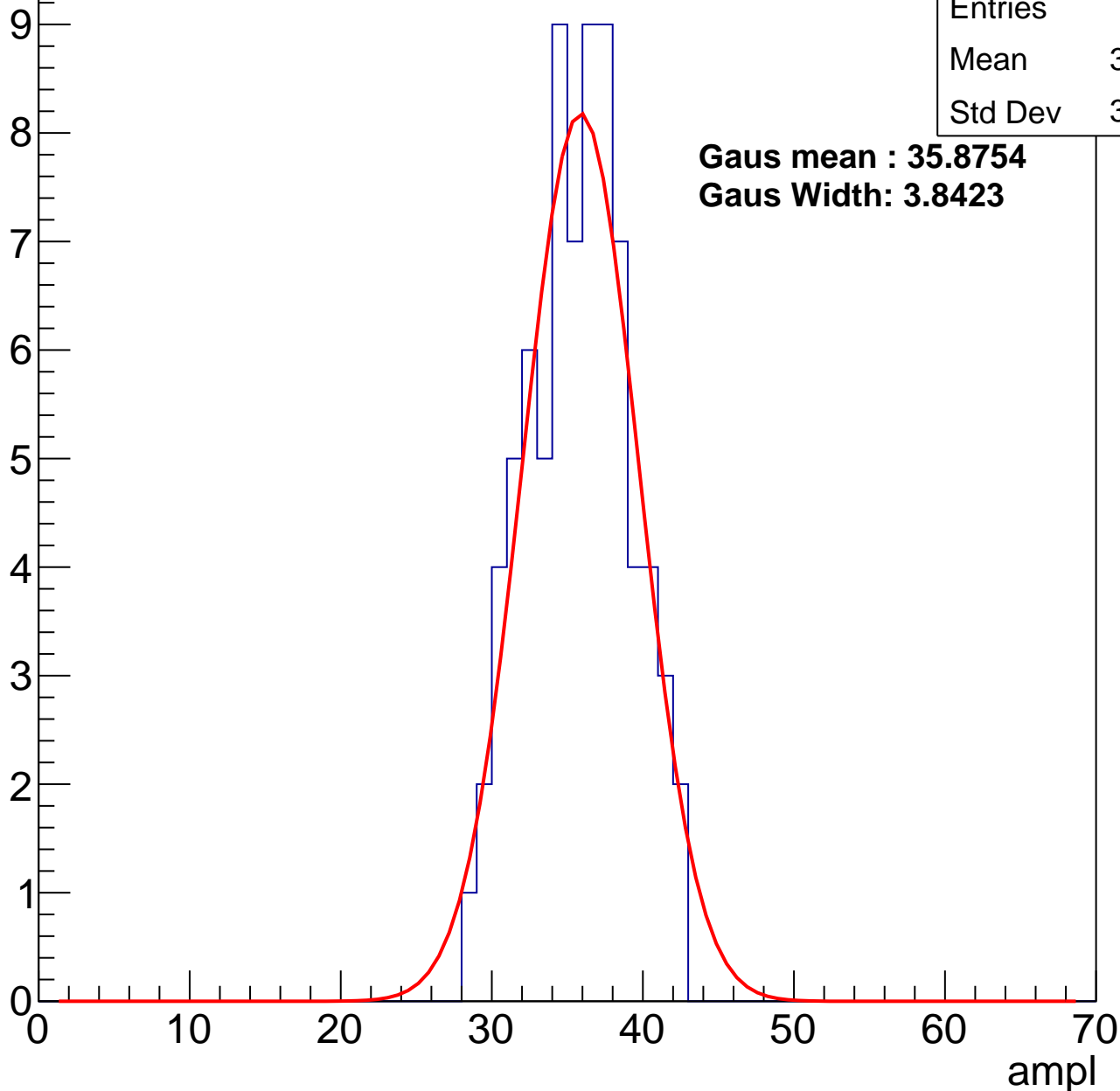
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	35.26
Std Dev	3.336

**Gaus mean : 35.8754**

**Gaus Width: 3.8423**



# B1L000S, U24-ch9, adc2

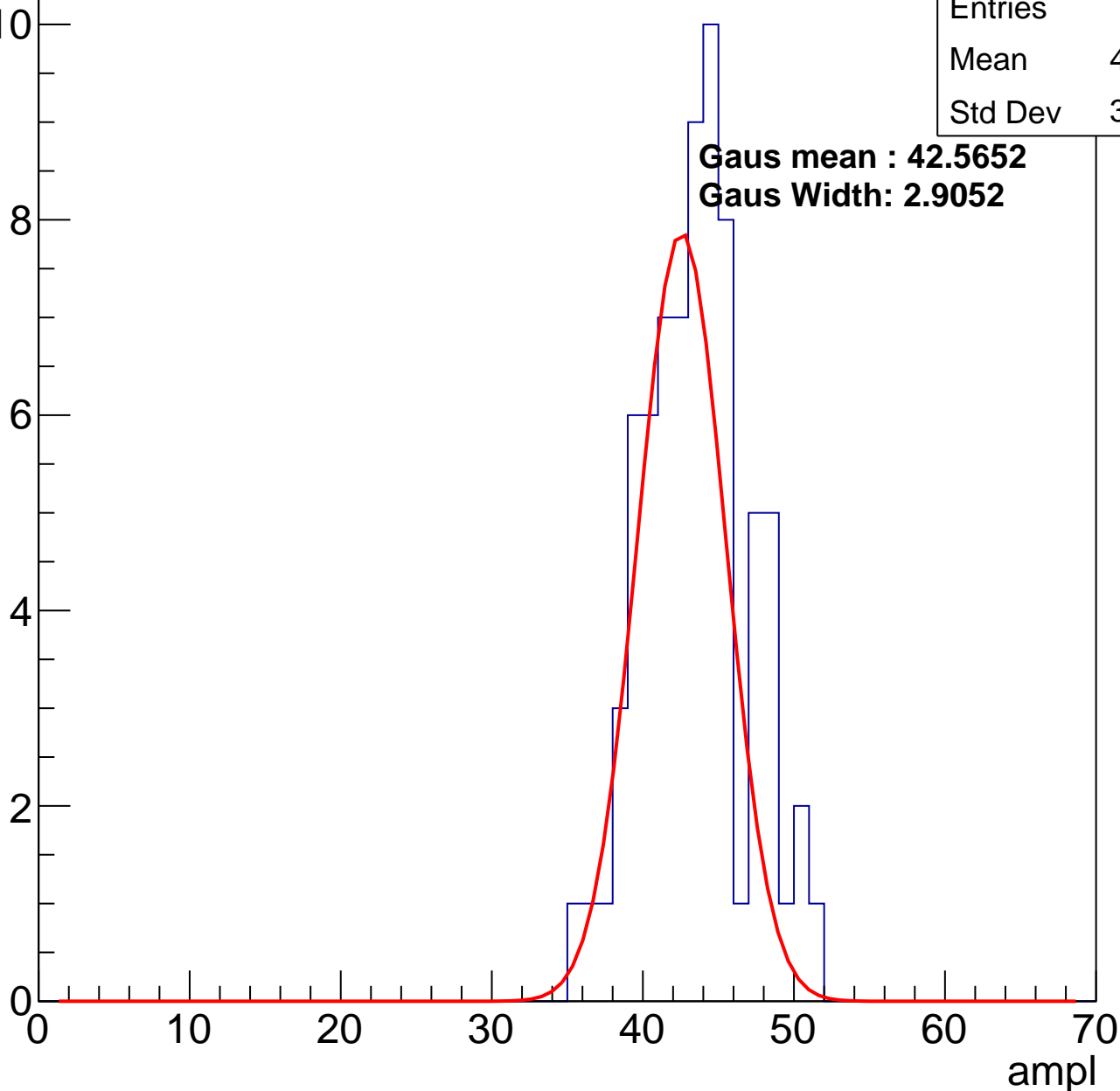
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	43.04
Std Dev	3.415

**Gaus mean : 42.5652**

**Gaus Width: 2.9052**

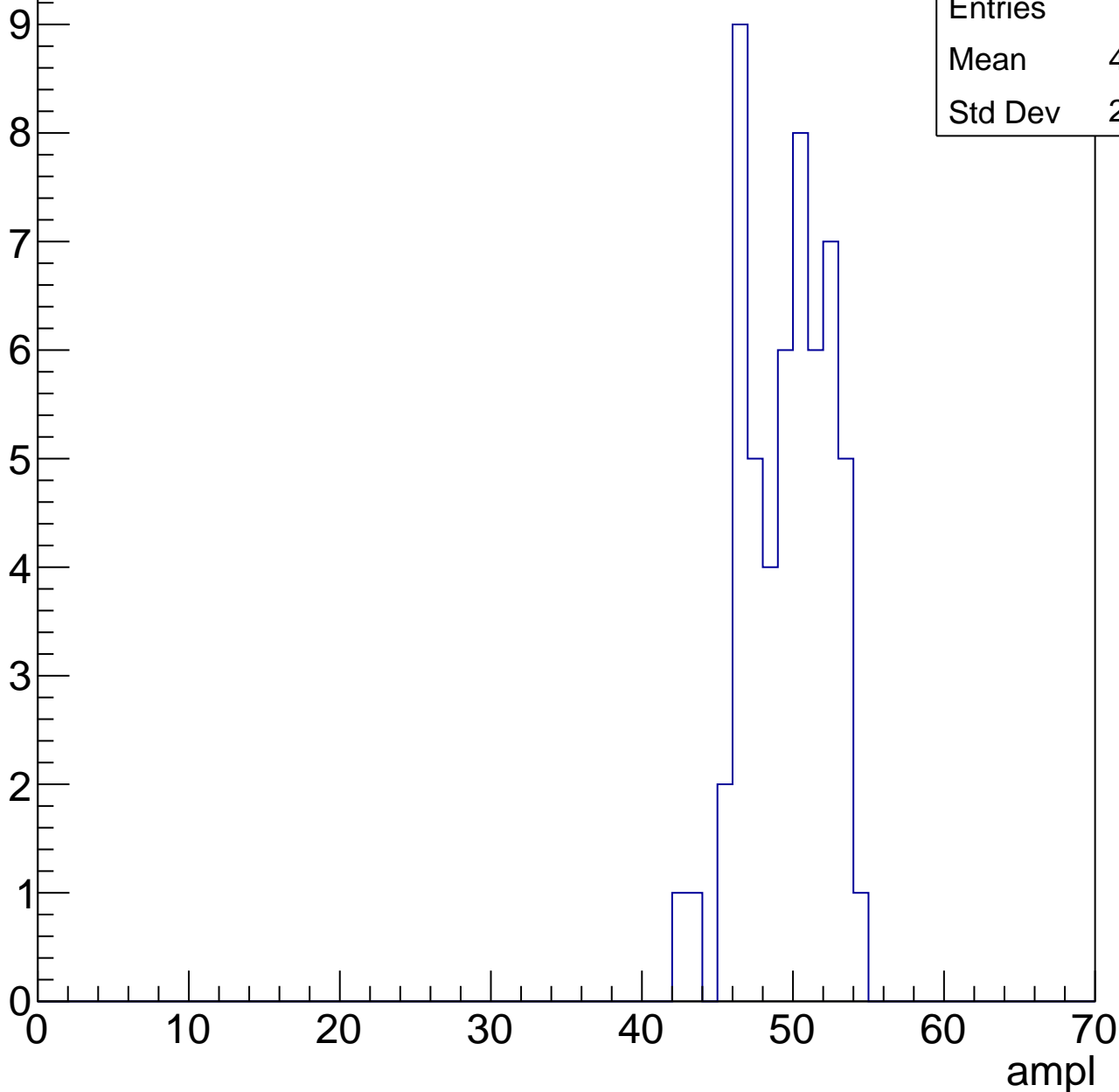


# B1L000S, U24-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

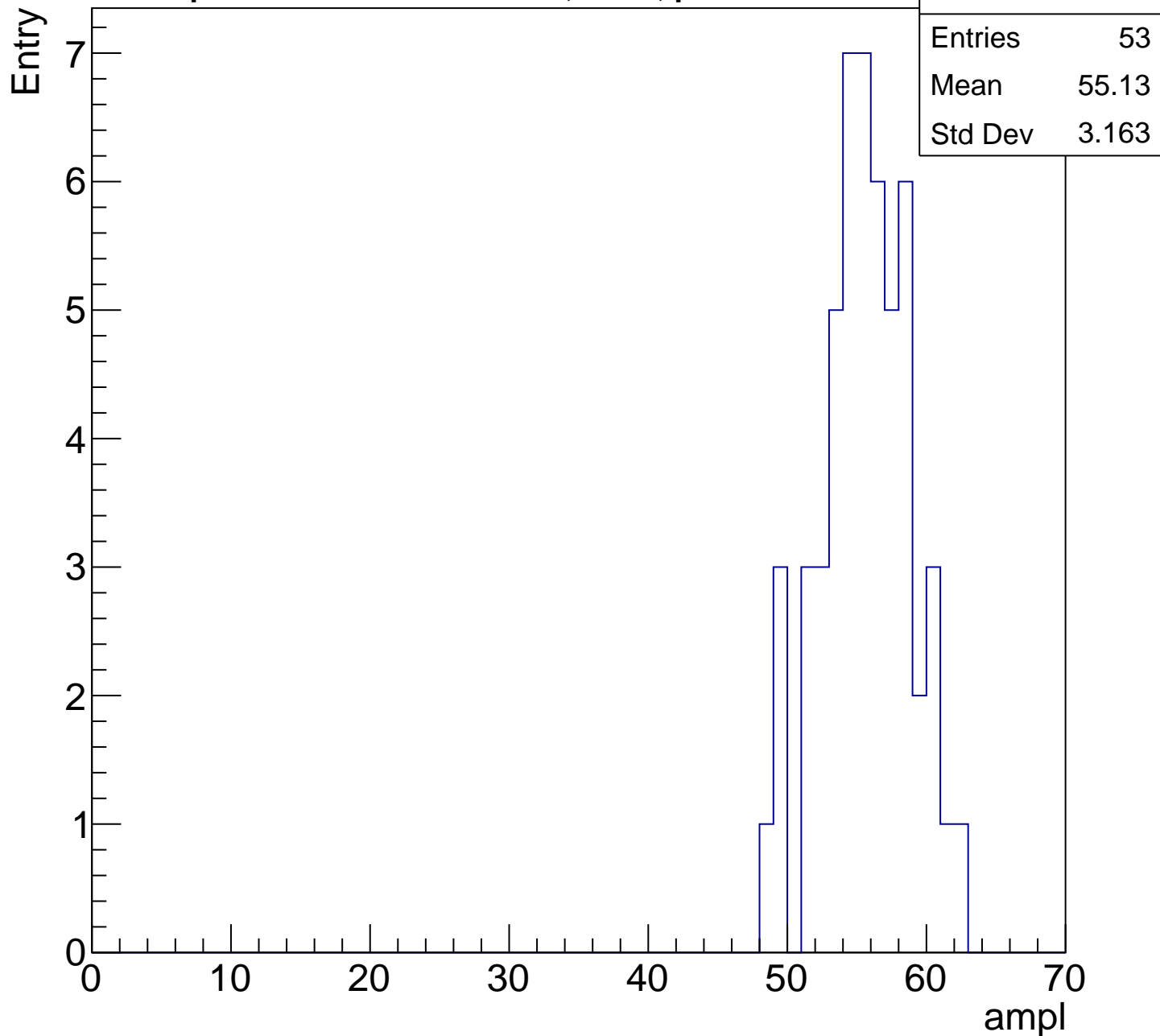
Entry

Entries	55
Mean	49.07
Std Dev	2.769



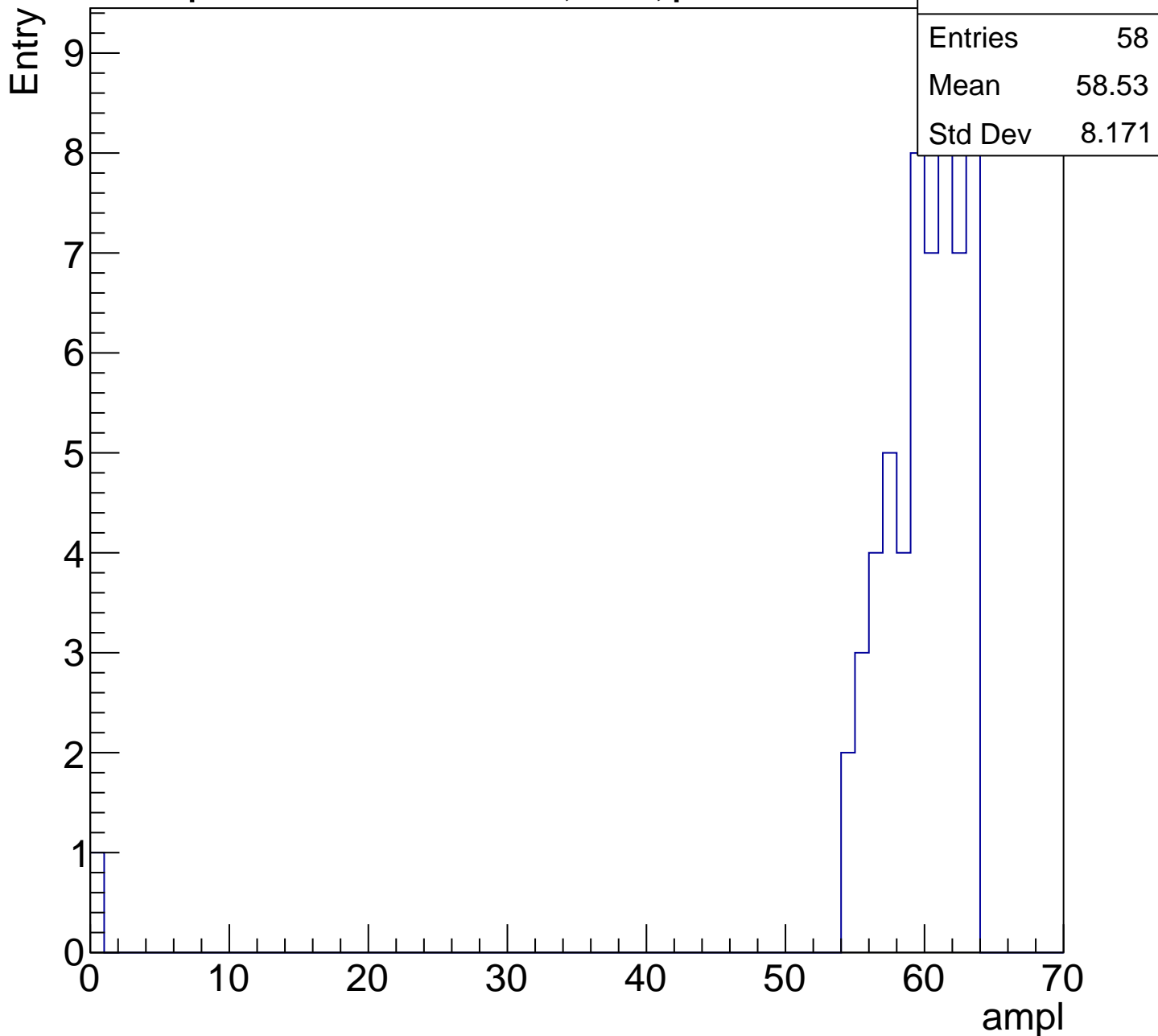
# B1L000S, U24-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch10, adc0

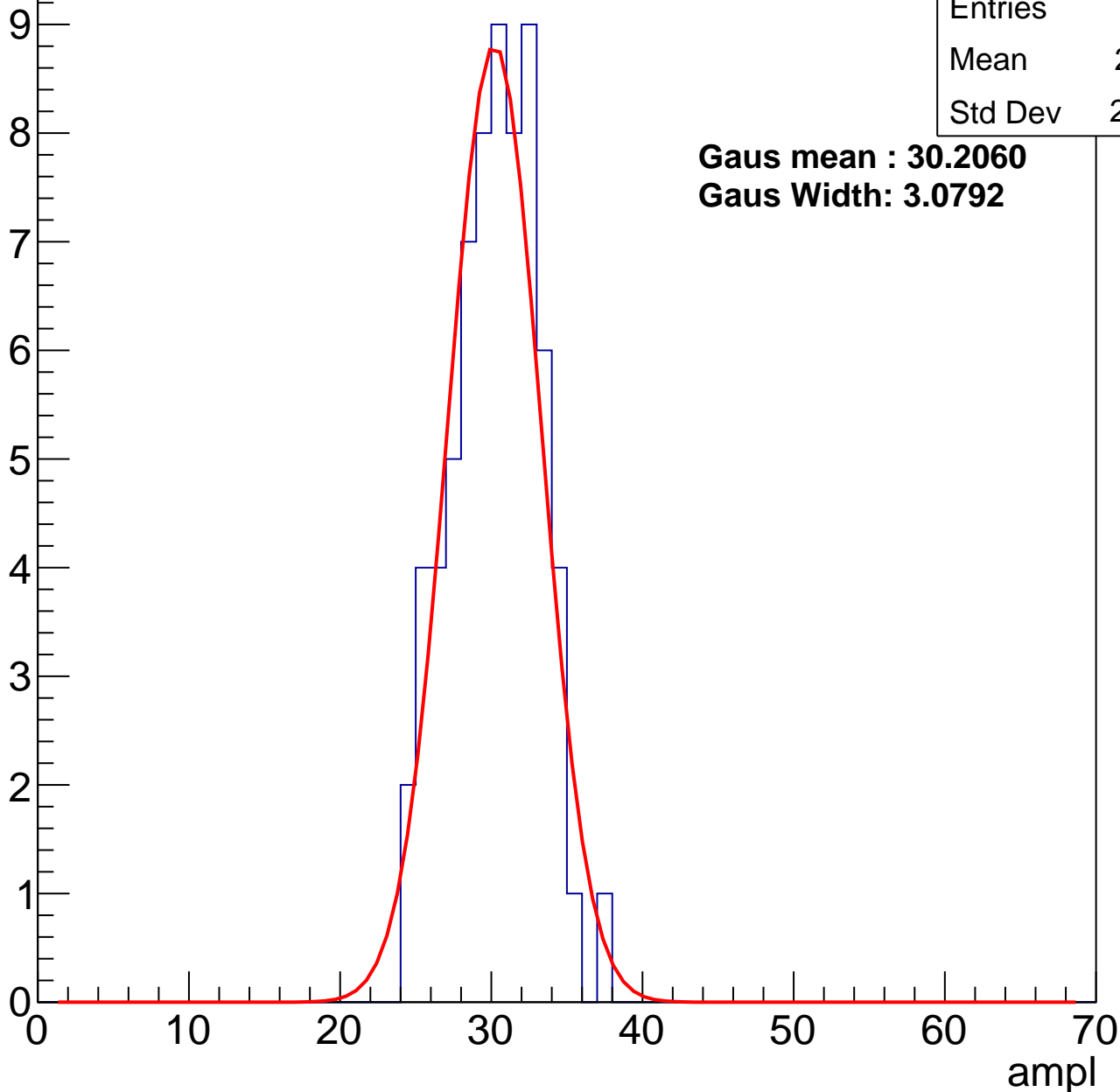
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.81
Std Dev	2.845

**Gaus mean : 30.2060**

**Gaus Width: 3.0792**



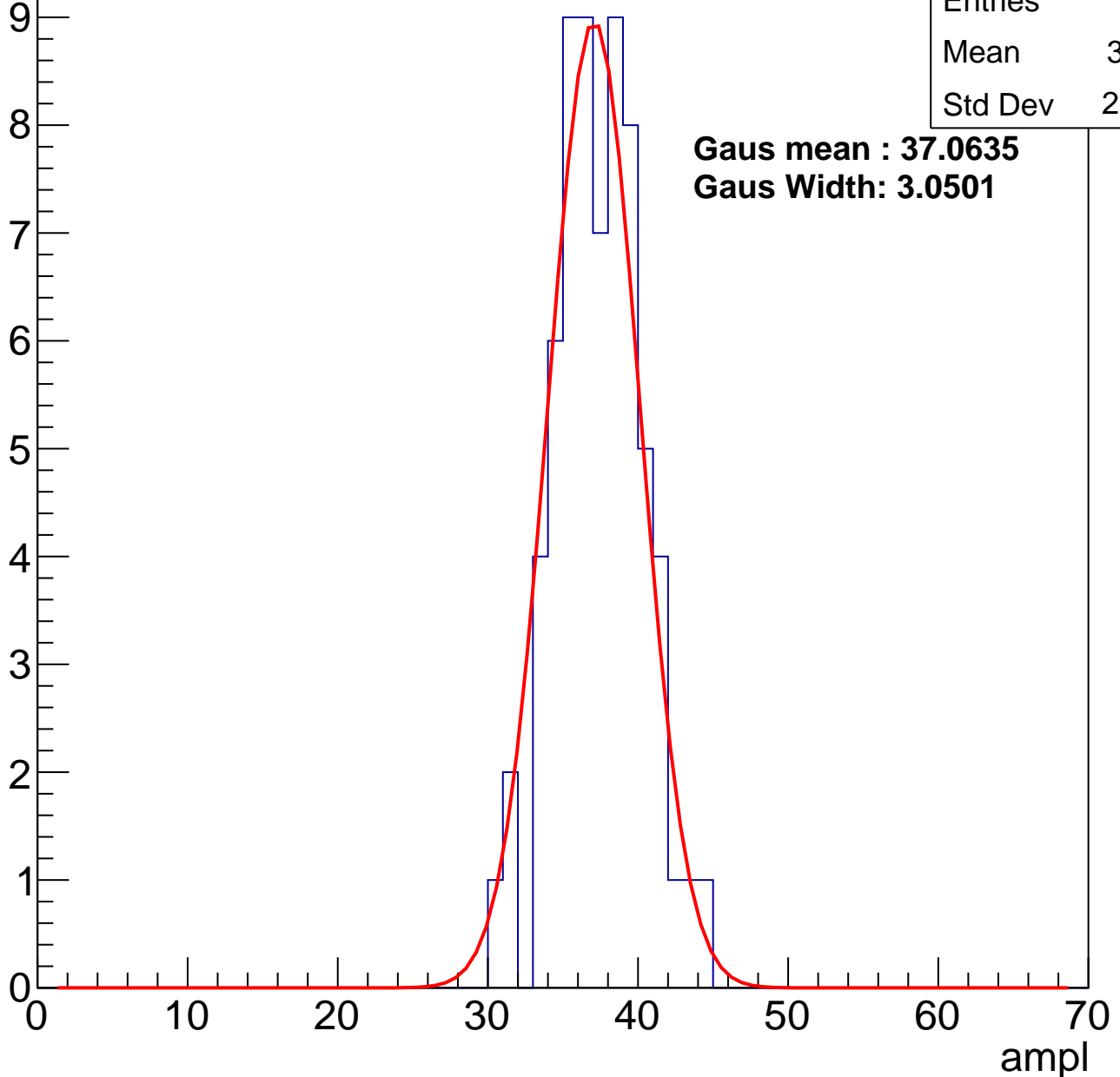
# B1L000S, U24-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.91
Std Dev	2.848

**Gaus mean : 37.0635**  
**Gaus Width: 3.0501**



# B1L000S, U24-ch10, adc2

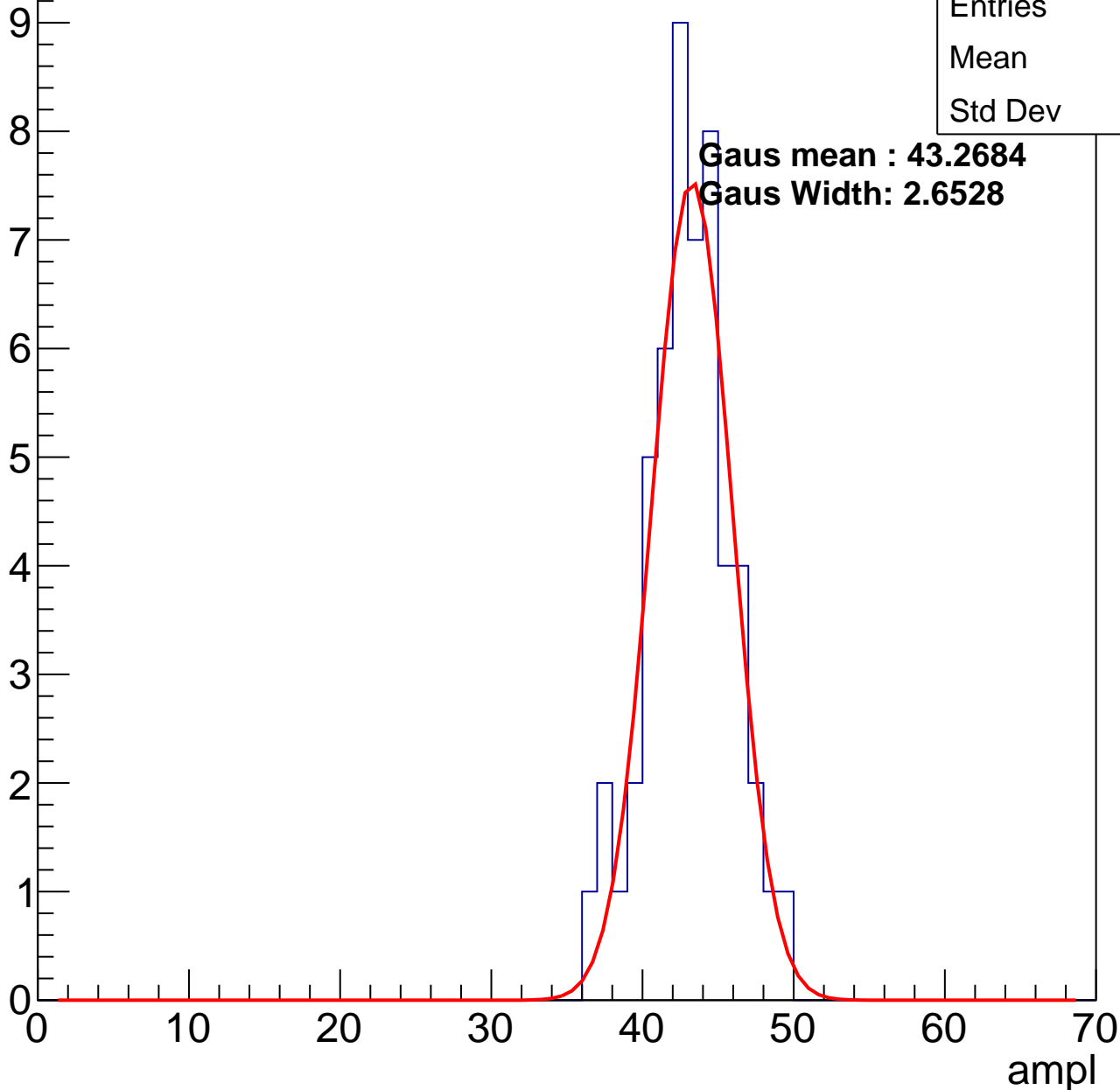
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	42.6
Std Dev	2.75

**Gaus mean : 43.2684**

**Gaus Width: 2.6528**

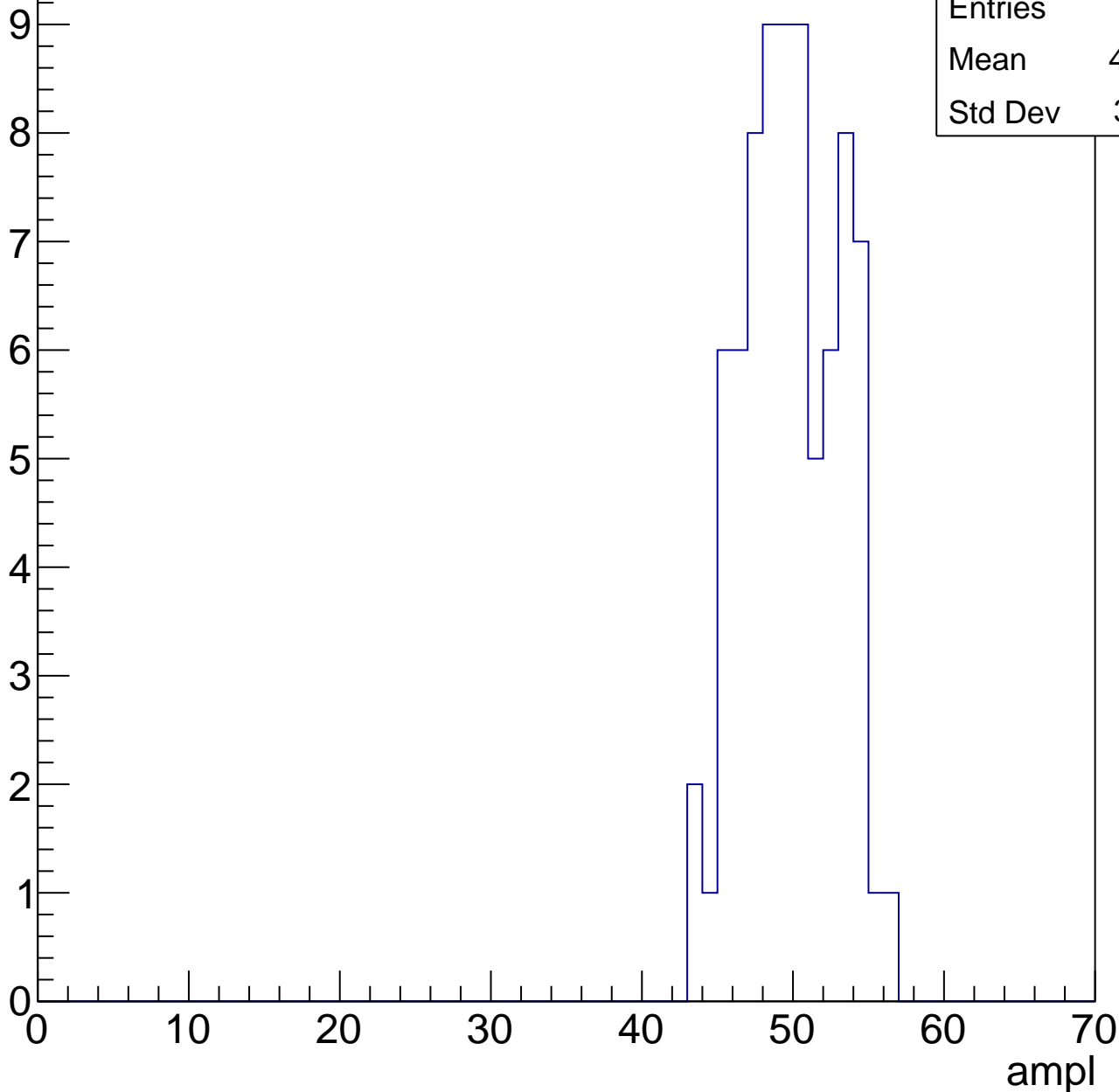


# B1L000S, U24-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

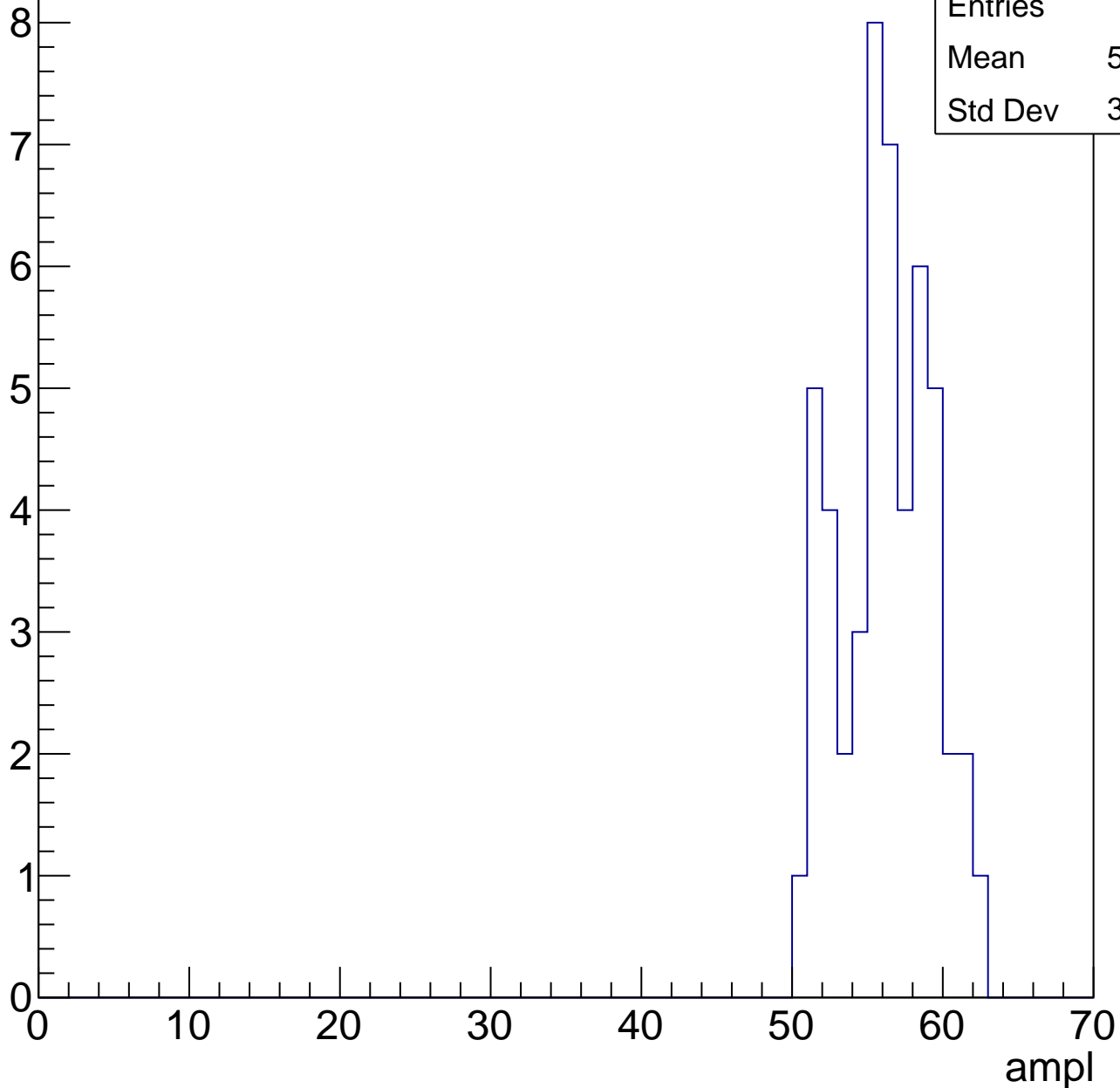
Entries	78
Mean	49.42
Std Dev	3.091



# B1L000S, U24-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

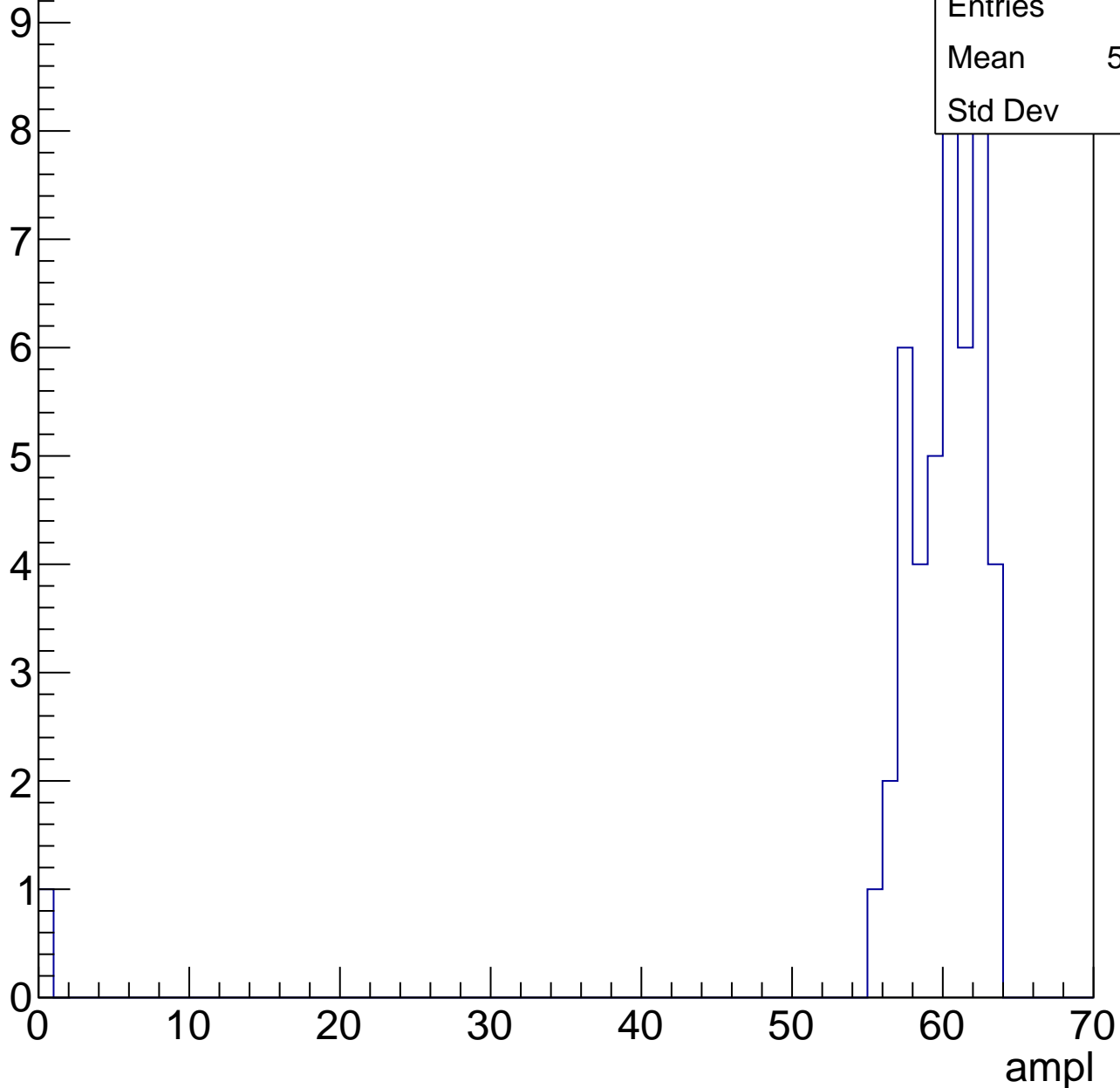
Entry



# B1L000S, U24-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

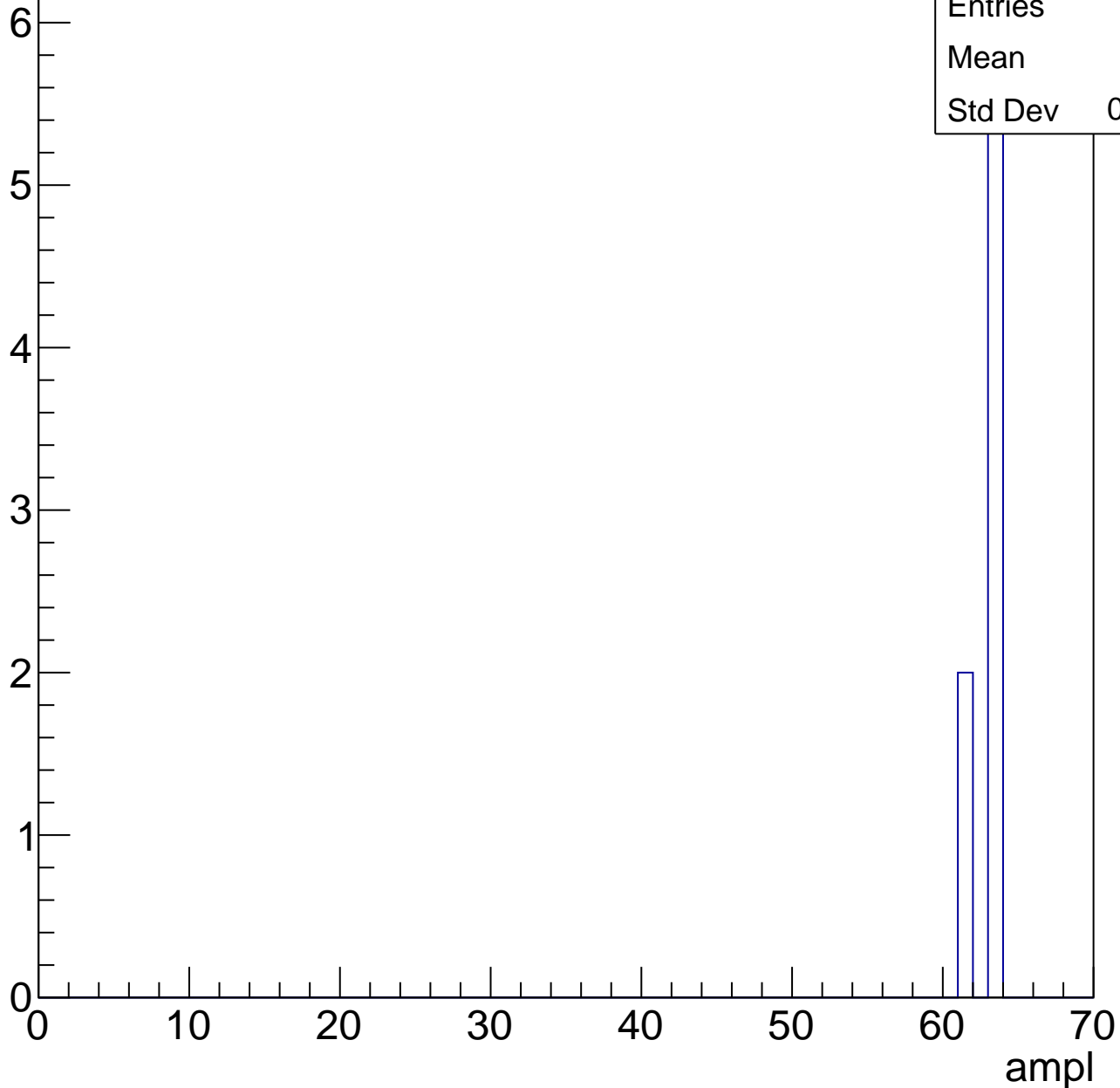


# B1L000S, U24-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.5
Std Dev	0.866

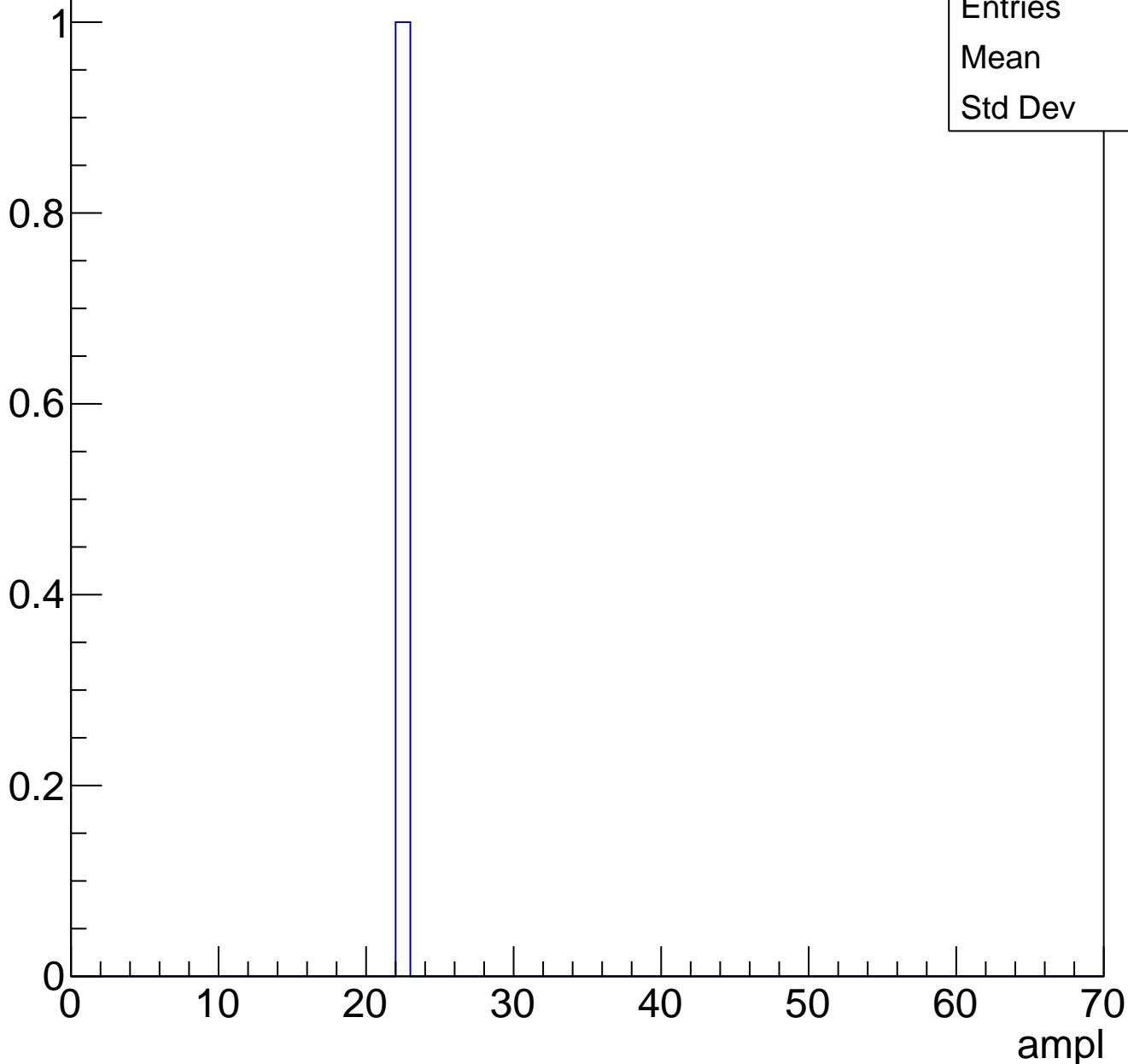




# B1L000S, U24-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch11, adc0

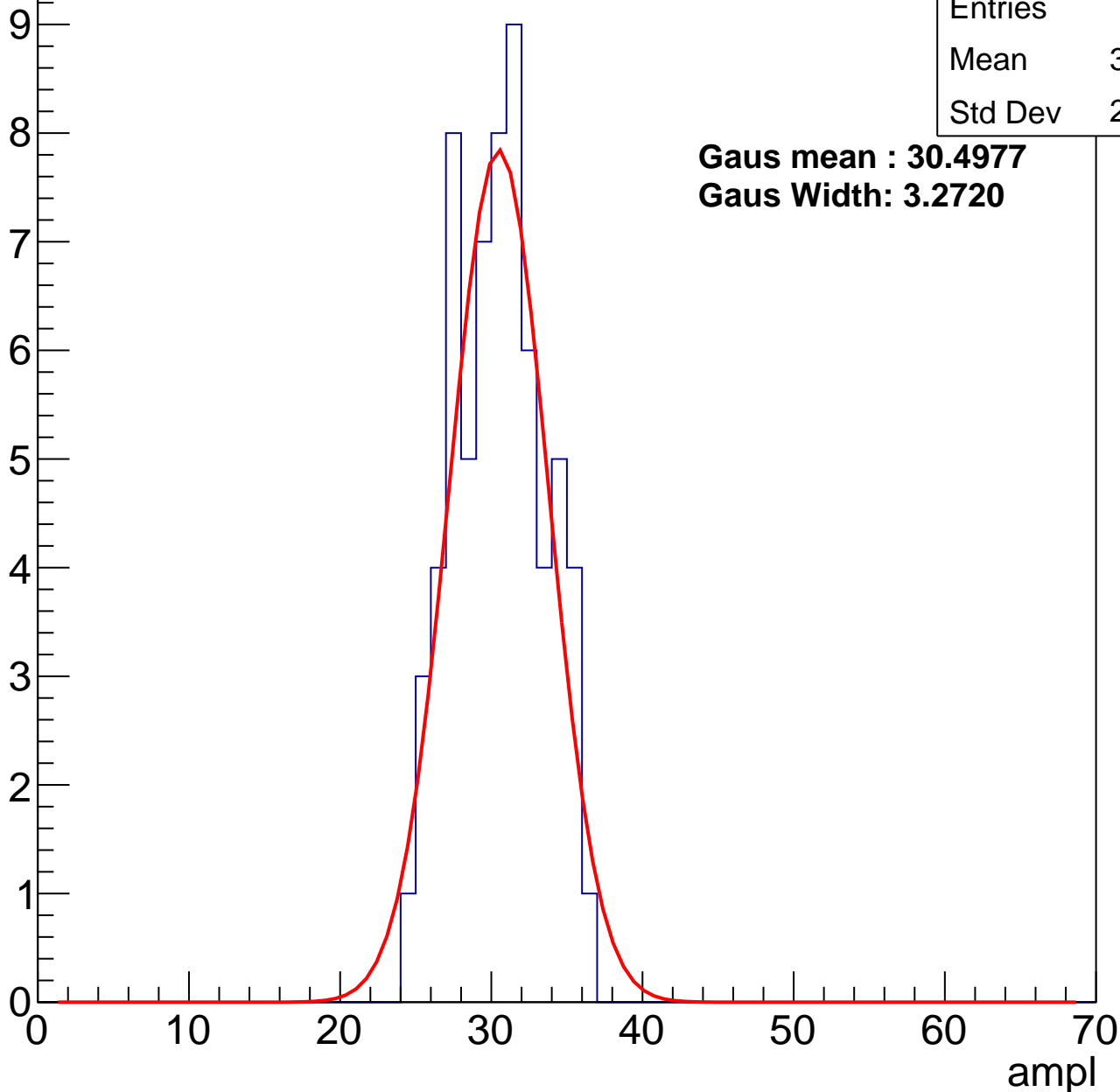
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	30.02
Std Dev	2.933

**Gaus mean : 30.4977**

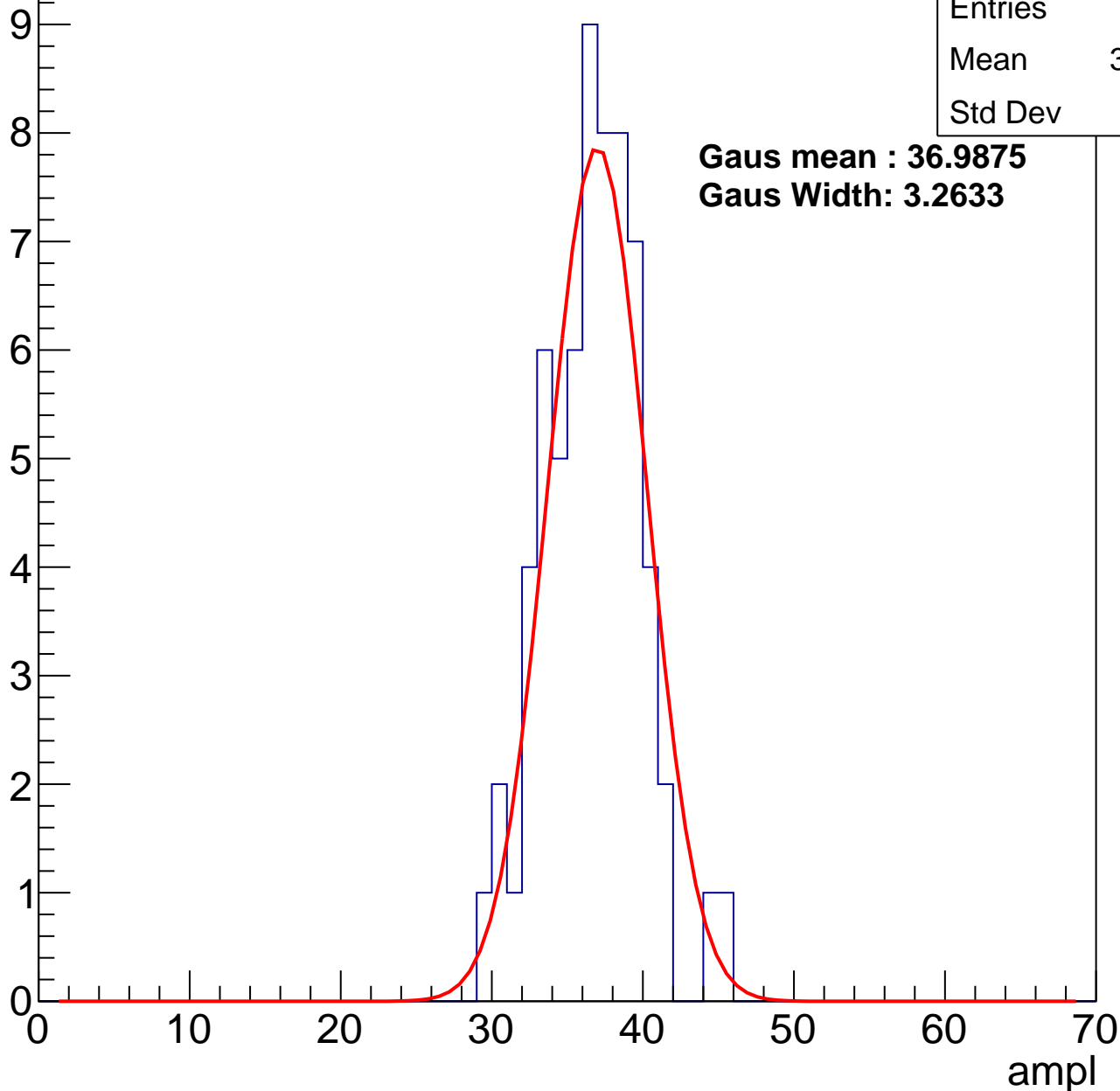
**Gaus Width: 3.2720**



# B1L000S, U24-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch11, adc2

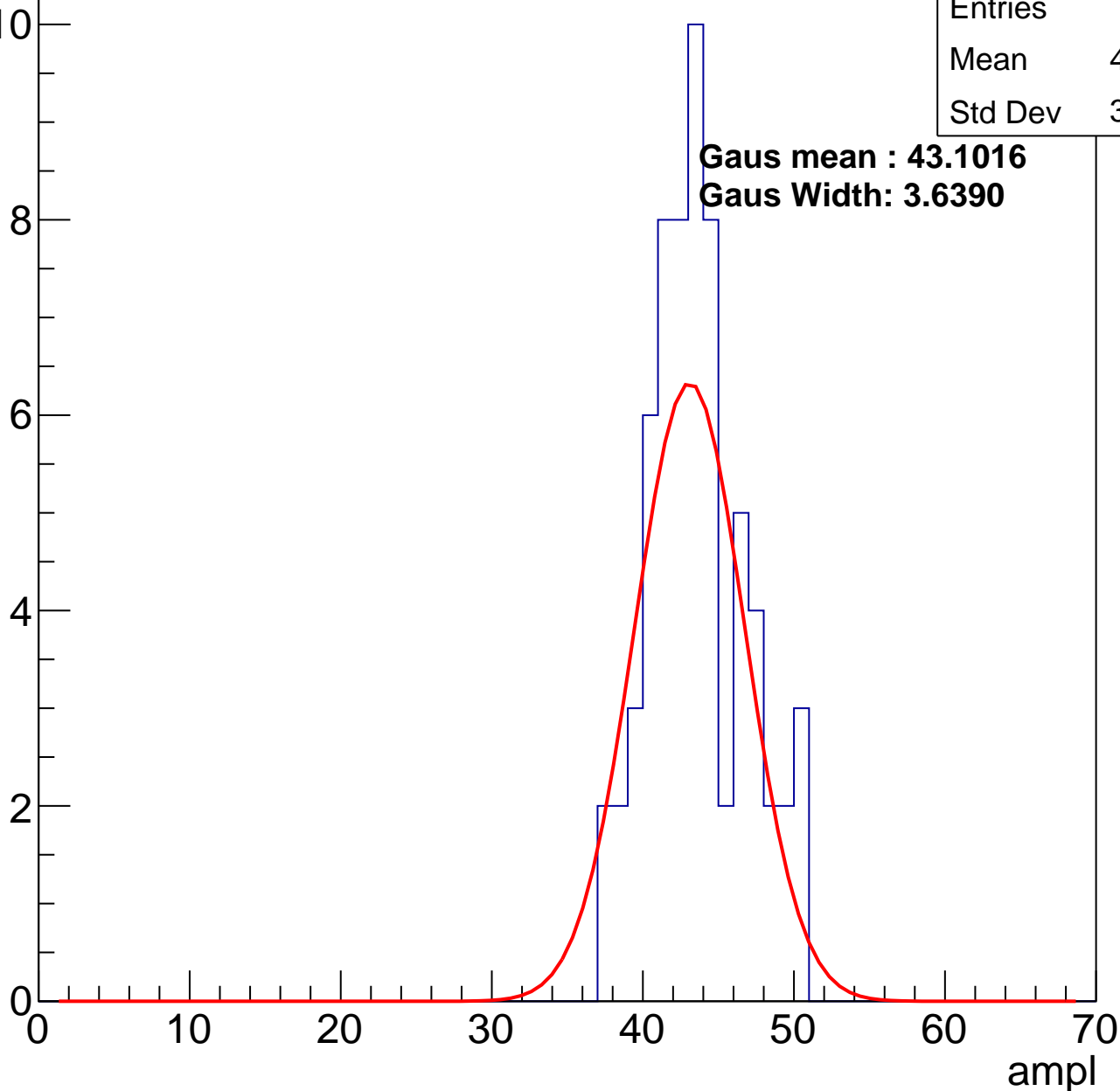
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.15
Std Dev	3.178

**Gaus mean : 43.1016**

**Gaus Width: 3.6390**

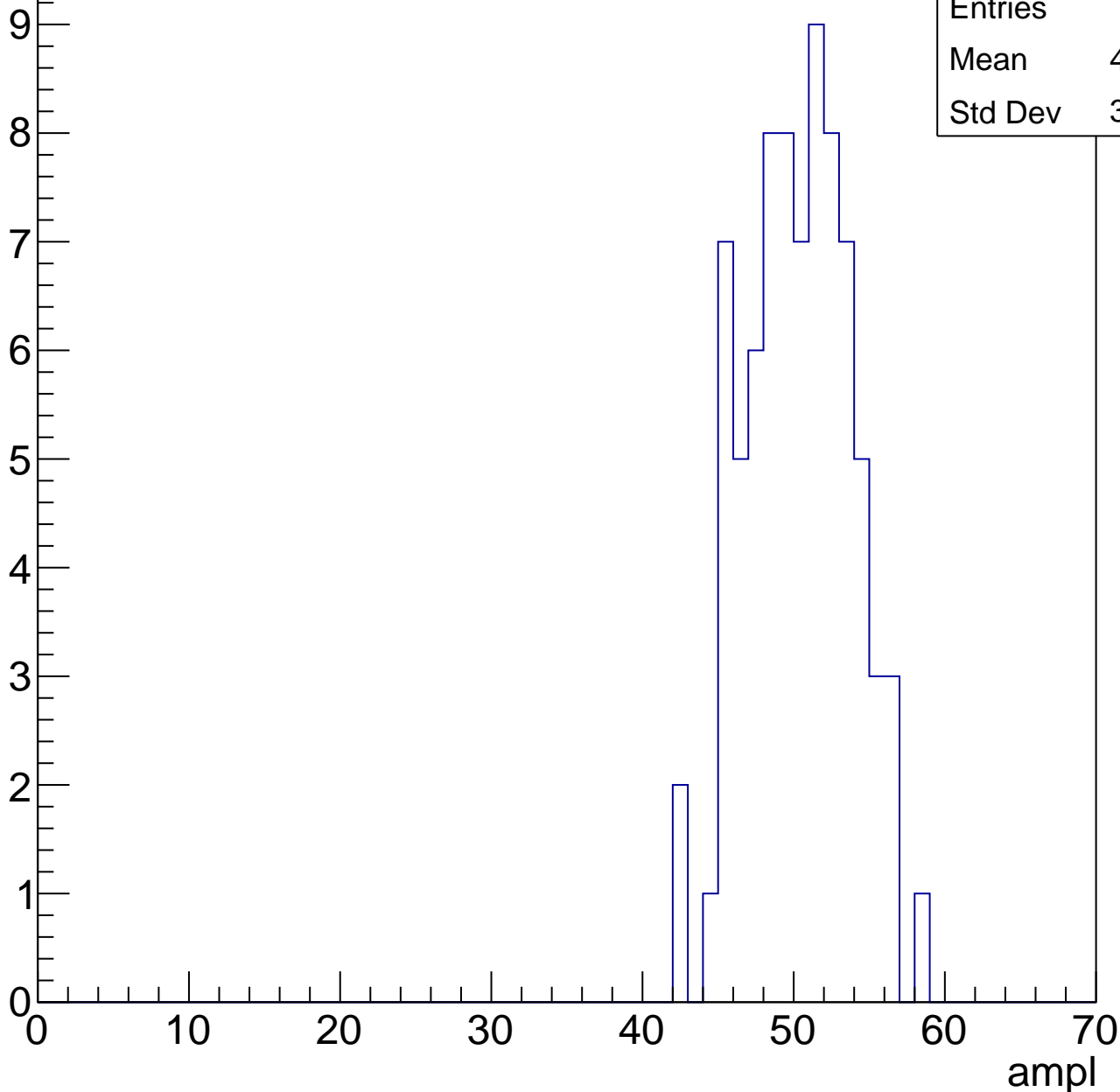


# B1L000S, U24-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	49.85
Std Dev	3.432

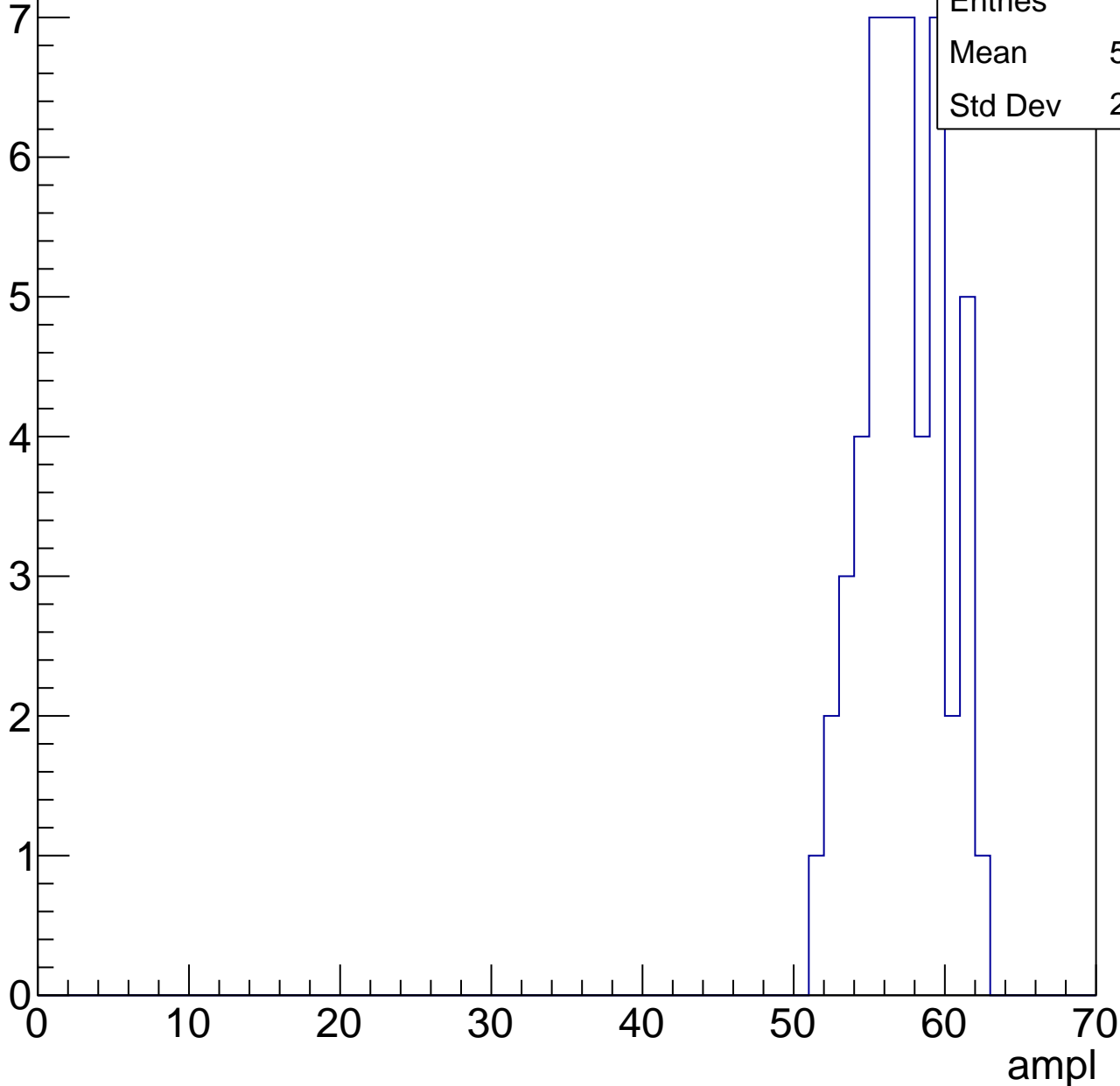


# B1L000S, U24-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	56.76
Std Dev	2.673

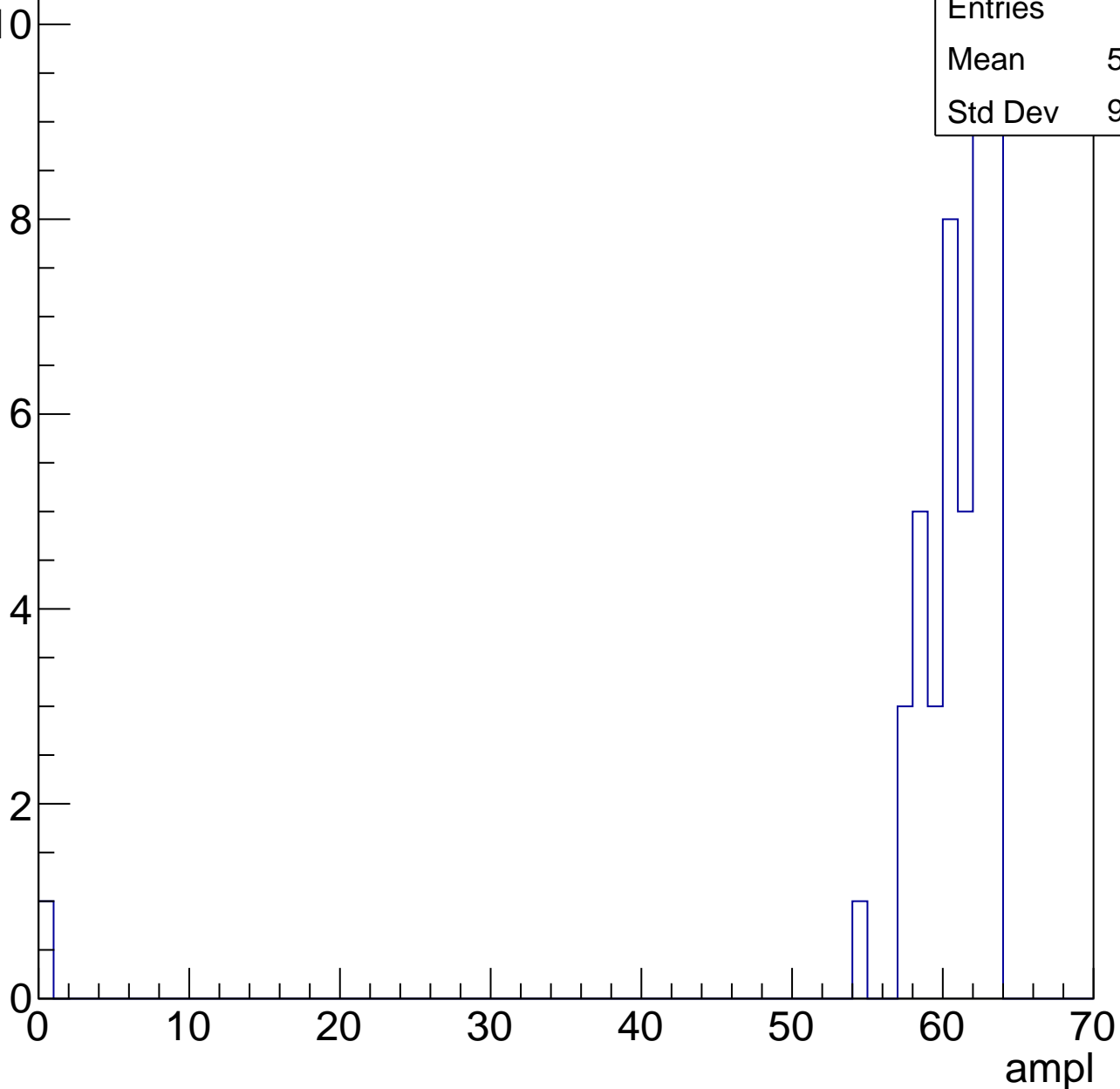


# B1L000S, U24-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	59.22
Std Dev	9.177



# B1L000S, U24-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0



# B1L000S, U24-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U24-ch12, adc0

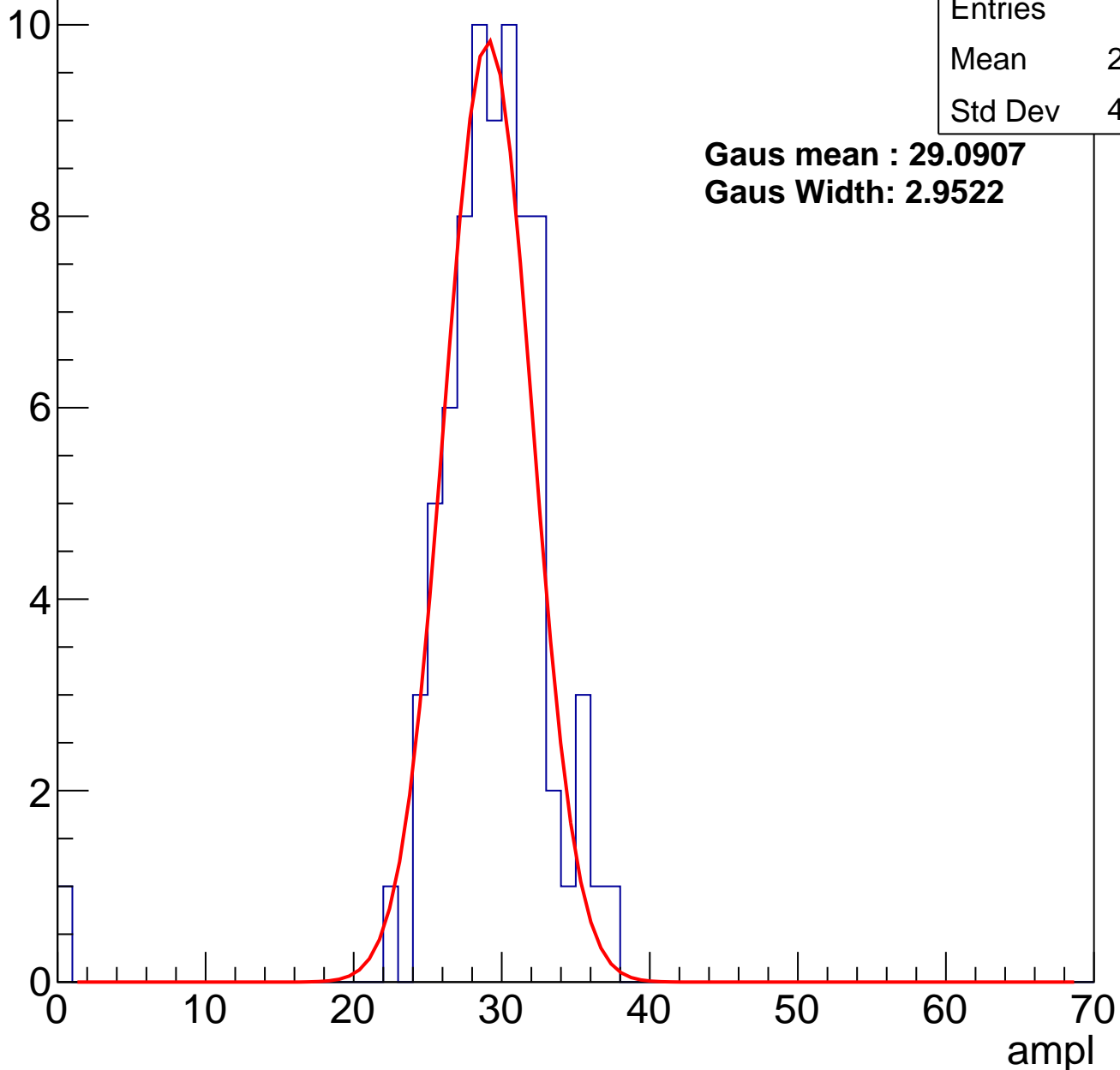
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	28.75
Std Dev	4.455

**Gaus mean : 29.0907**

**Gaus Width: 2.9522**

Entry



# B1L000S, U24-ch12, adc1

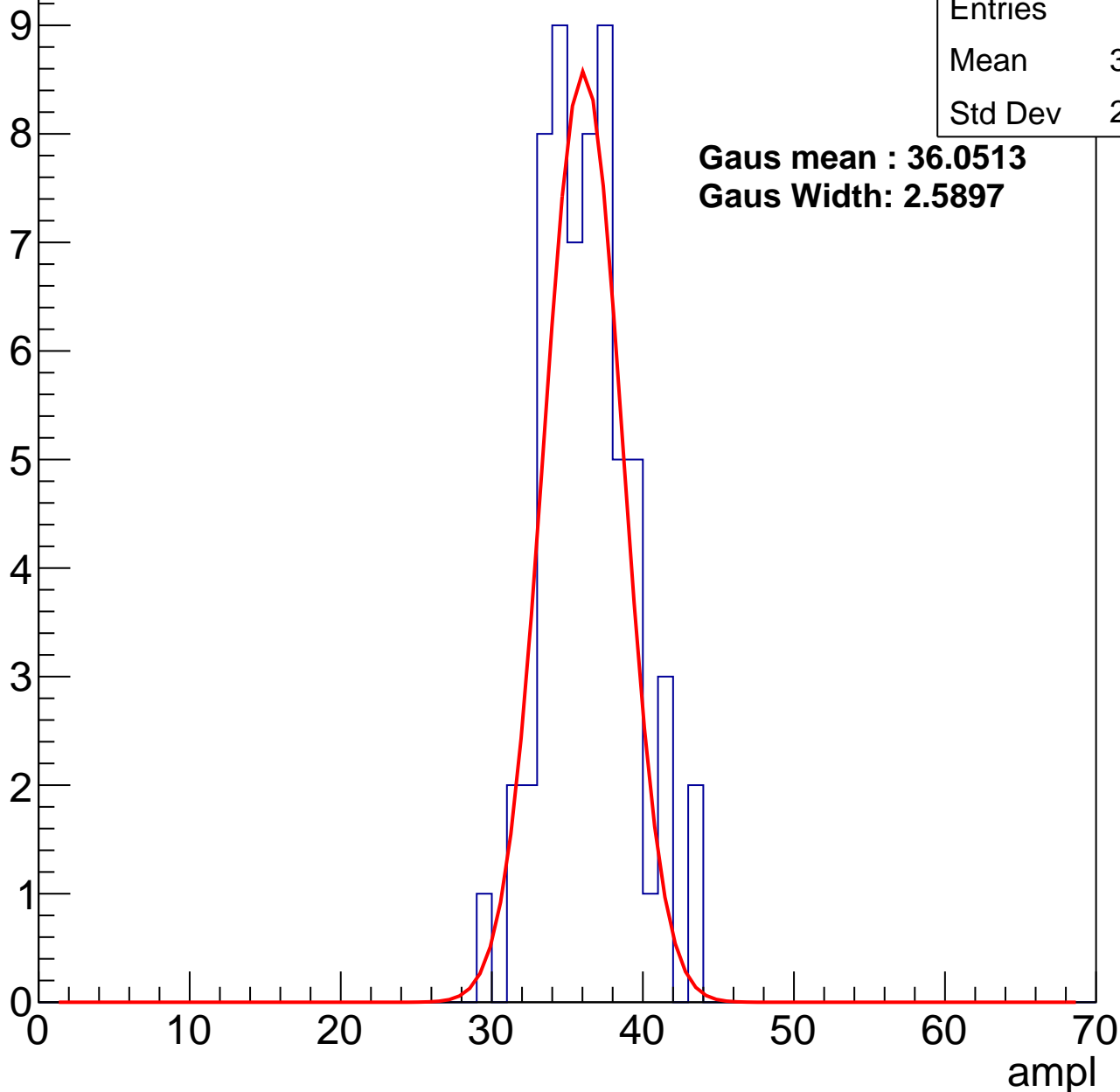
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.89
Std Dev	2.863

**Gaus mean : 36.0513**

**Gaus Width: 2.5897**



# B1L000S, U24-ch12, adc2

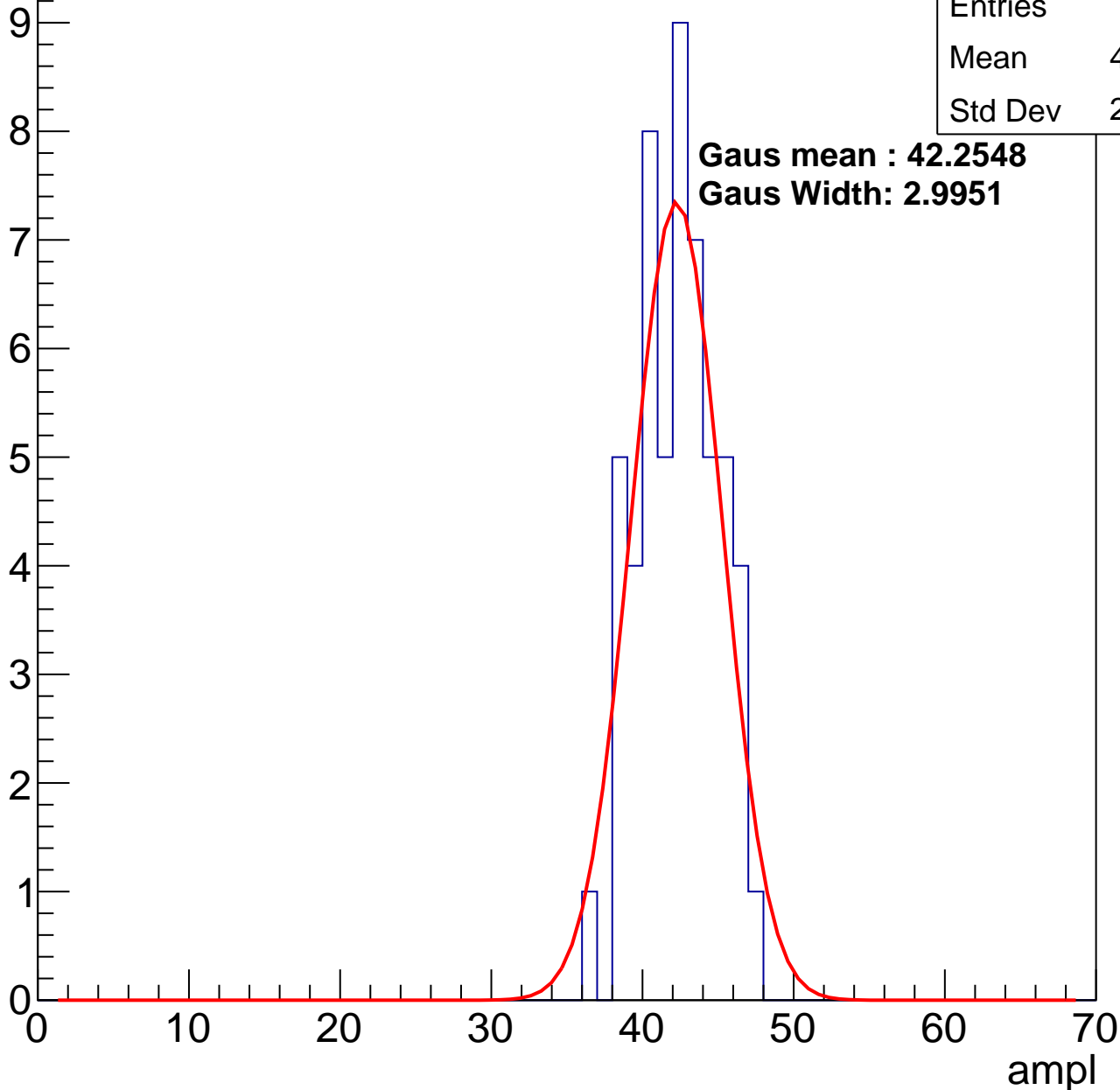
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.89
Std Dev	2.543

**Gaus mean : 42.2548**

**Gaus Width: 2.9951**

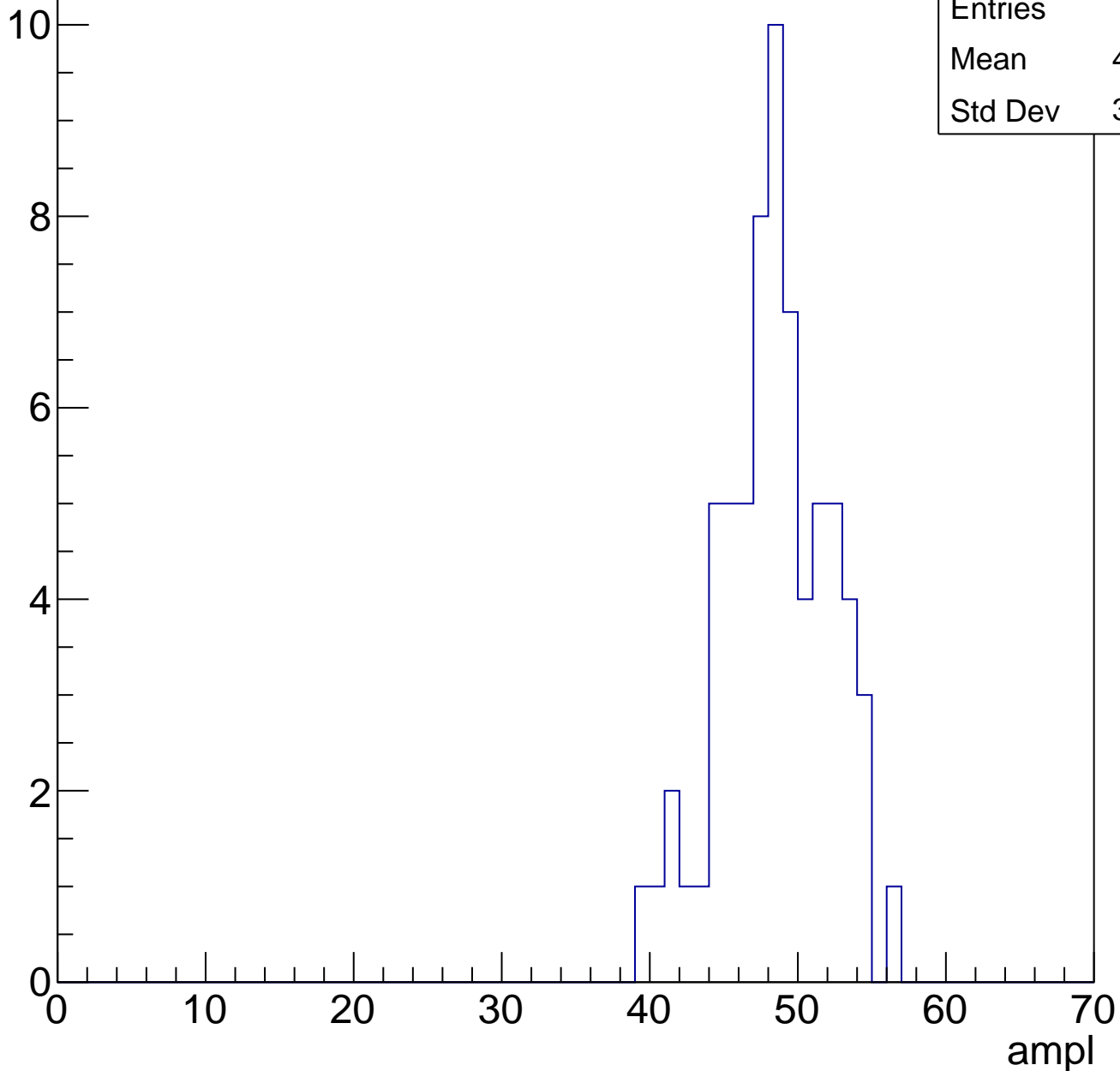


# B1L000S, U24-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	48.01
Std Dev	3.591

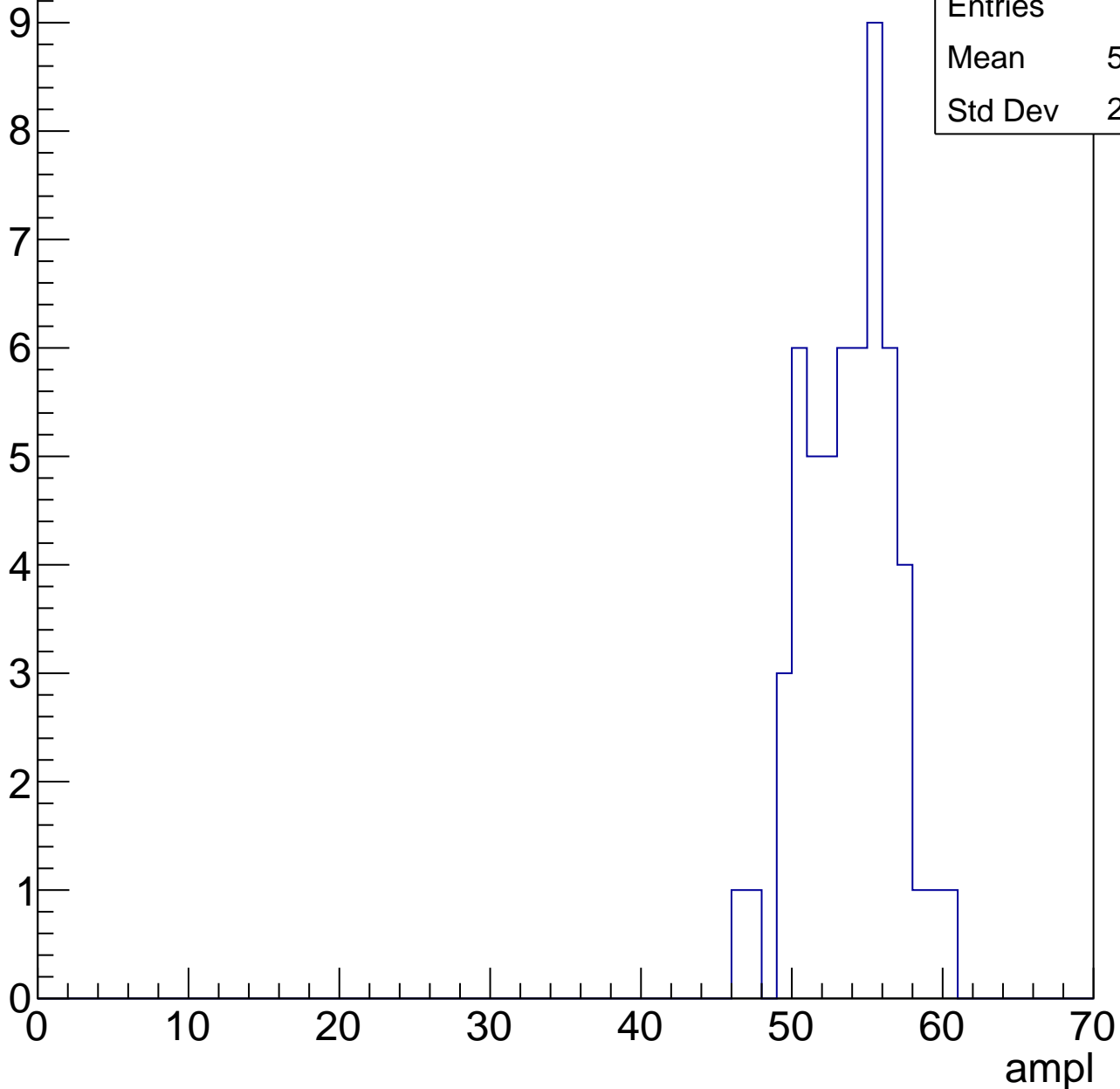


# B1L000S, U24-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

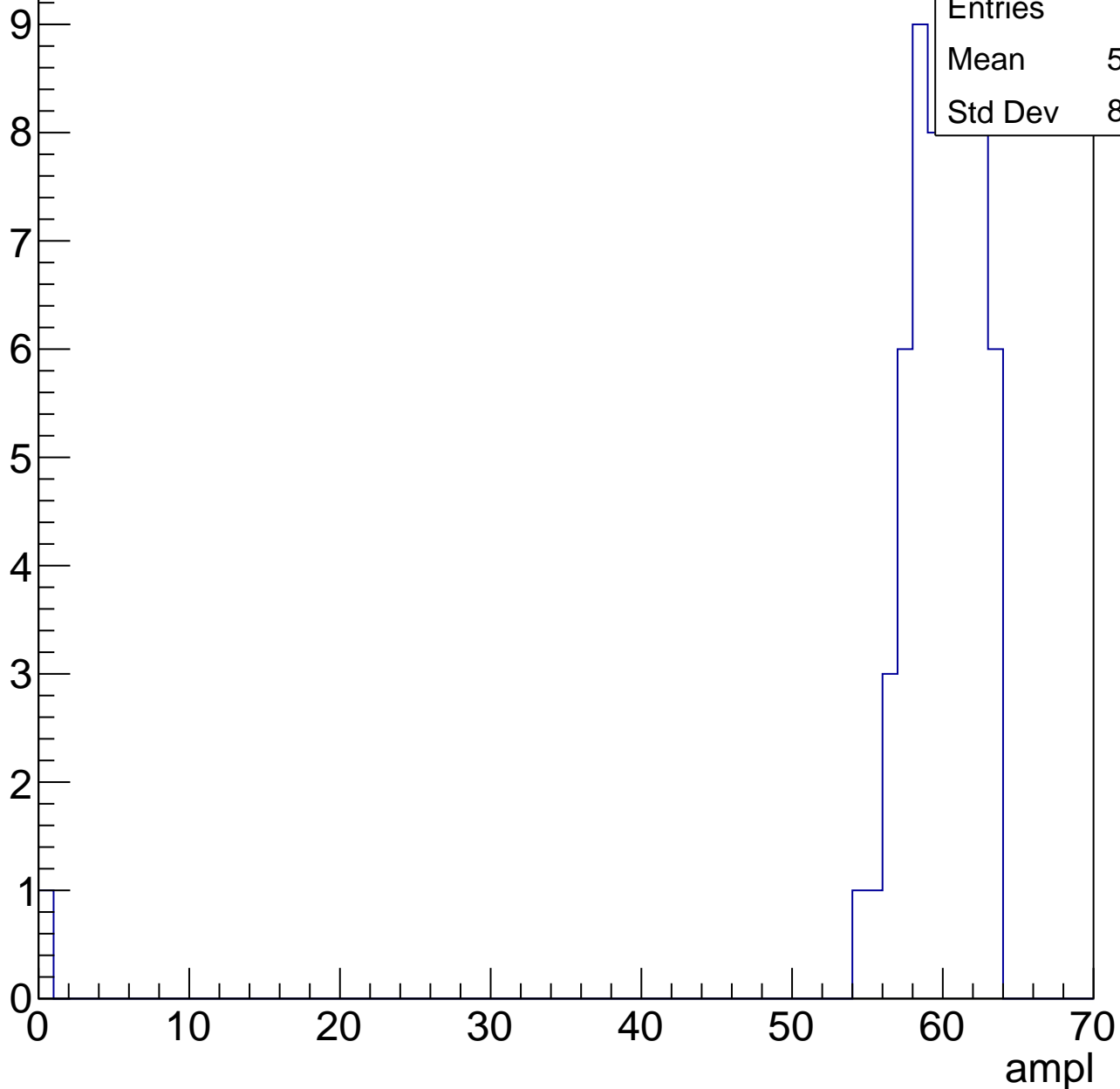
Entries	55
Mean	53.33
Std Dev	2.942



# B1L000S, U24-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

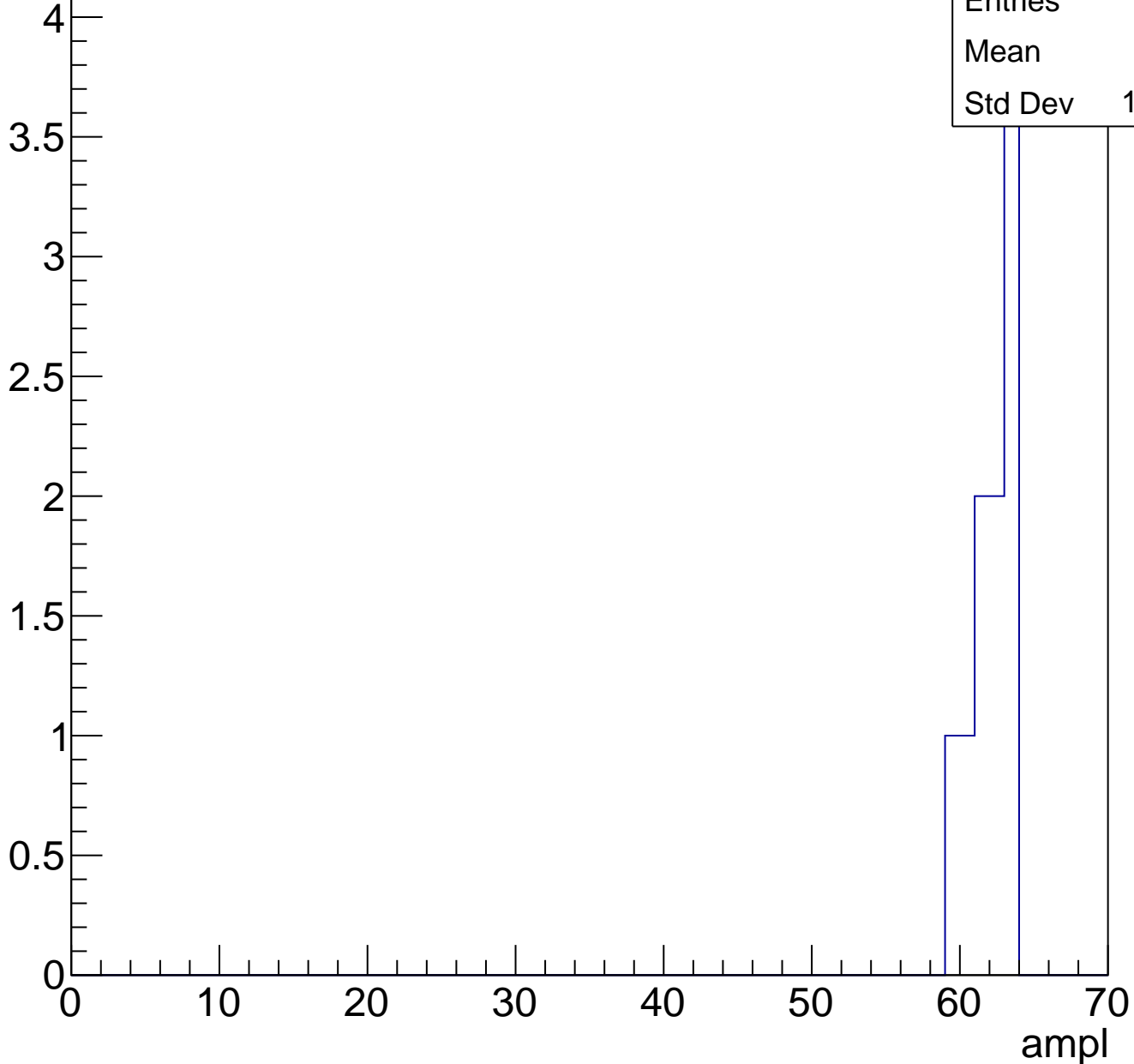
Entry



# B1L000S, U24-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	29.28
Std Dev	3.433

**Gaus mean : 29.3371**

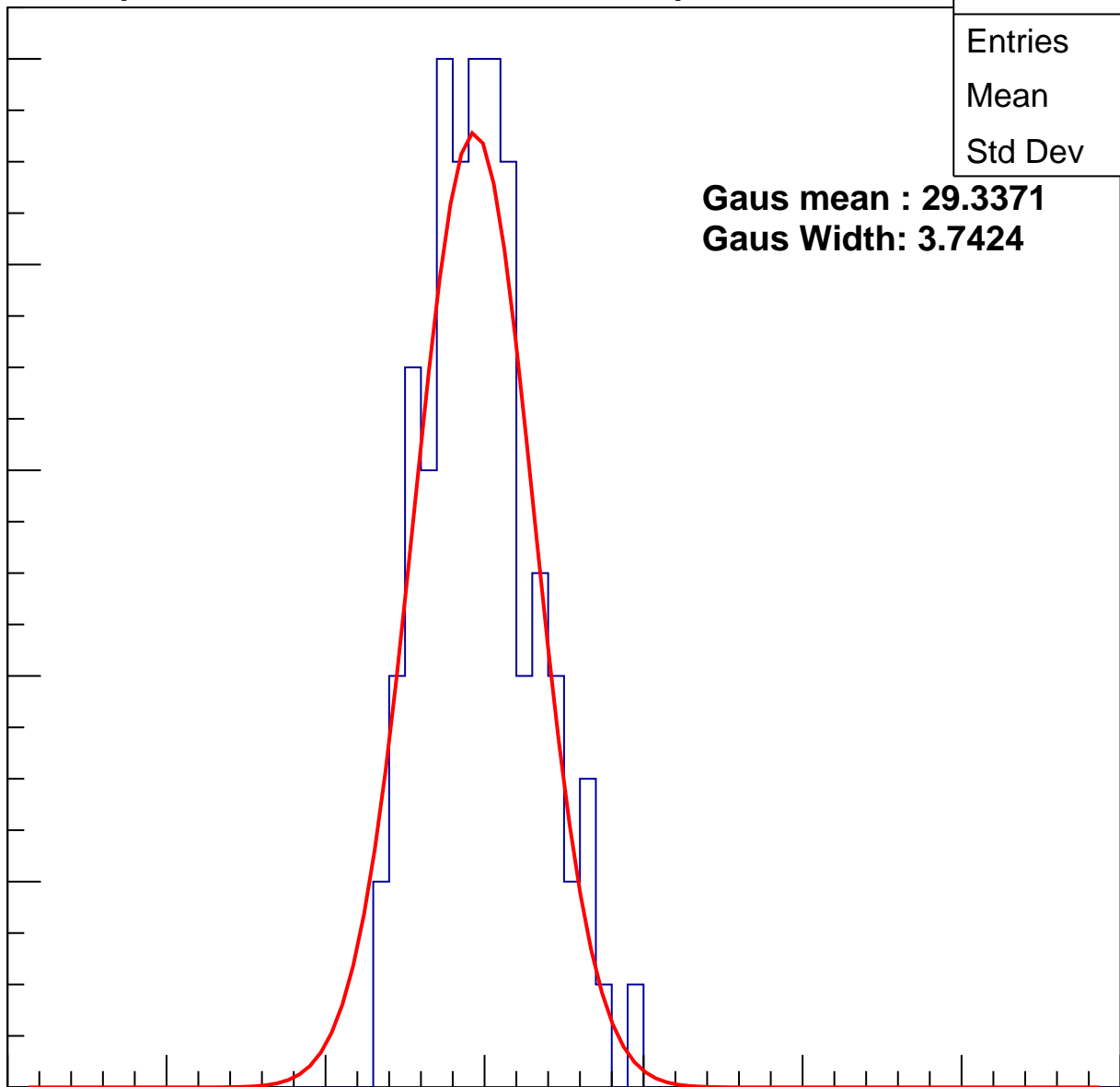
**Gaus Width: 3.7424**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch13, adc1

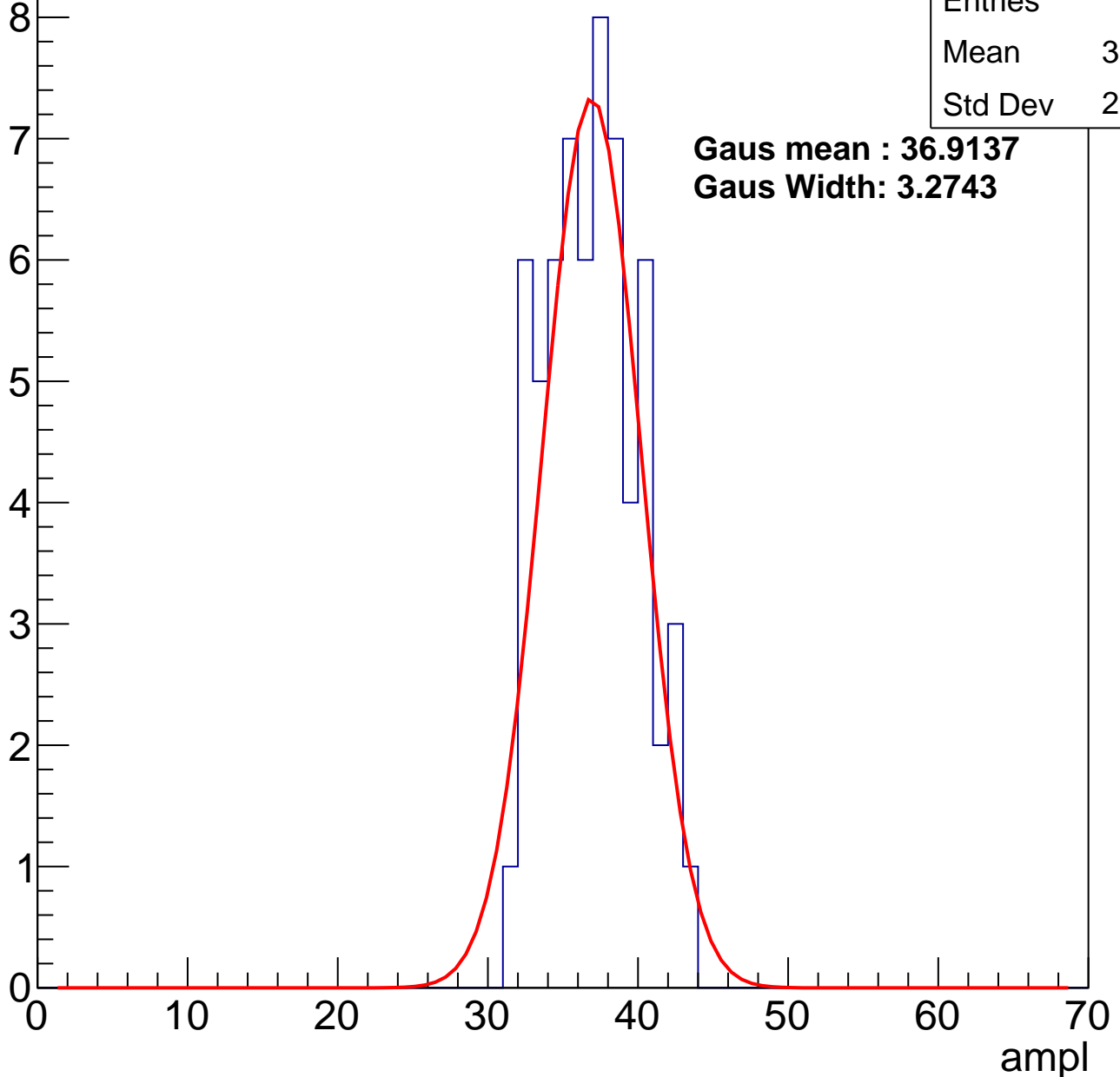
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.48
Std Dev	2.999

**Gaus mean : 36.9137**

**Gaus Width: 3.2743**



# B1L000S, U24-ch13, adc2

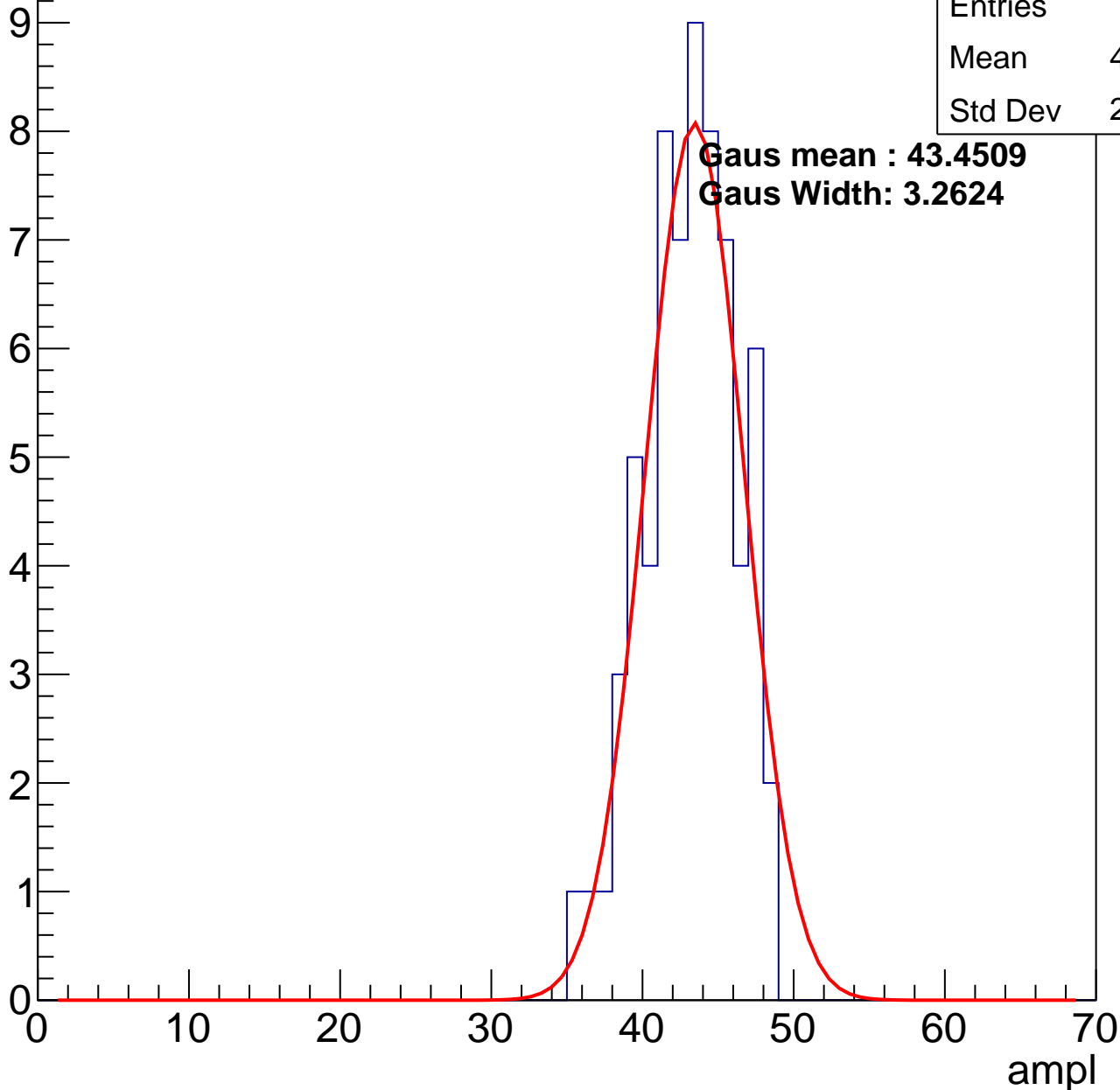
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	42.65
Std Dev	2.987

**Gaus mean : 43.4509**

**Gaus Width: 3.2624**



# B1L000S, U24-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

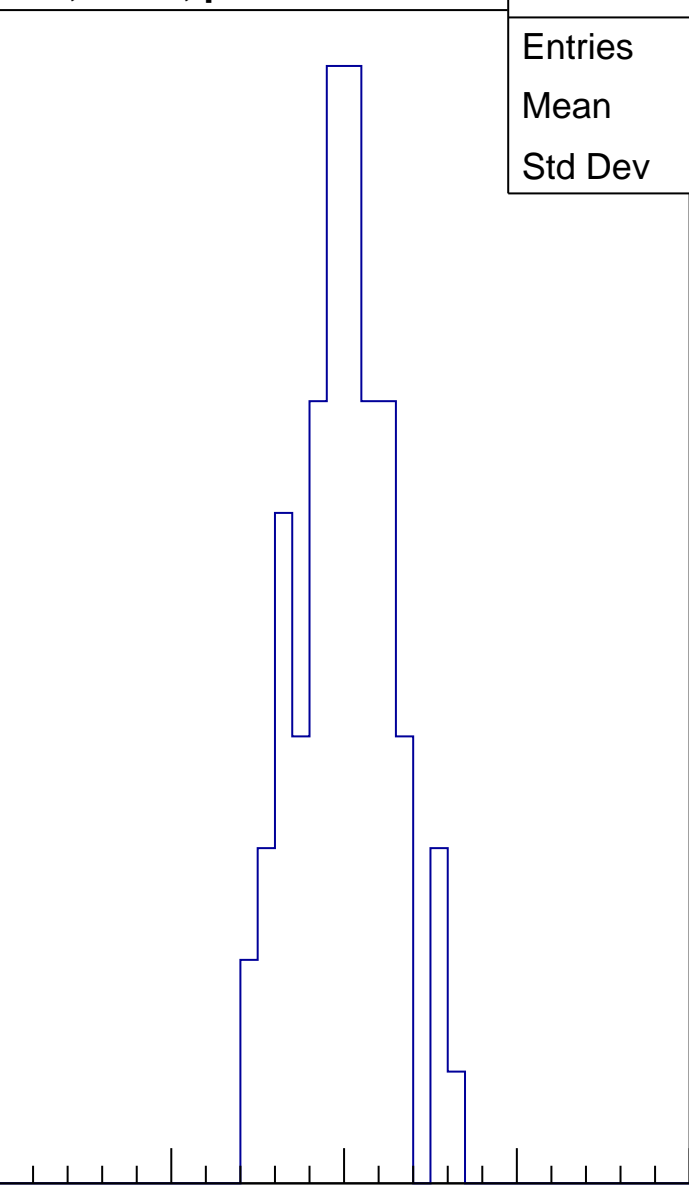
Entries	64
Mean	49.48
Std Dev	2.744

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

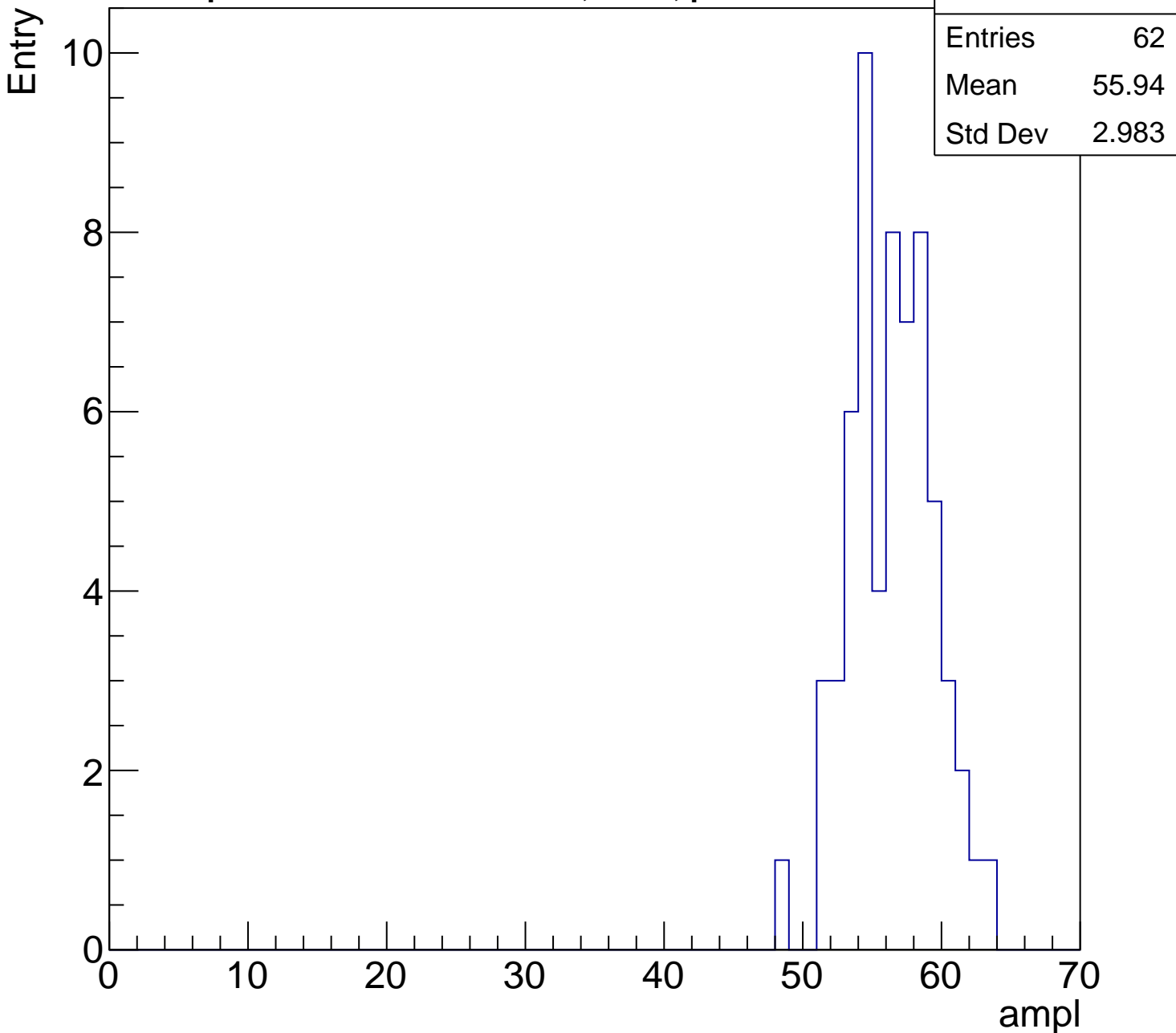
Entries	62
Mean	55.94
Std Dev	2.983

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

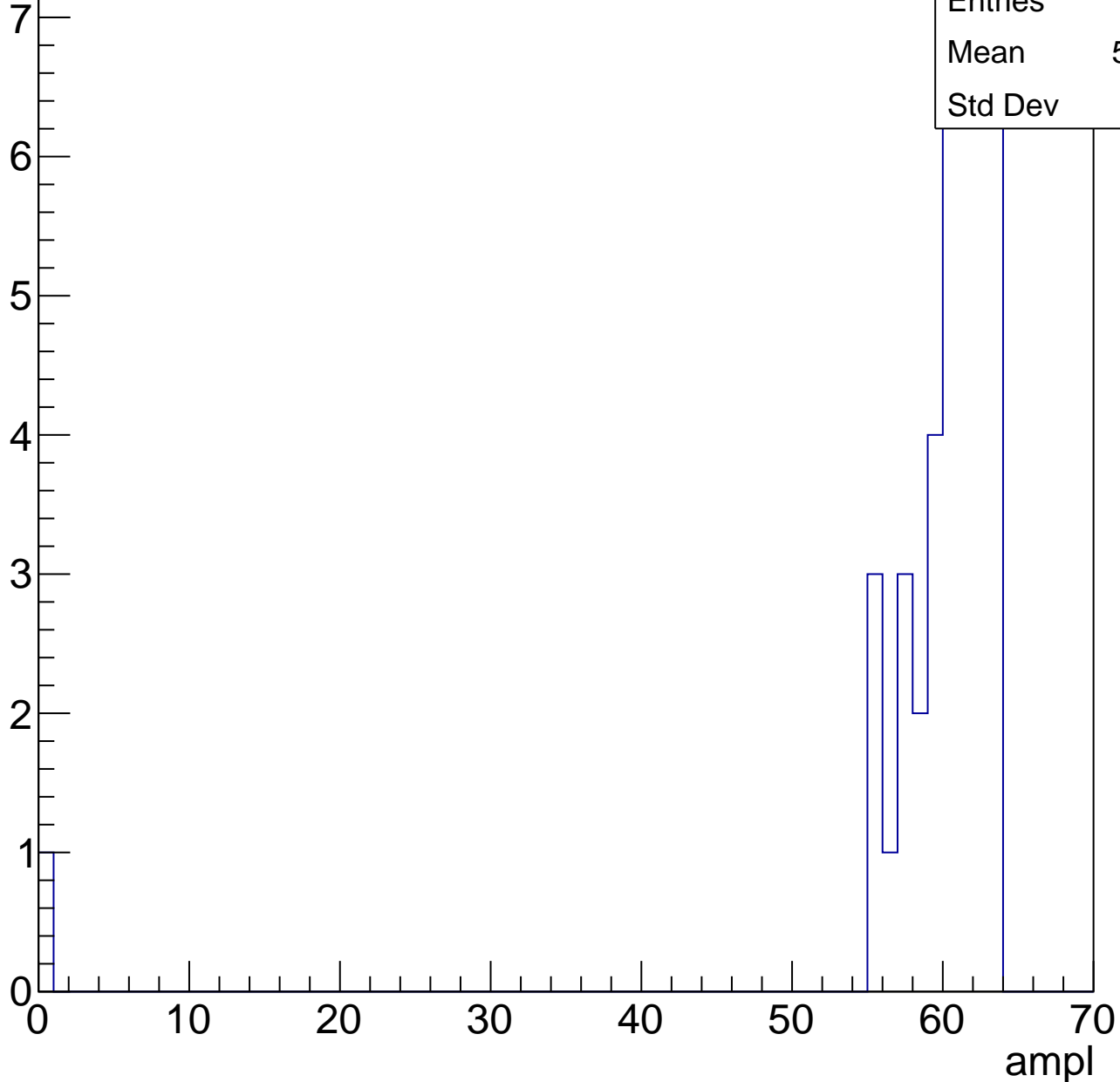
ampl



# B1L000S, U24-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

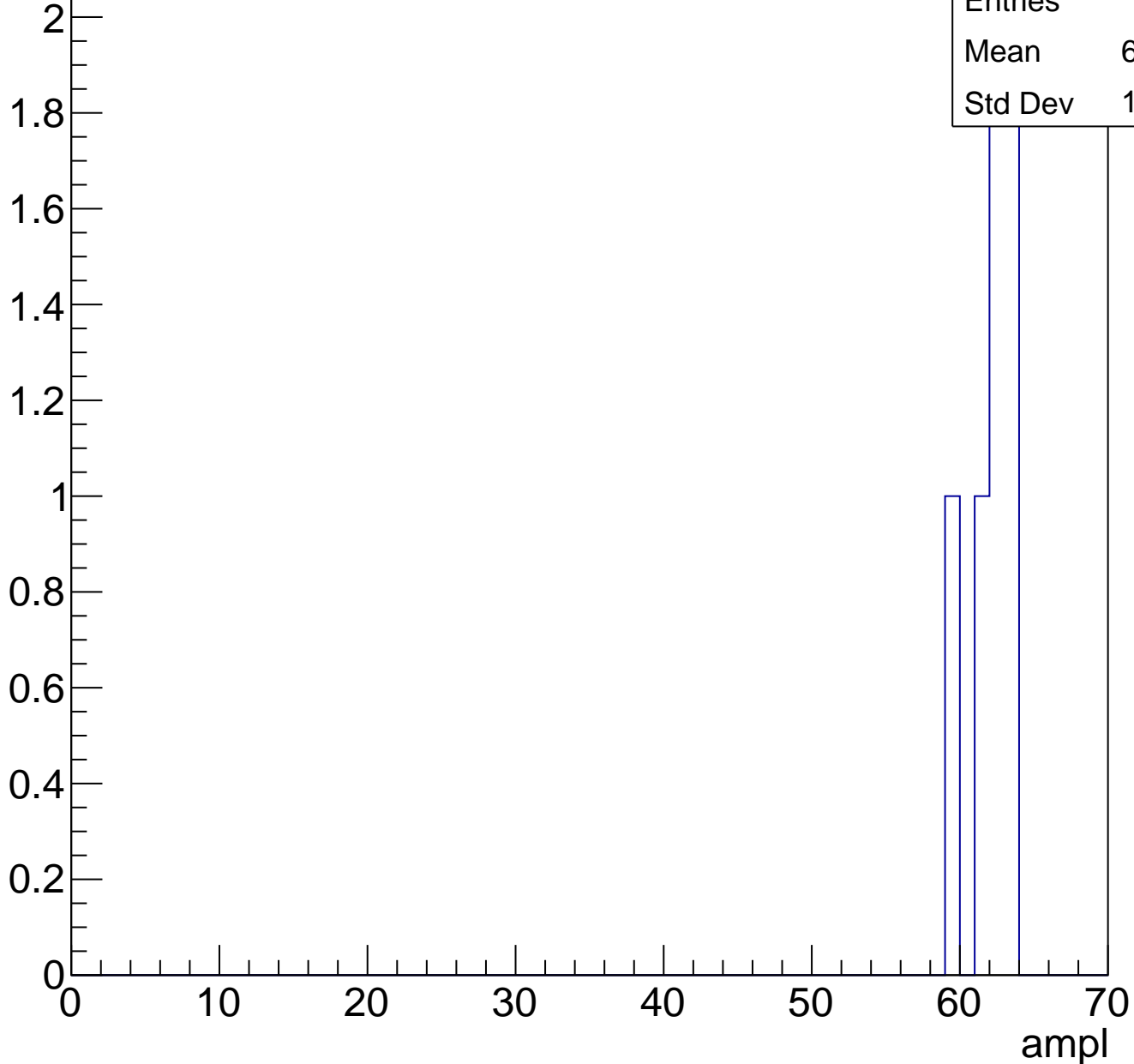
Entry



# B1L000S, U24-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

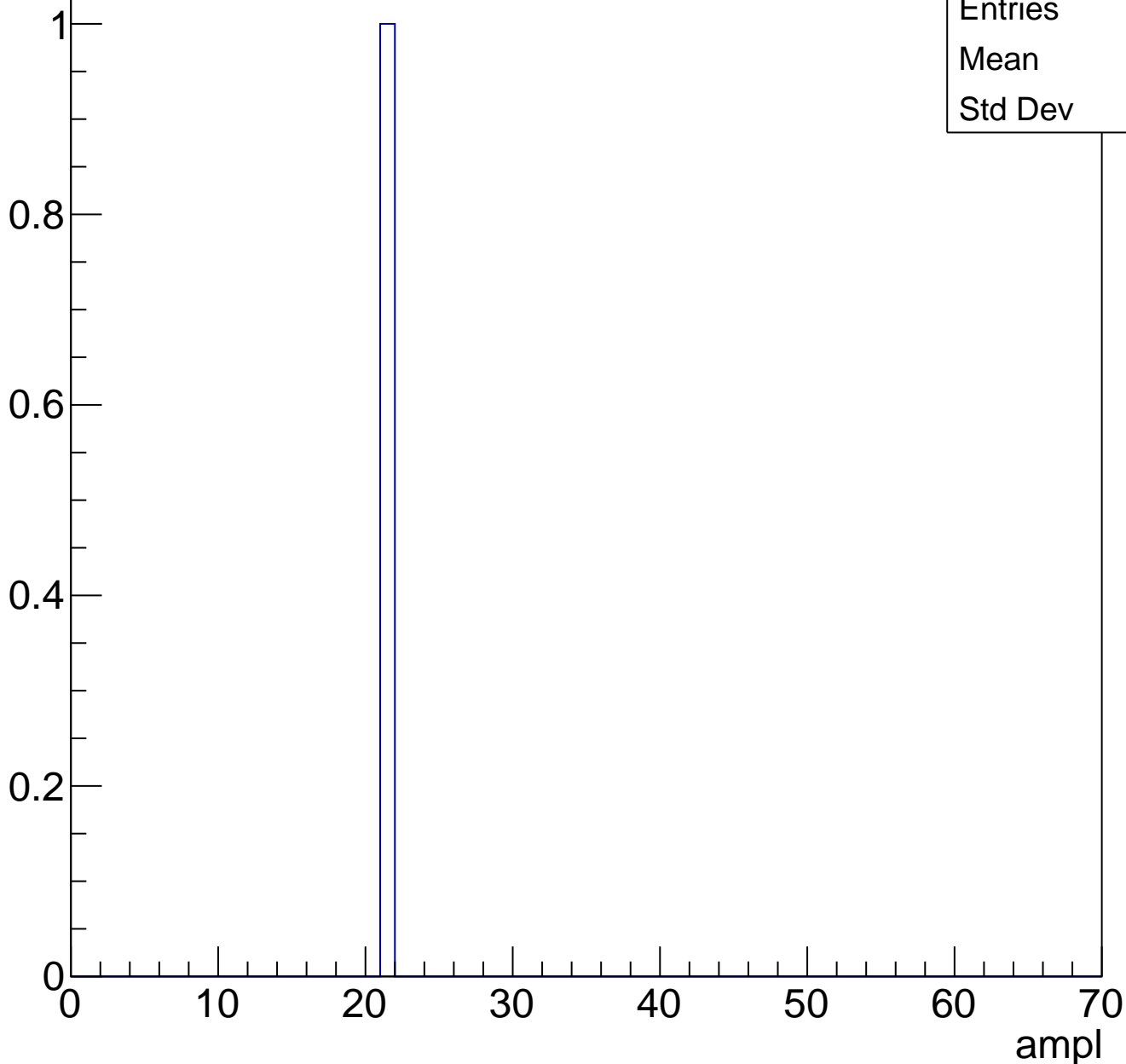




# B1L000S, U24-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch14, adc0

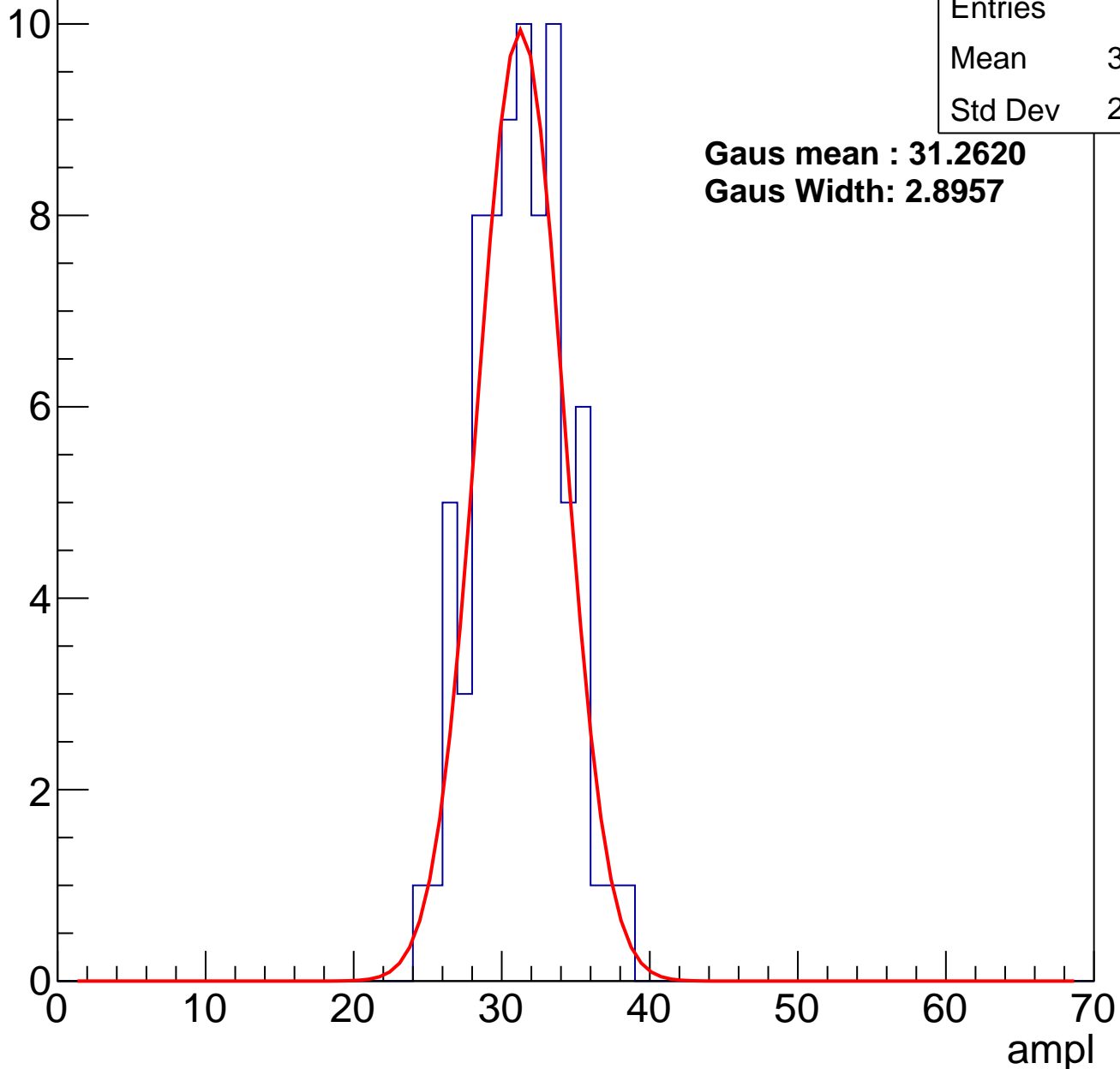
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	30.82
Std Dev	2.944

**Gaus mean : 31.2620**

**Gaus Width: 2.8957**

Entry



# B1L000S, U24-ch14, adc1

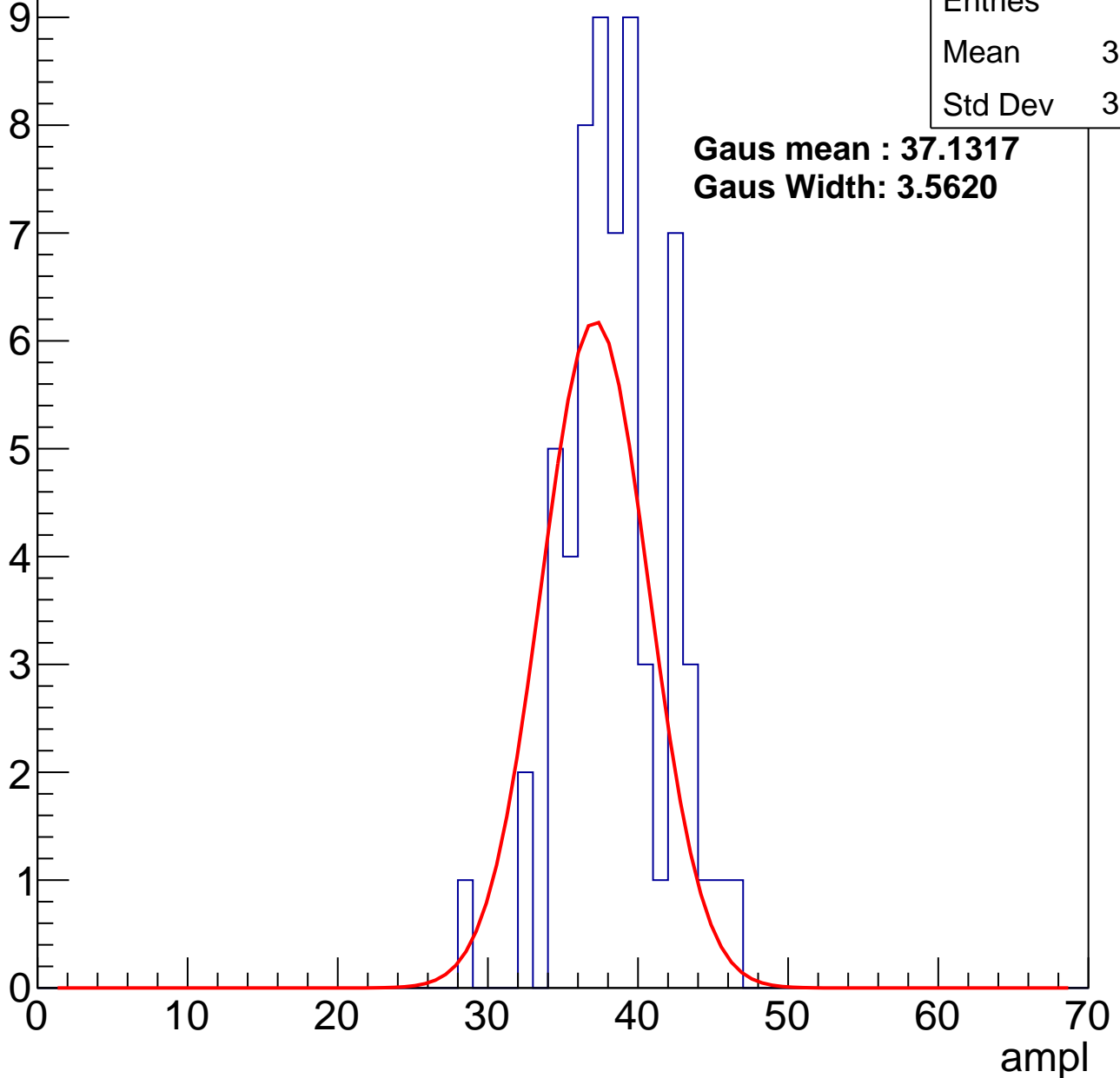
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	38.05
Std Dev	3.348

**Gaus mean : 37.1317**

**Gaus Width: 3.5620**



# B1L000S, U24-ch14, adc2

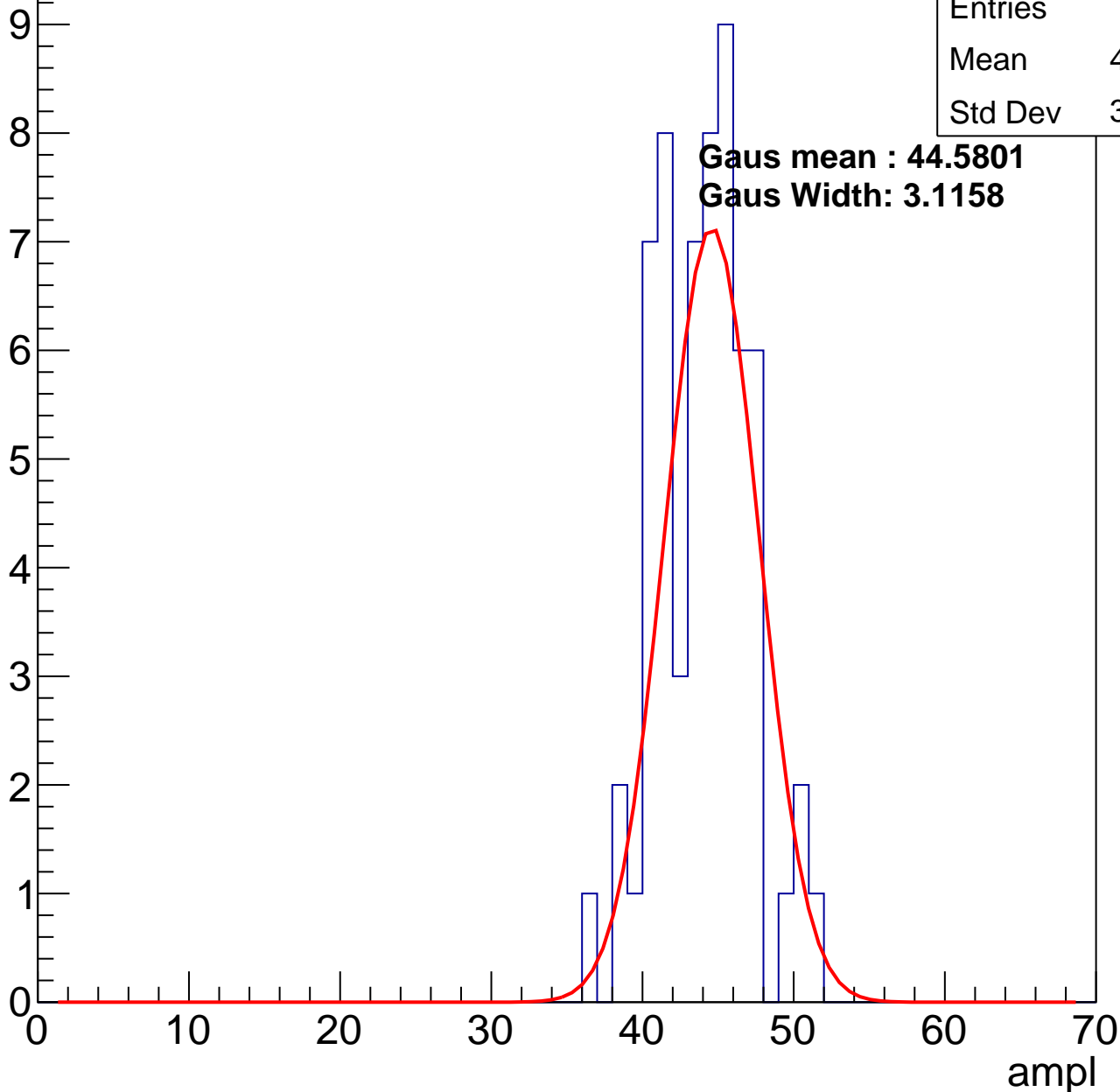
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.56
Std Dev	3.072

**Gaus mean : 44.5801**

**Gaus Width: 3.1158**

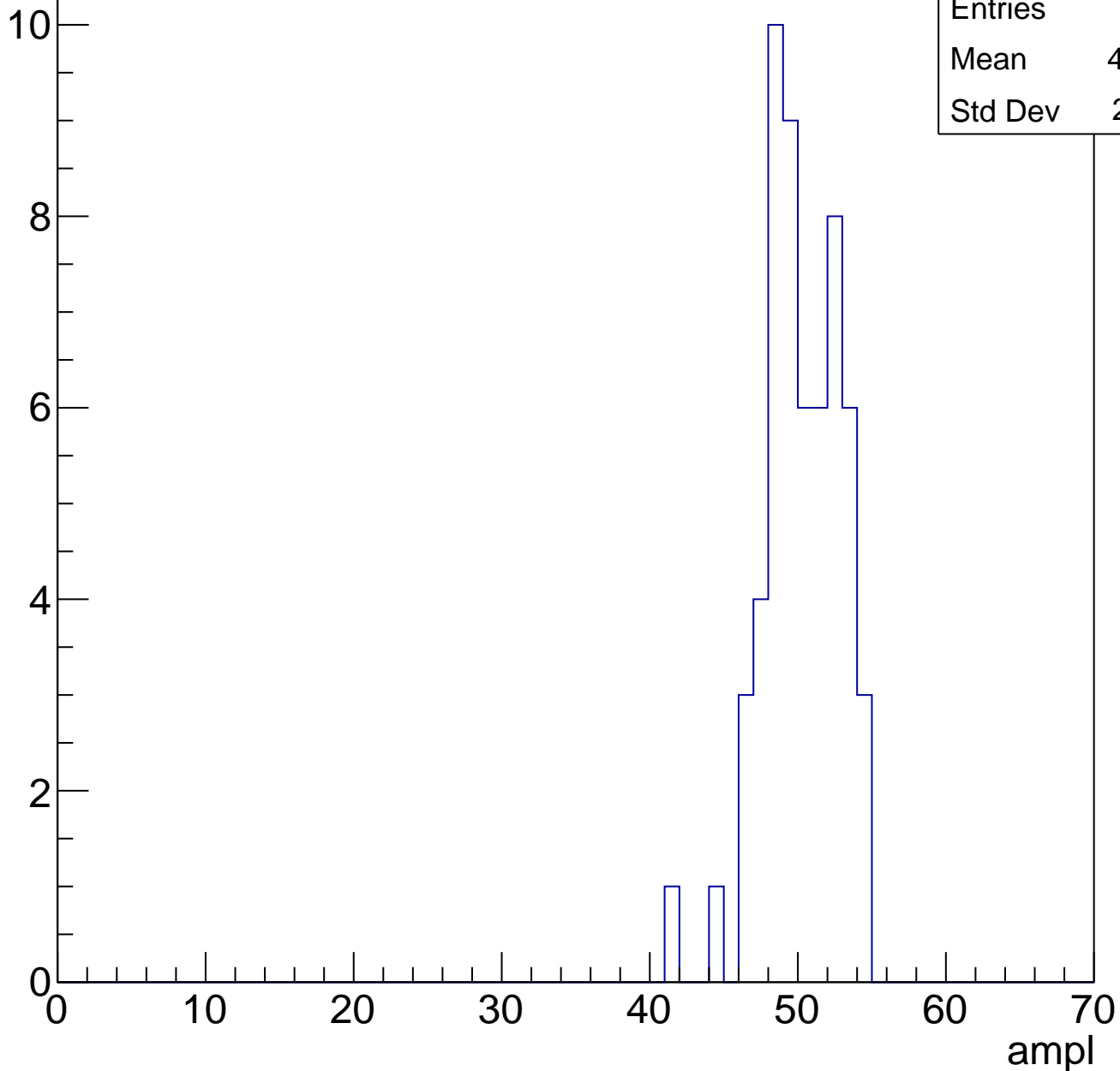


# B1L000S, U24-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	49.72
Std Dev	2.601

Entry



# B1L000S, U24-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	56.15
Std Dev	3.014

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

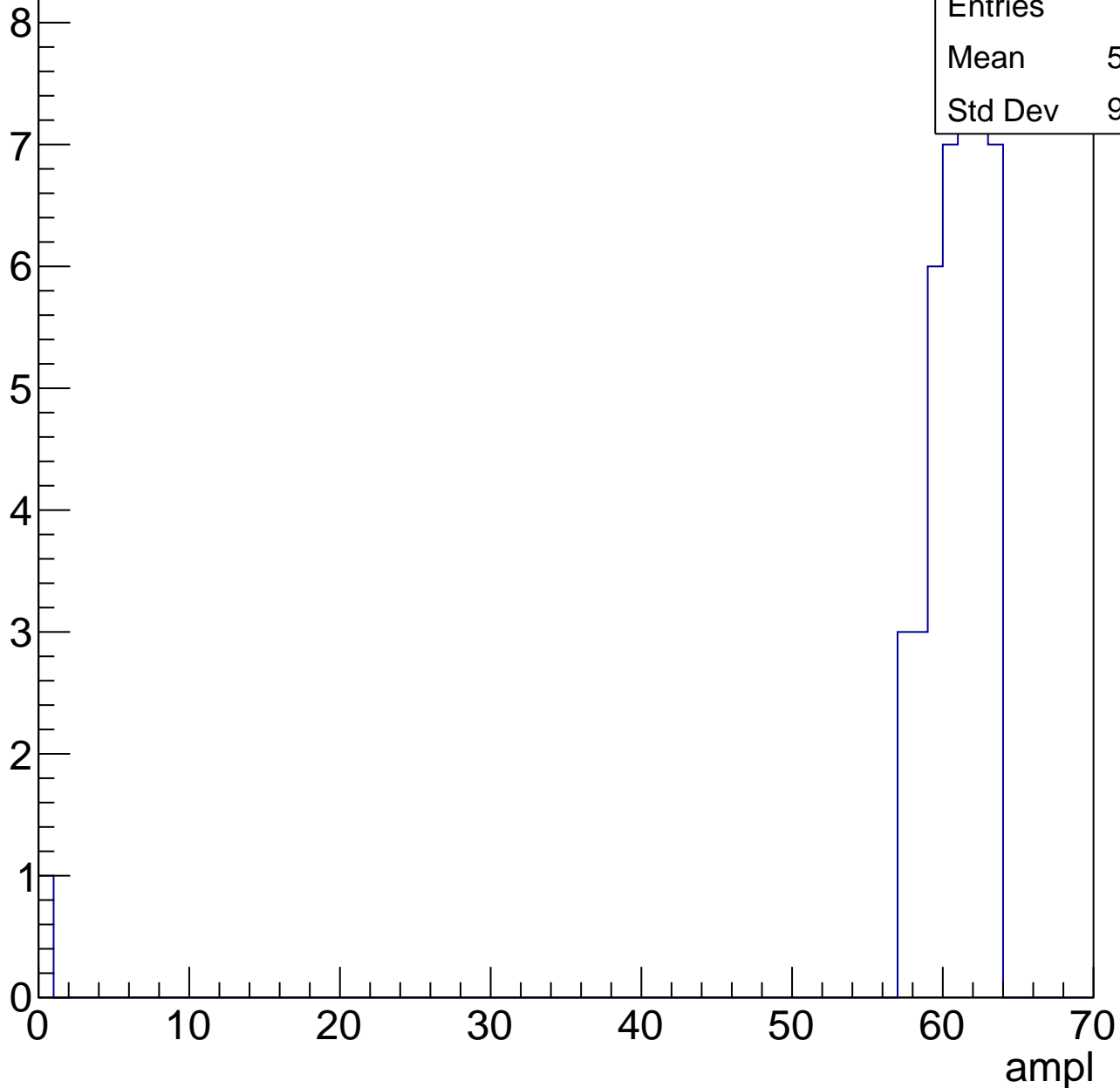
70

# B1L000S, U24-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

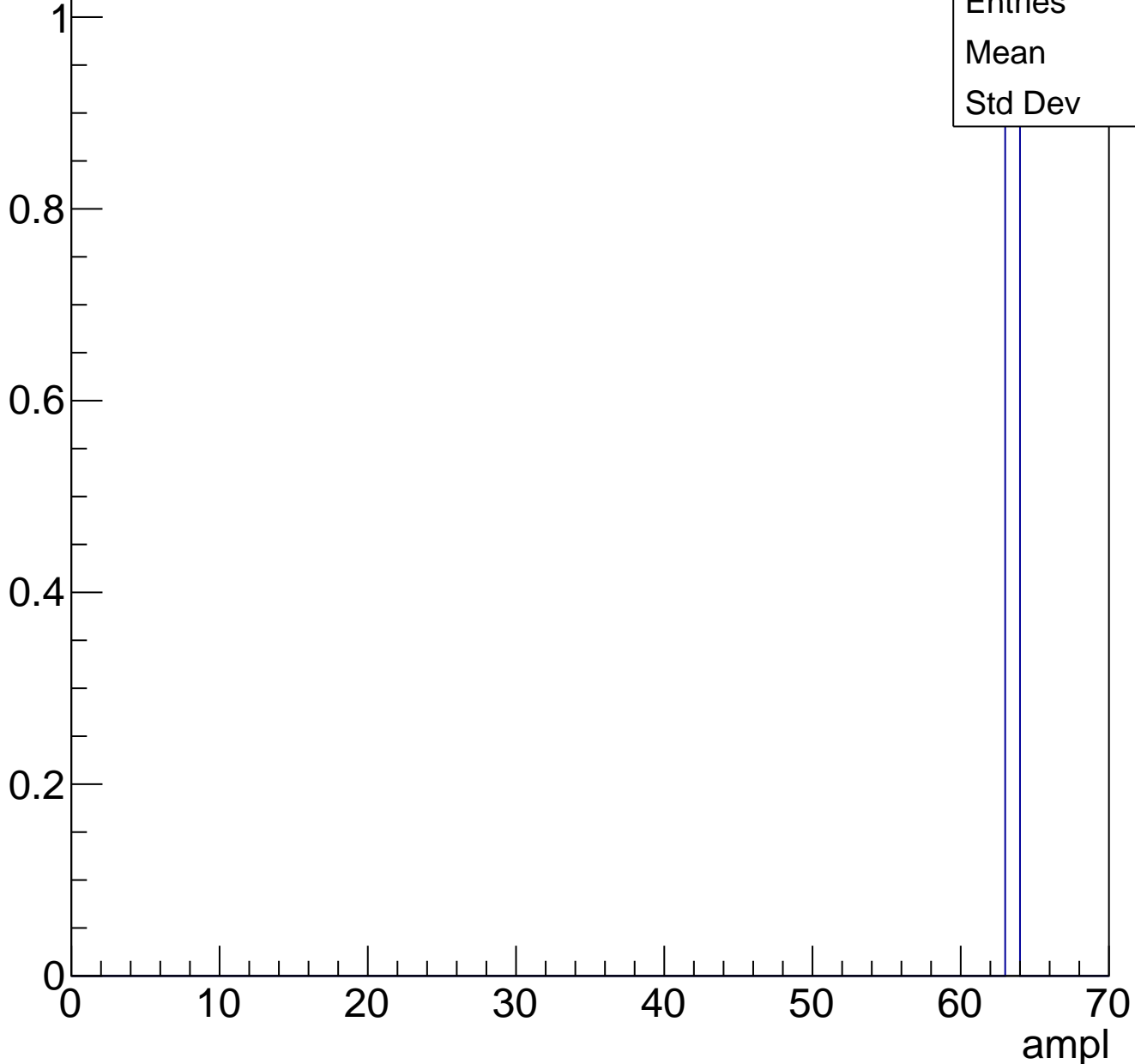
Entries	43
Mean	59.16
Std Dev	9.298



# B1L000S, U24-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch15, adc0

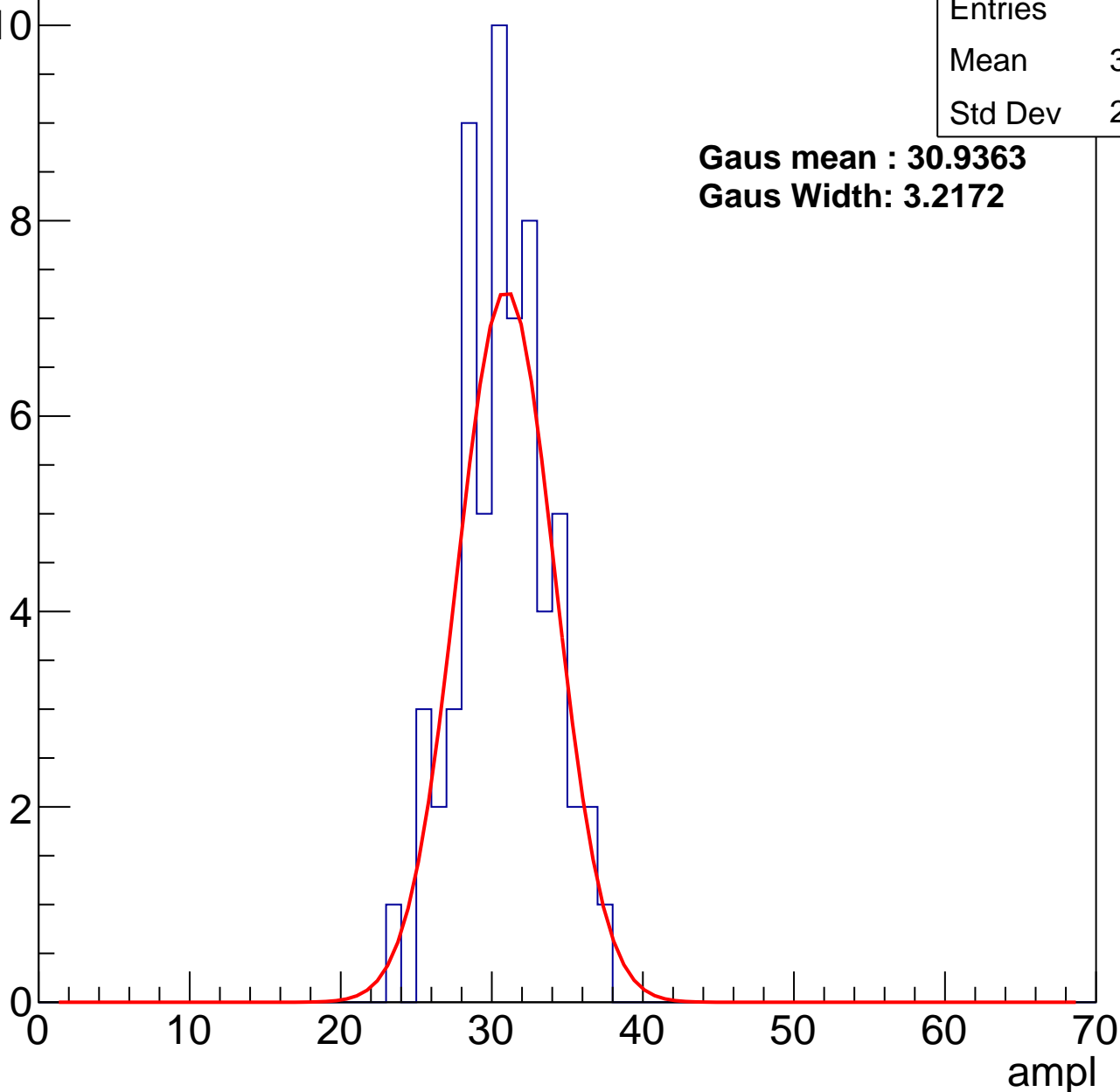
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	30.35
Std Dev	2.957

**Gaus mean : 30.9363**

**Gaus Width: 3.2172**



# B1L000S, U24-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	37.76
Std Dev	2.974

**Gaus mean : 37.3271**

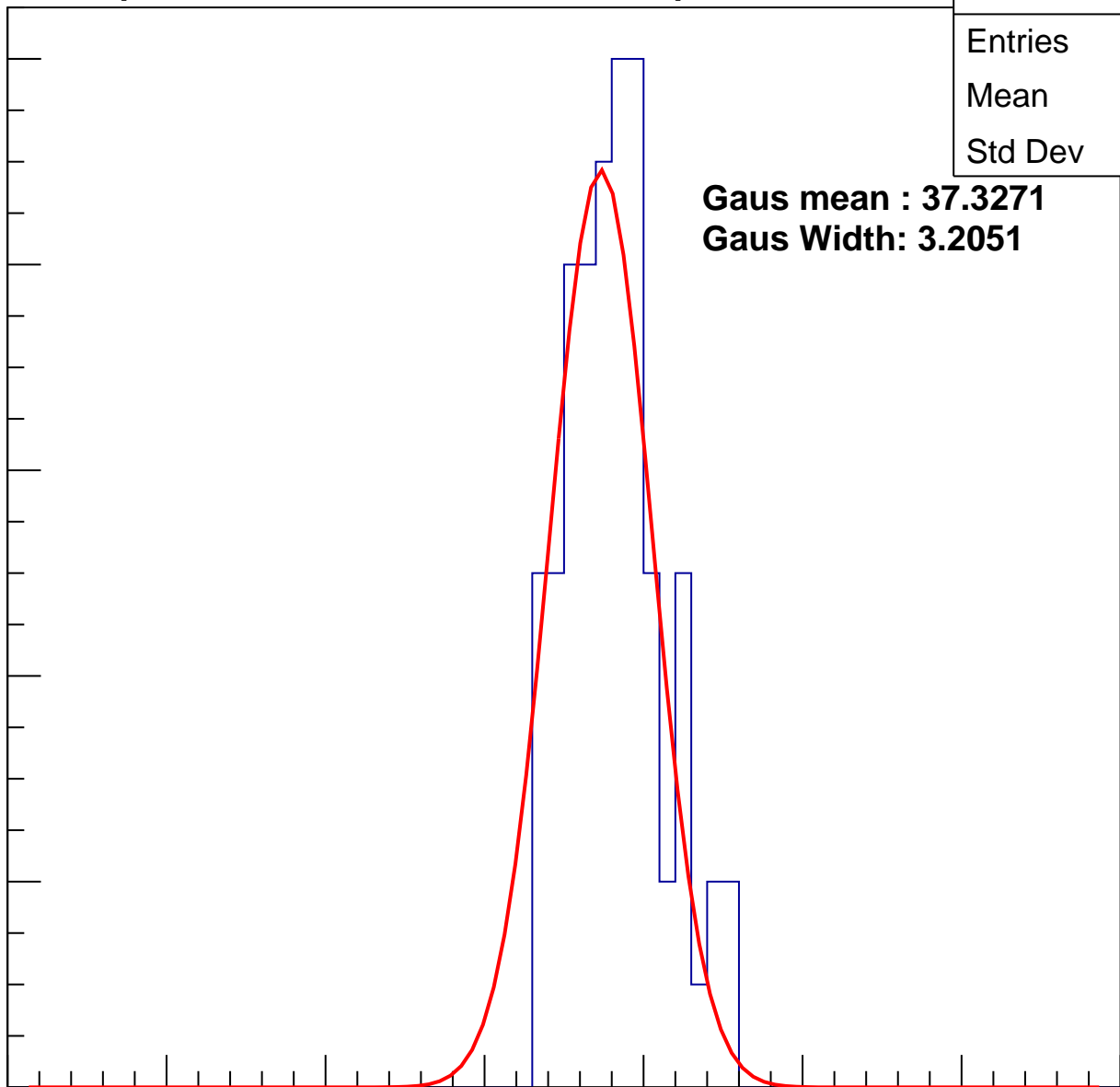
**Gaus Width: 3.2051**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch15, adc2

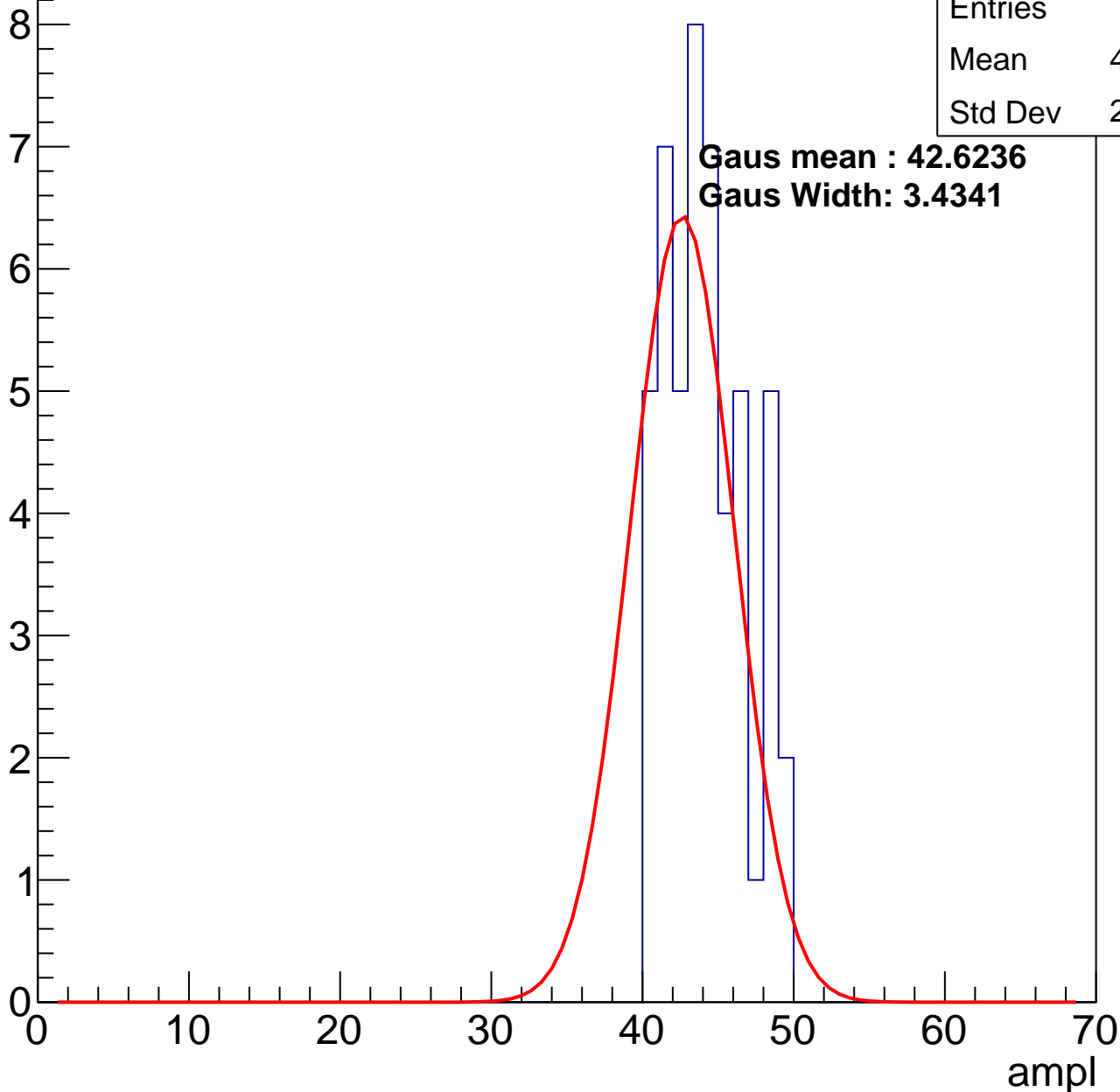
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	43.76
Std Dev	2.599

**Gaus mean : 42.6236**

**Gaus Width: 3.4341**

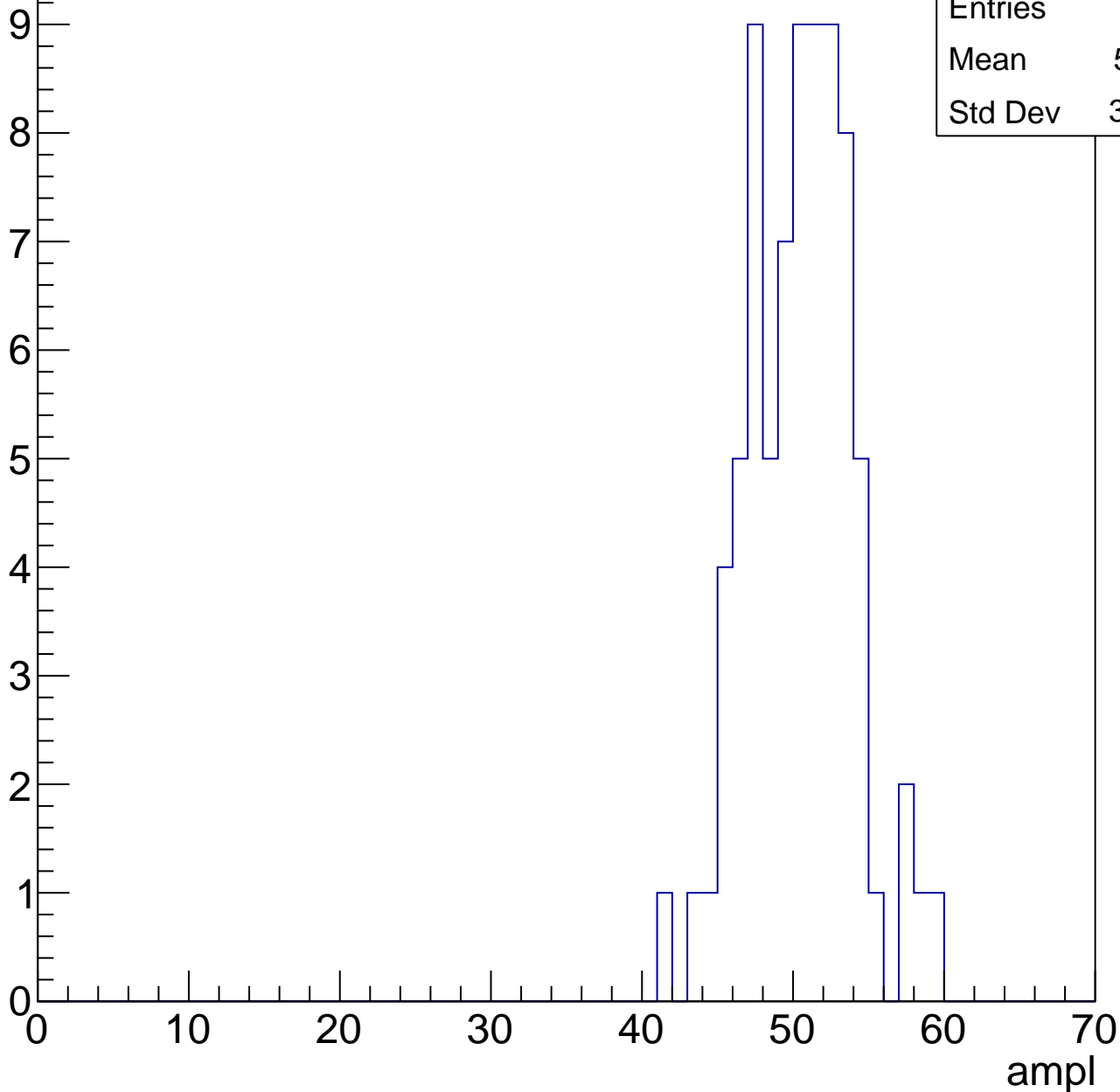


# B1L000S, U24-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	50.01
Std Dev	3.432



# B1L000S, U24-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	55.89
Std Dev	7.569

Entry

10

8

6

4

2

0

0

10

20

30

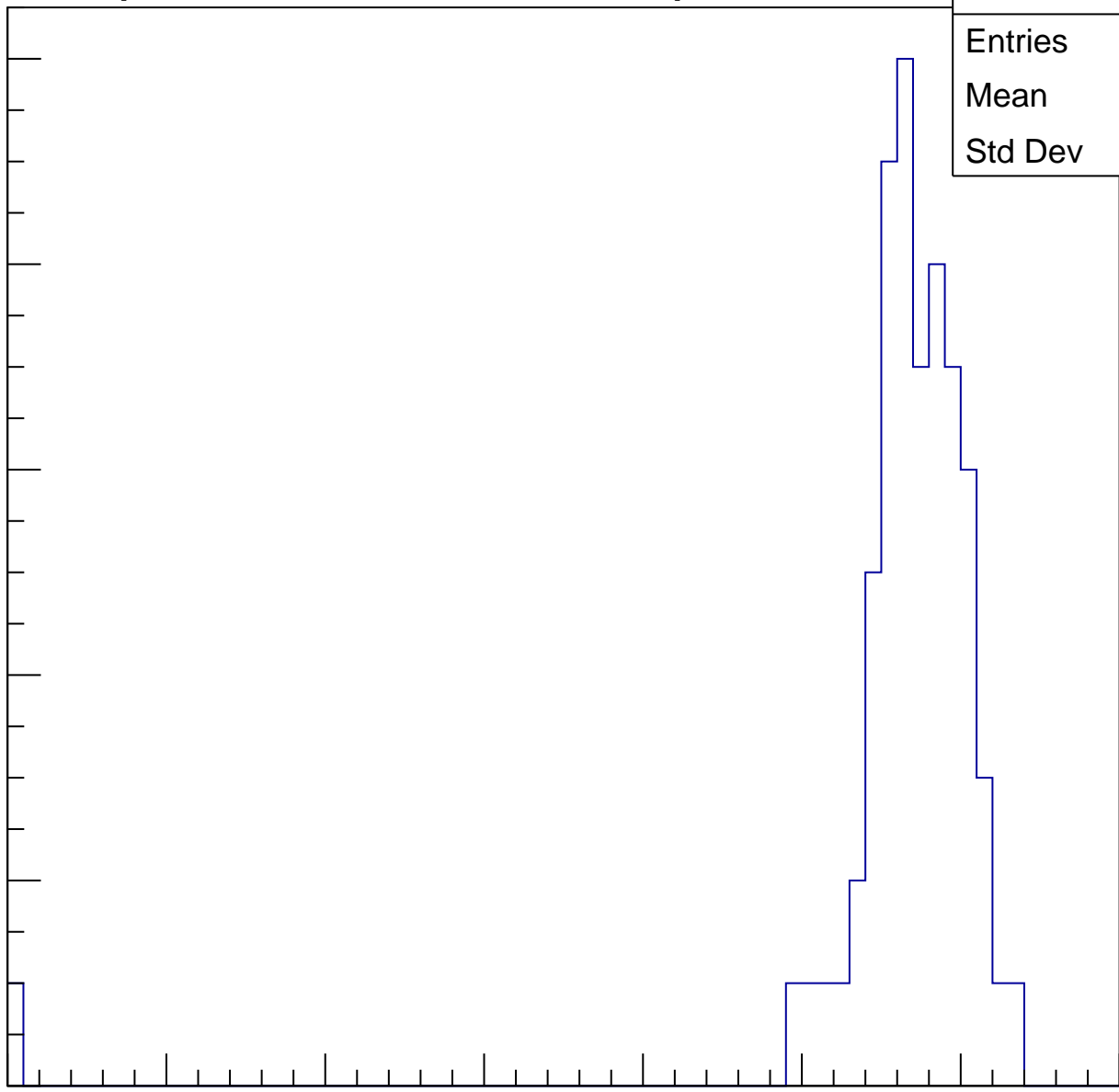
40

50

60

70

ampl

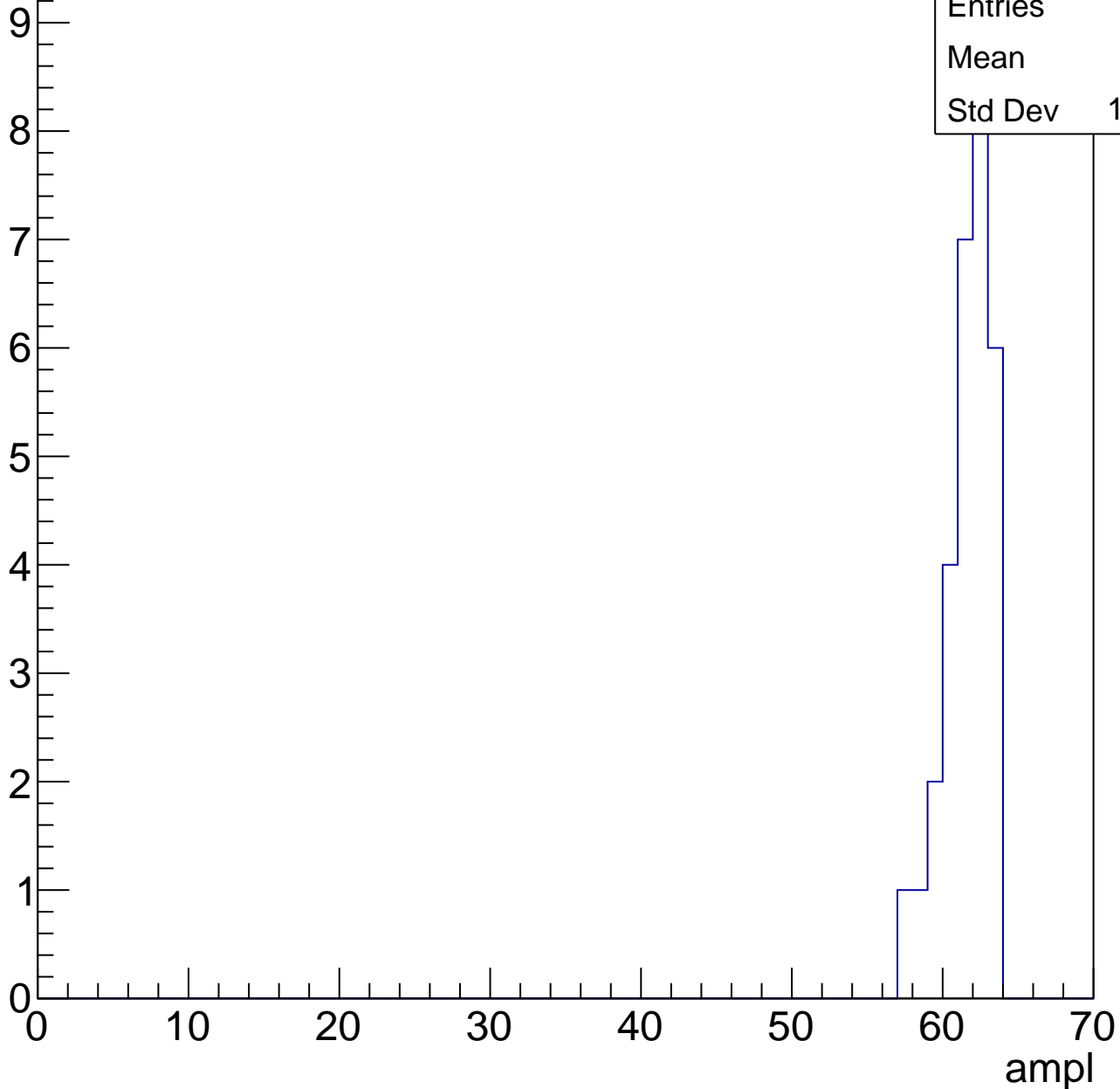


# B1L000S, U24-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	61.2
Std Dev	1.514



# B1L000S, U24-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch16, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	29.52
Std Dev	3.1

**Gaus mean : 29.5913**

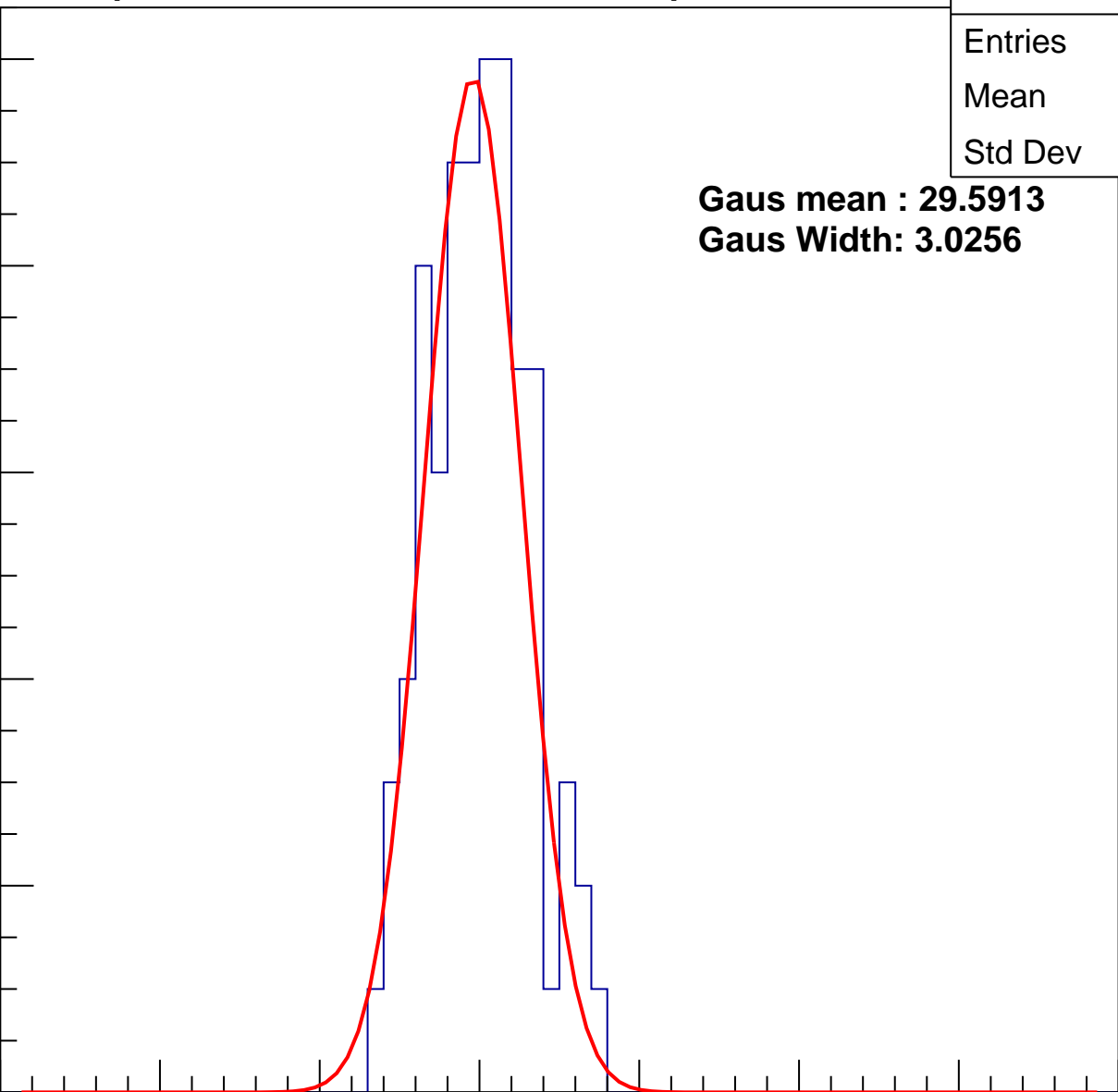
**Gaus Width: 3.0256**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch16, adc1

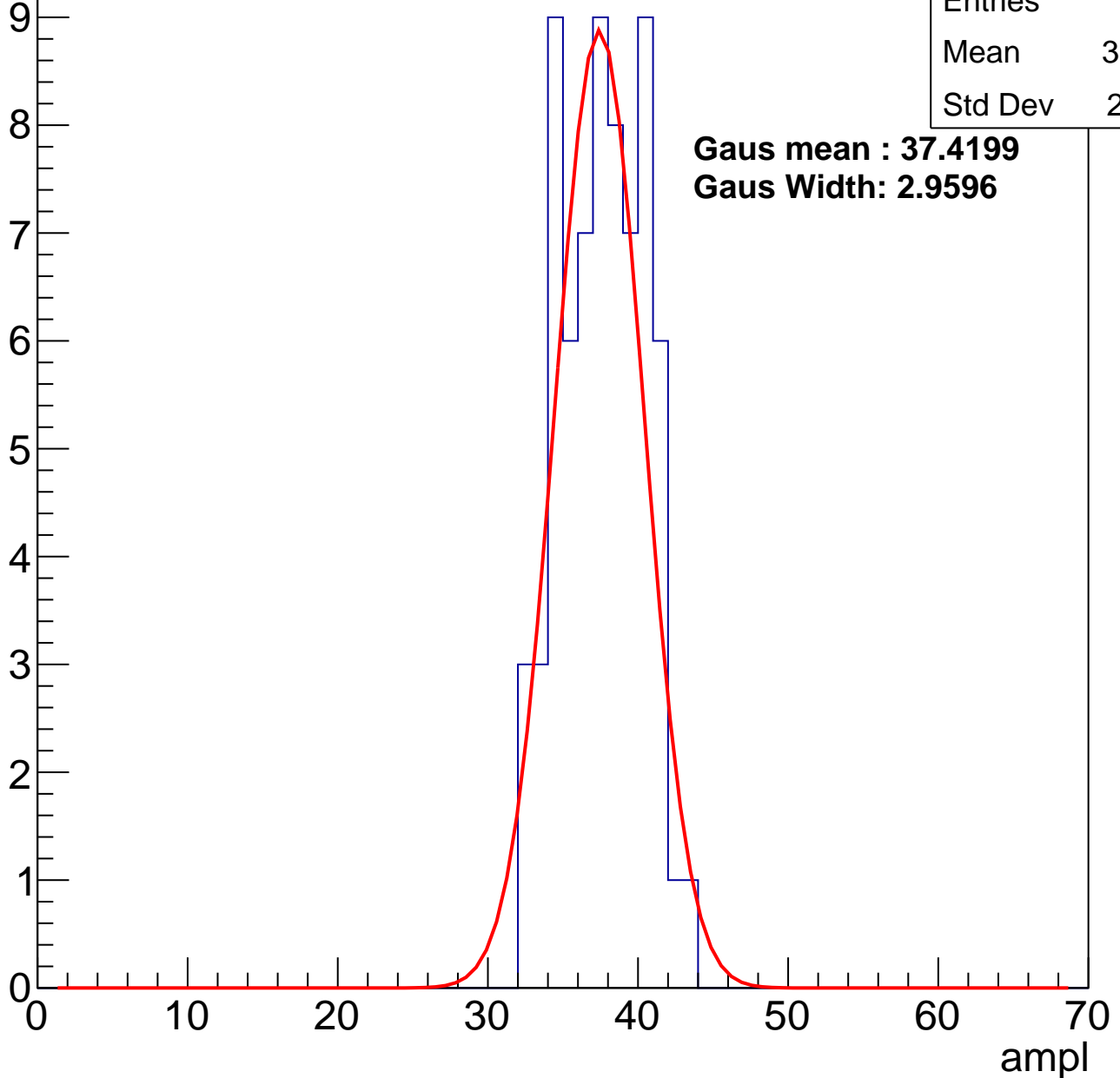
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	37.16
Std Dev	2.711

**Gaus mean : 37.4199**

**Gaus Width: 2.9596**



# B1L000S, U24-ch16, adc2

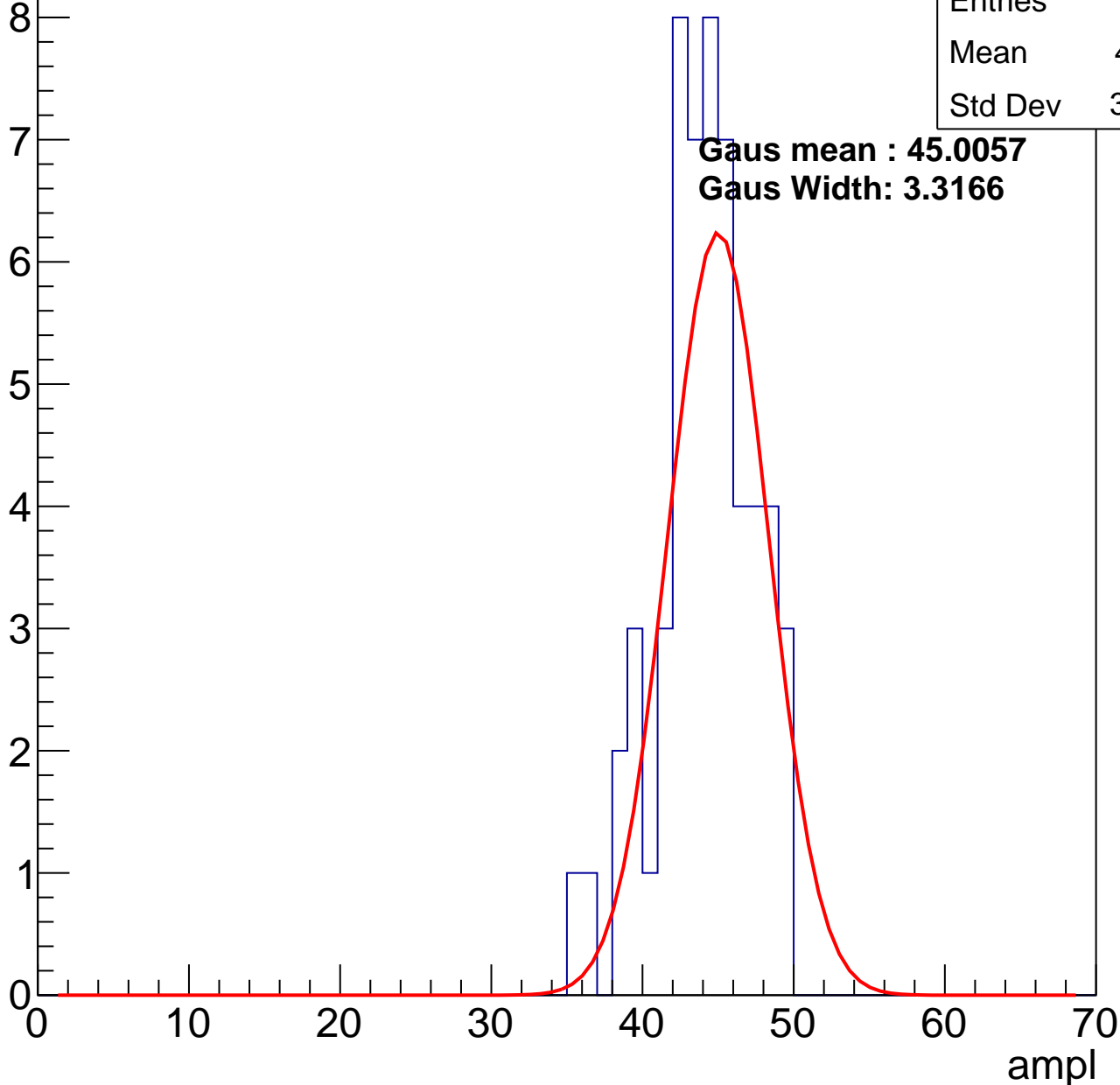
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	43.61
Std Dev	3.172

**Gaus mean : 45.0057**

**Gaus Width: 3.3166**

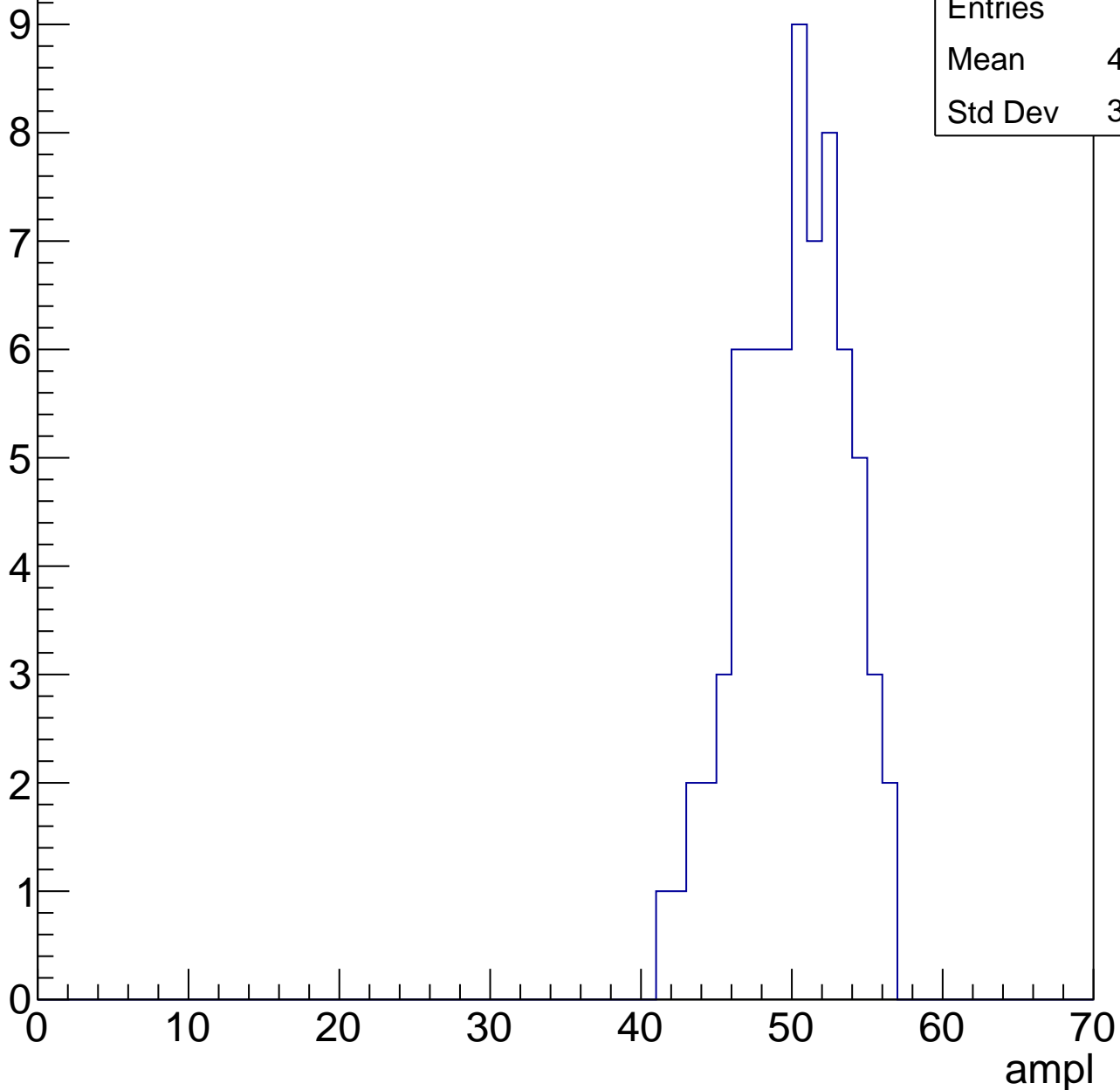


# B1L000S, U24-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	49.59
Std Dev	3.467



# B1L000S, U24-ch16, adc4

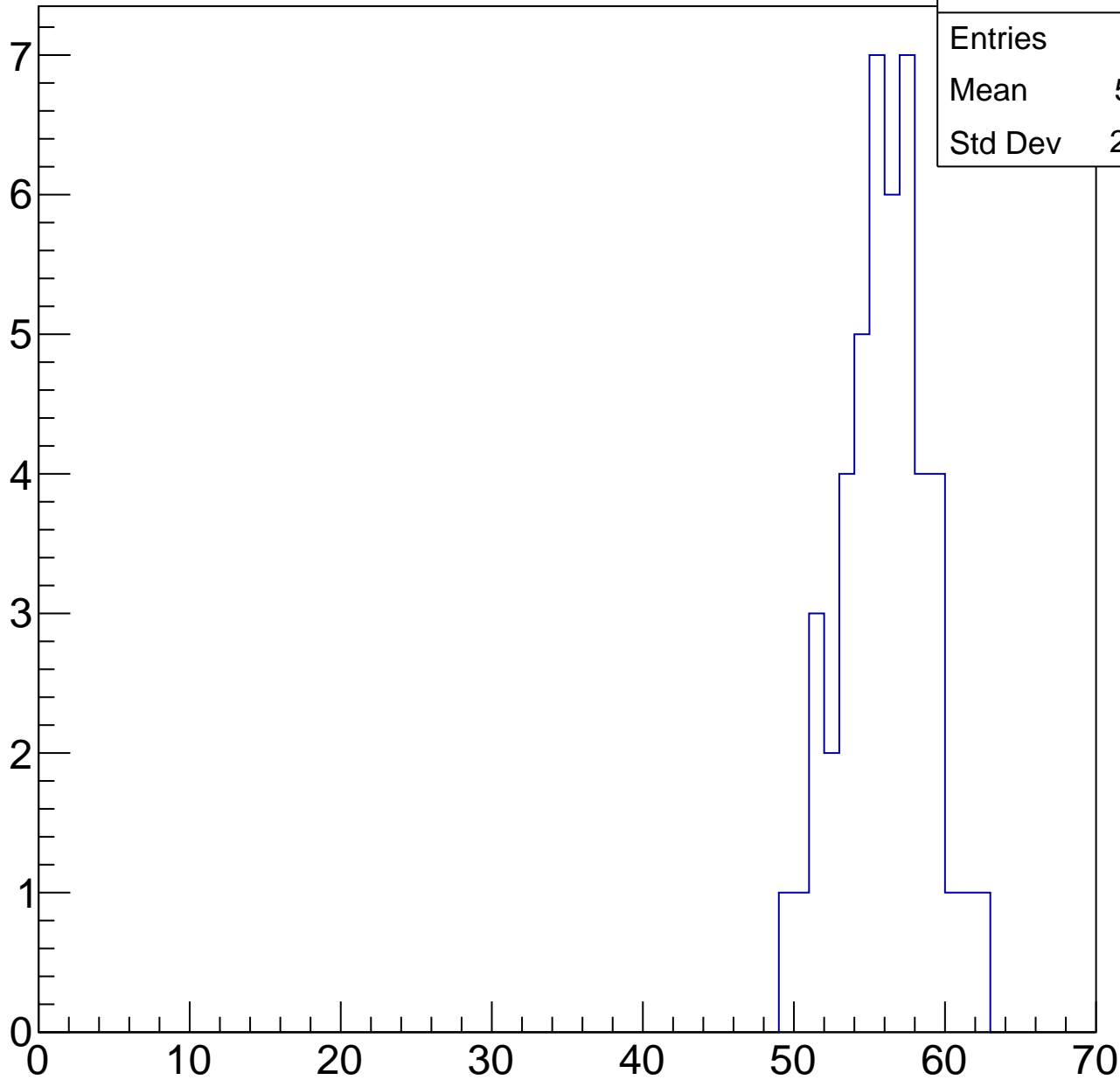
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	55.51
Std Dev	2.842

ampl



# B1L000S, U24-ch16, adc5

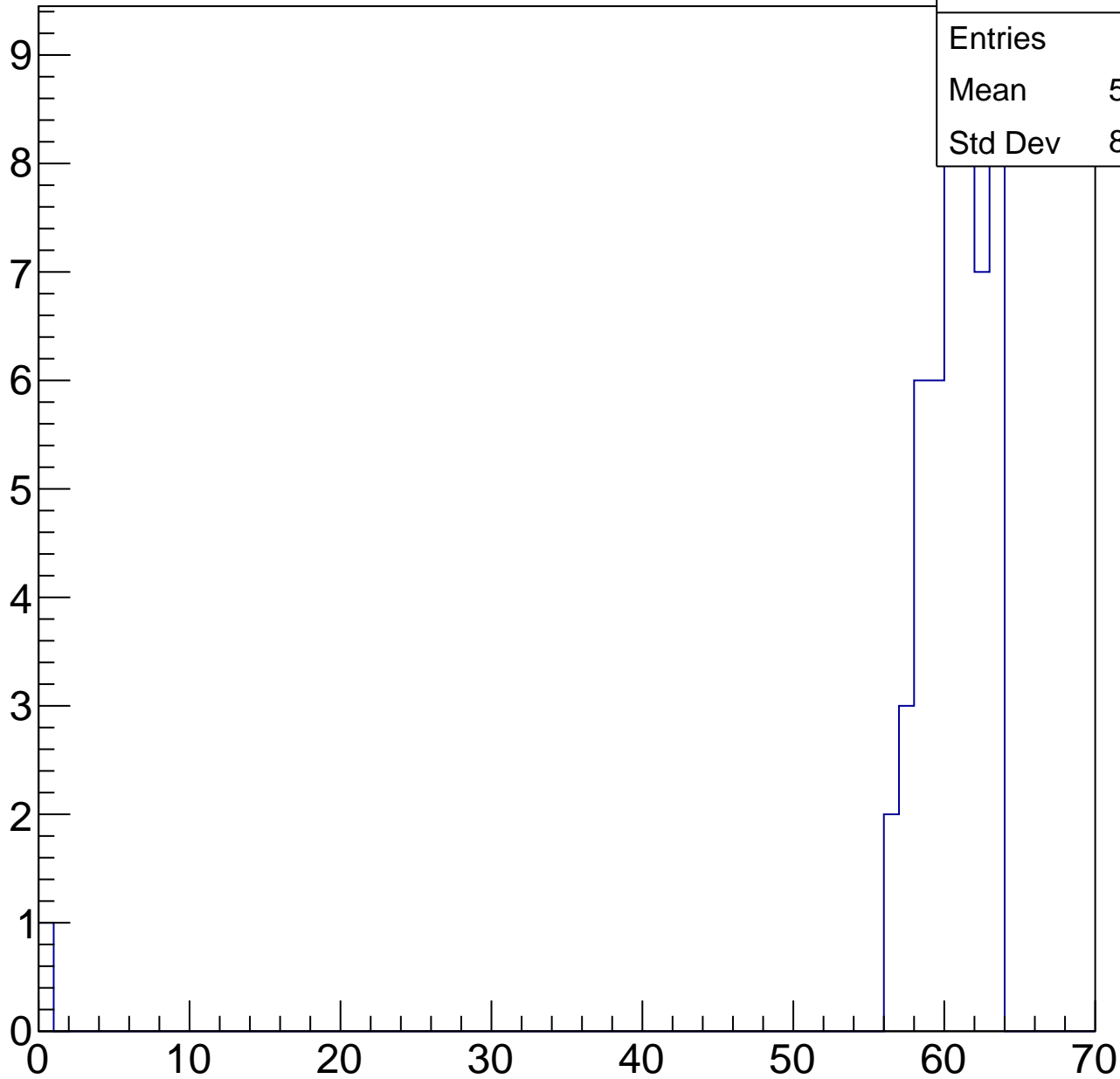
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.06
Std Dev	8.578

ampl



# B1L000S, U24-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

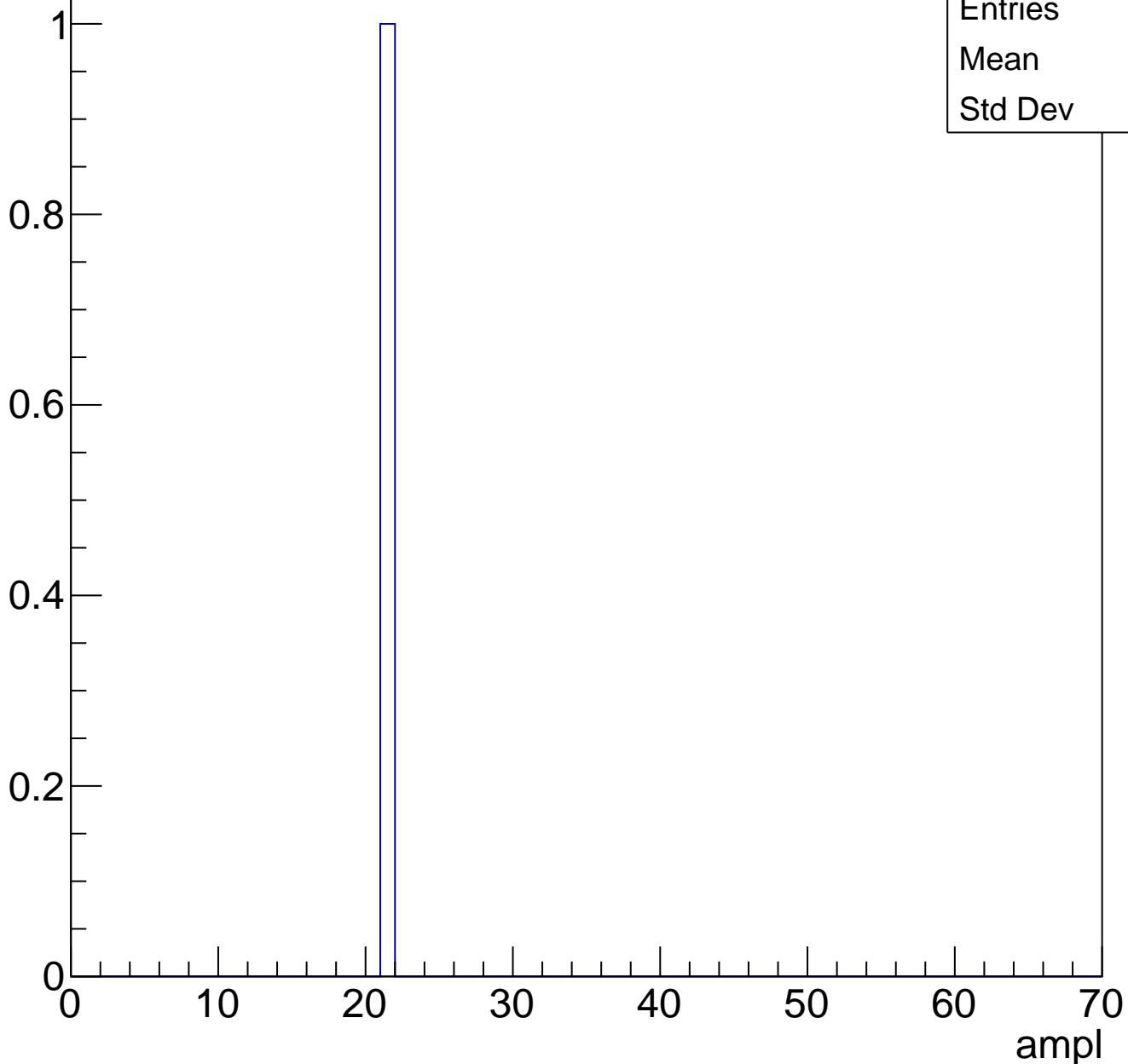




# B1L000S, U24-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	21
Std Dev	0

# B1L000S, U24-ch17, adc0

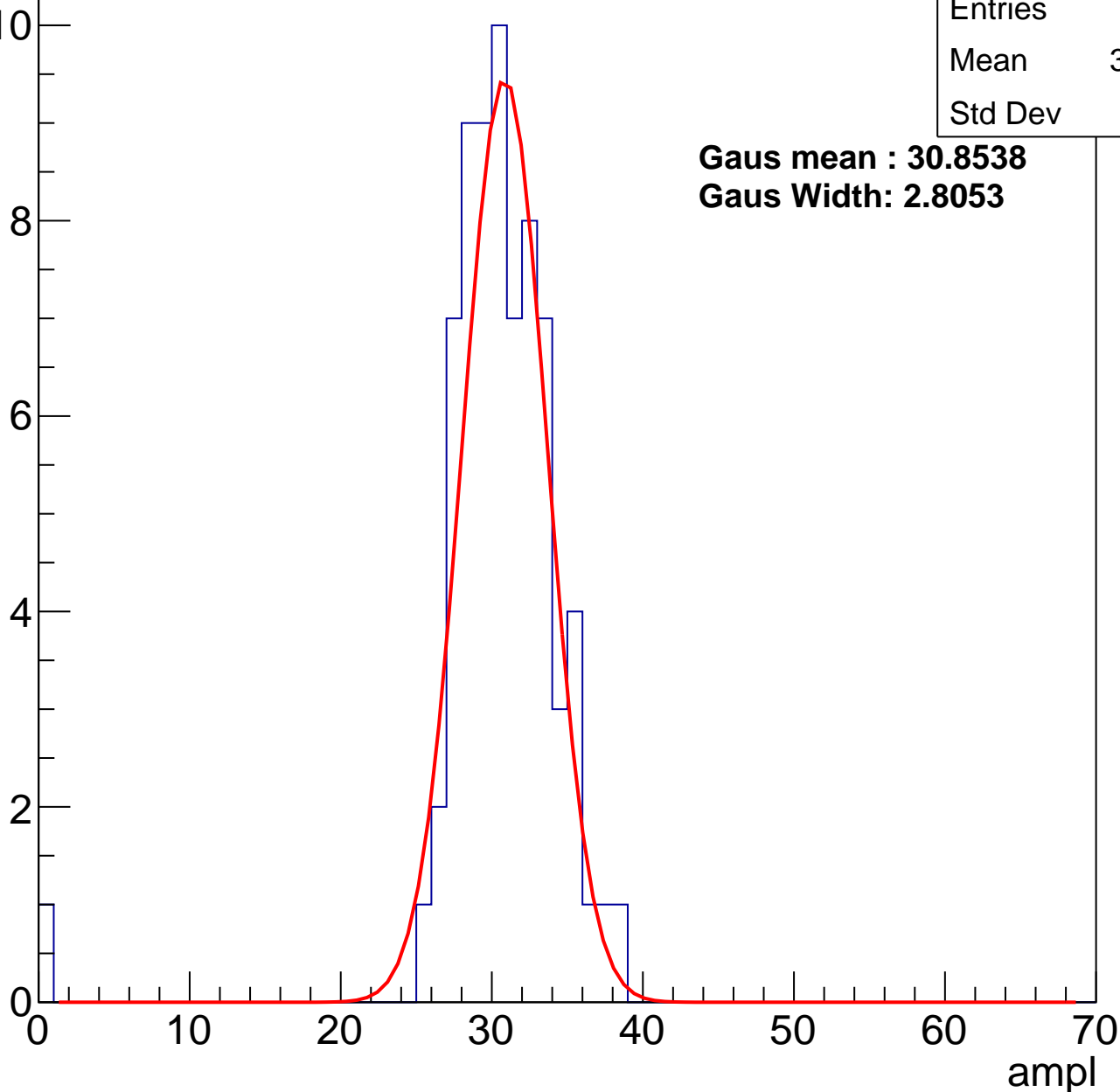
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	30.08
Std Dev	4.54

**Gaus mean : 30.8538**

**Gaus Width: 2.8053**



# B1L000S, U24-ch17, adc1

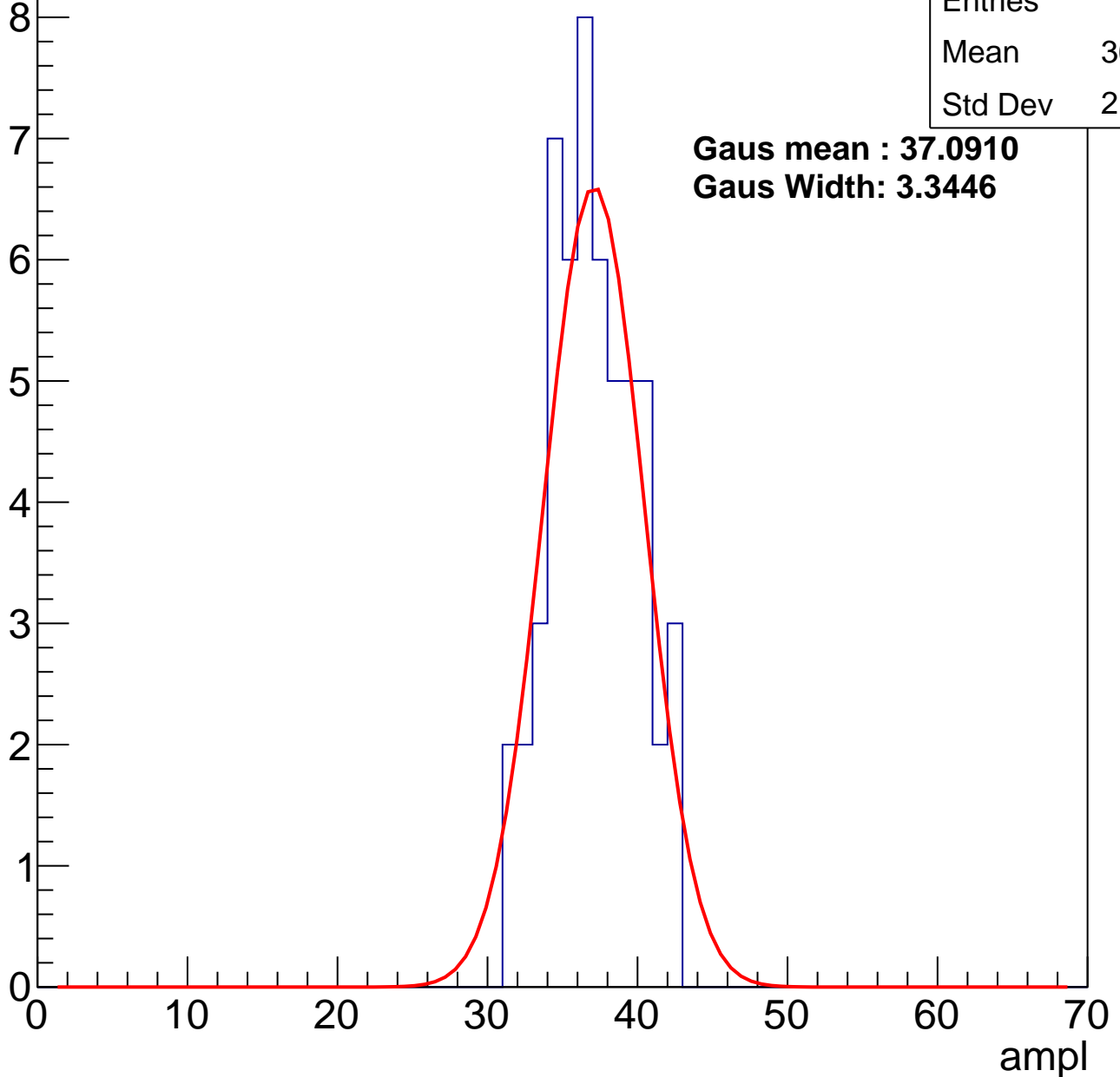
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	36.59
Std Dev	2.832

**Gaus mean : 37.0910**

**Gaus Width: 3.3446**



# B1L000S, U24-ch17, adc2

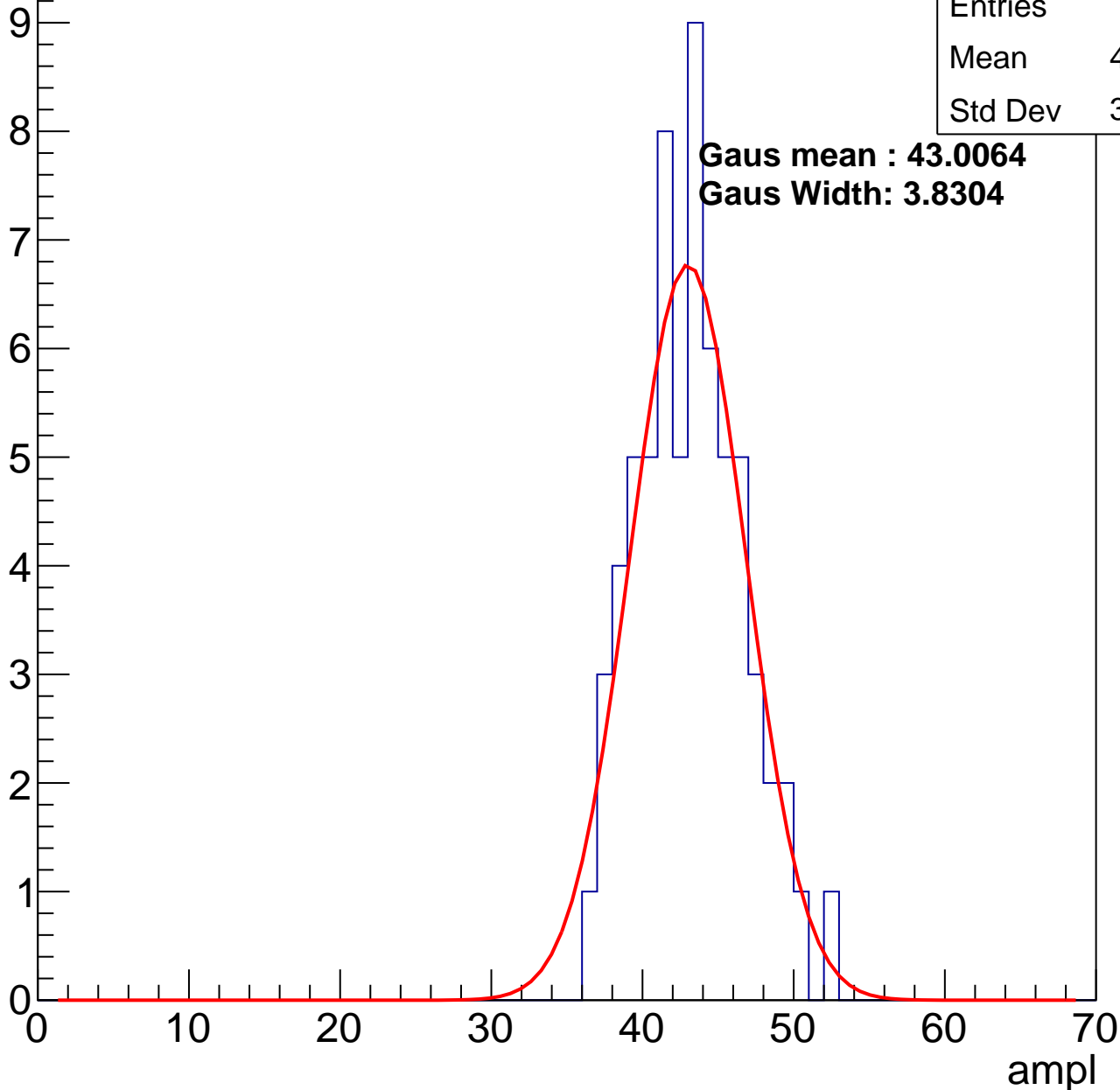
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	42.69
Std Dev	3.473

**Gaus mean : 43.0064**

**Gaus Width: 3.8304**

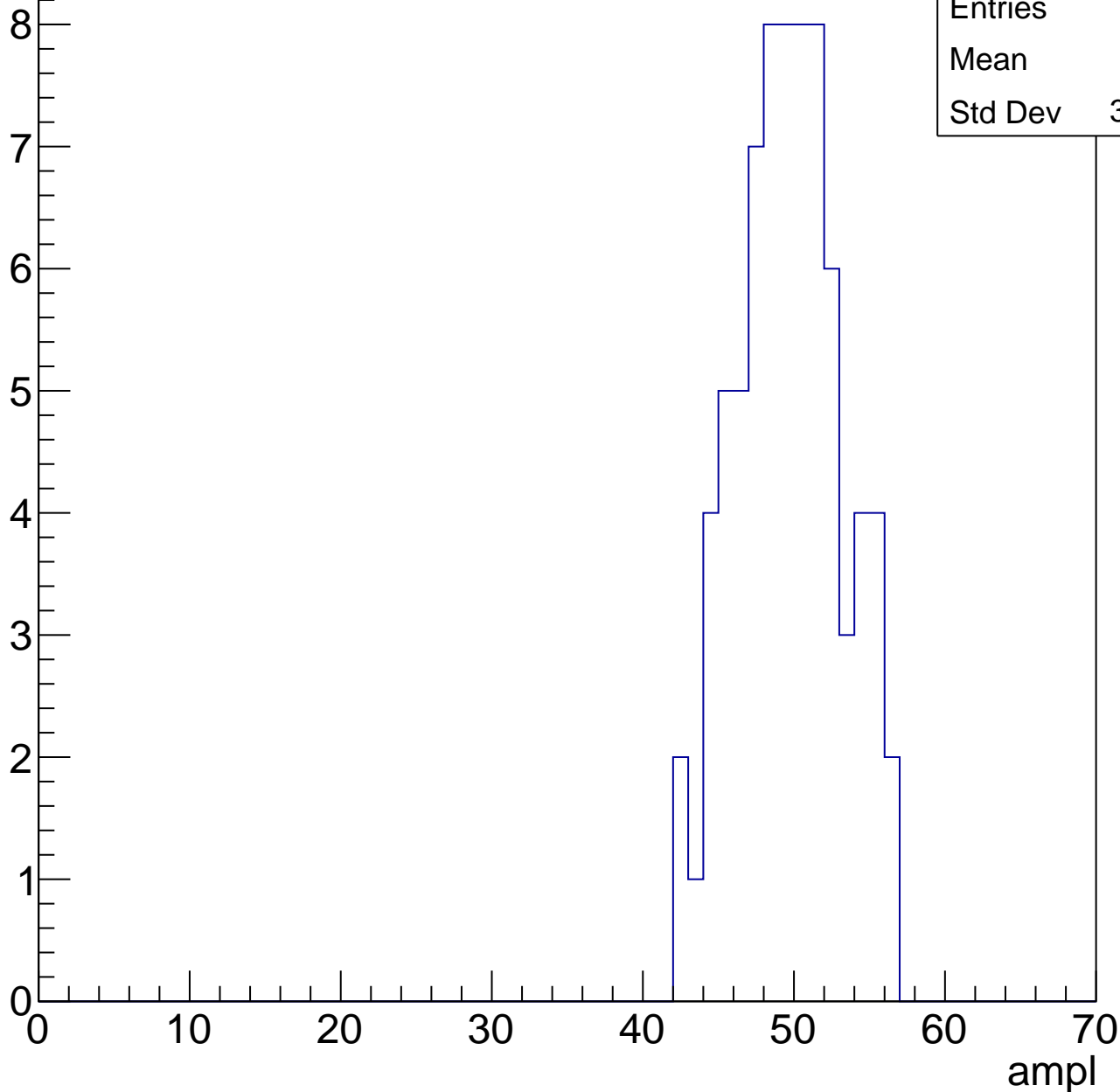


# B1L000S, U24-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	49.2
Std Dev	3.418

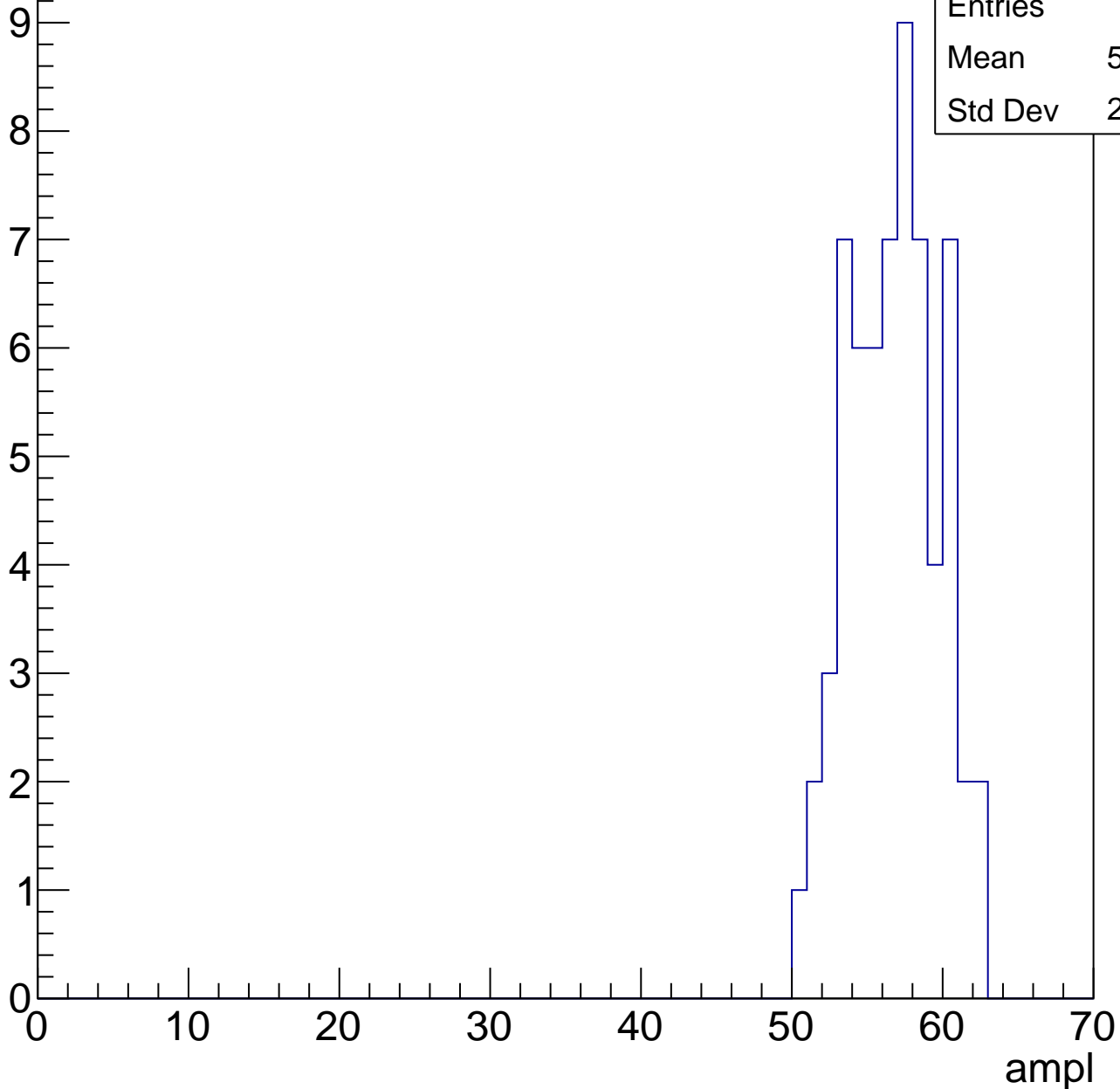


# B1L000S, U24-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

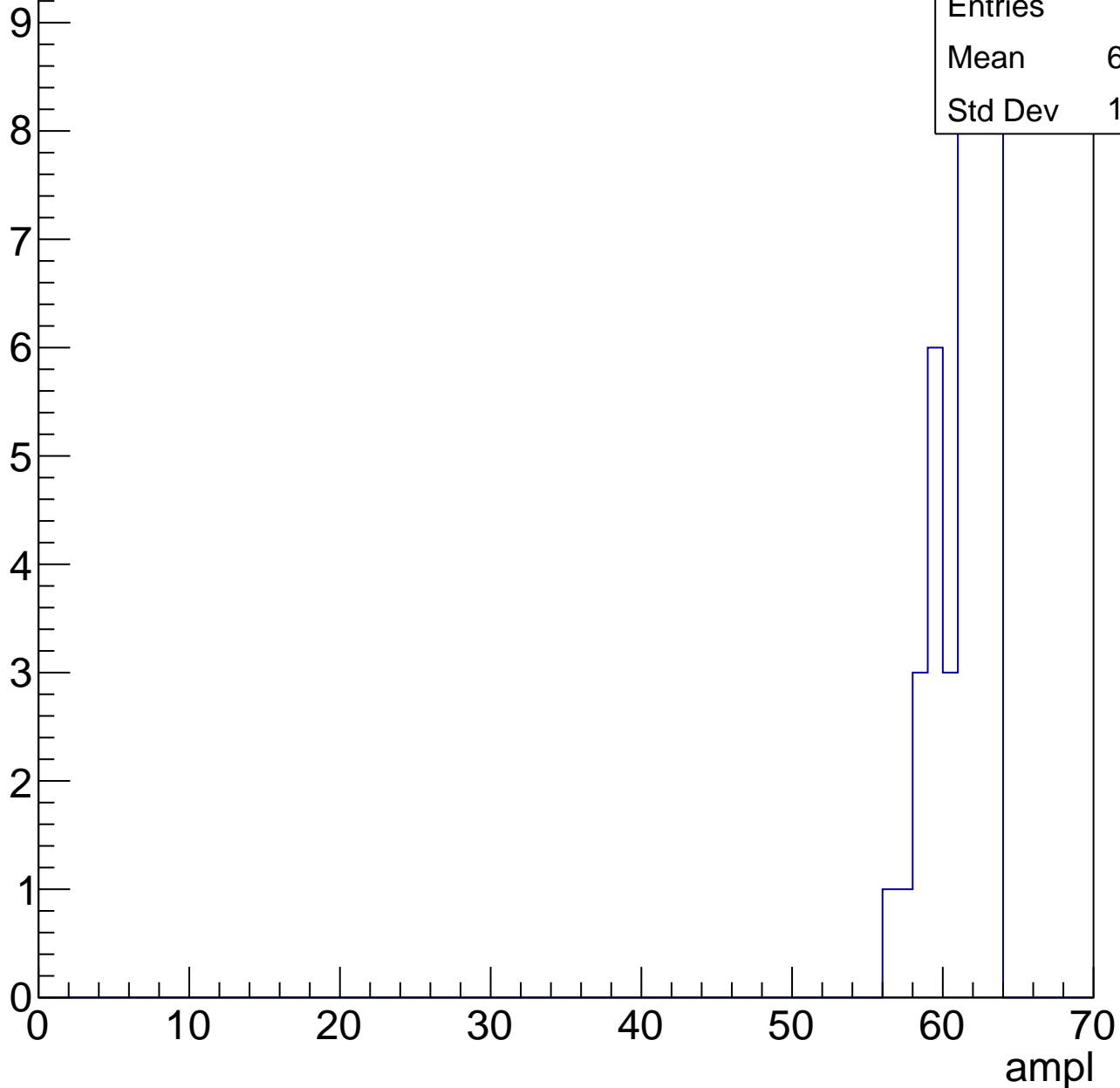
Entries	63
Mean	56.29
Std Dev	2.897



# B1L000S, U24-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	28.11
Std Dev	3.434

**Gaus mean : 28.4009**

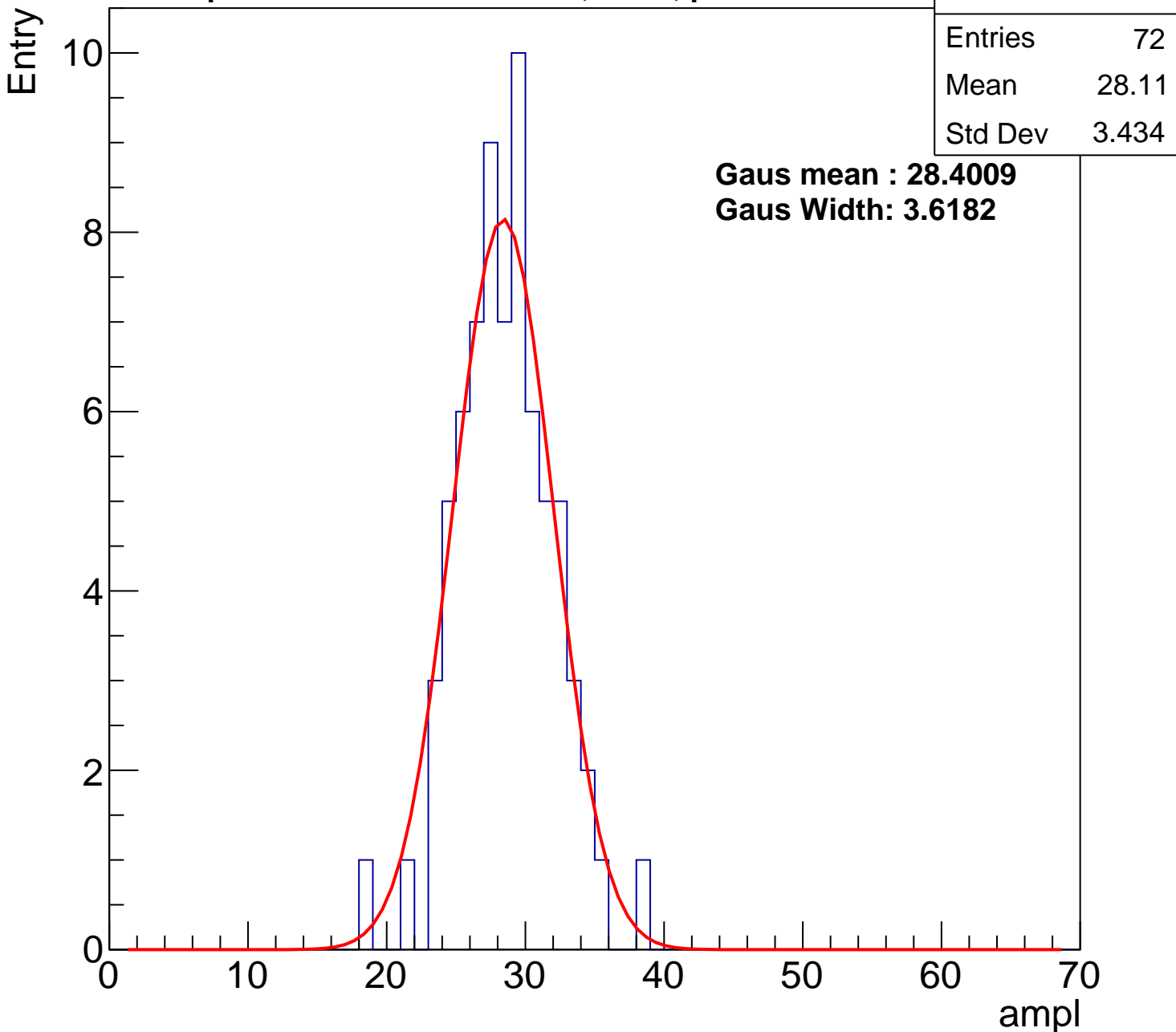
**Gaus Width: 3.6182**

Entry

10  
8  
6  
4  
2  
0

ampl

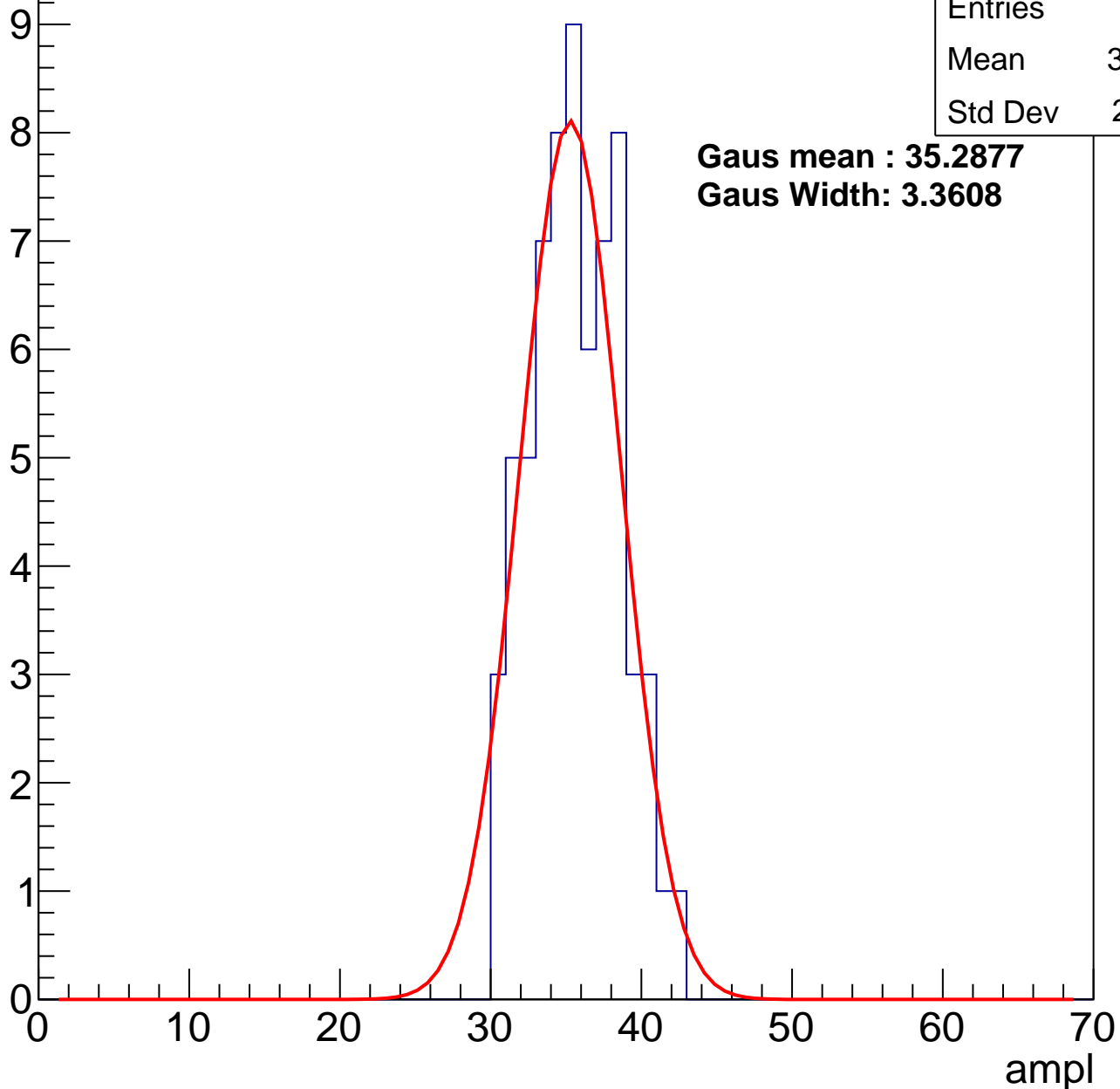
0 10 20 30 40 50 60 70



# B1L000S, U24-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	66
Mean	35.18
Std Dev	2.881

# B1L000S, U24-ch18, adc2

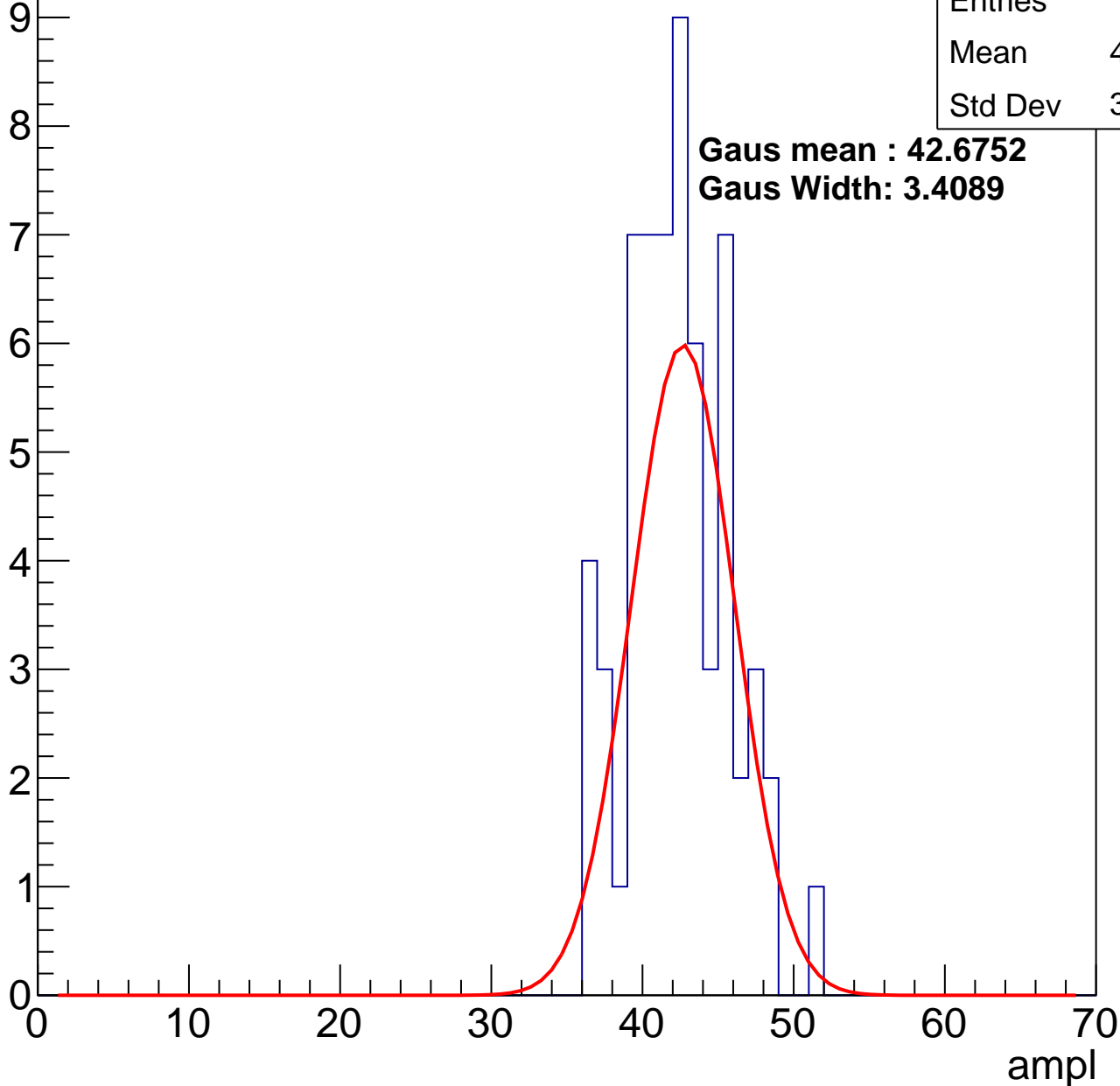
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.87
Std Dev	3.295

**Gaus mean : 42.6752**

**Gaus Width: 3.4089**

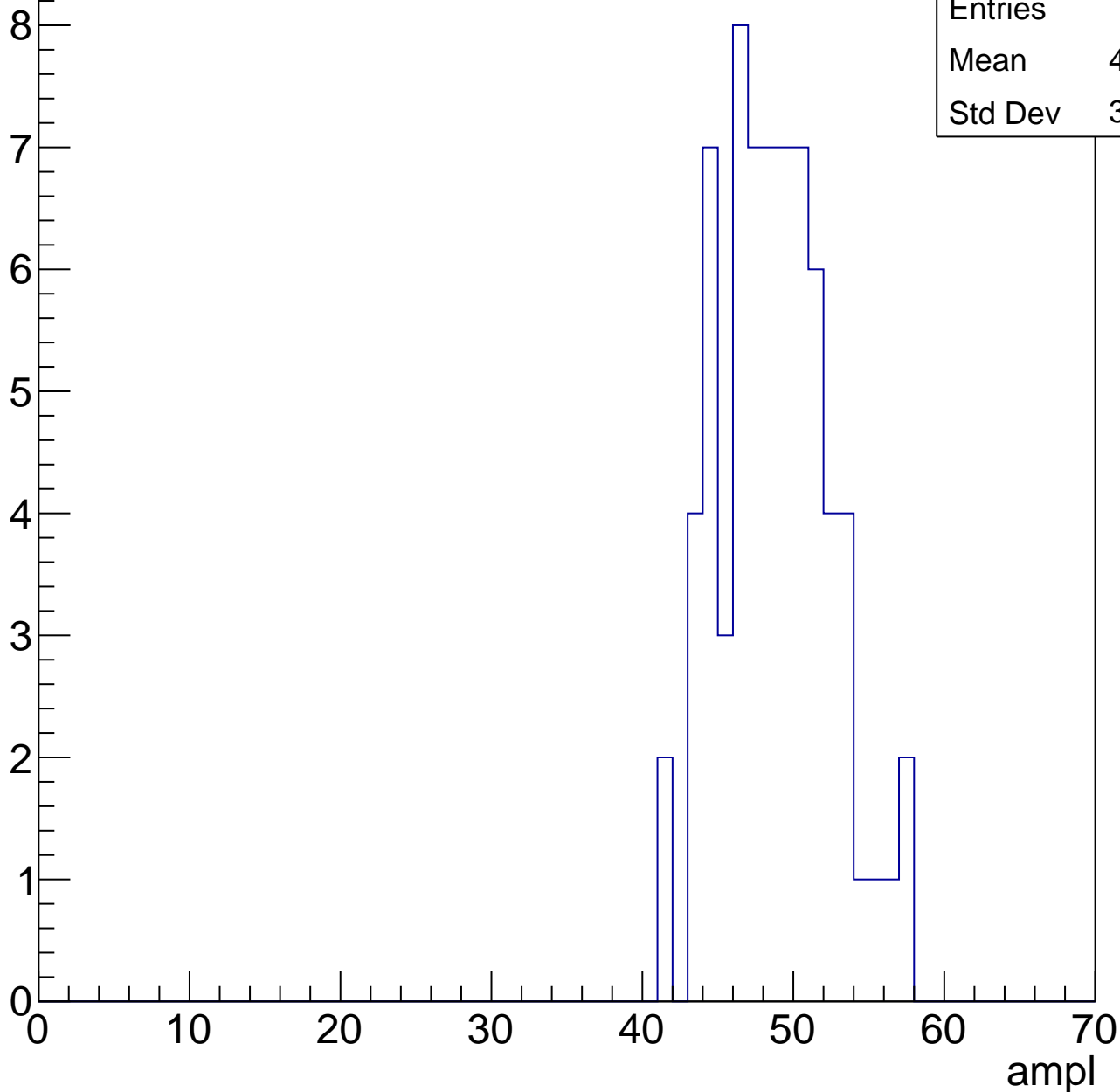


# B1L000S, U24-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	48.28
Std Dev	3.628

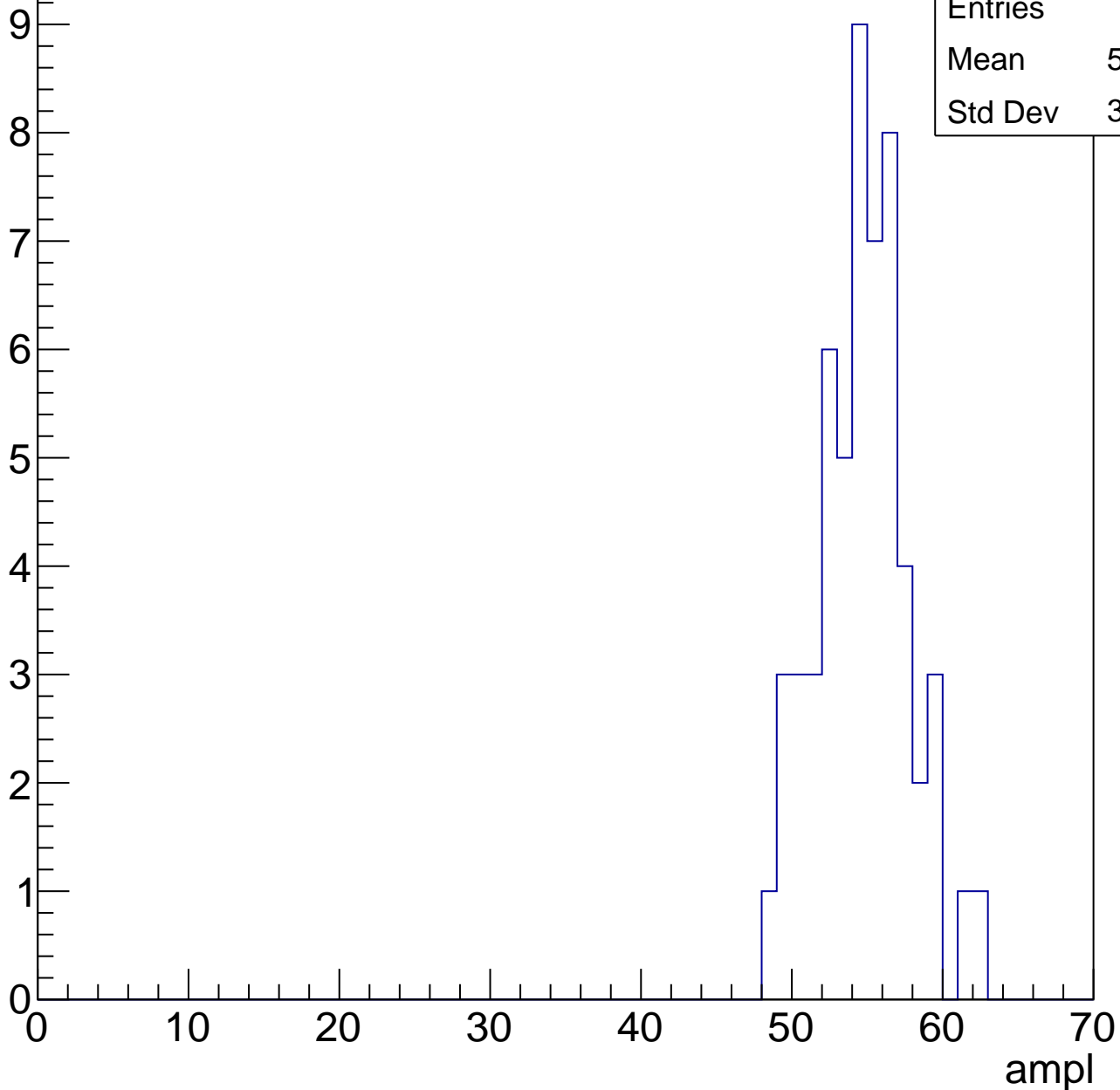


# B1L000S, U24-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	54.25
Std Dev	3.007



# B1L000S, U24-ch18, adc5

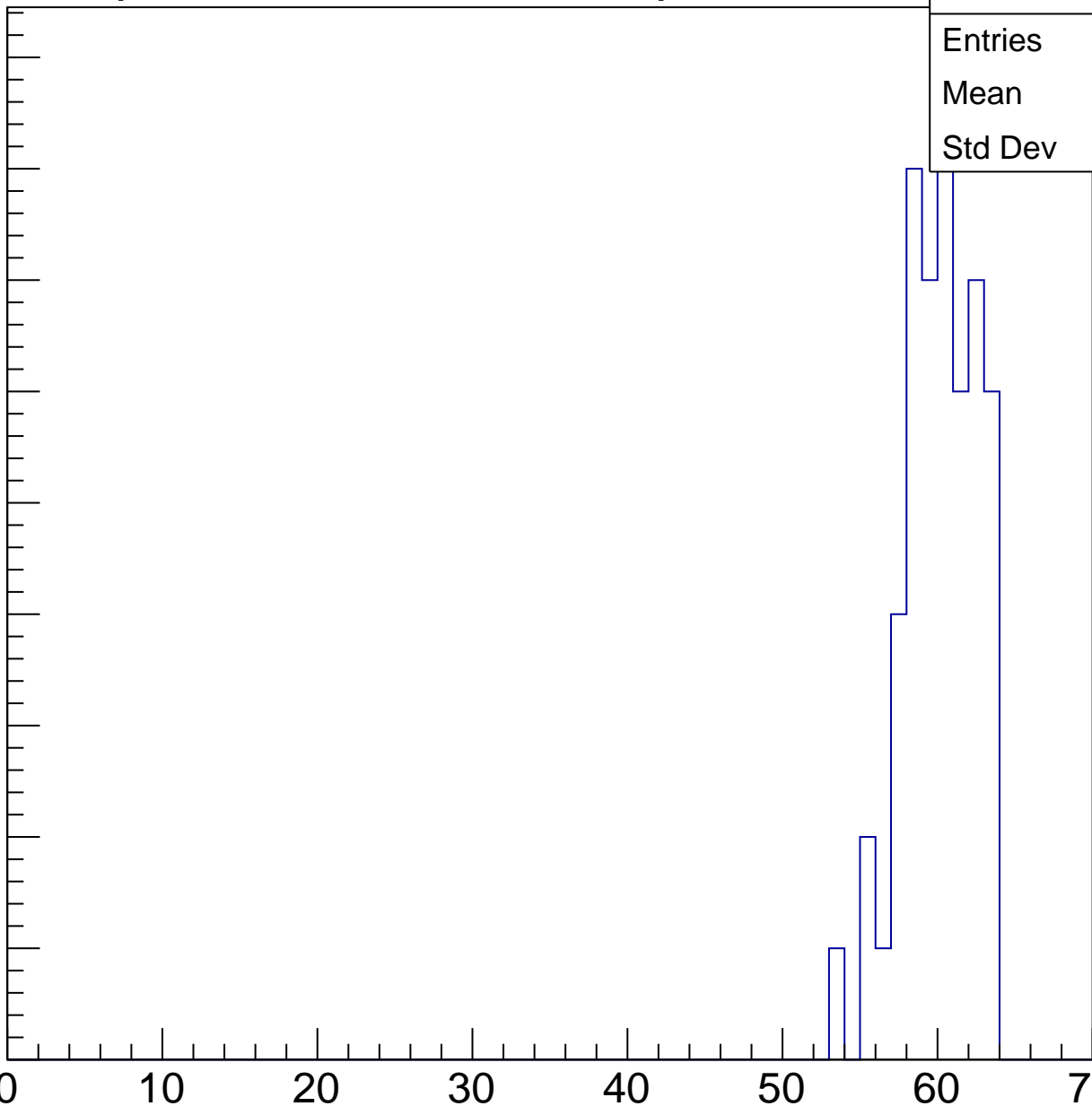
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.65
Std Dev	2.308

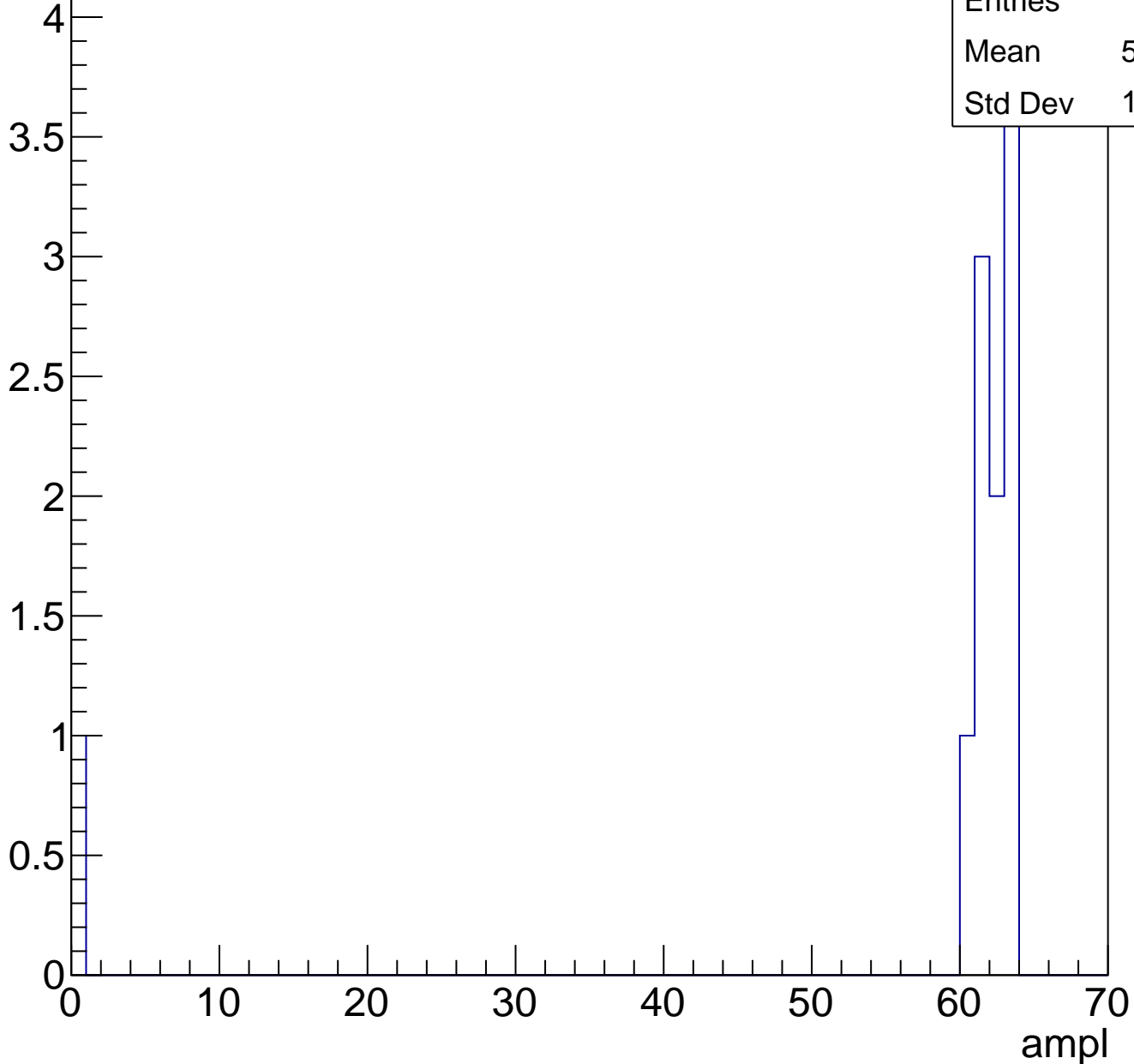
ampl



# B1L000S, U24-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

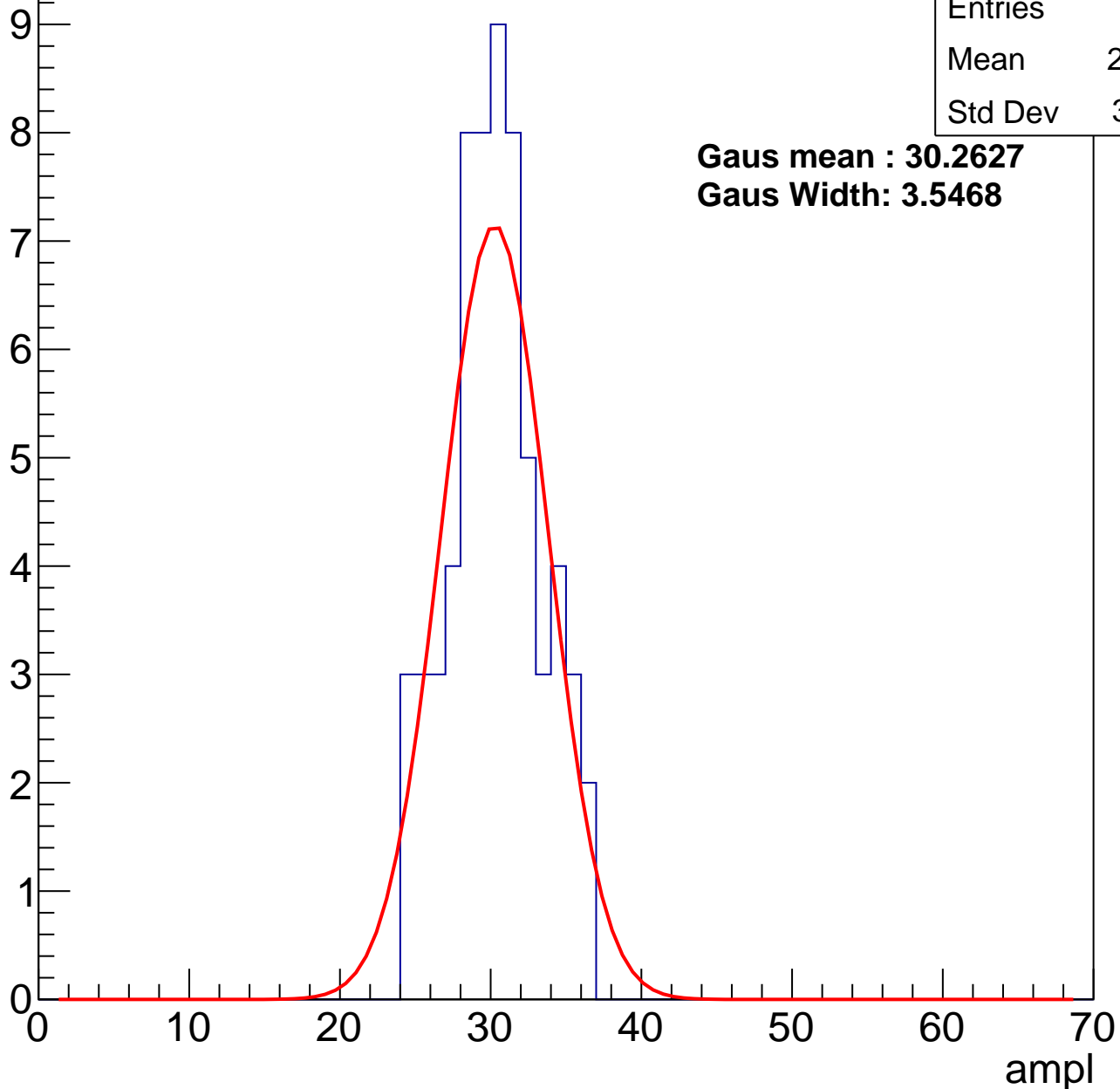


Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	63
Mean	29.83
Std Dev	3.011

# B1L000S, U24-ch19, adc1

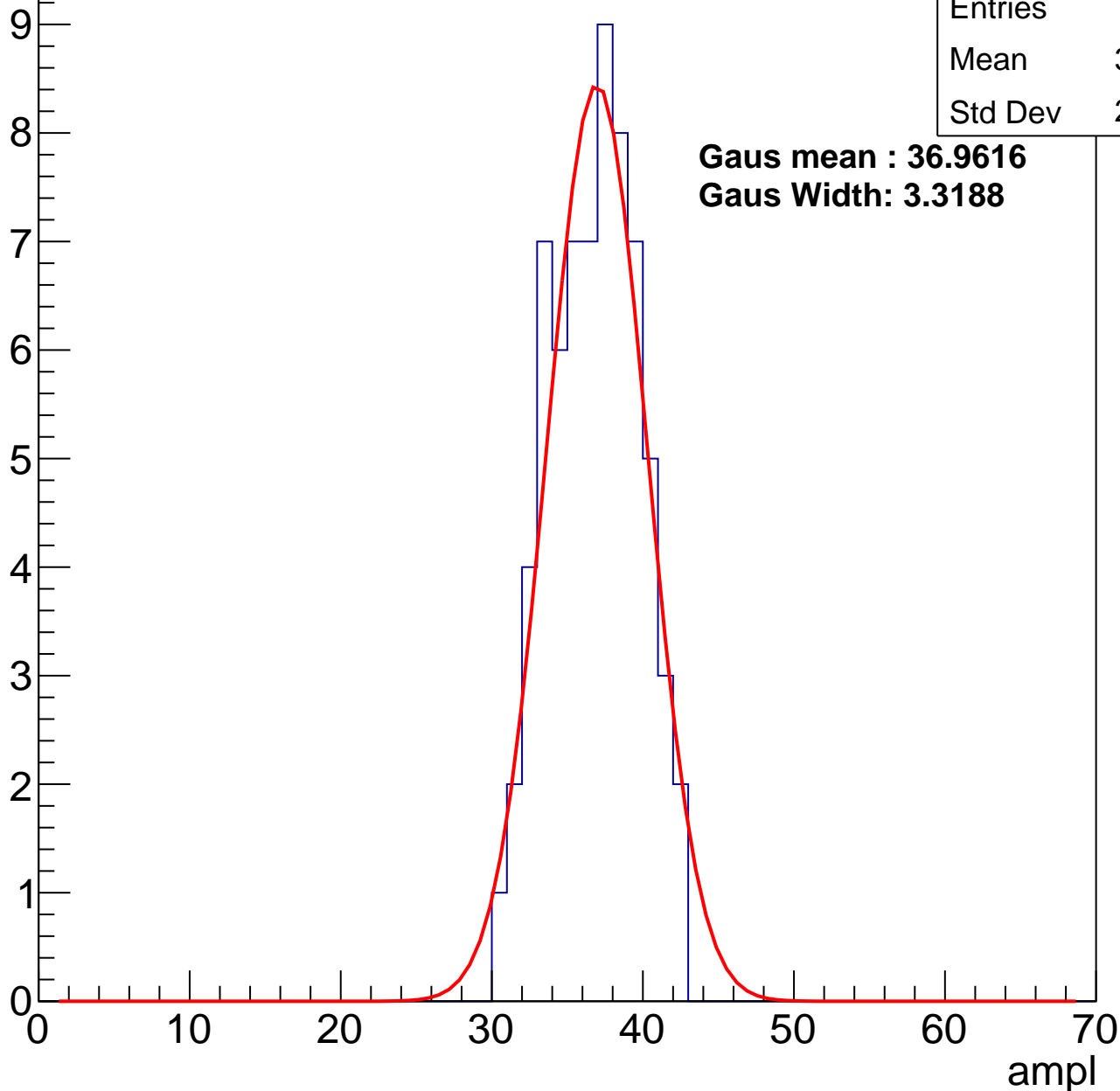
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.31
Std Dev	2.891

**Gaus mean : 36.9616**

**Gaus Width: 3.3188**



# B1L000S, U24-ch19, adc2

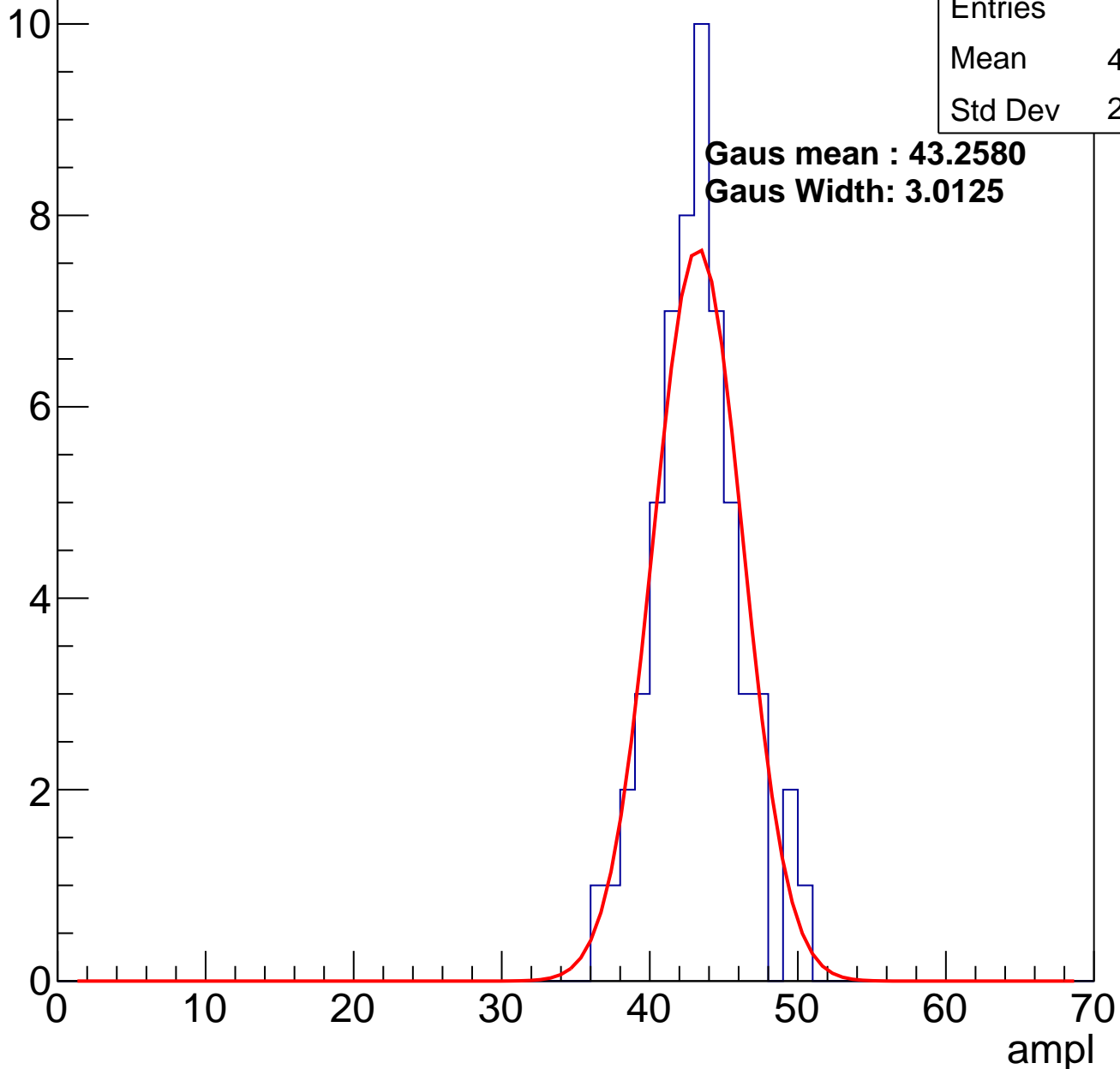
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	42.74
Std Dev	2.886

**Gaus mean : 43.2580**

**Gaus Width: 3.0125**

Entry

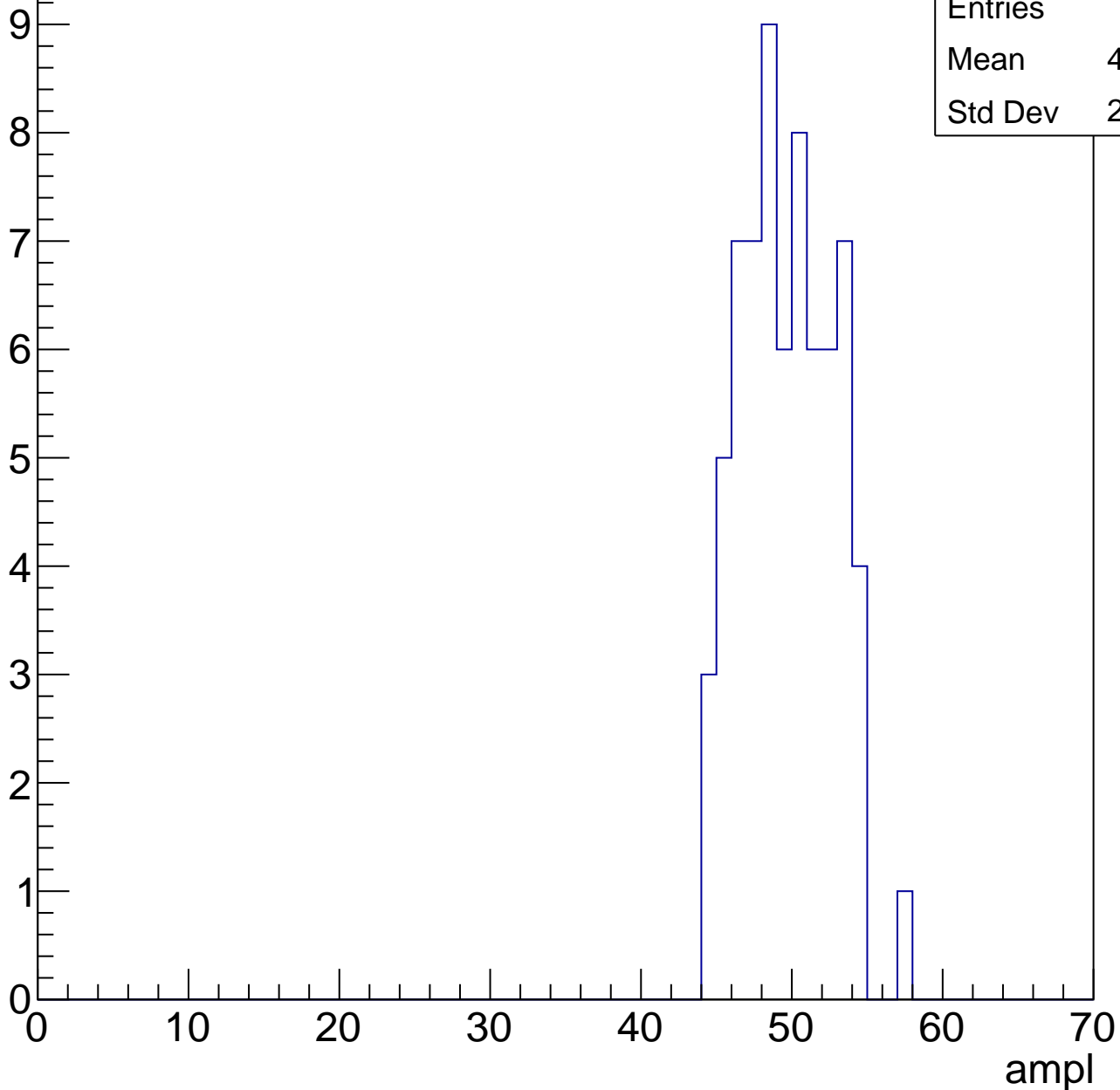


# B1L000S, U24-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	49.22
Std Dev	2.982



# B1L000S, U24-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

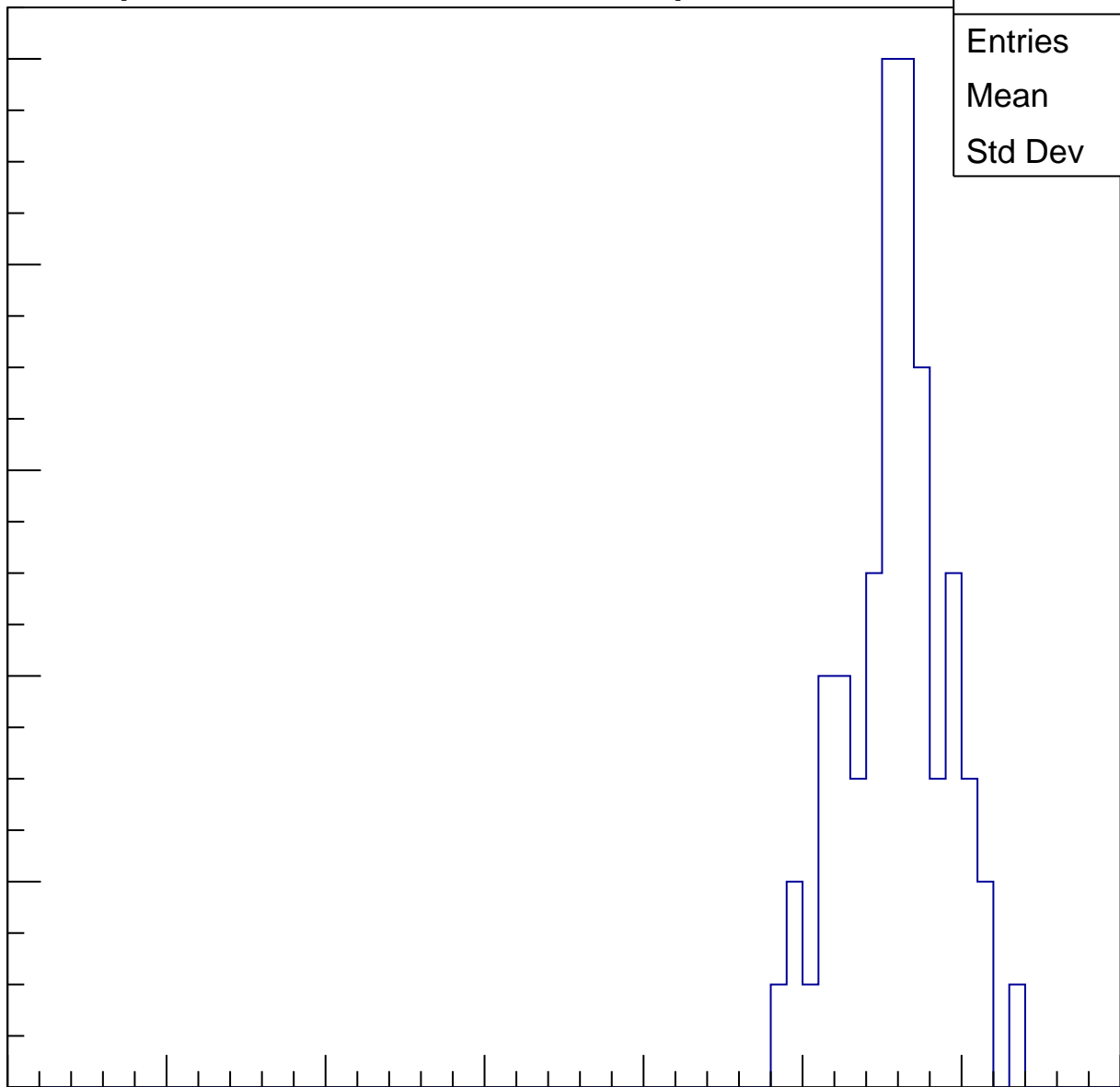
Entries	61
Mean	55.41
Std Dev	3.159

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

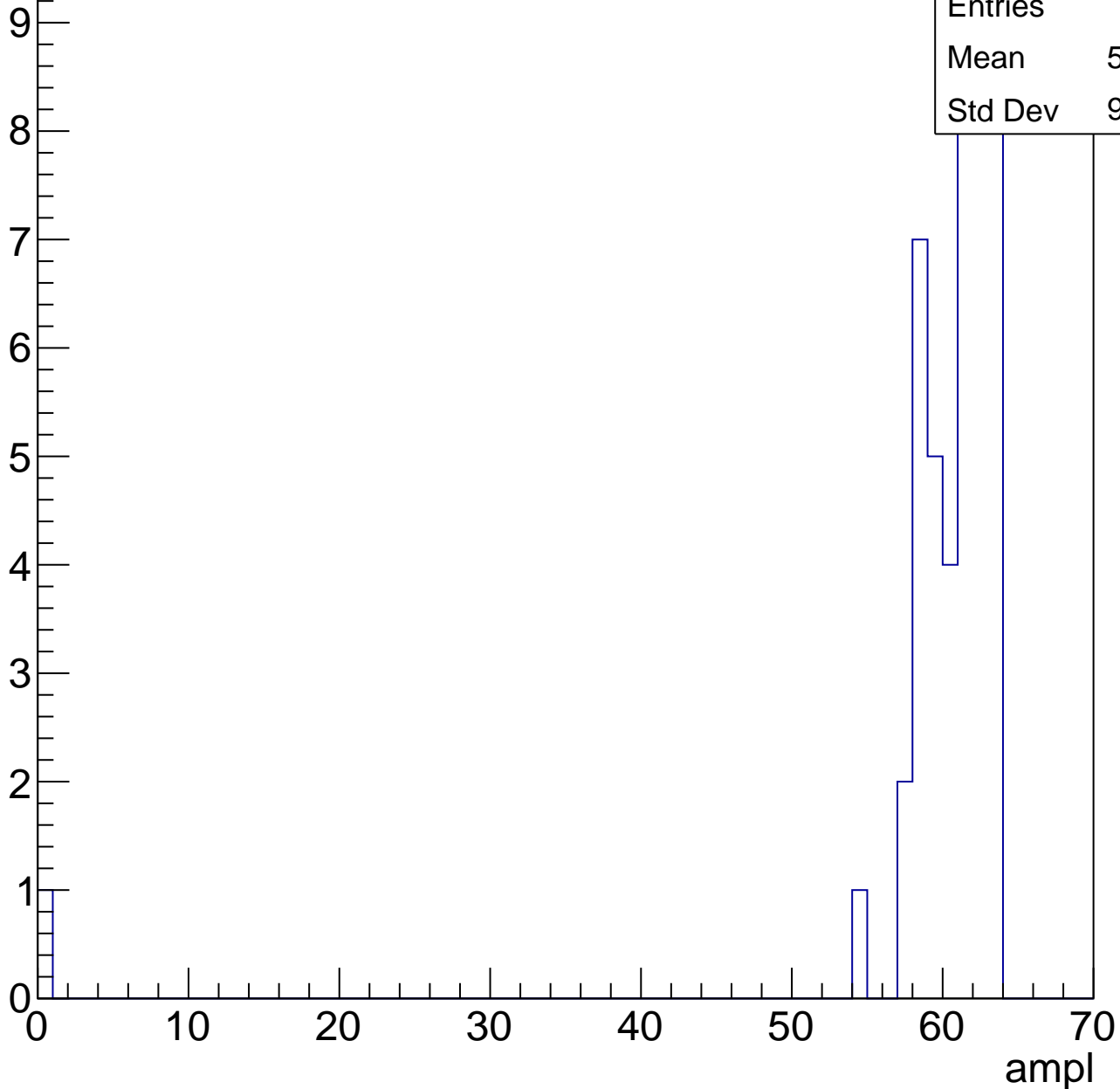


# B1L000S, U24-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

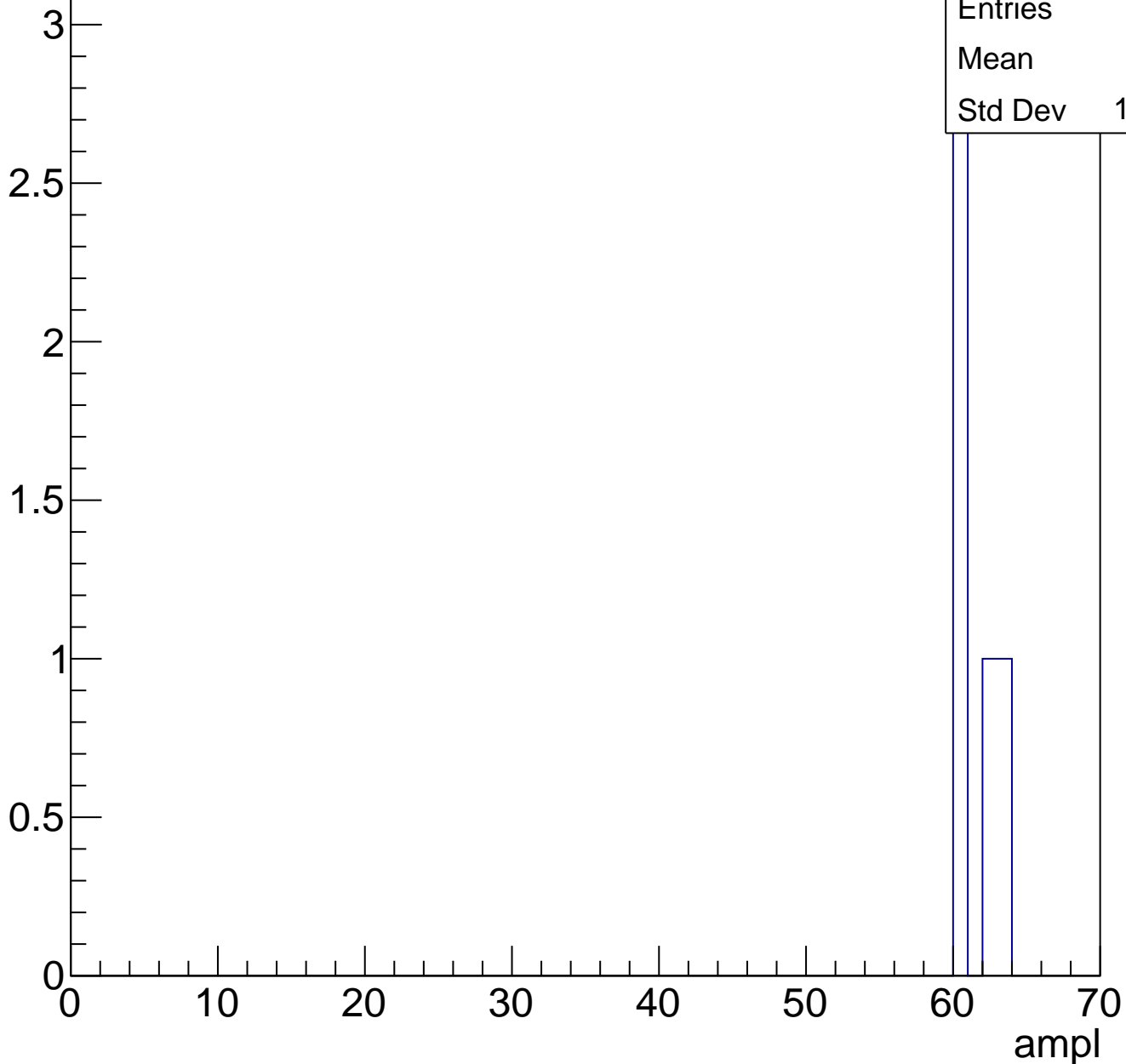
Entries	45
Mean	59.09
Std Dev	9.148



# B1L000S, U24-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch20, adc0

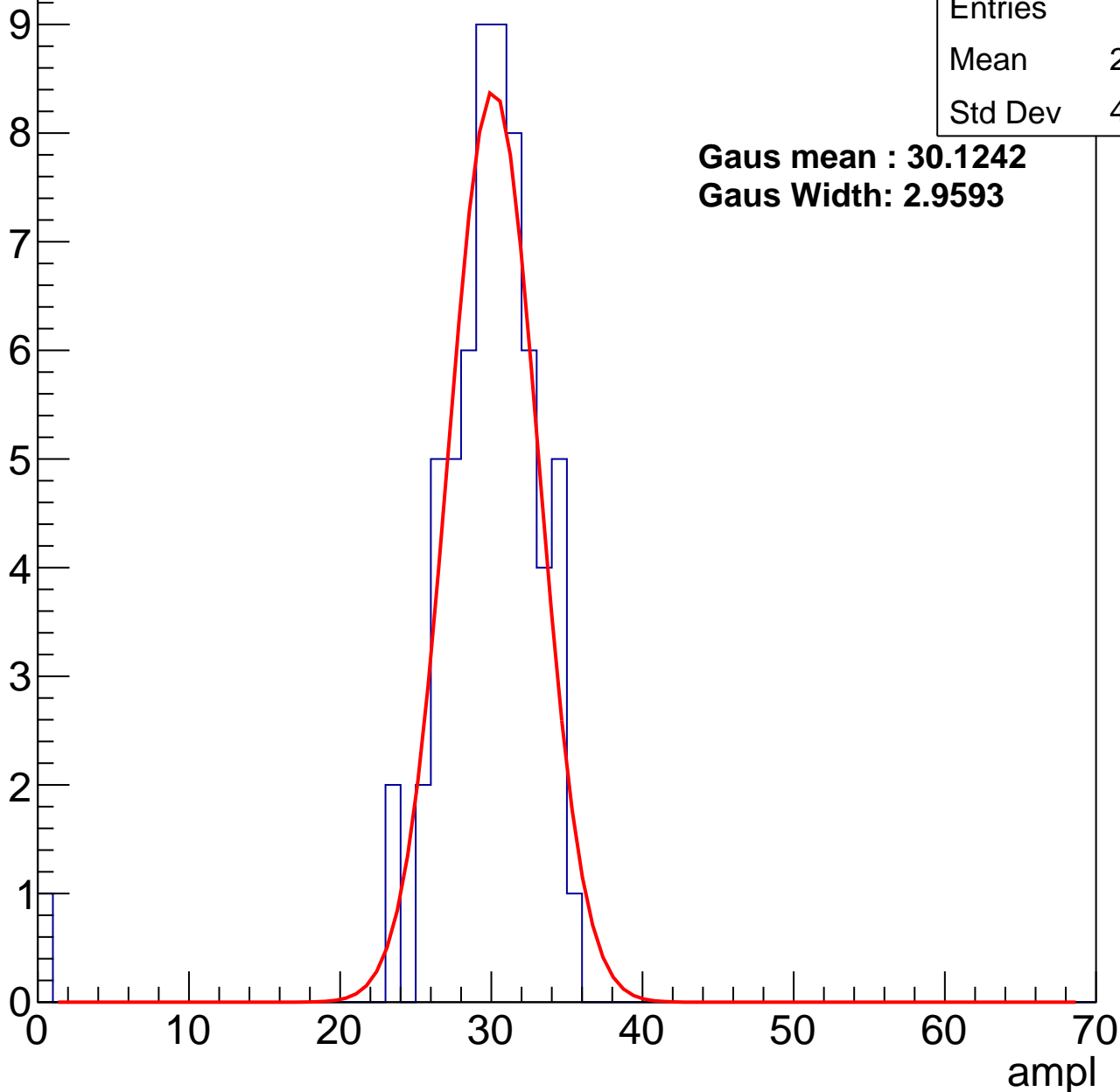
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.16
Std Dev	4.602

**Gaus mean : 30.1242**

**Gaus Width: 2.9593**



# B1L000S, U24-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	36.75
Std Dev	2.999

**Gaus mean : 37.1890**

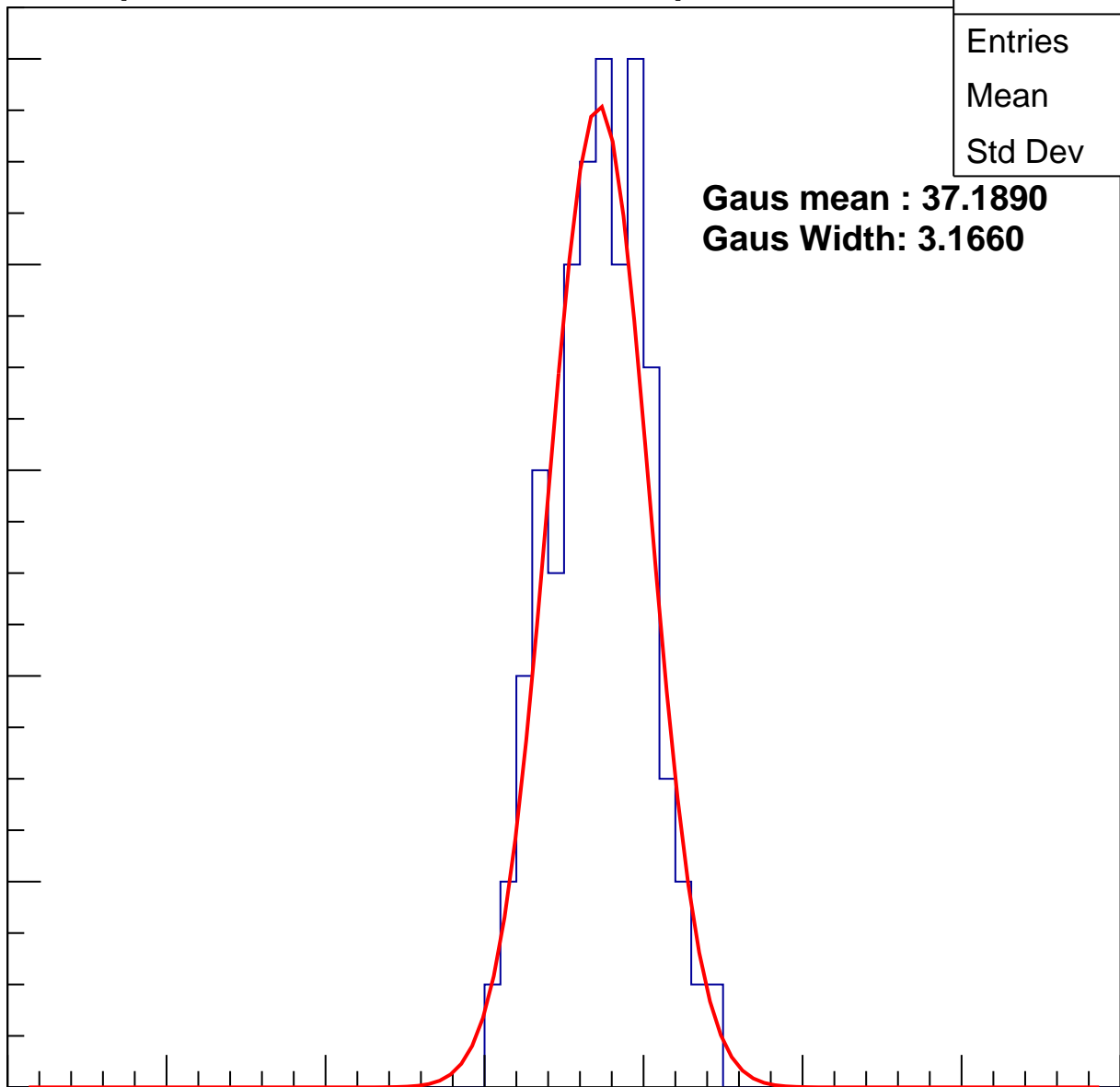
**Gaus Width: 3.1660**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch20, adc2

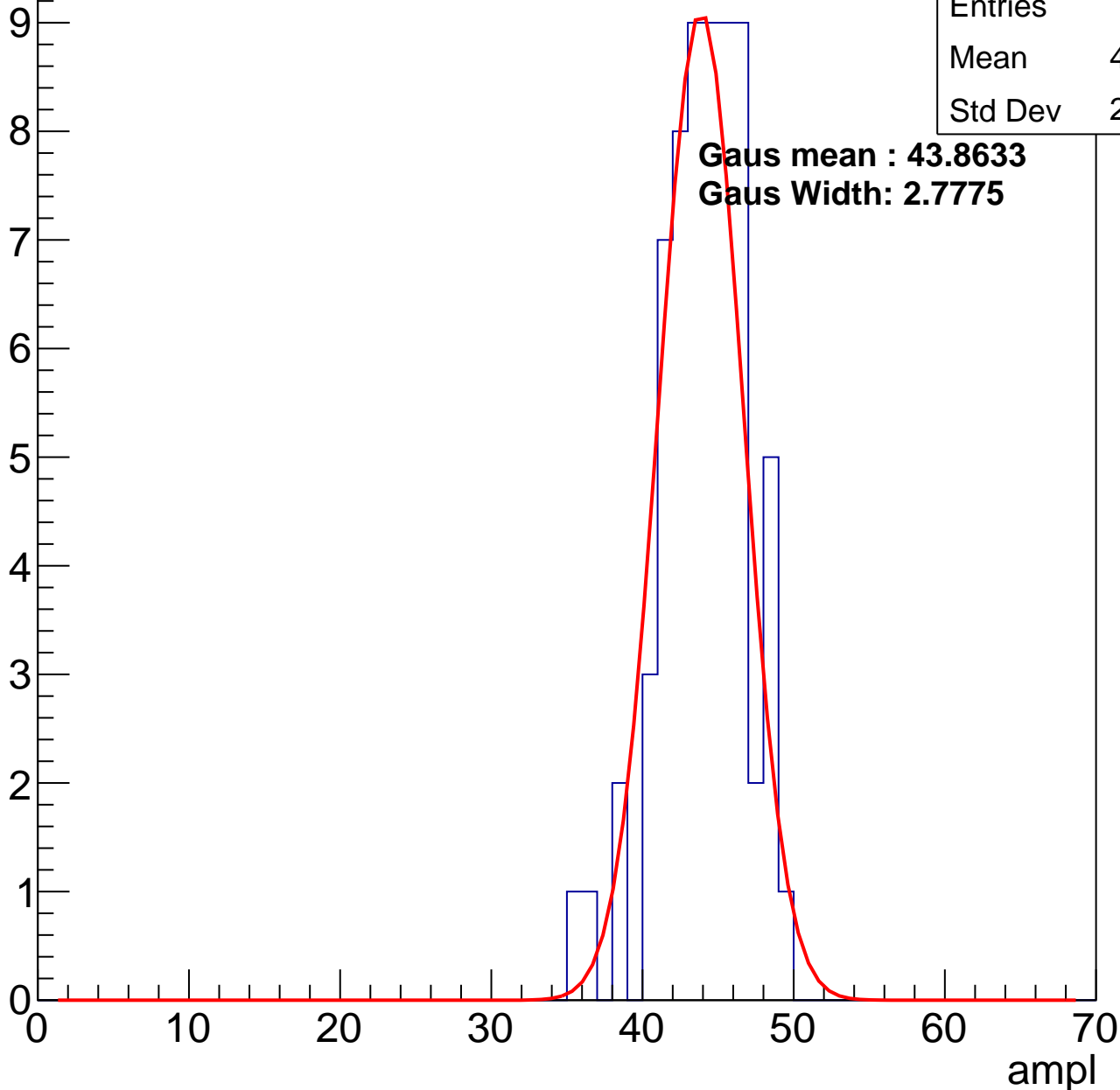
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.56
Std Dev	2.818

**Gaus mean : 43.8633**

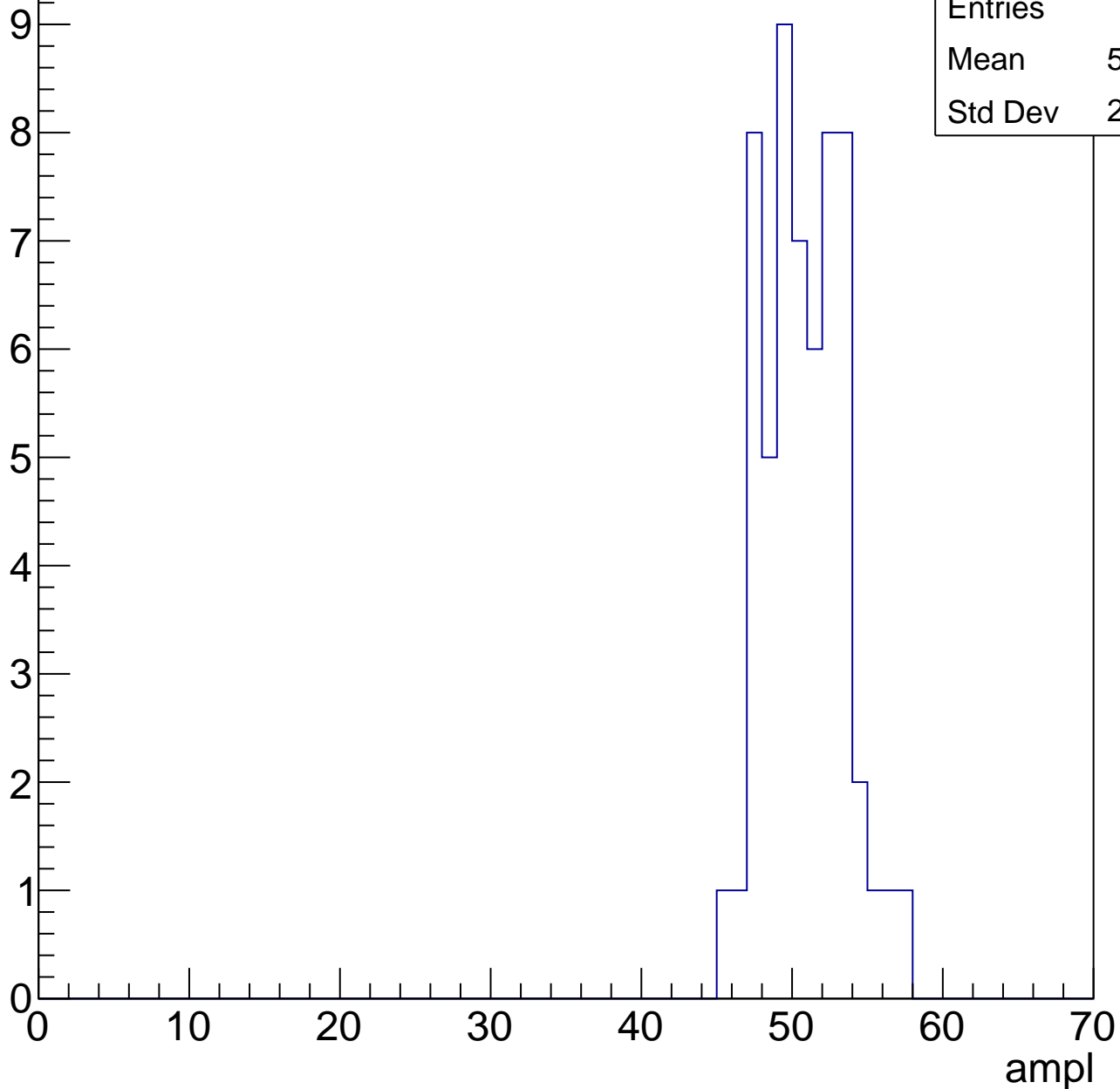
**Gaus Width: 2.7775**



# B1L000S, U24-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

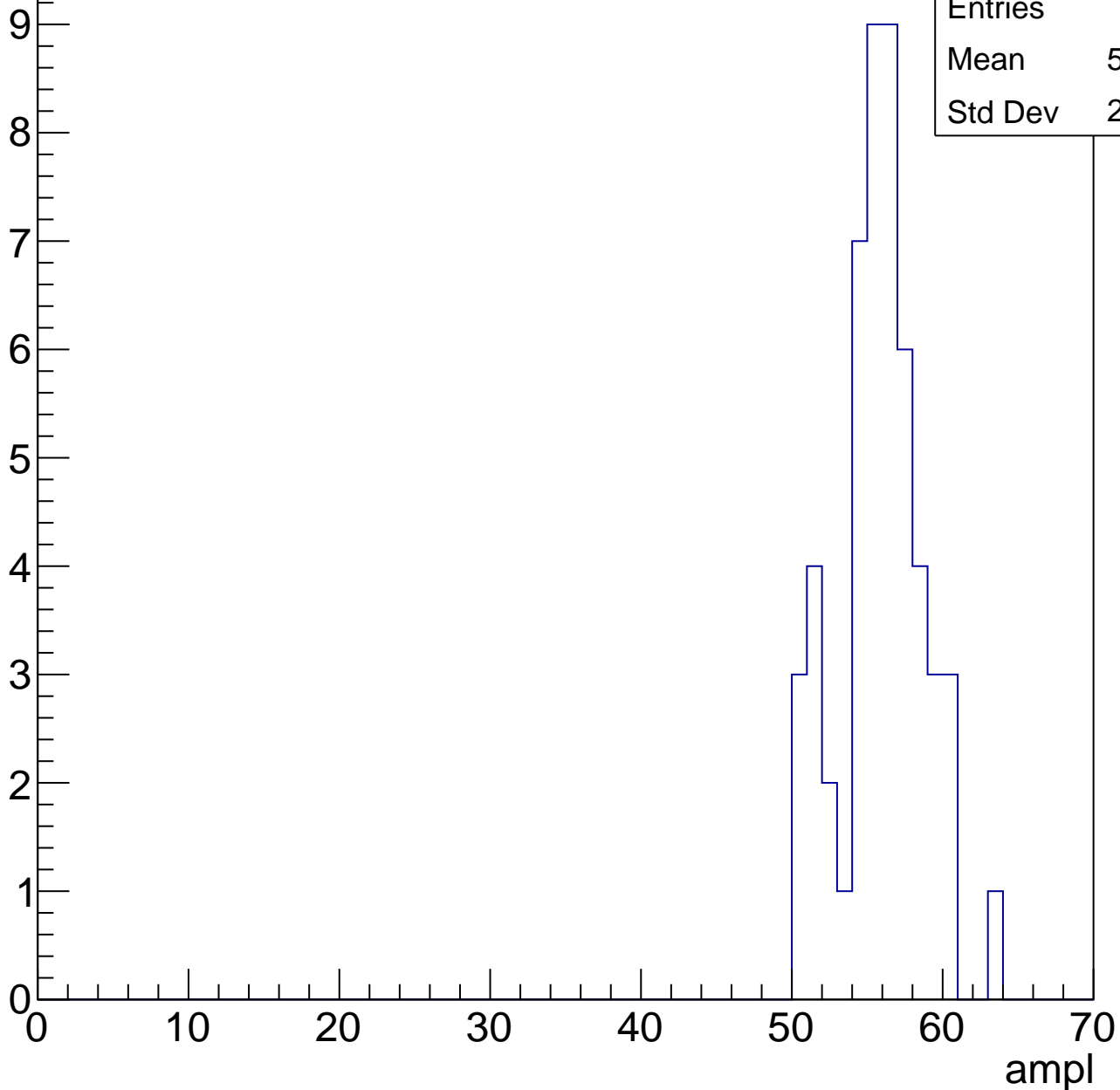


# B1L000S, U24-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	55.42
Std Dev	2.824

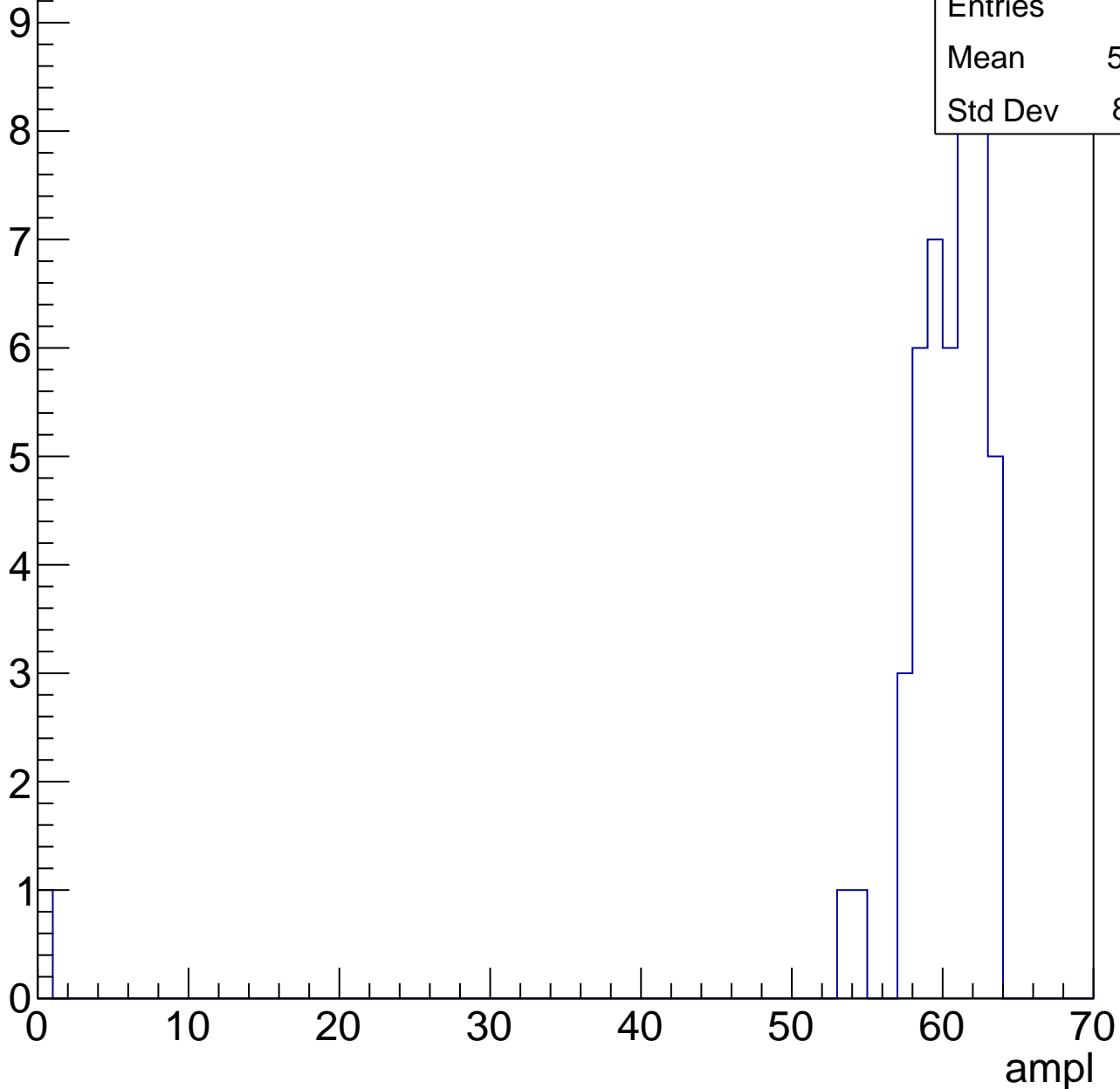


# B1L000S, U24-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	58.77
Std Dev	8.851



# B1L000S, U24-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch21, adc0

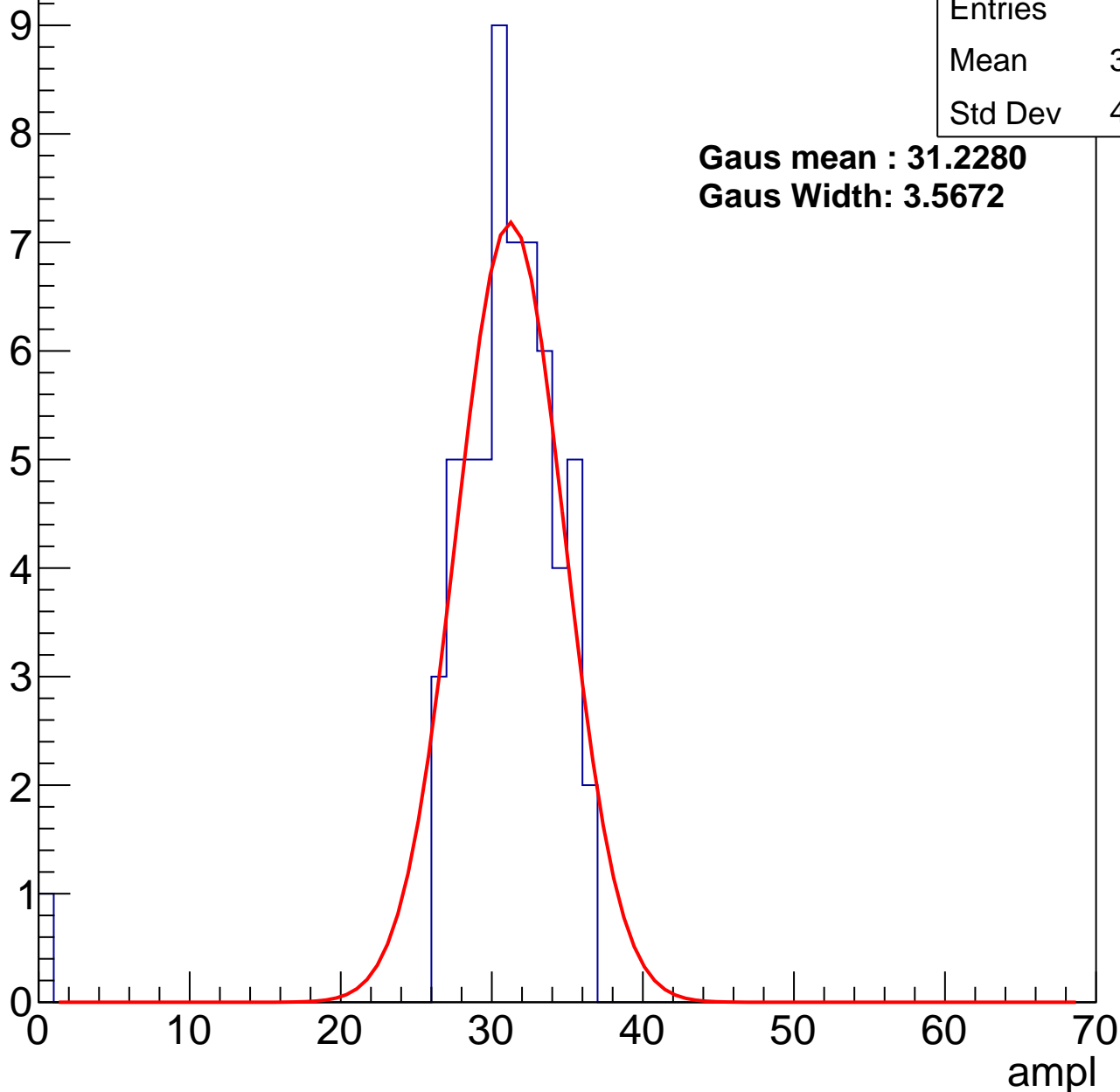
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	30.34
Std Dev	4.803

**Gaus mean : 31.2280**

**Gaus Width: 3.5672**



# B1L000S, U24-ch21, adc1

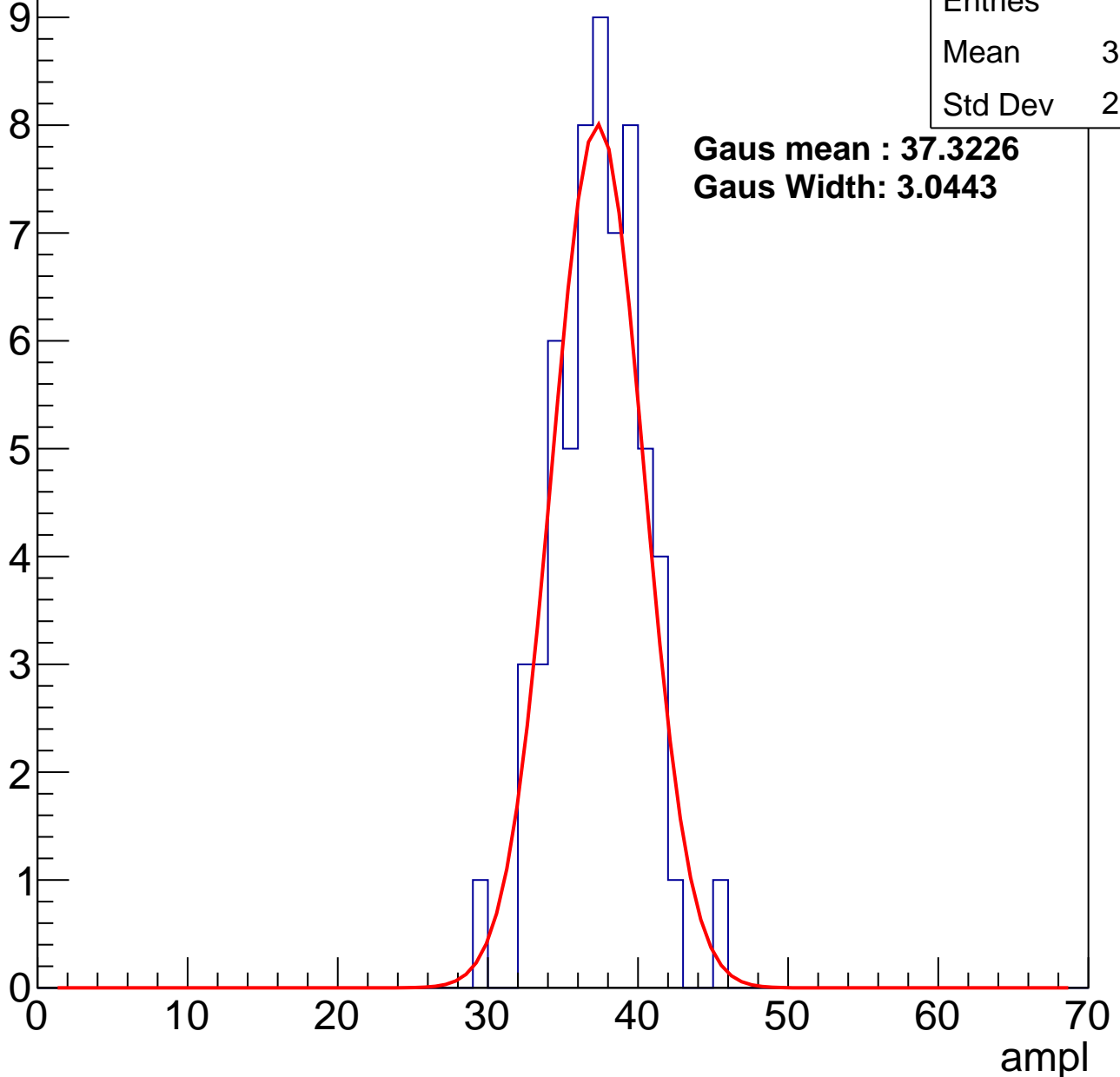
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	36.93
Std Dev	2.879

**Gaus mean : 37.3226**

**Gaus Width: 3.0443**



# B1L000S, U24-ch21, adc2

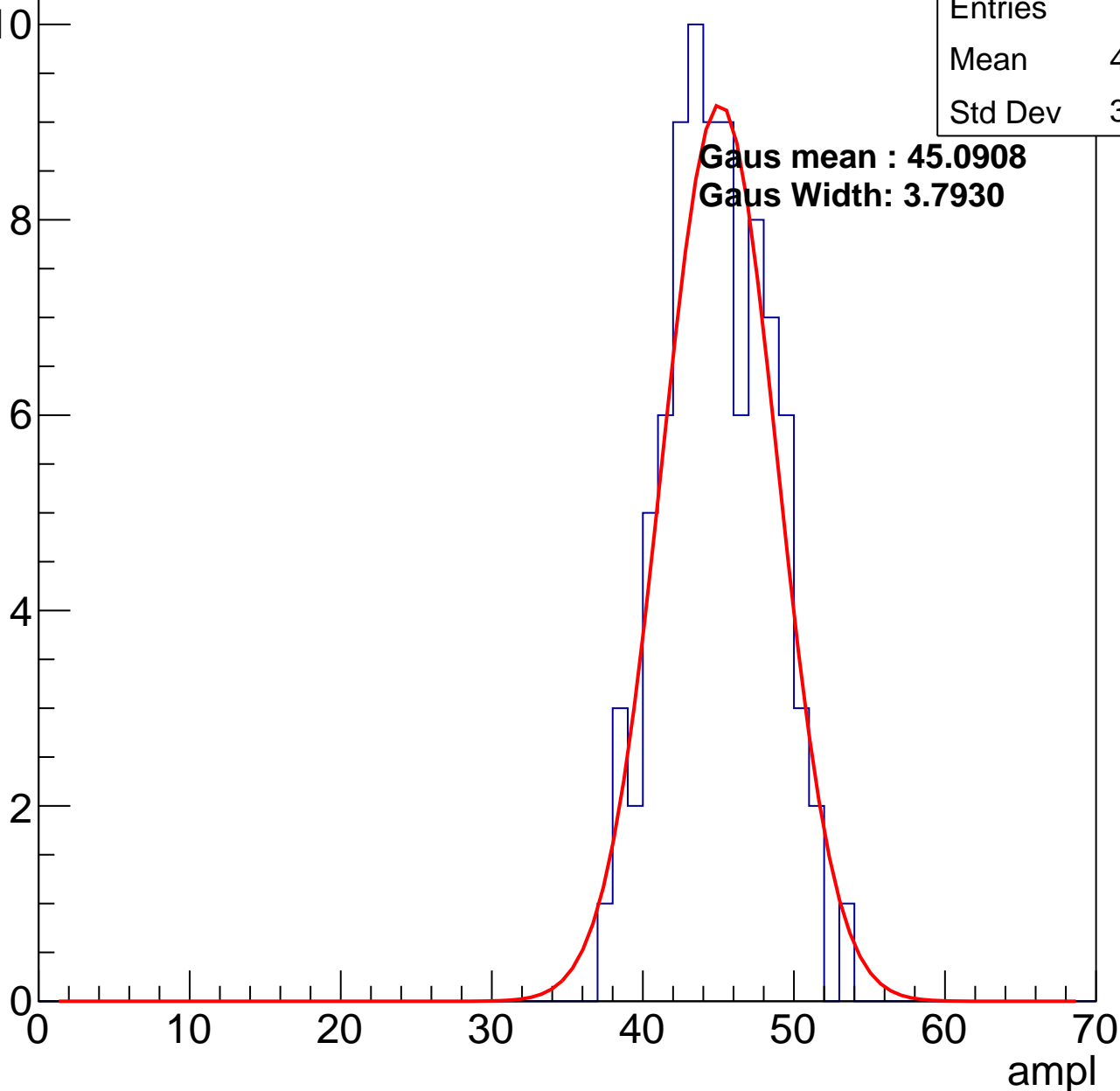
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	87
Mean	44.49
Std Dev	3.424

**Gaus mean : 45.0908**

**Gaus Width: 3.7930**

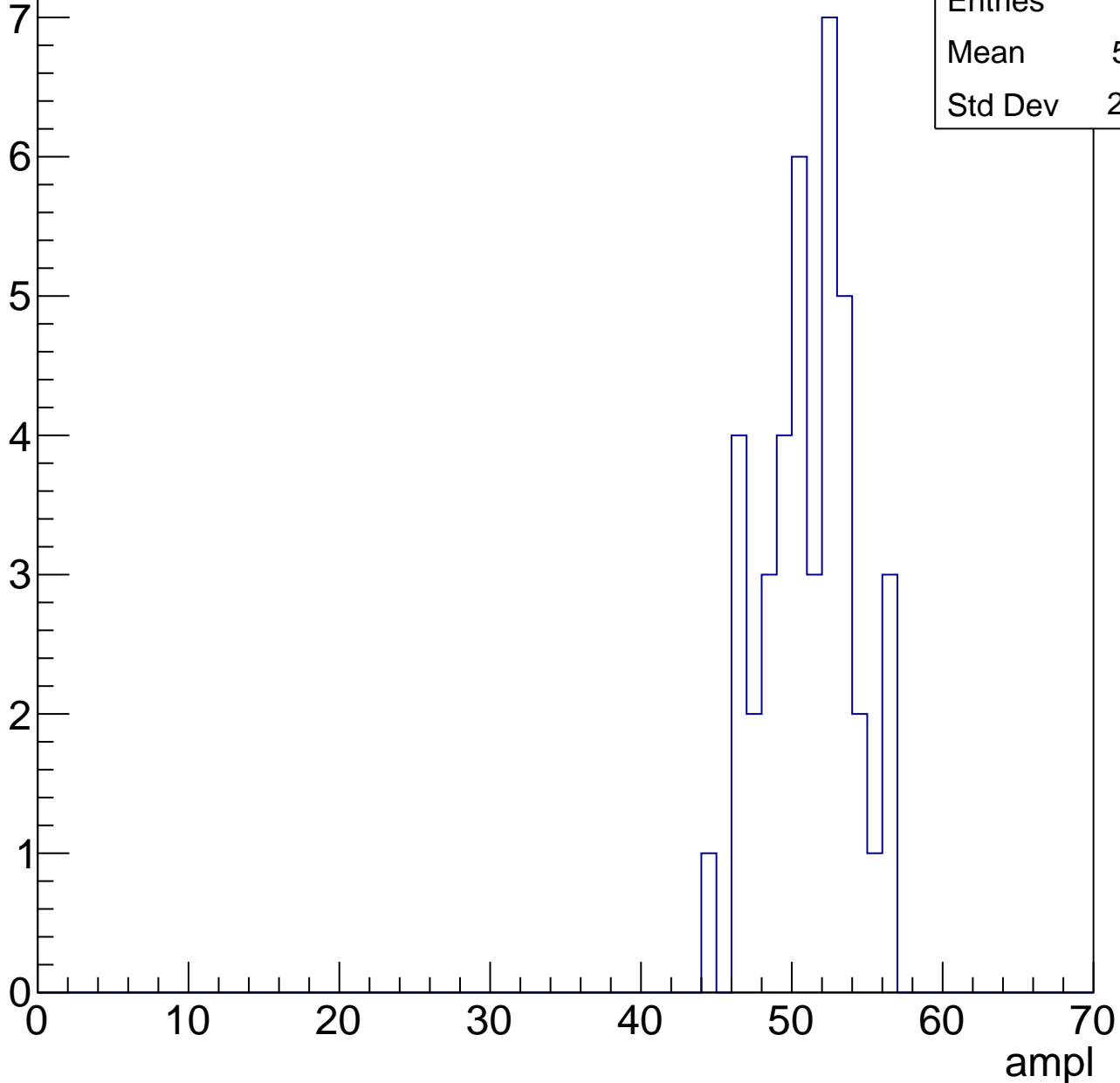


# B1L000S, U24-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

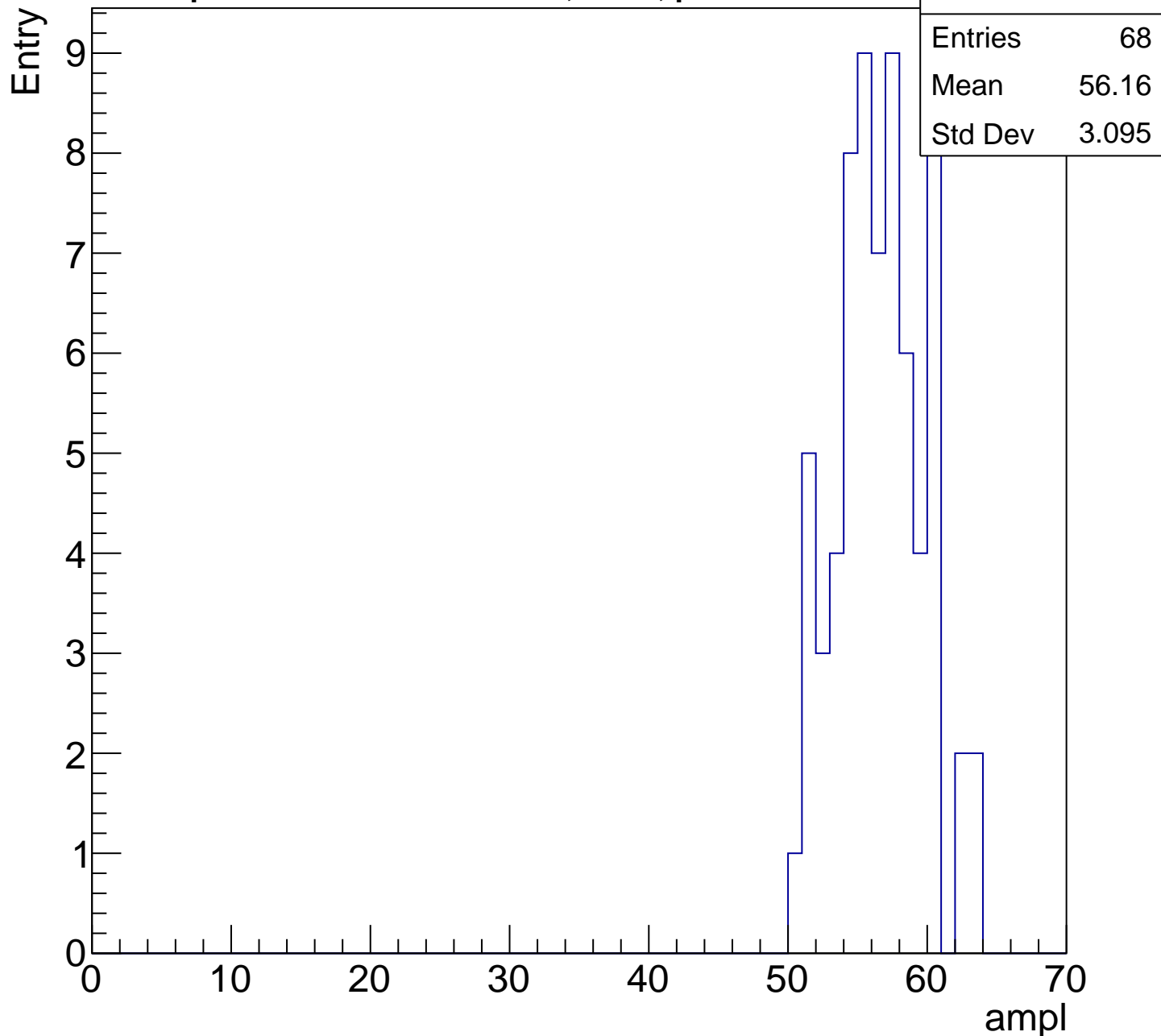
Entry

Entries	41
Mean	50.61
Std Dev	2.962



# B1L000S, U24-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	40
Mean	59.33
Std Dev	9.652

Entry

10

8

6

4

2

0

0

10

20

30

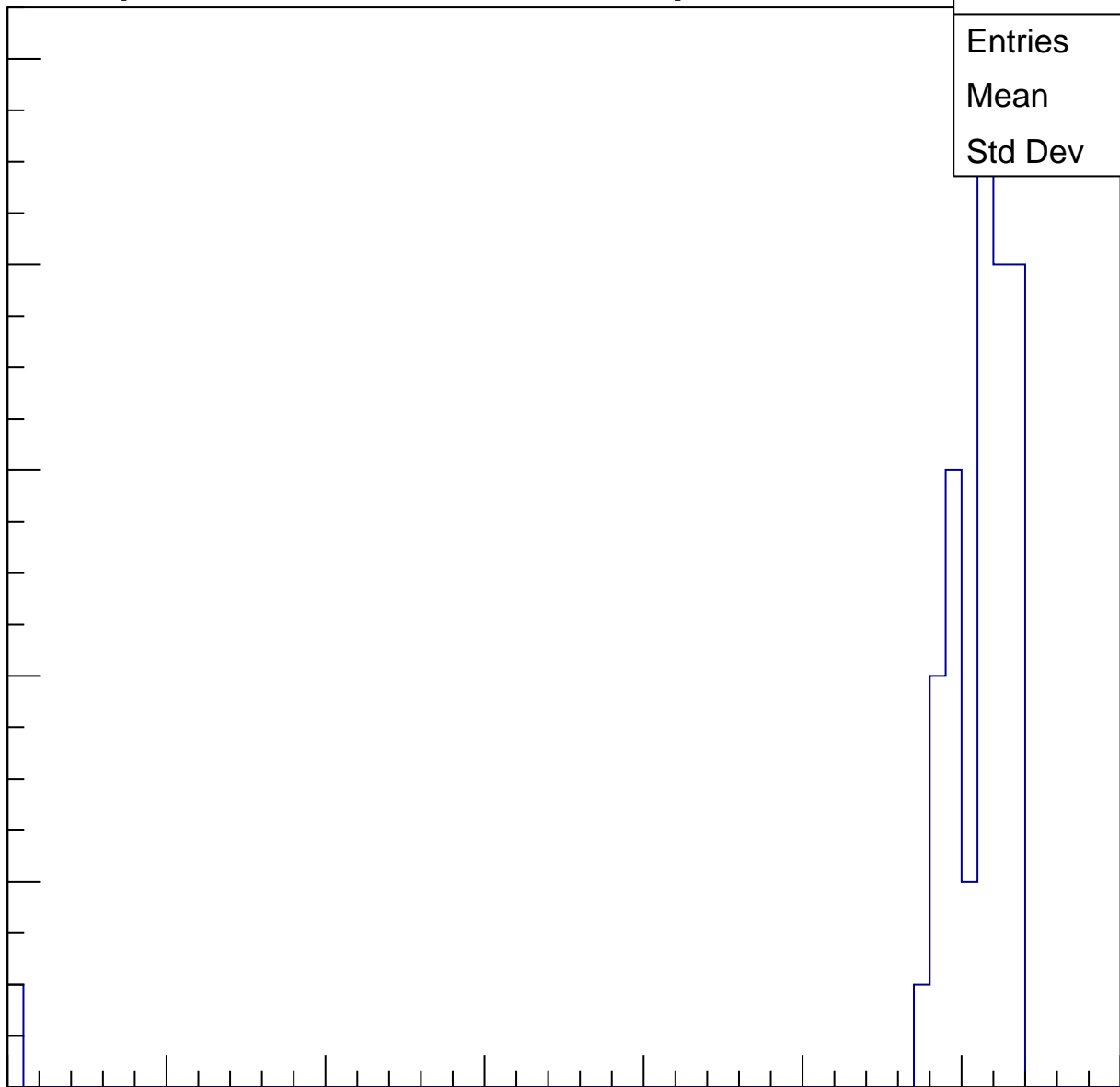
40

50

60

70

ampl



# B1L000S, U24-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0



# B1L000S, U24-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch22, adc0

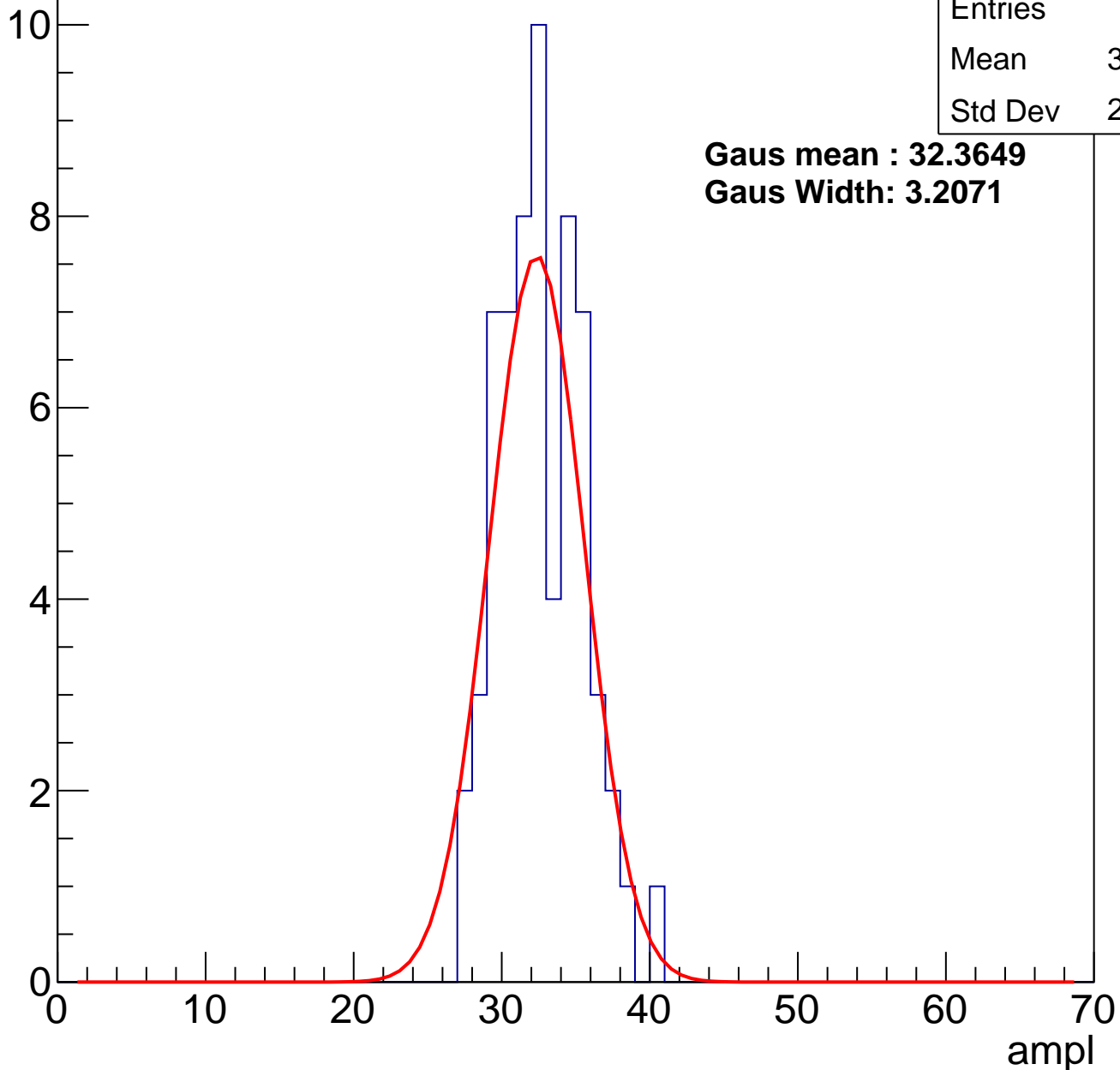
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	32.19
Std Dev	2.794

**Gaus mean : 32.3649**

**Gaus Width: 3.2071**

Entry



# B1L000S, U24-ch22, adc1

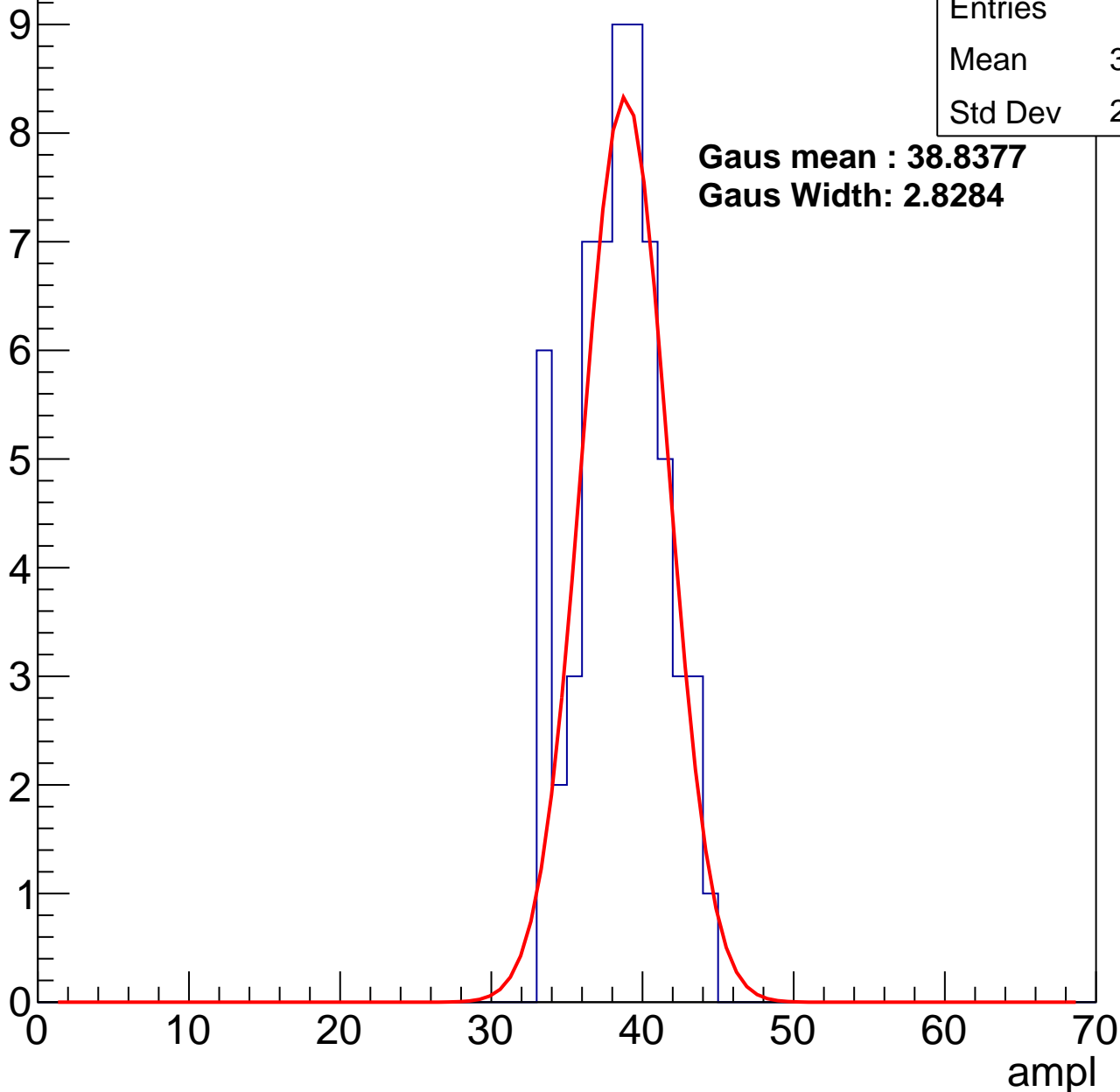
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	38.05
Std Dev	2.796

**Gaus mean : 38.8377**

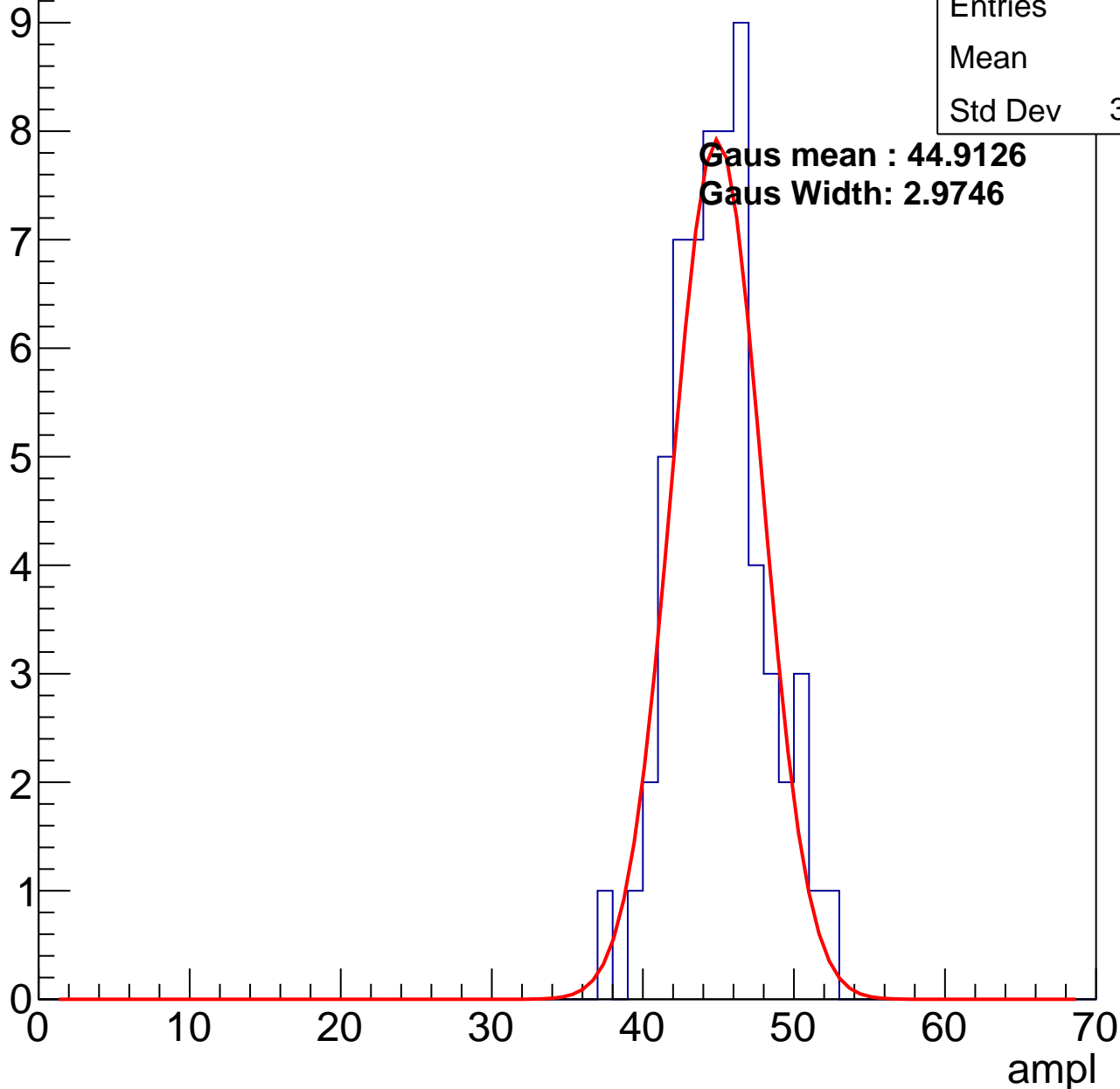
**Gaus Width: 2.8284**



# B1L000S, U24-ch22, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

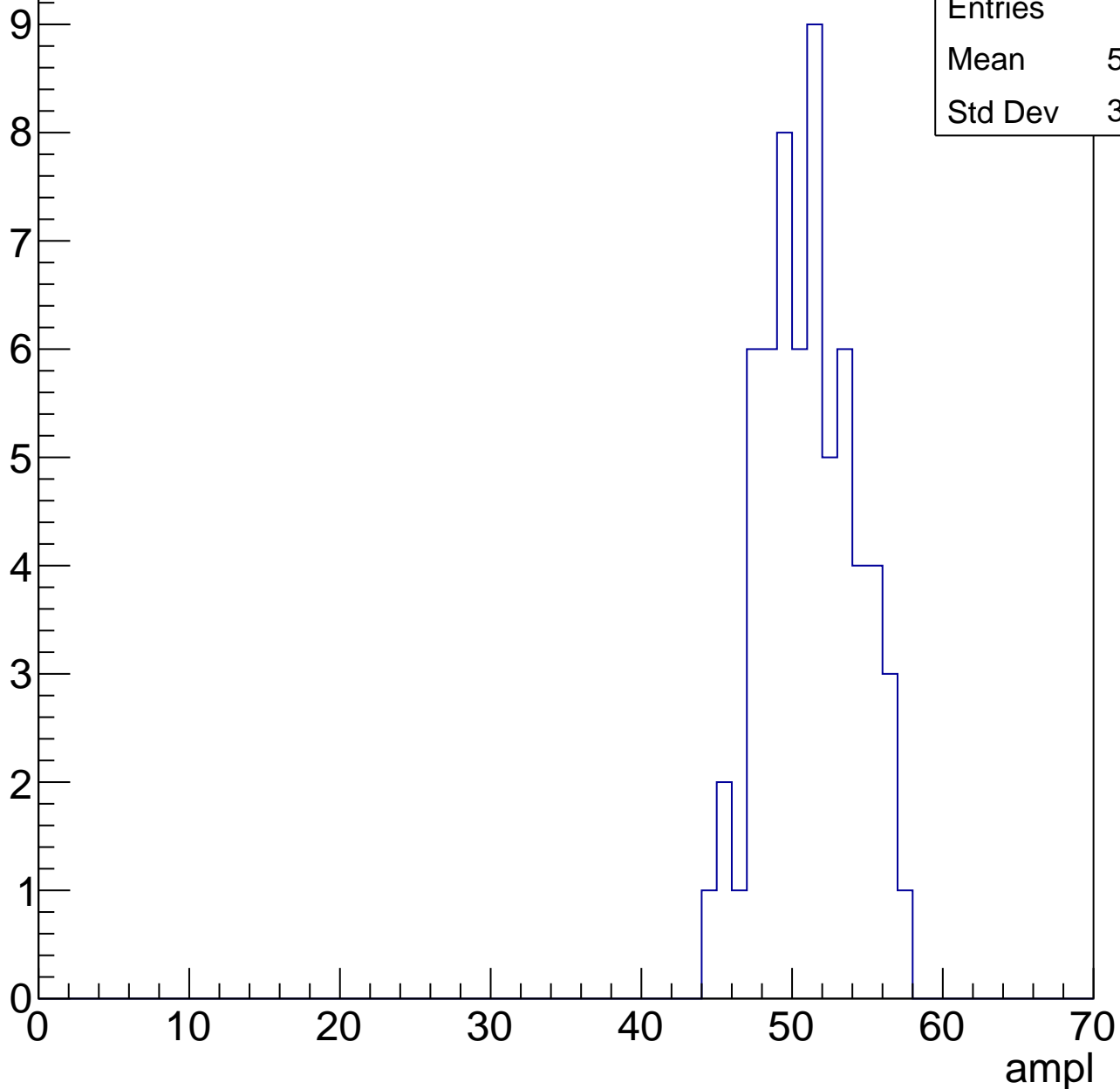
Entry



# B1L000S, U24-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

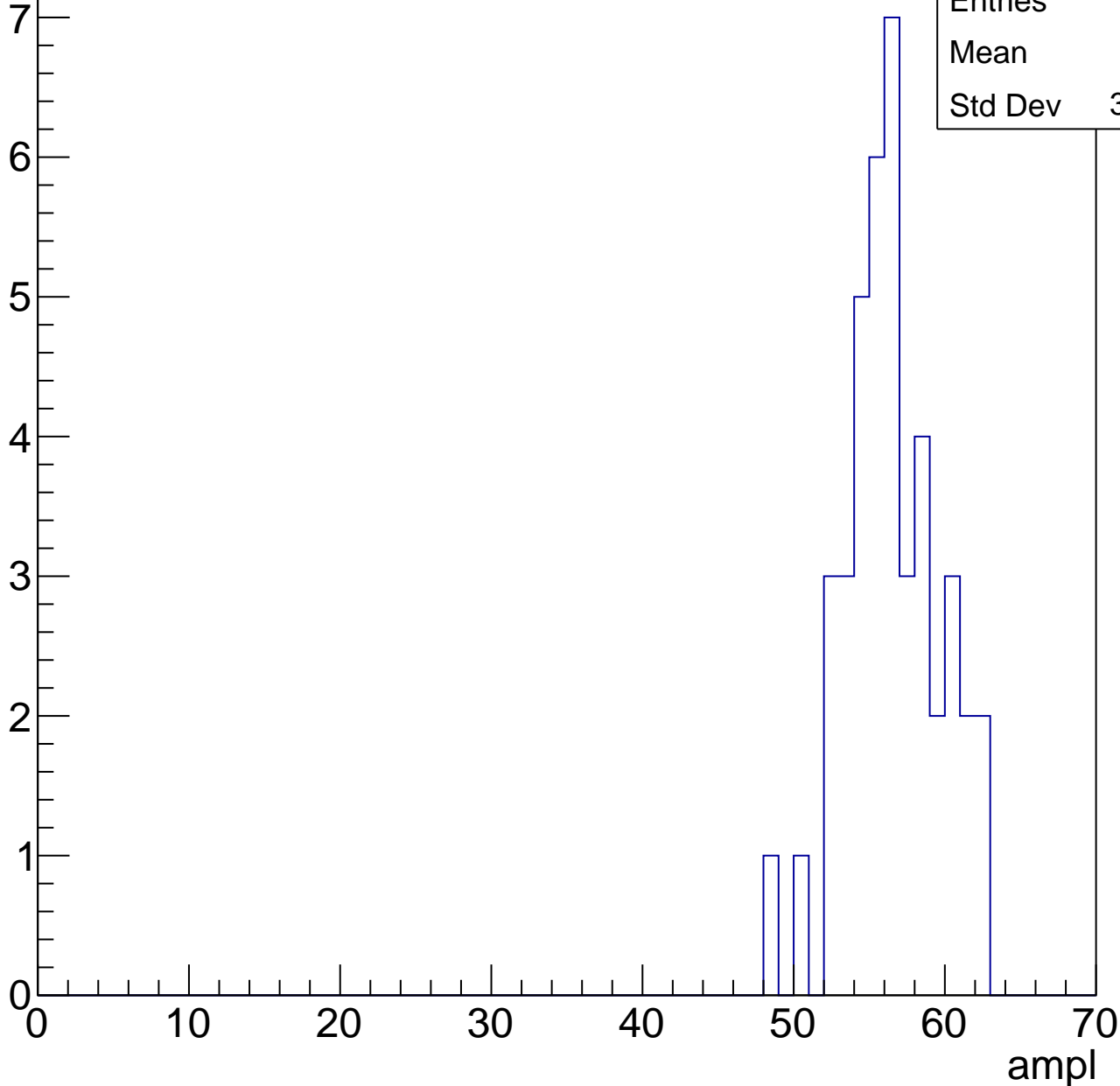


# B1L000S, U24-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	56
Std Dev	3.117



# B1L000S, U24-ch22, adc5

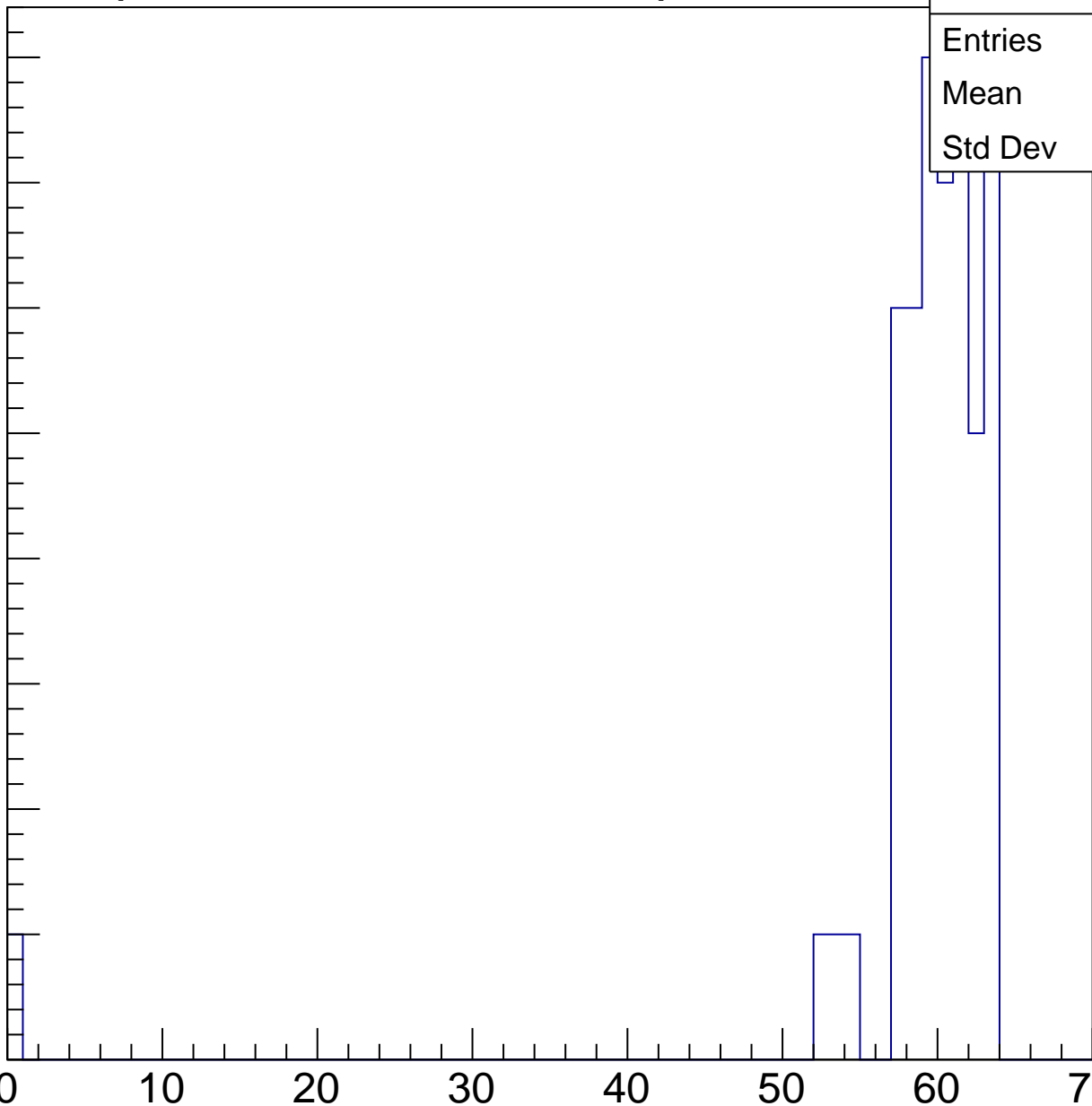
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.52
Std Dev	8.572

ampl



# B1L000S, U24-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

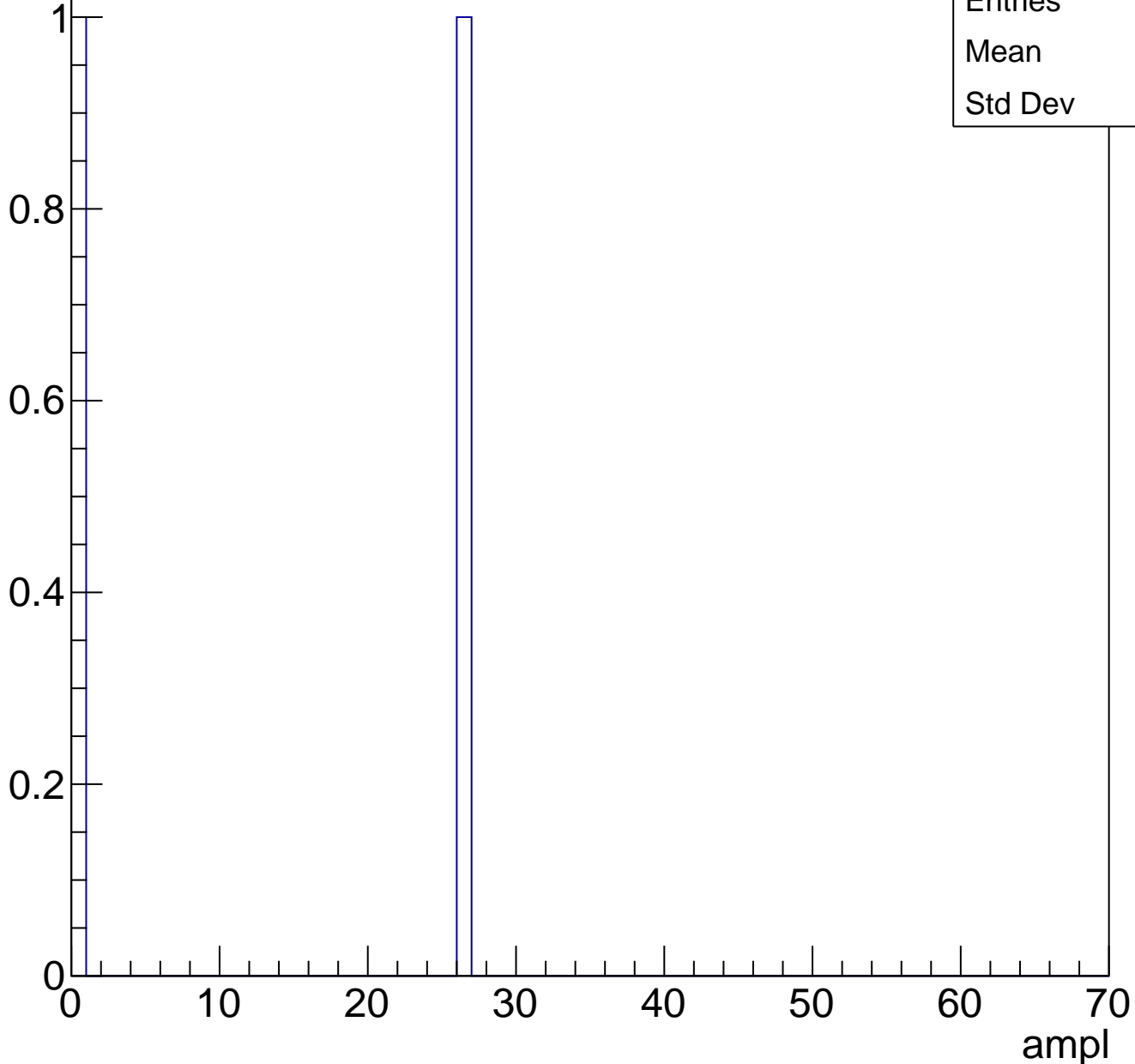




# B1L000S, U24-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	13
Std Dev	13

# B1L000S, U24-ch23, adc0

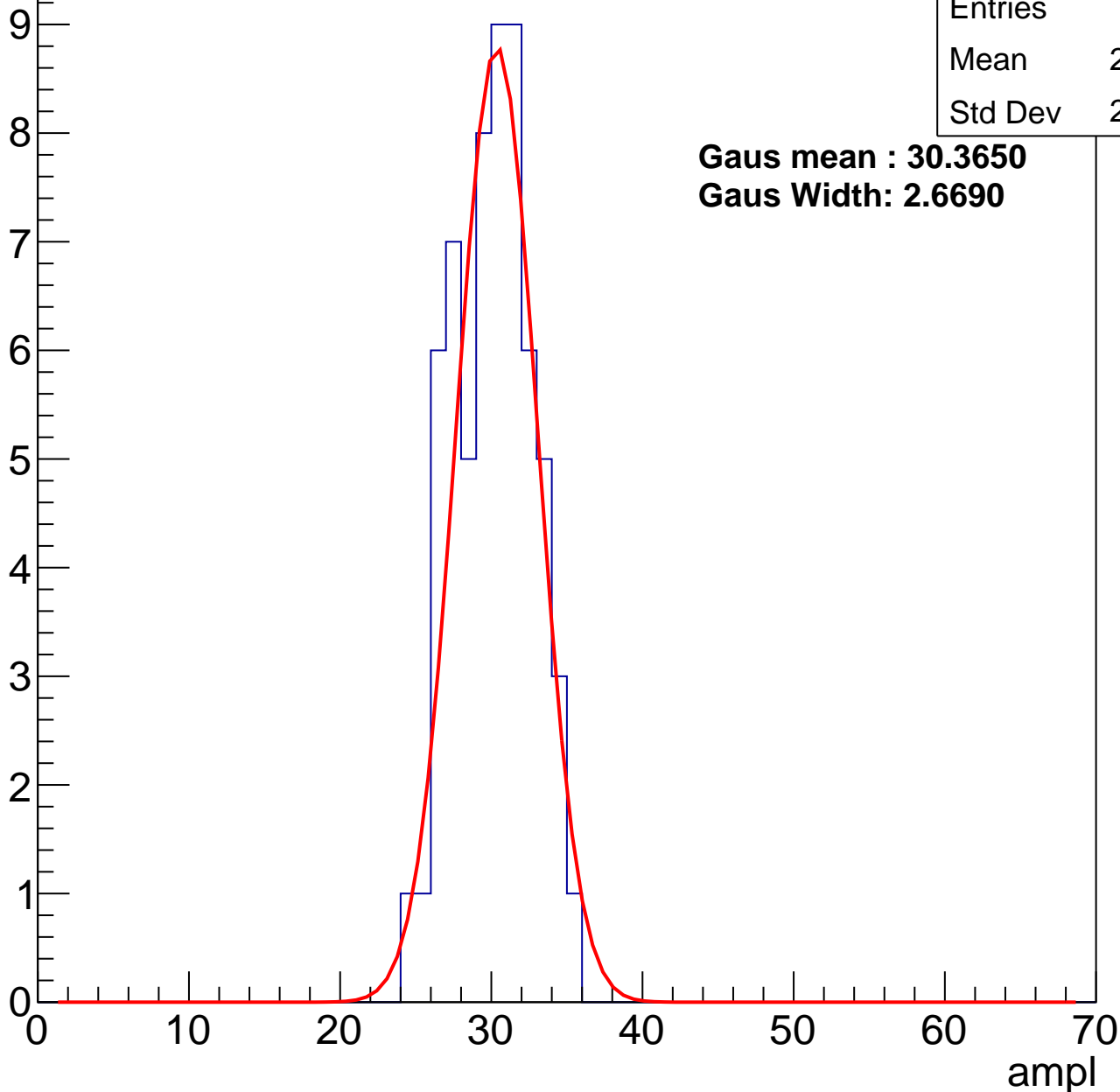
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	29.66
Std Dev	2.534

**Gaus mean : 30.3650**

**Gaus Width: 2.6690**



# B1L000S, U24-ch23, adc1

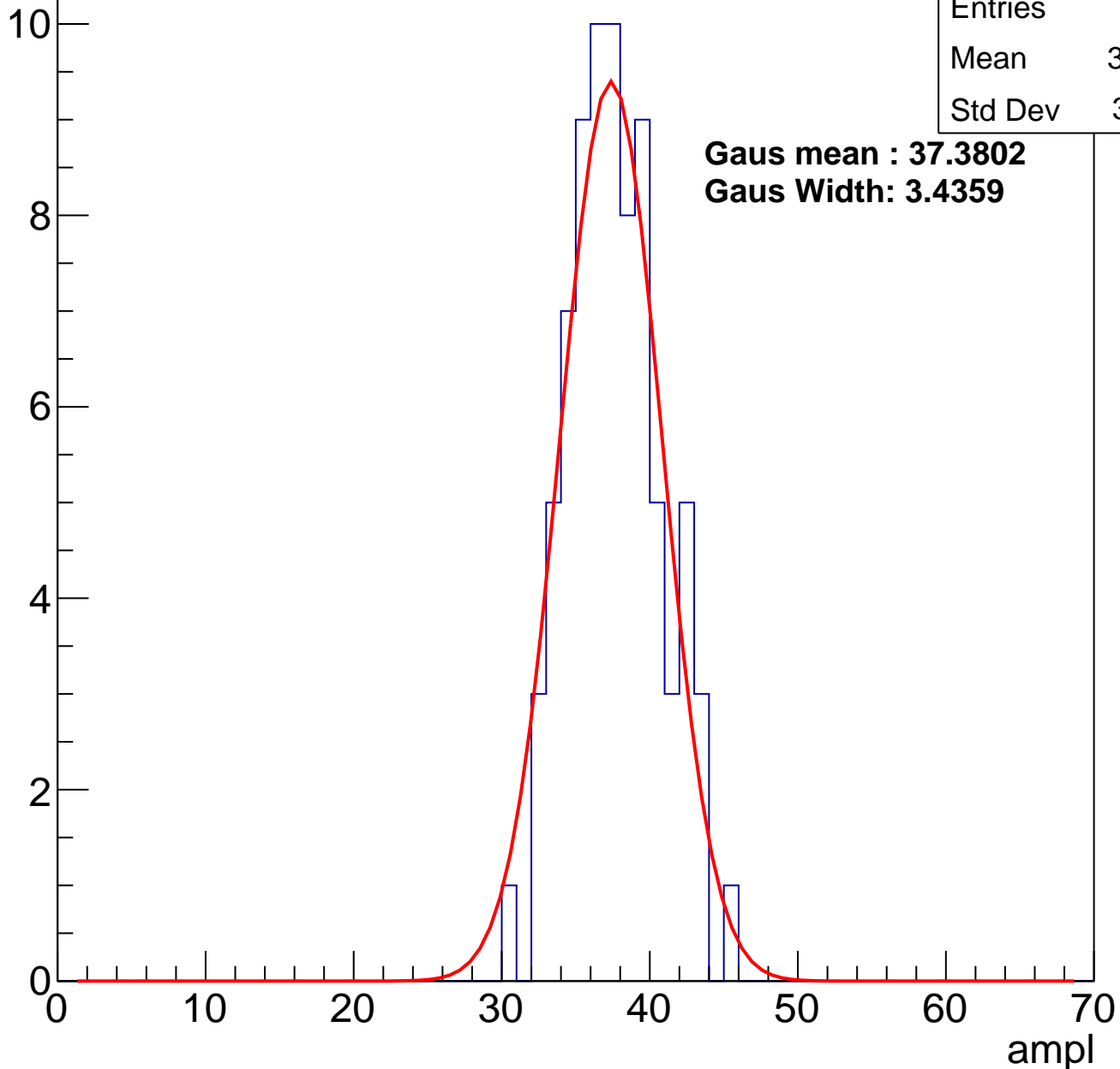
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	37.16
Std Dev	3.071

**Gaus mean : 37.3802**

**Gaus Width: 3.4359**

Entry



# B1L000S, U24-ch23, adc2

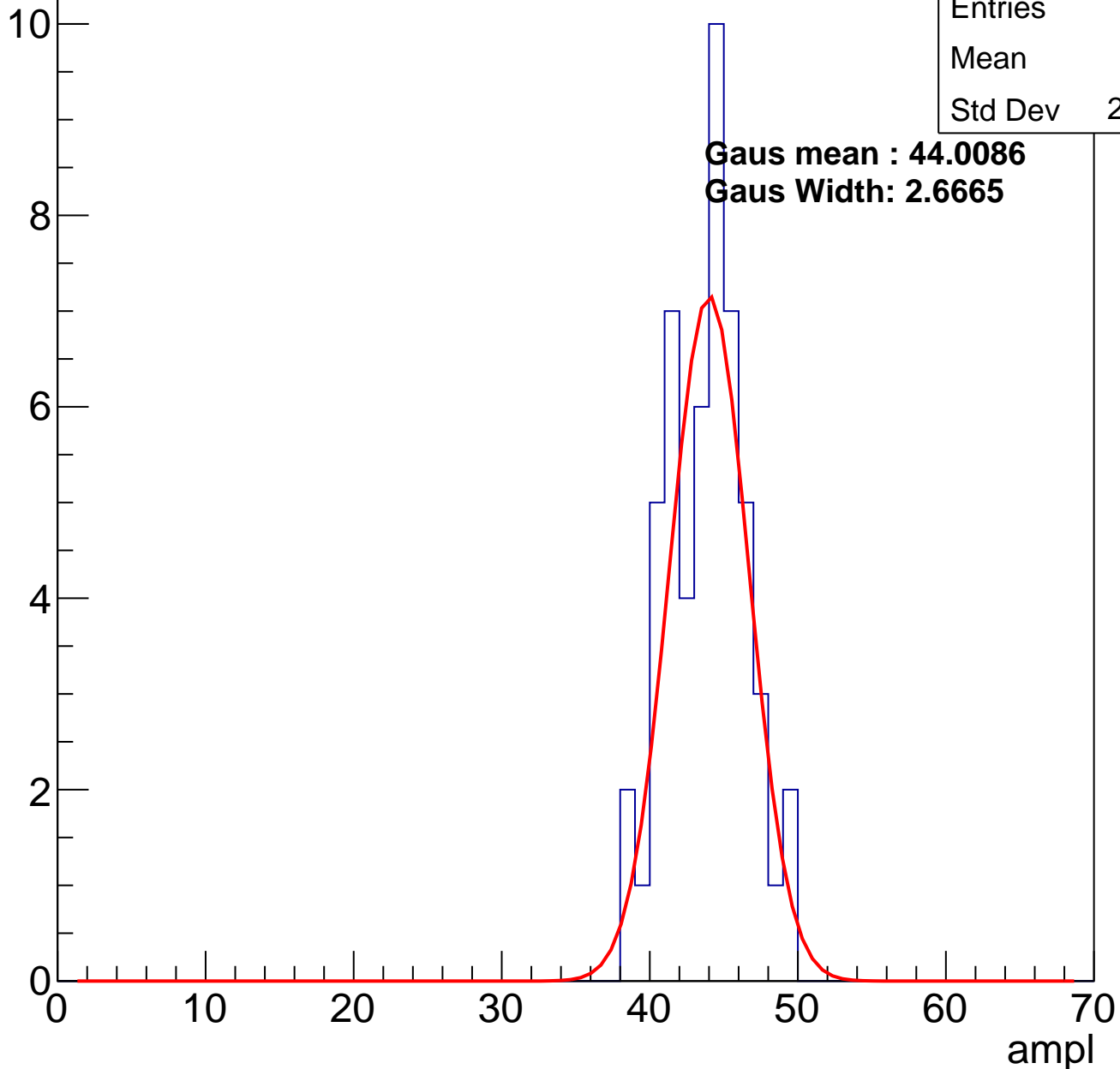
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	53
Mean	43.4
Std Dev	2.616

**Gaus mean : 44.0086**

**Gaus Width: 2.6665**

Entry

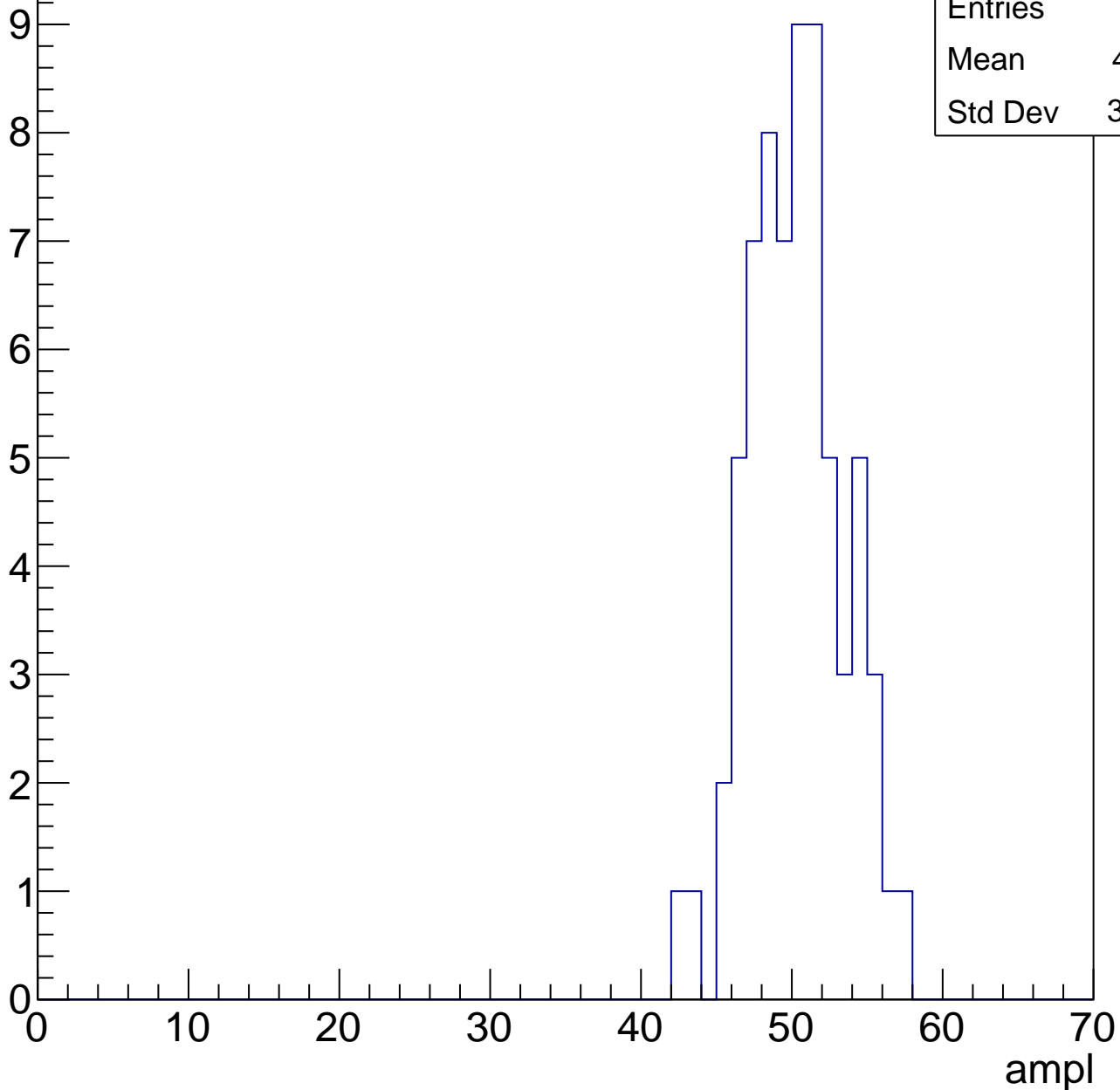


# B1L000S, U24-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

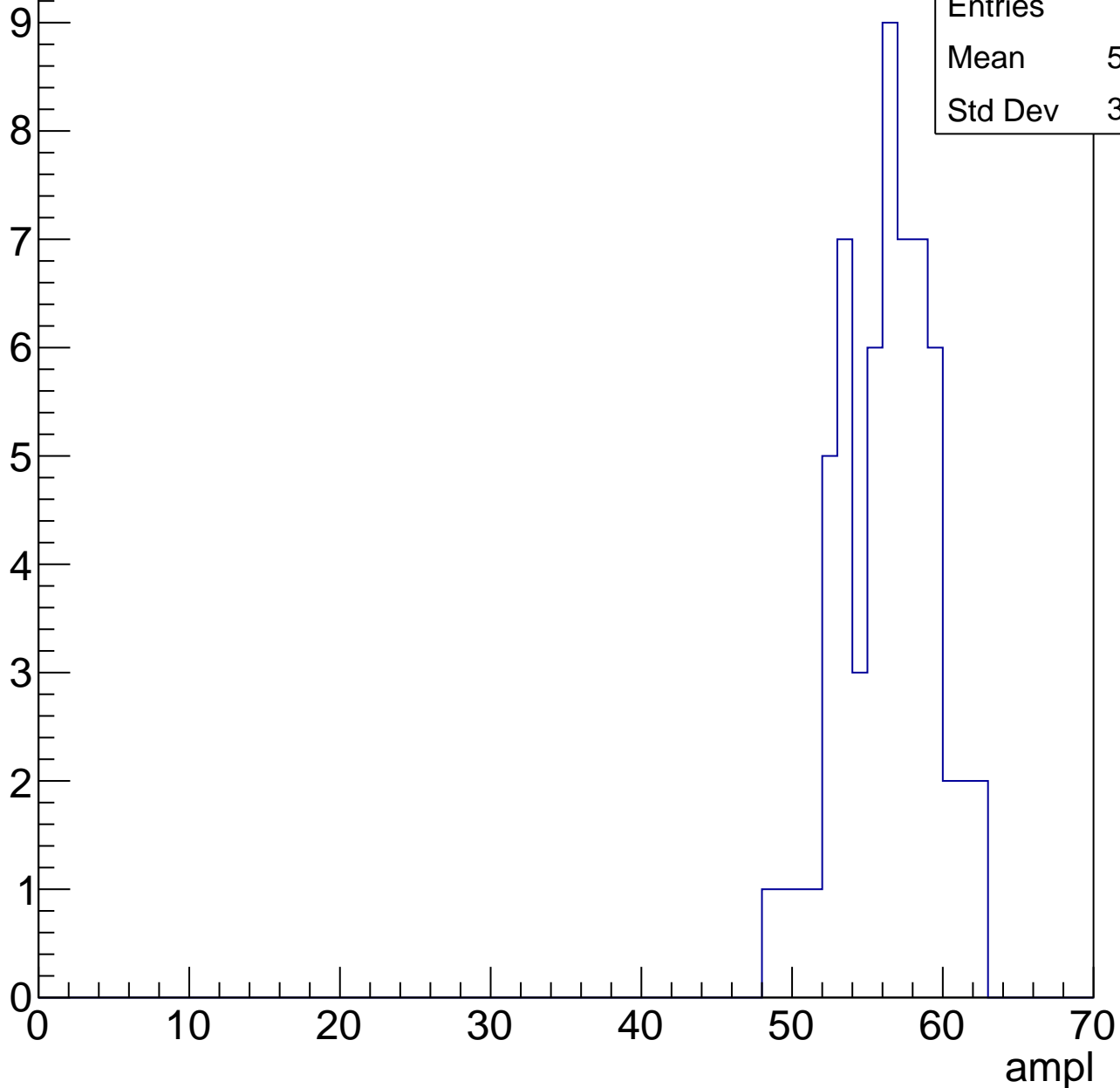
Entries	67
Mean	49.81
Std Dev	3.087



# B1L000S, U24-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

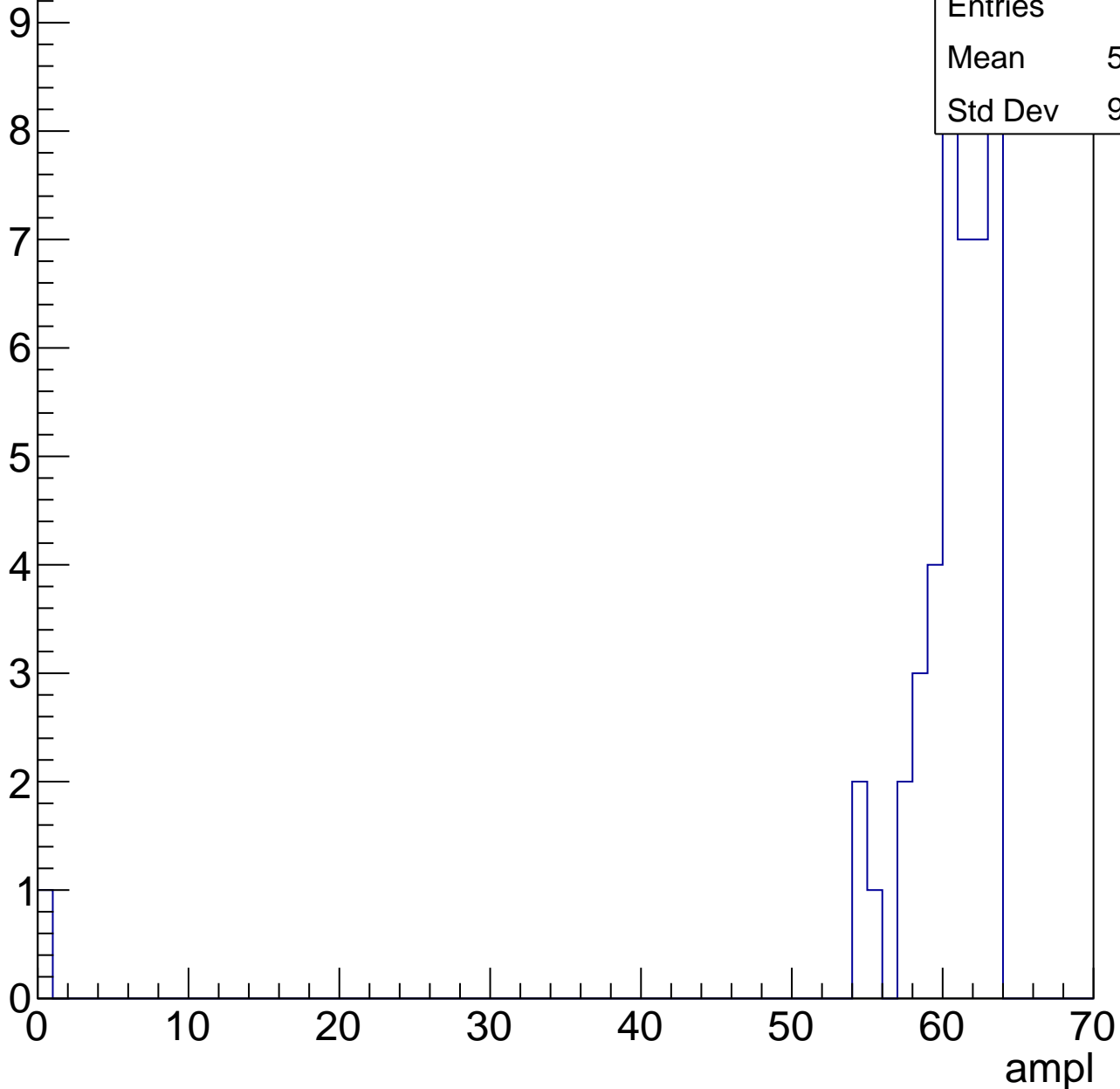


# B1L000S, U24-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	58.98
Std Dev	9.294



# B1L000S, U24-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U24-ch24, adc0

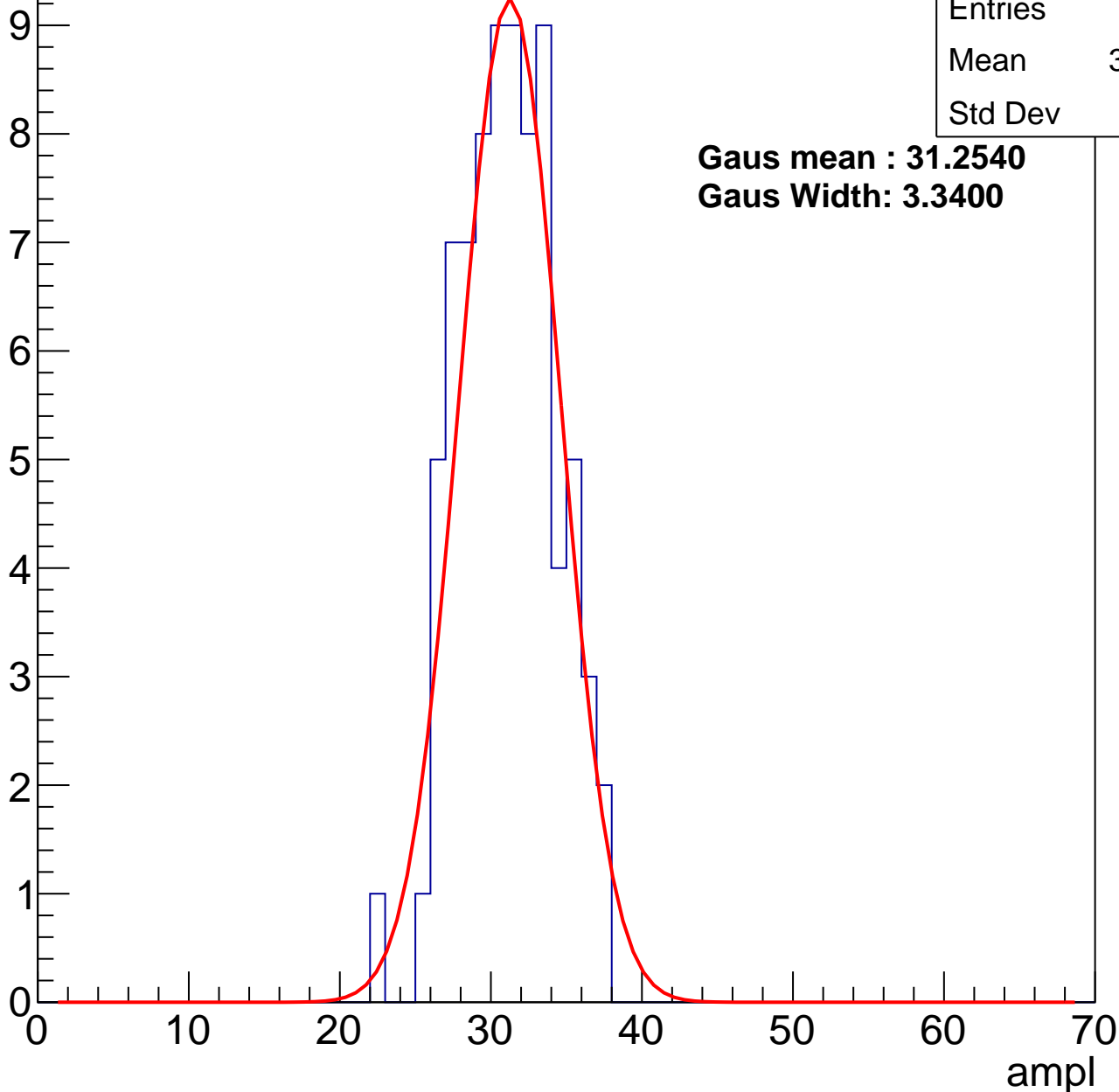
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	30.63
Std Dev	3.11

**Gaus mean : 31.2540**

**Gaus Width: 3.3400**



# B1L000S, U24-ch24, adc1

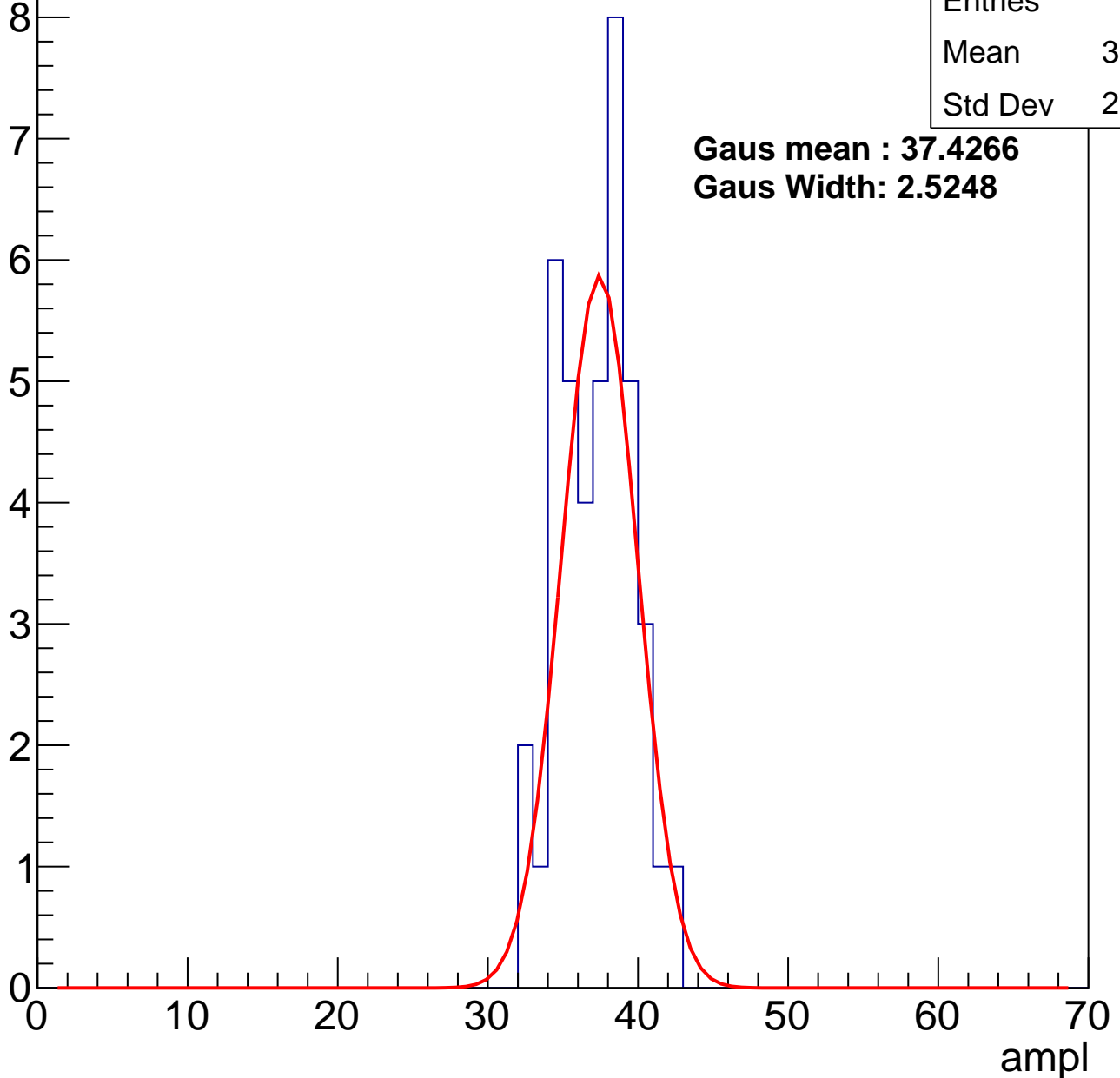
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	41
Mean	36.76
Std Dev	2.407

**Gaus mean : 37.4266**

**Gaus Width: 2.5248**



# B1L000S, U24-ch24, adc2

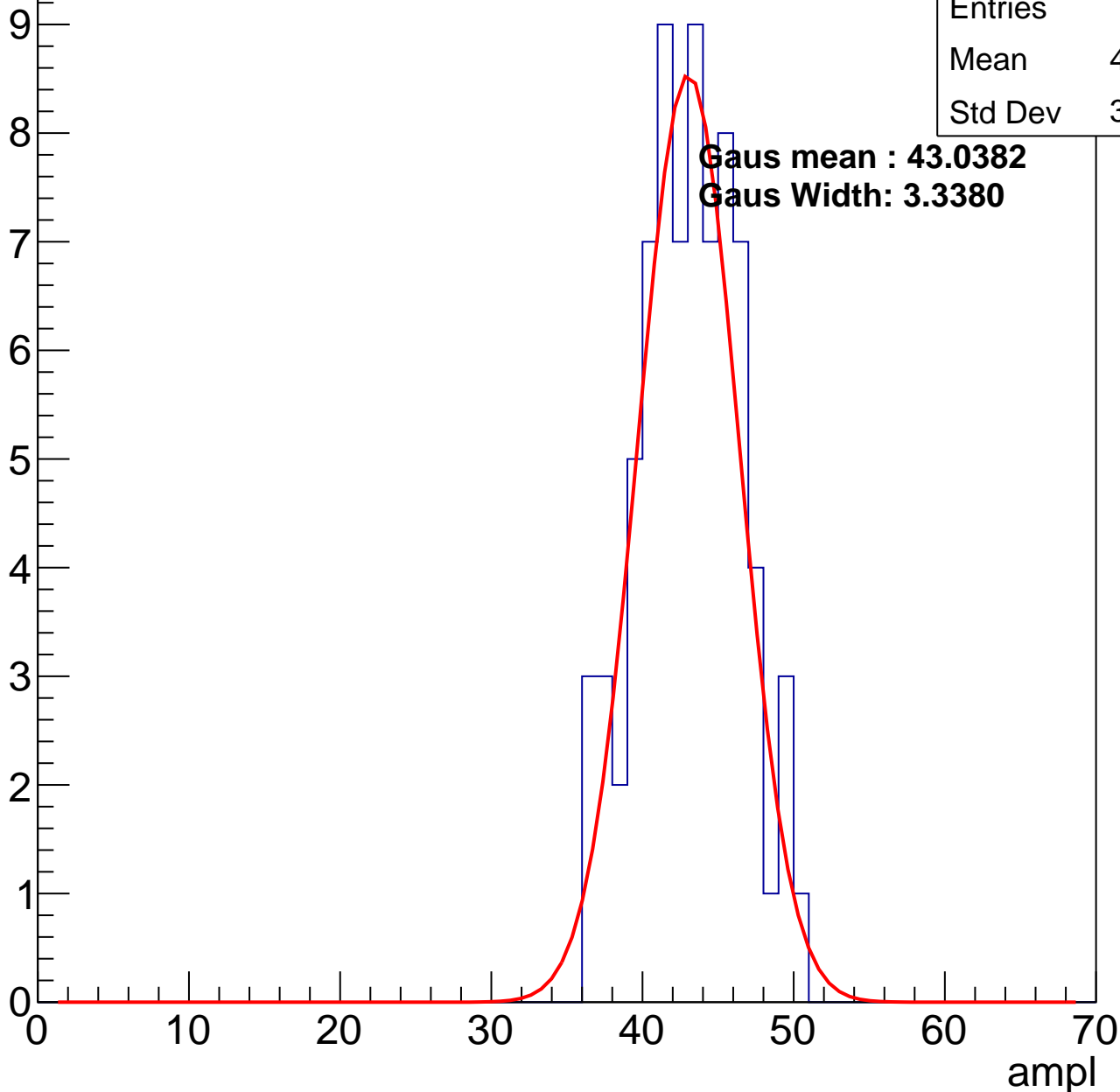
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	42.67
Std Dev	3.306

**Gaus mean : 43.0382**

**Gaus Width: 3.3380**

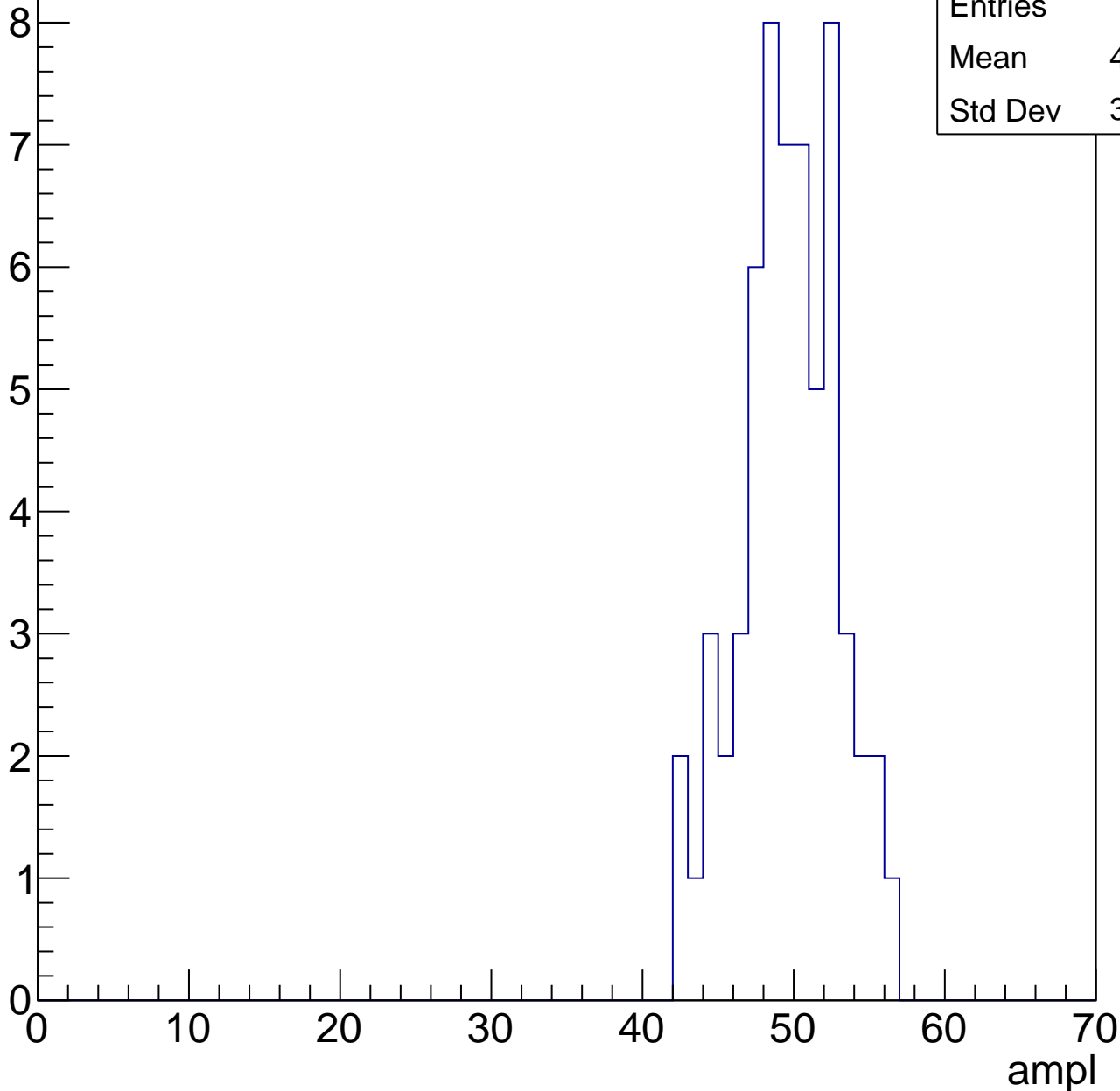


# B1L000S, U24-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	49.17
Std Dev	3.205

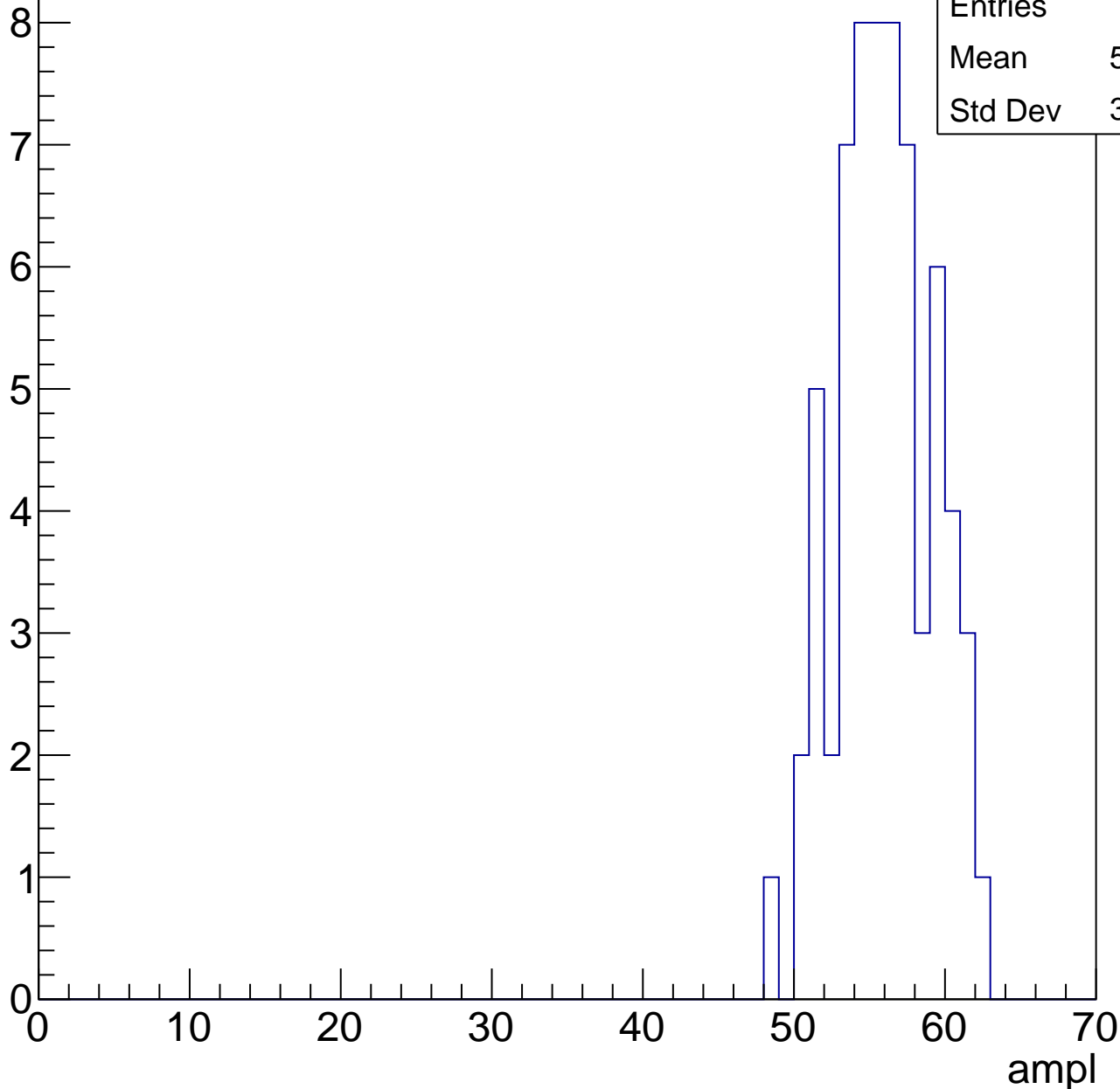


# B1L000S, U24-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

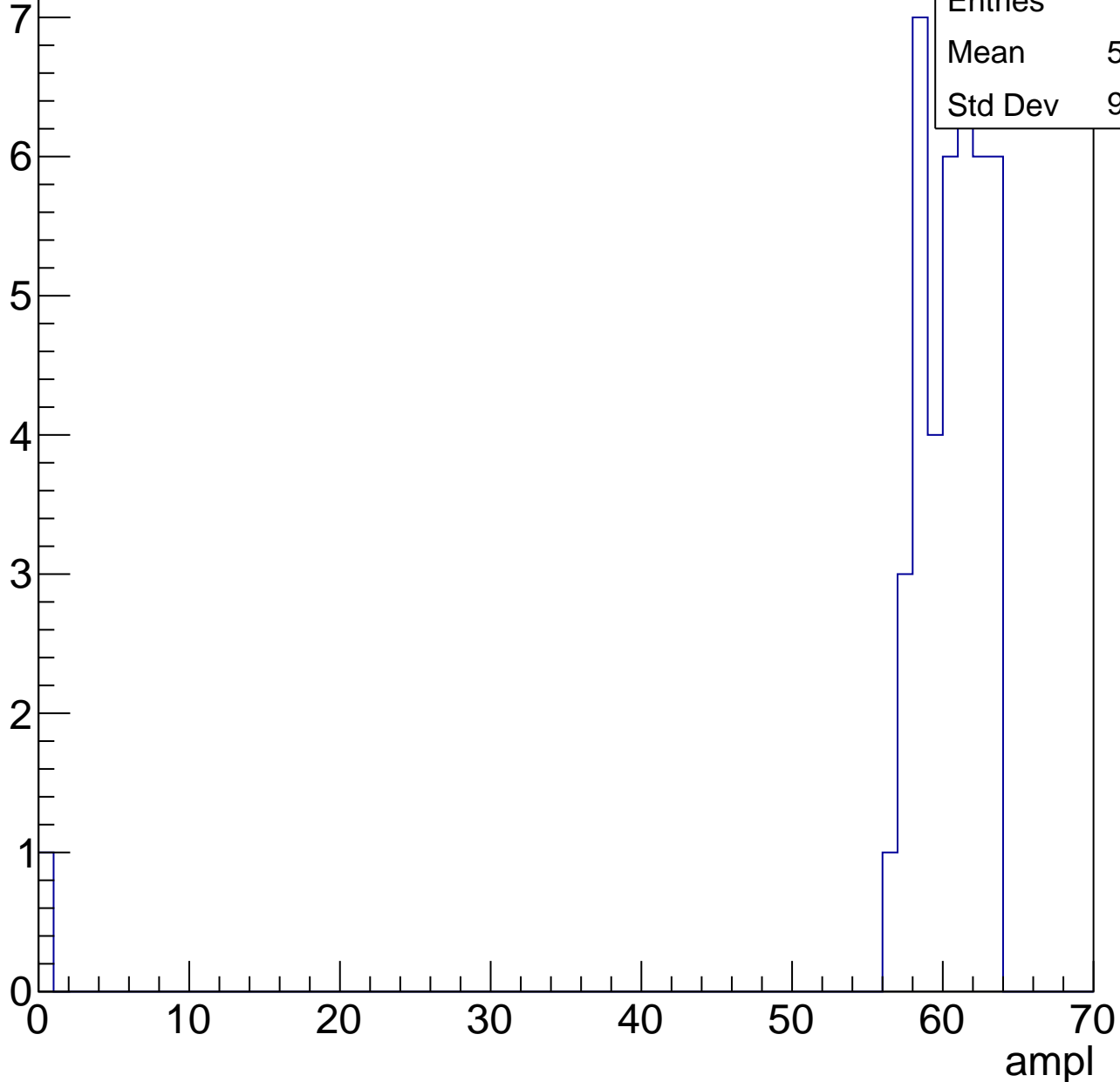
Entries	65
Mean	55.54
Std Dev	3.114



# B1L000S, U24-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch25, adc0

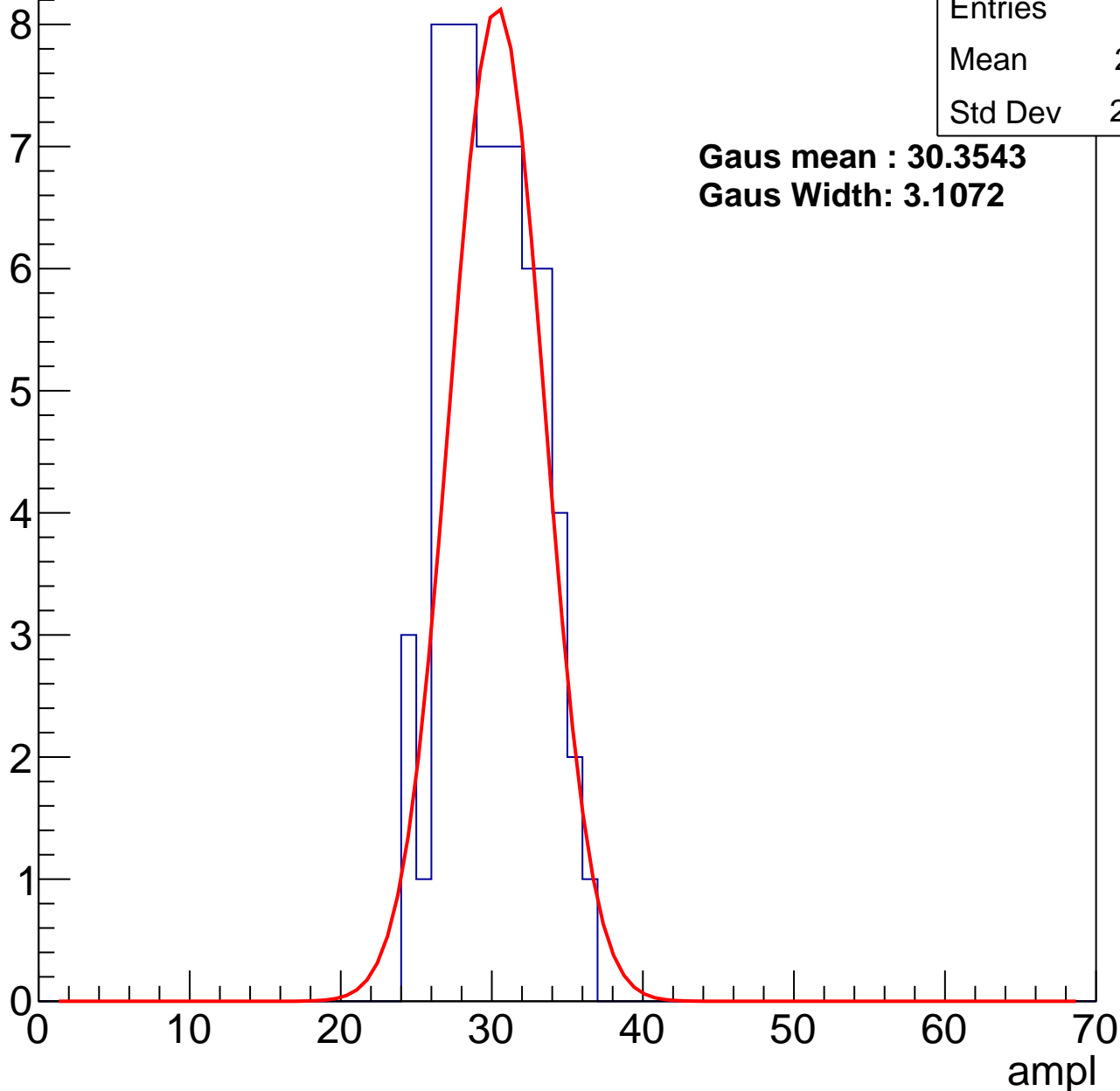
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.51
Std Dev	2.948

**Gaus mean : 30.3543**

**Gaus Width: 3.1072**



# B1L000S, U24-ch25, adc1

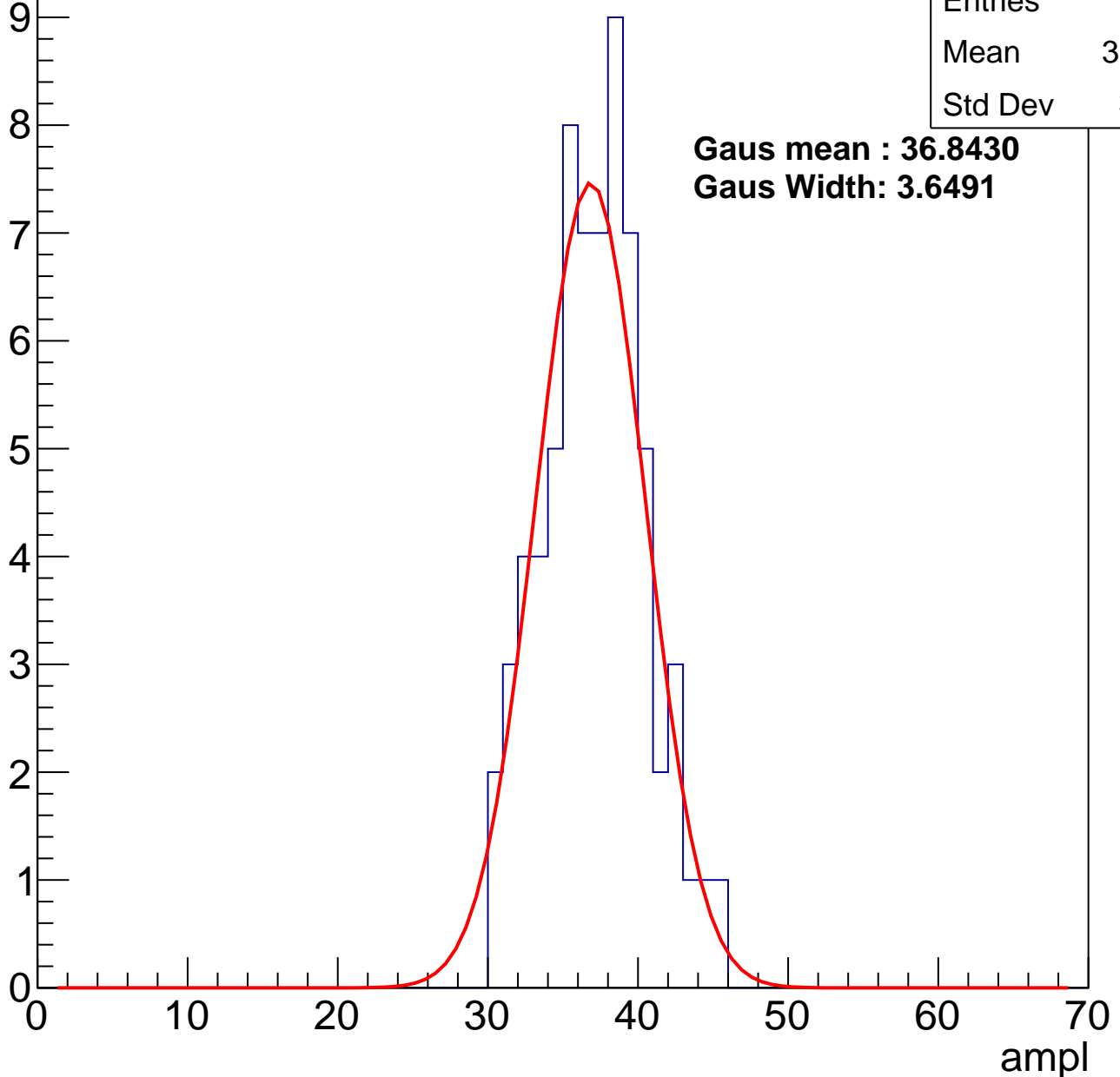
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	36.65
Std Dev	3.37

**Gaus mean : 36.8430**

**Gaus Width: 3.6491**



# B1L000S, U24-ch25, adc2

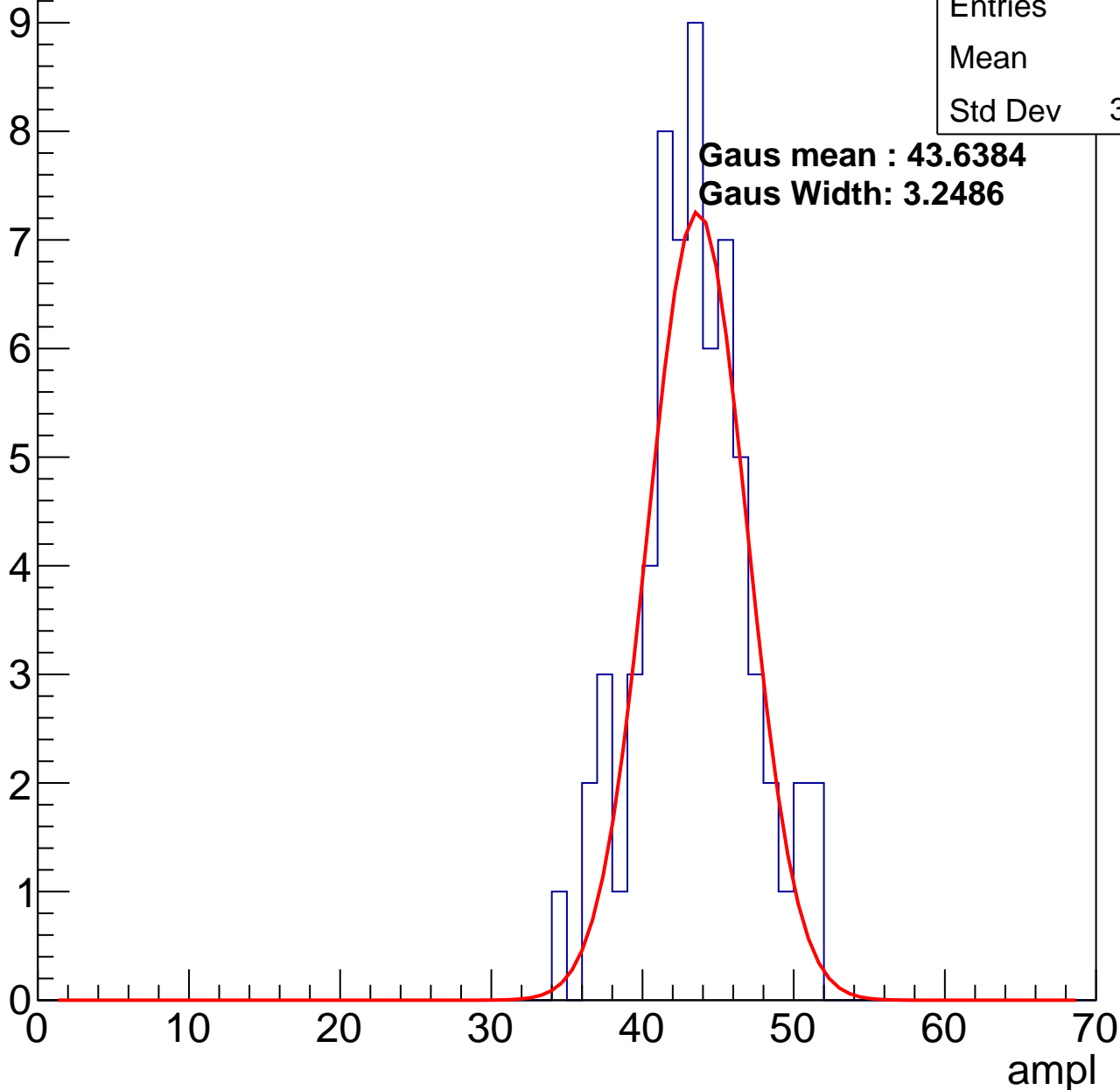
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43
Std Dev	3.639

**Gaus mean : 43.6384**

**Gaus Width: 3.2486**

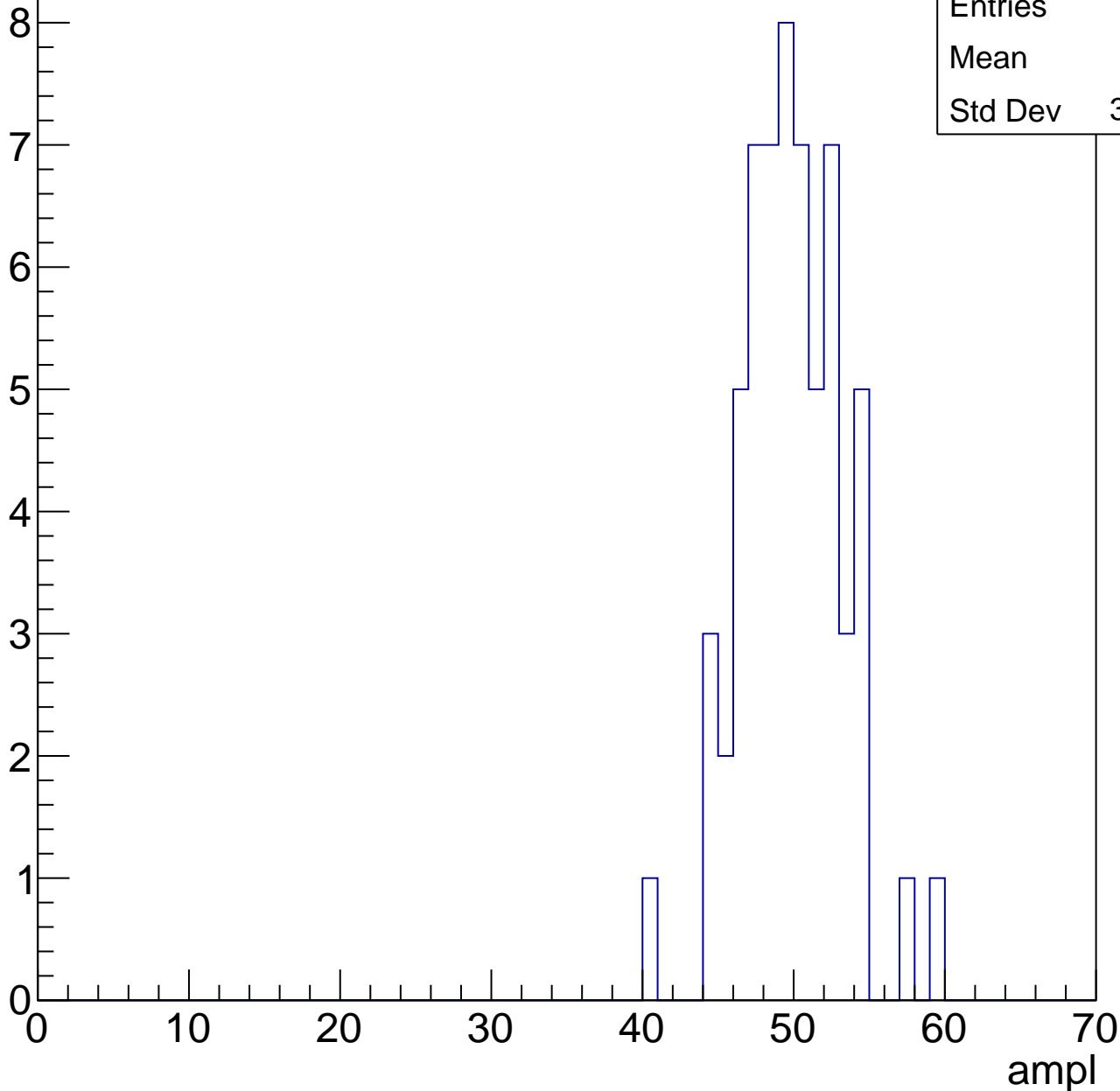


# B1L000S, U24-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	49.4
Std Dev	3.324



# B1L000S, U24-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

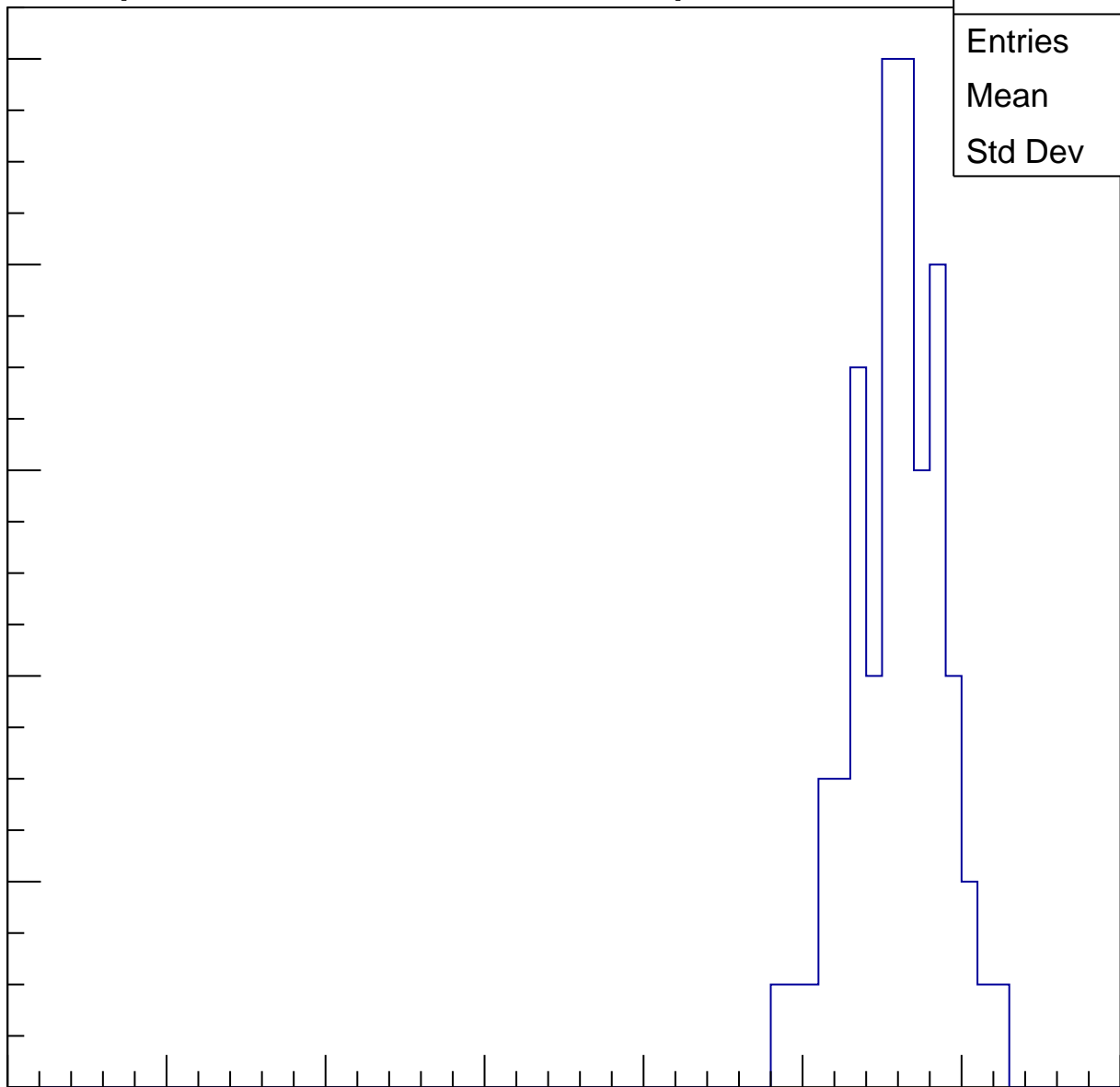
Entries	62
Mean	55.45
Std Dev	2.866

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

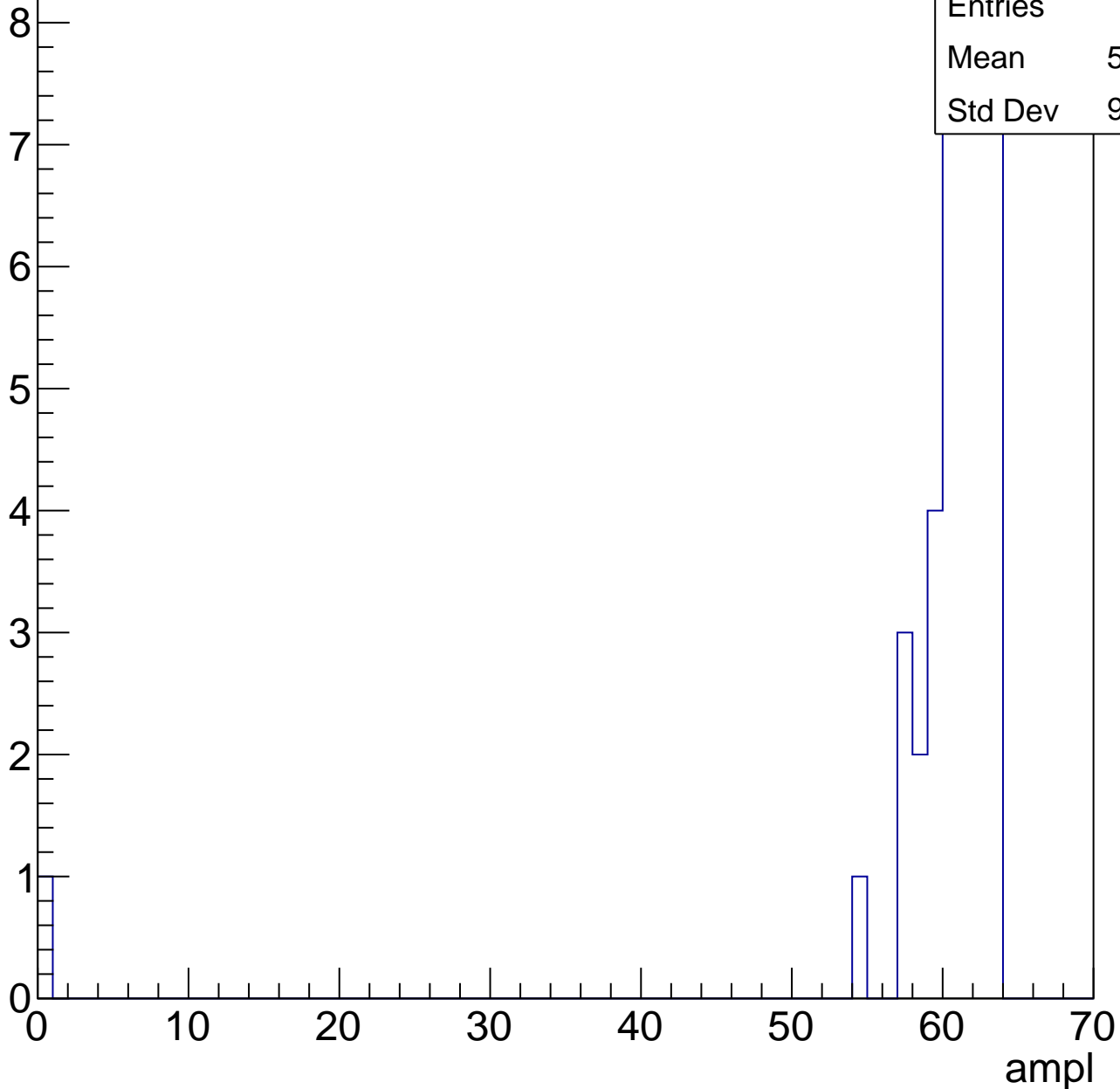


# B1L000S, U24-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

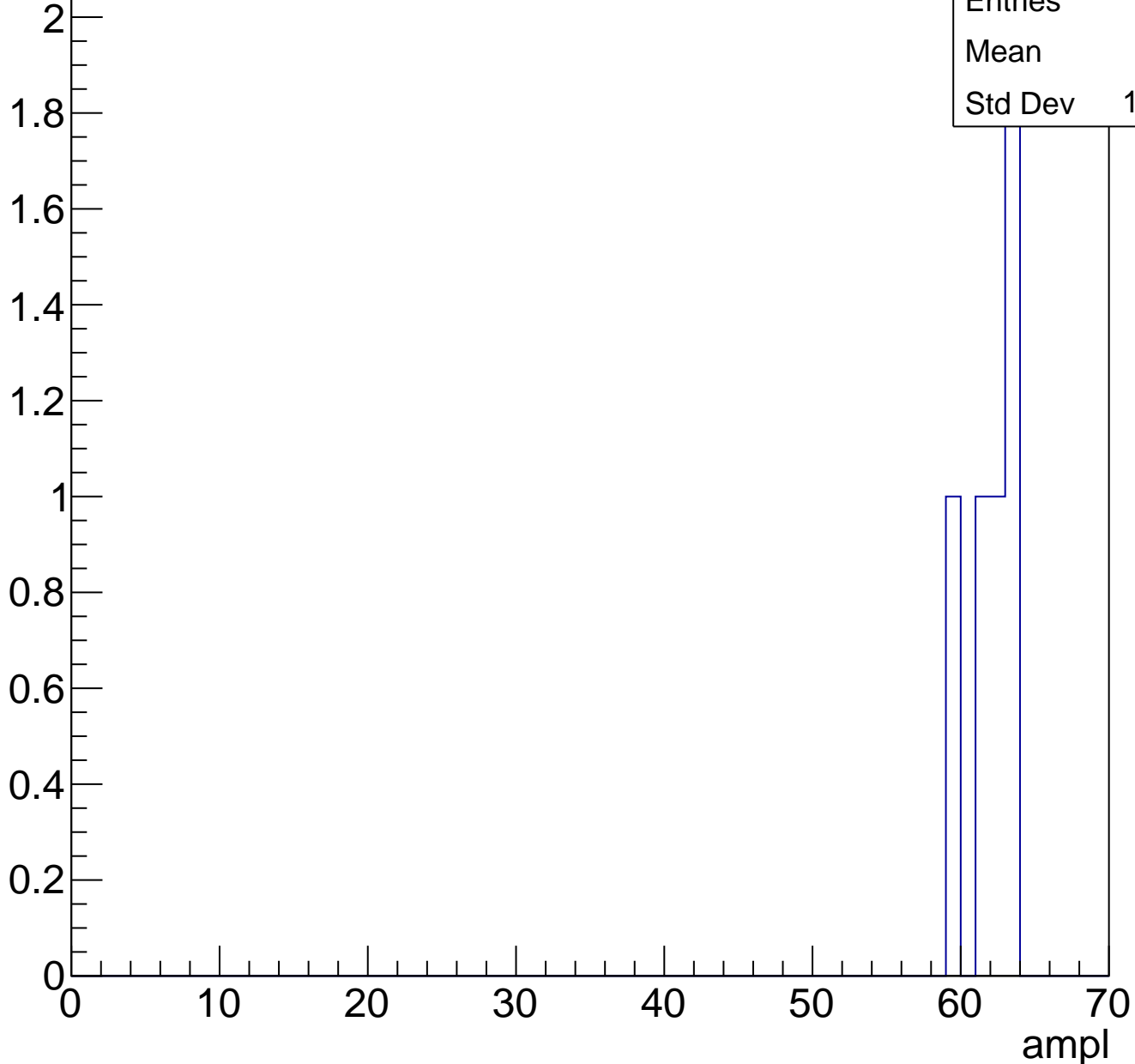
Entries	43
Mean	59.19
Std Dev	9.349



# B1L000S, U24-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B1L000S, U24-ch26, adc0

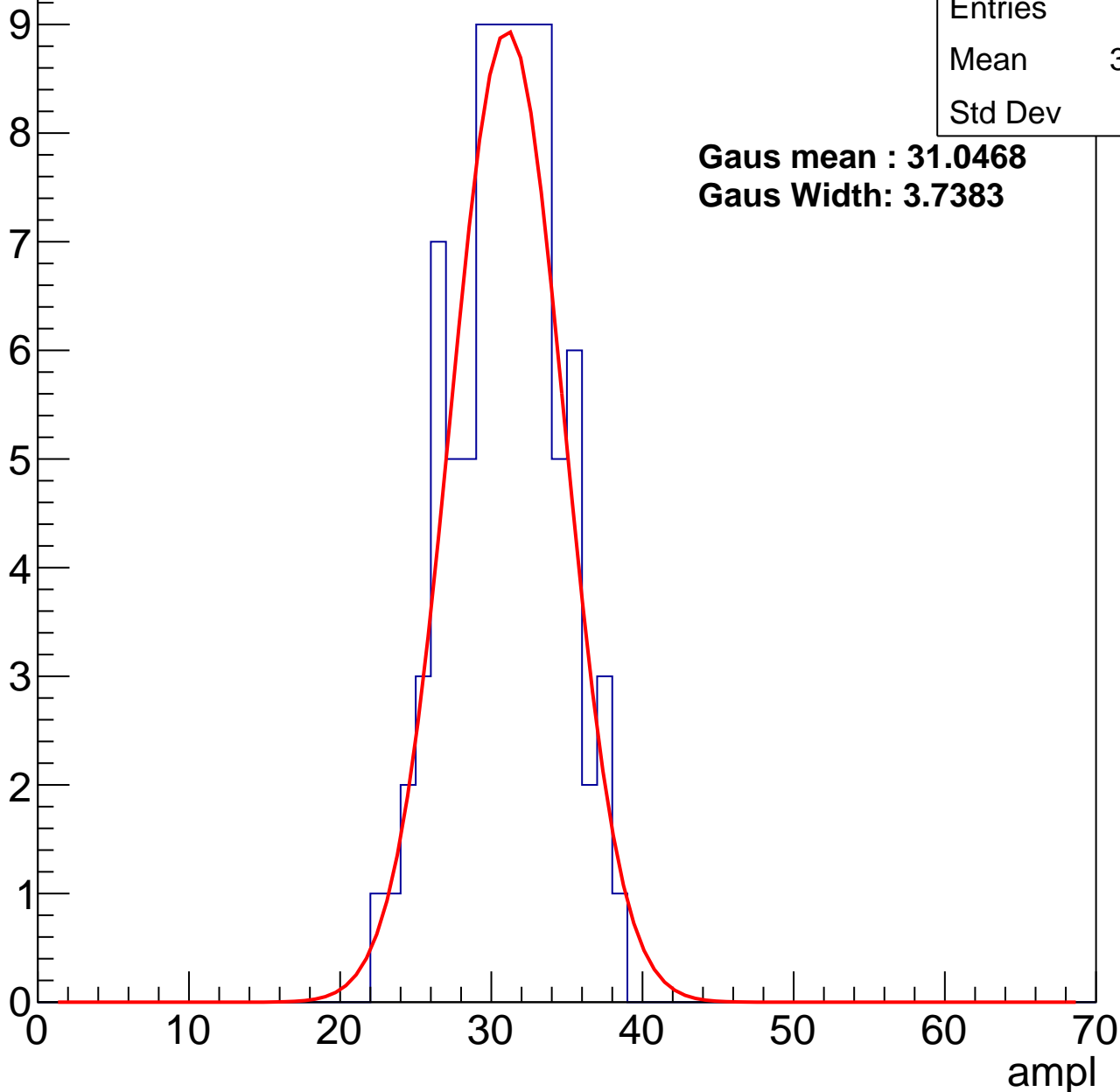
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	86
Mean	30.48
Std Dev	3.52

**Gaus mean : 31.0468**

**Gaus Width: 3.7383**



# B1L000S, U24-ch26, adc1

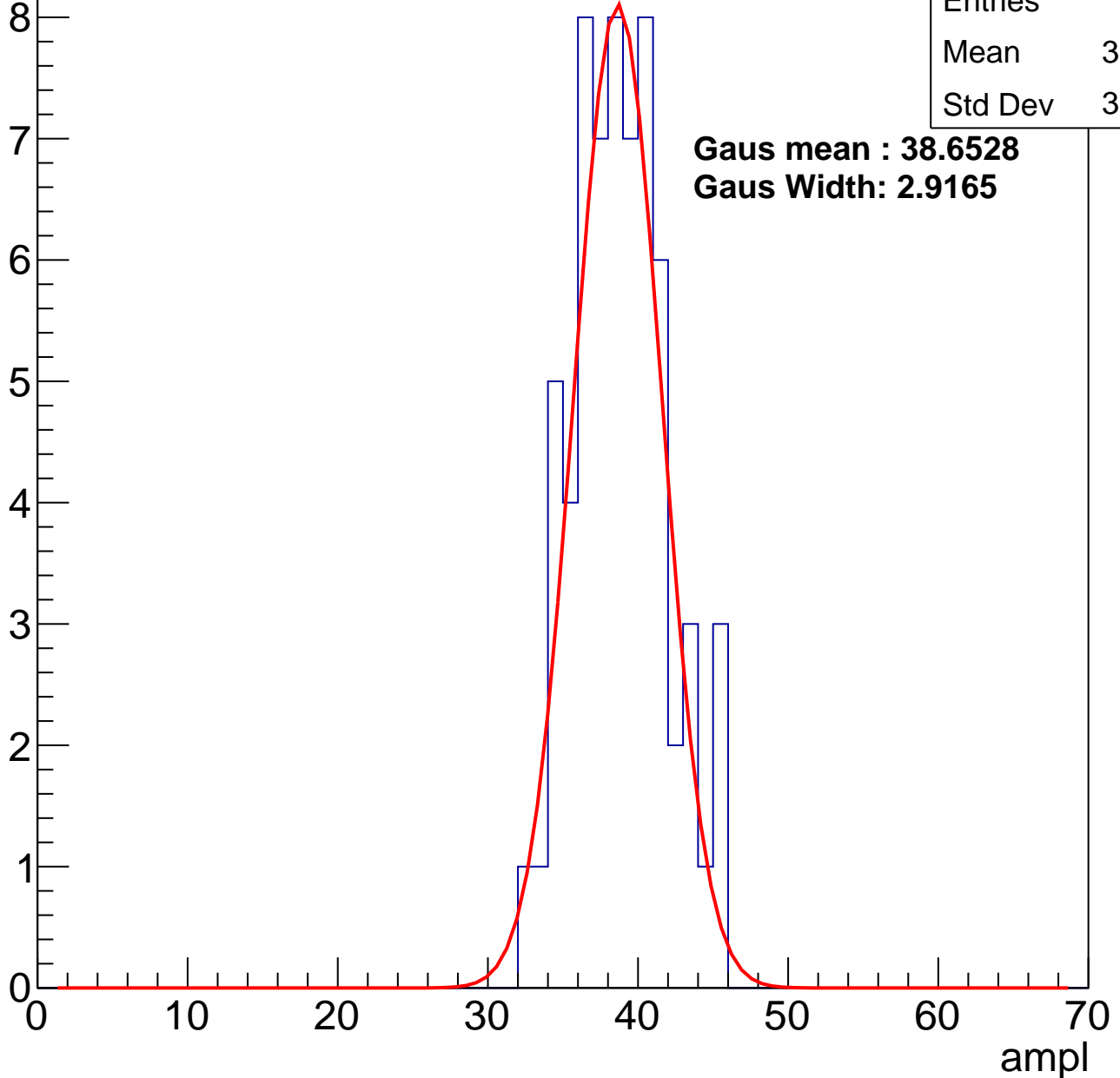
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	38.39
Std Dev	3.034

**Gaus mean : 38.6528**

**Gaus Width: 2.9165**



# B1L000S, U24-ch26, adc2

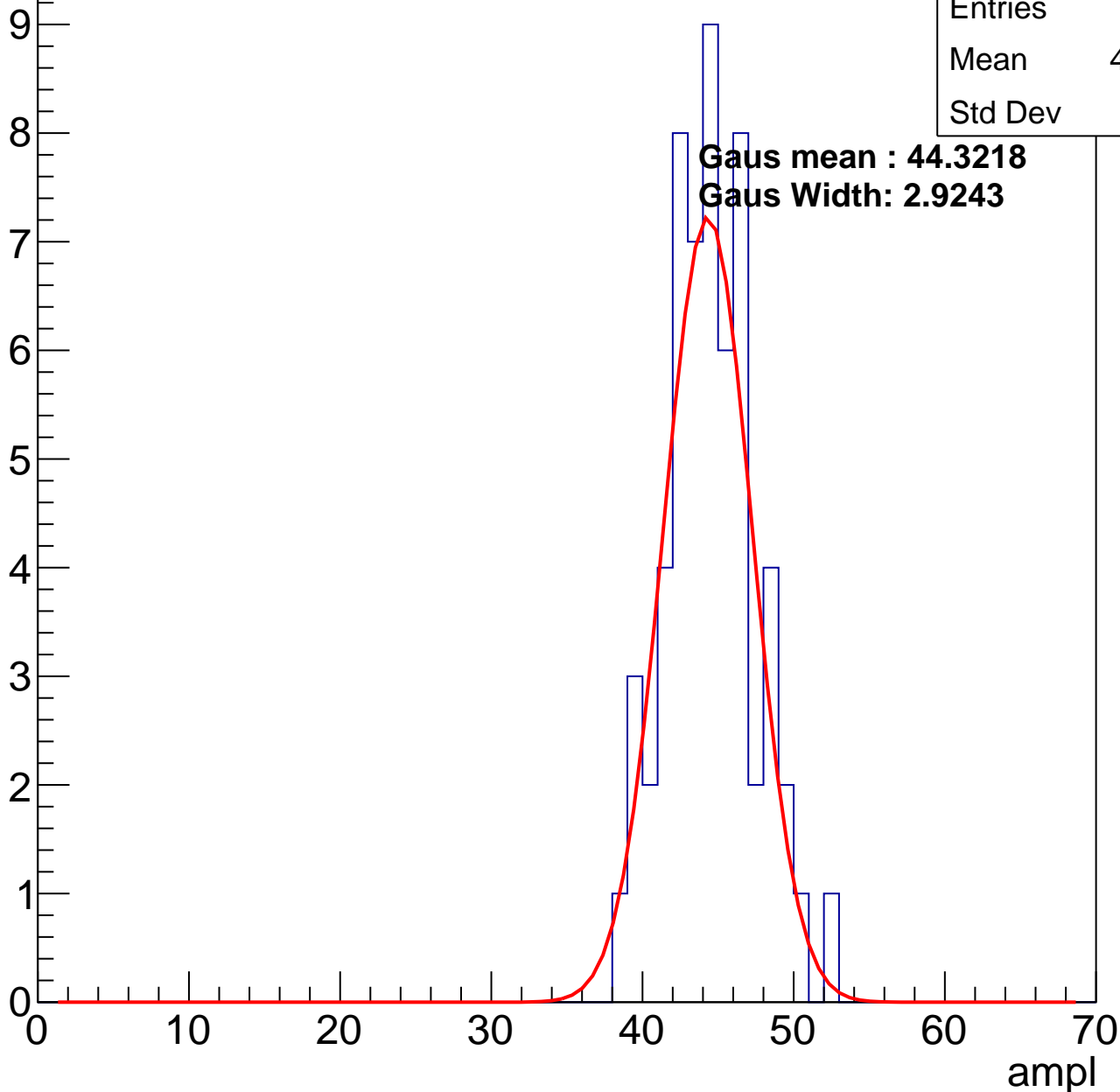
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	44.07
Std Dev	2.9

**Gaus mean : 44.3218**

**Gaus Width: 2.9243**



# B1L000S, U24-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

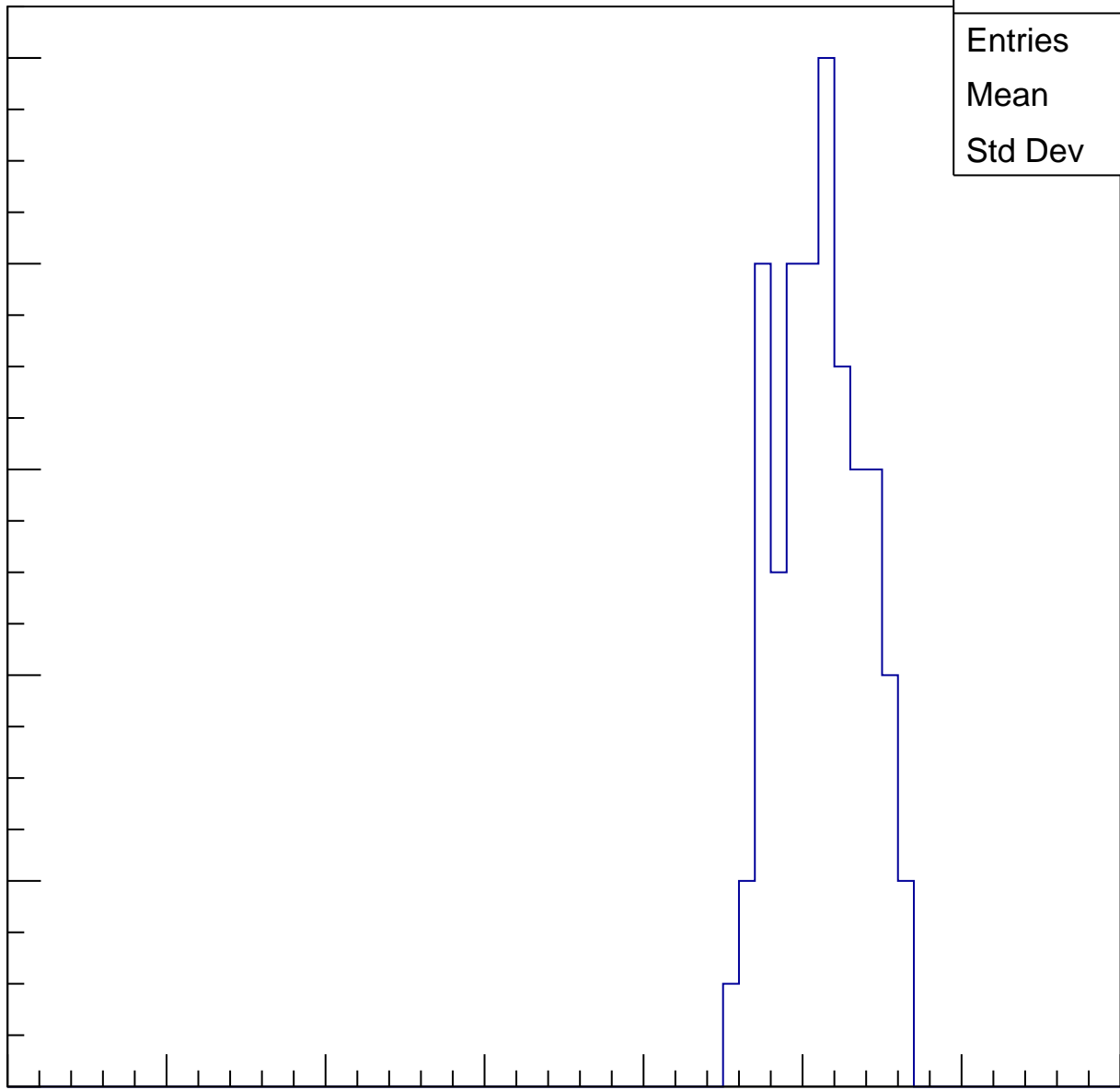
Entries	67
Mean	50.64
Std Dev	2.703

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

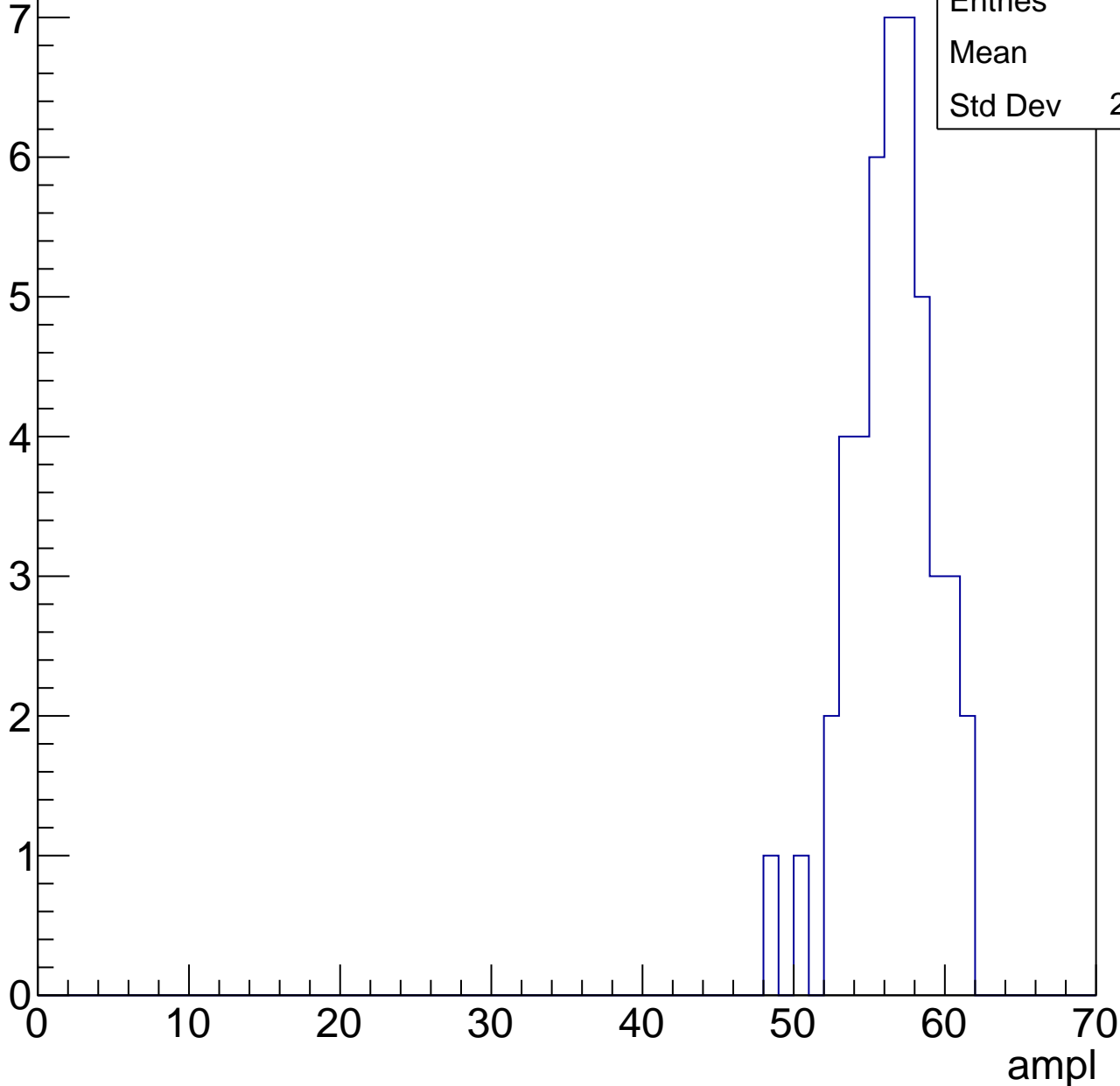


# B1L000S, U24-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

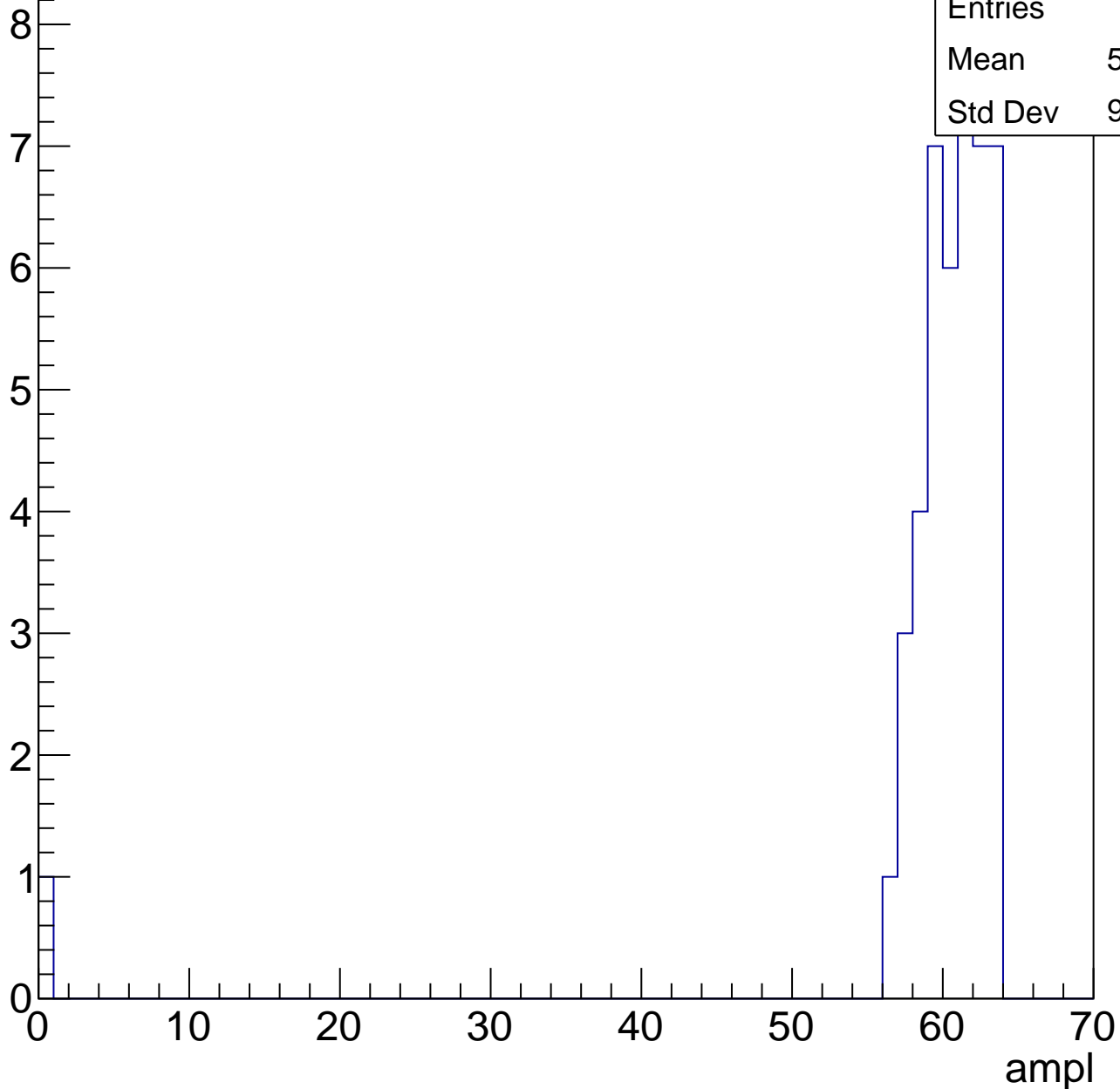
Entries	45
Mean	56
Std Dev	2.757



# B1L000S, U24-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

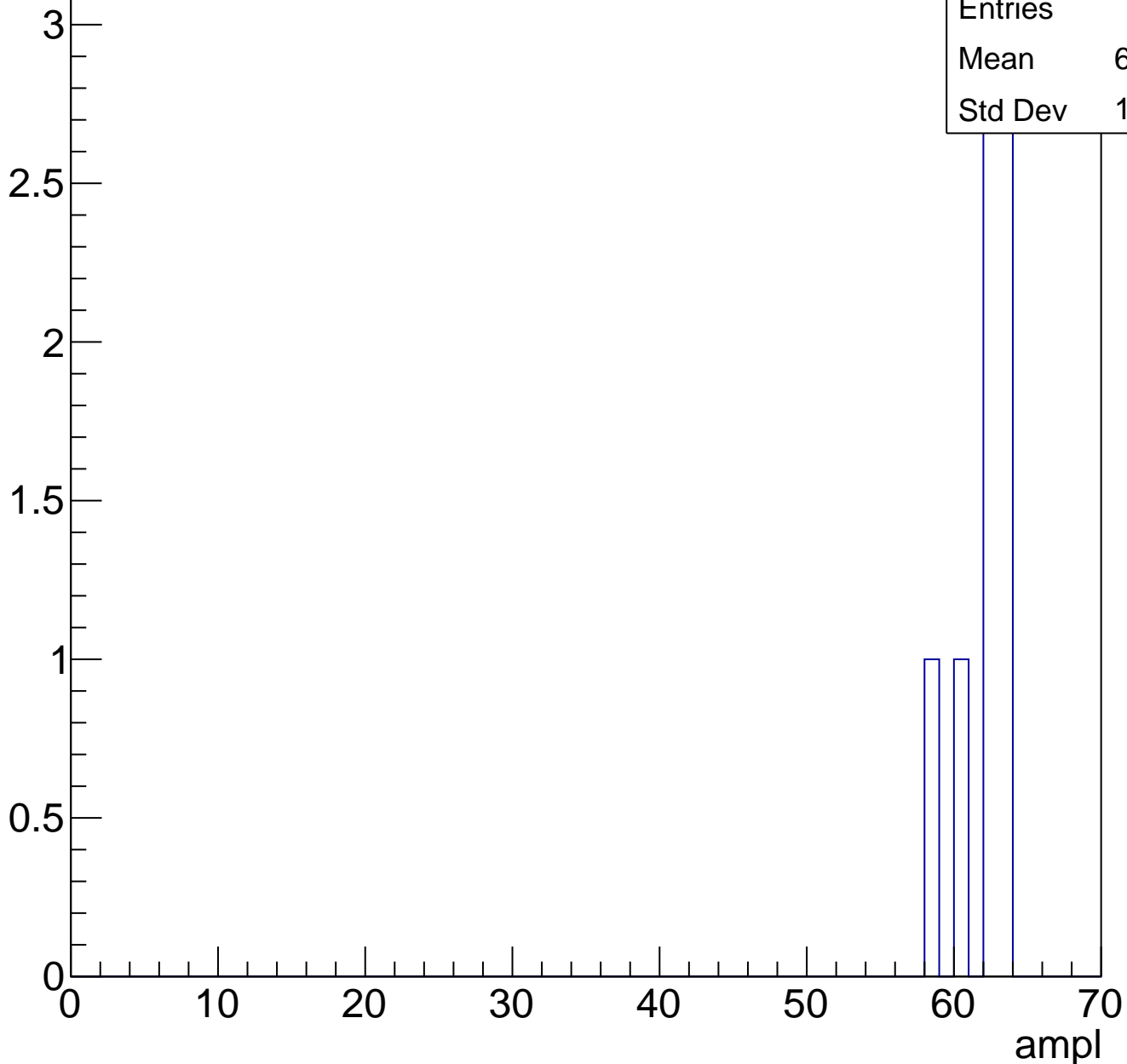
Entry



# B1L000S, U24-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	28.67
Std Dev	2.845

**Gaus mean : 29.4569**

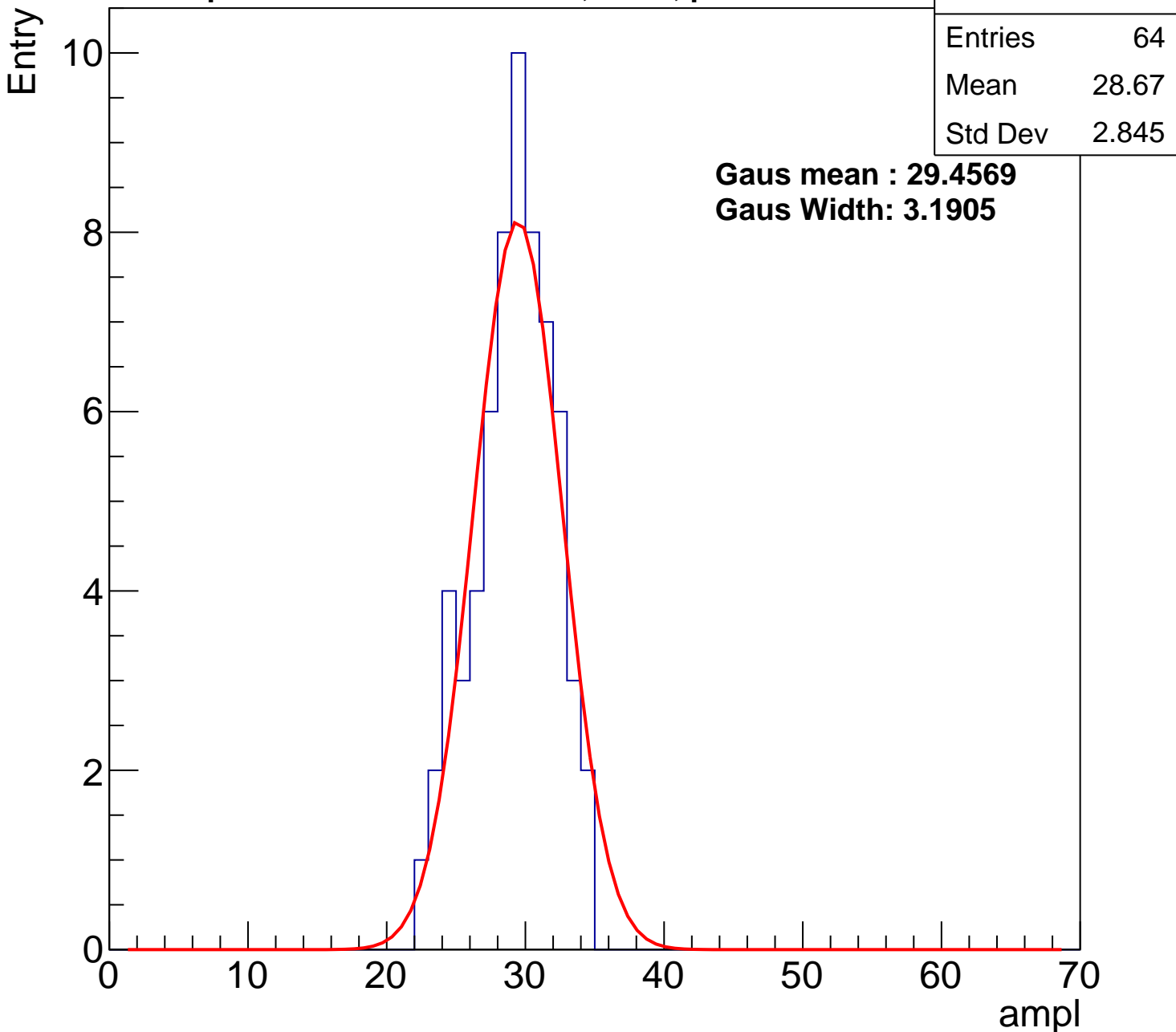
**Gaus Width: 3.1905**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch27, adc1

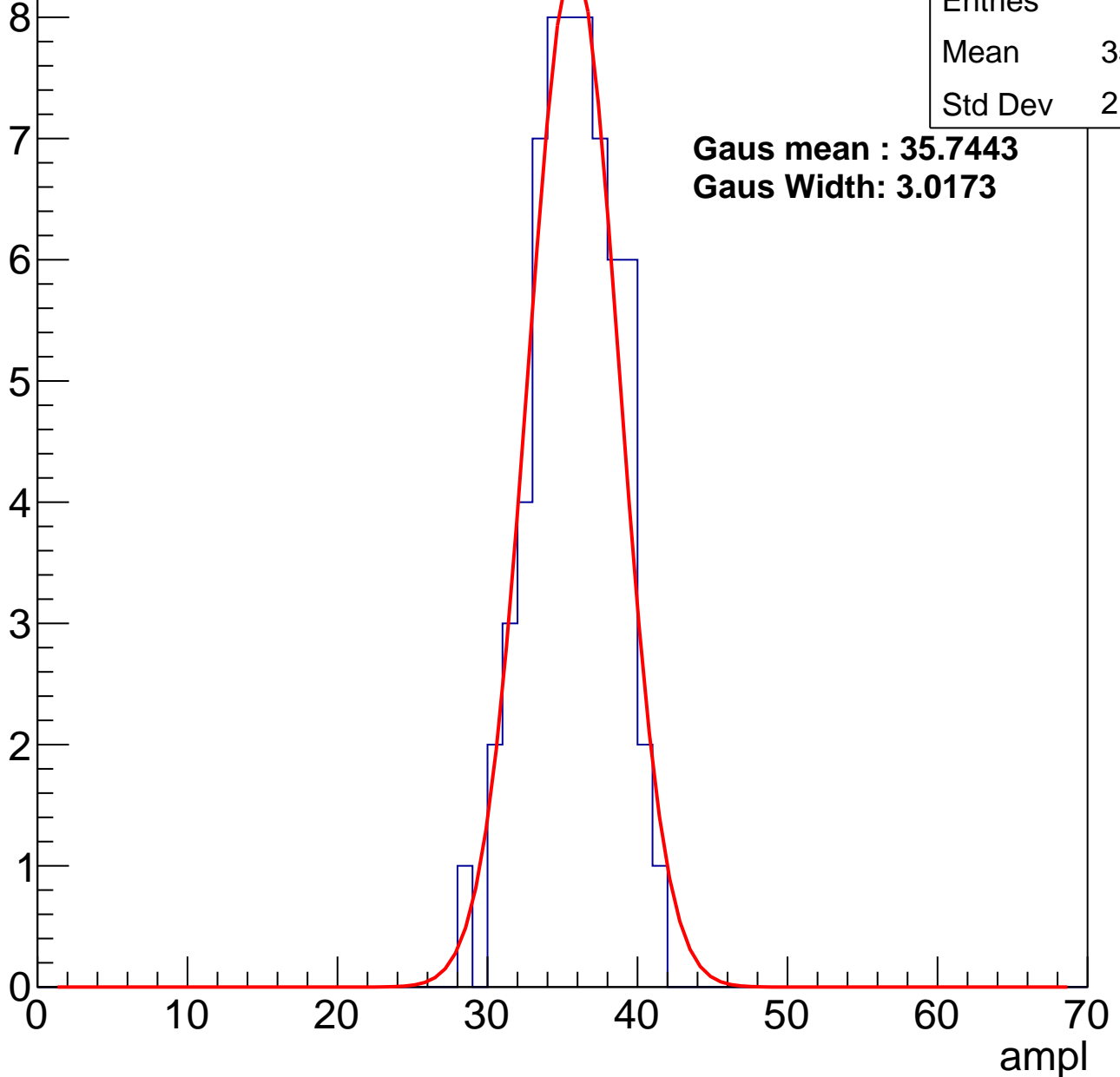
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.27
Std Dev	2.779

**Gaus mean : 35.7443**

**Gaus Width: 3.0173**



# B1L000S, U24-ch27, adc2

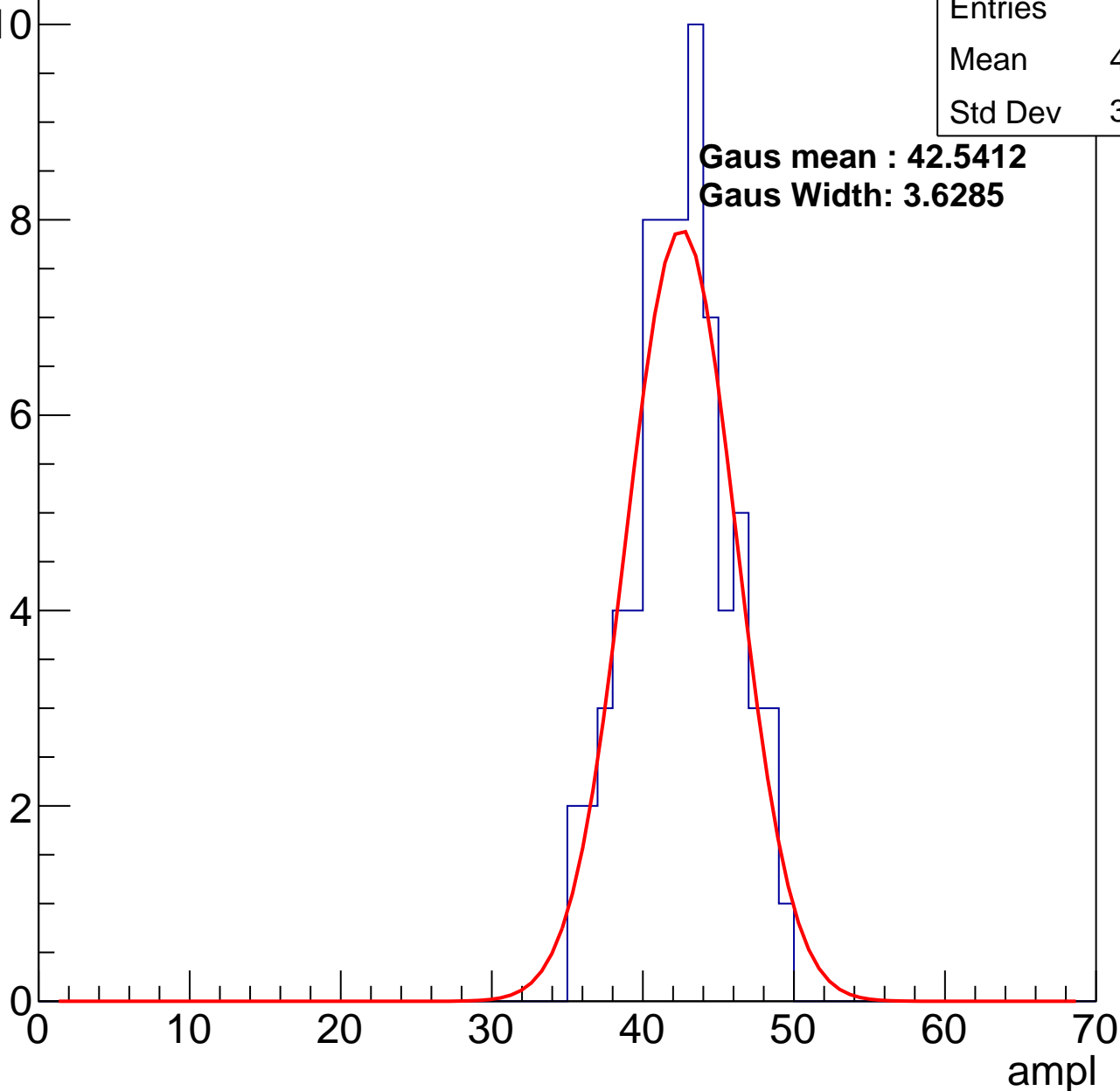
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	42.04
Std Dev	3.272

**Gaus mean : 42.5412**

**Gaus Width: 3.6285**

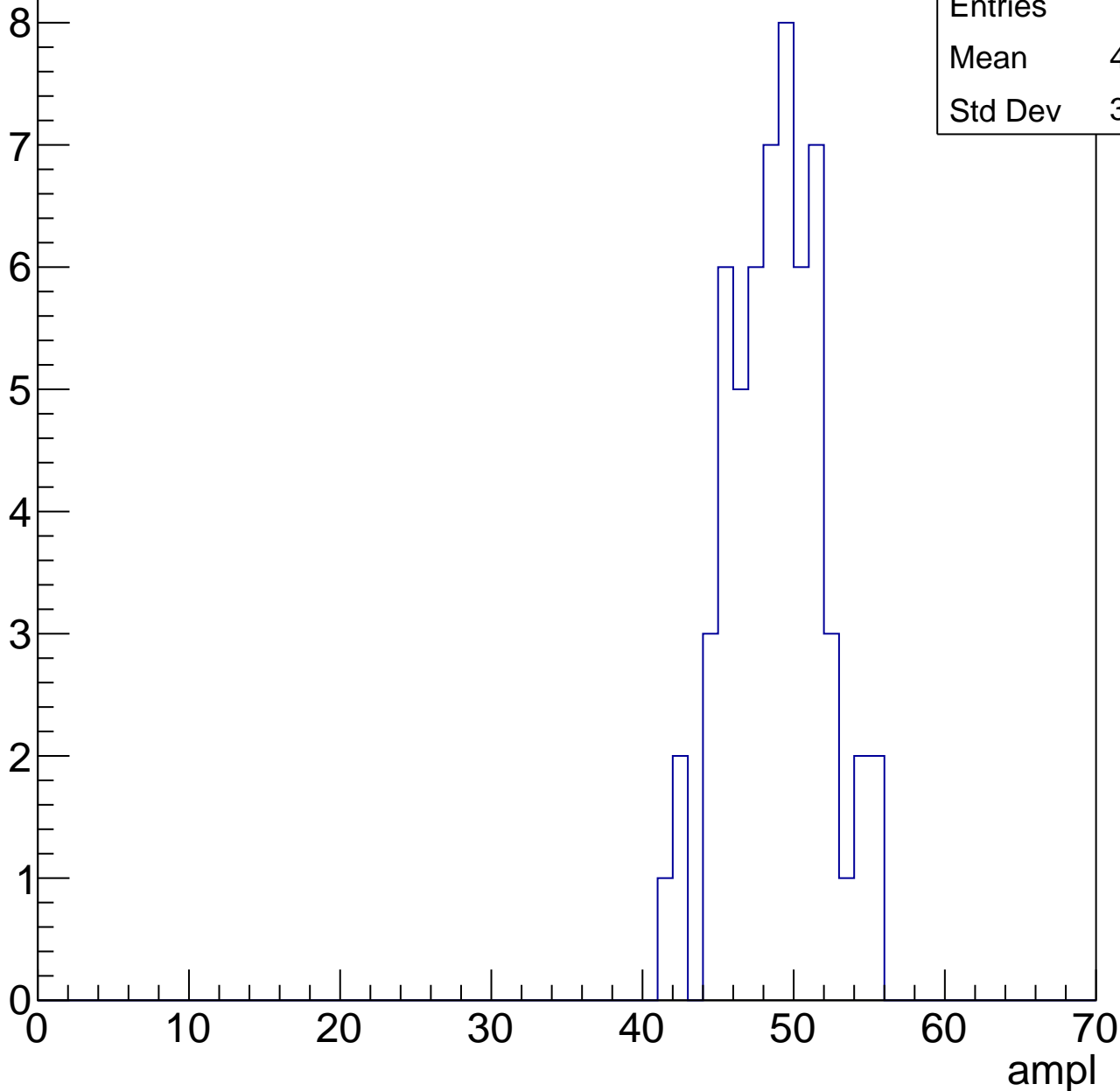


# B1L000S, U24-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	48.32
Std Dev	3.138

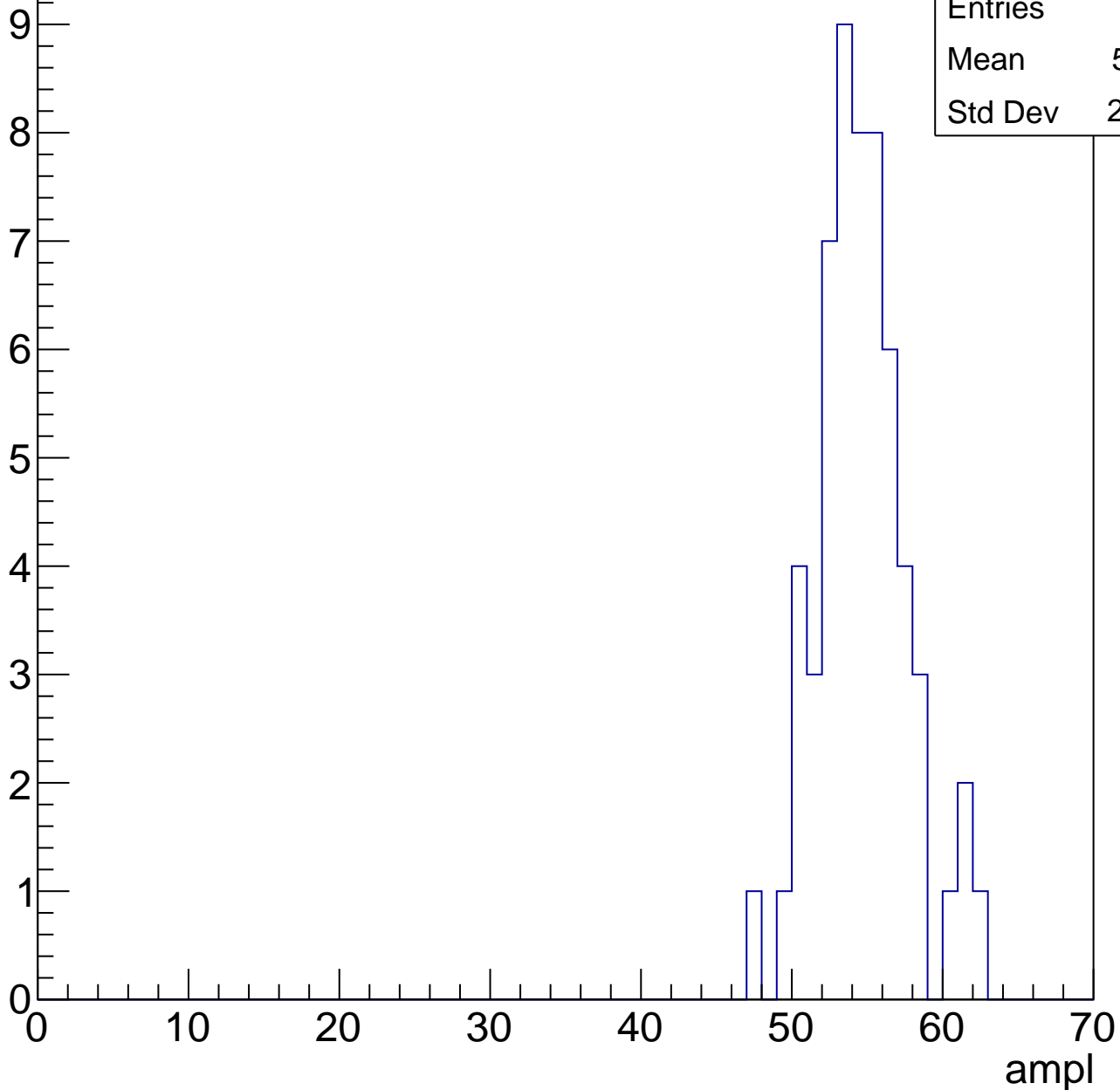


# B1L000S, U24-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	54.21
Std Dev	2.976



# B1L000S, U24-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 56

Mean 58.62

Std Dev 8.18

8

6

4

2

0

0

10

20

30

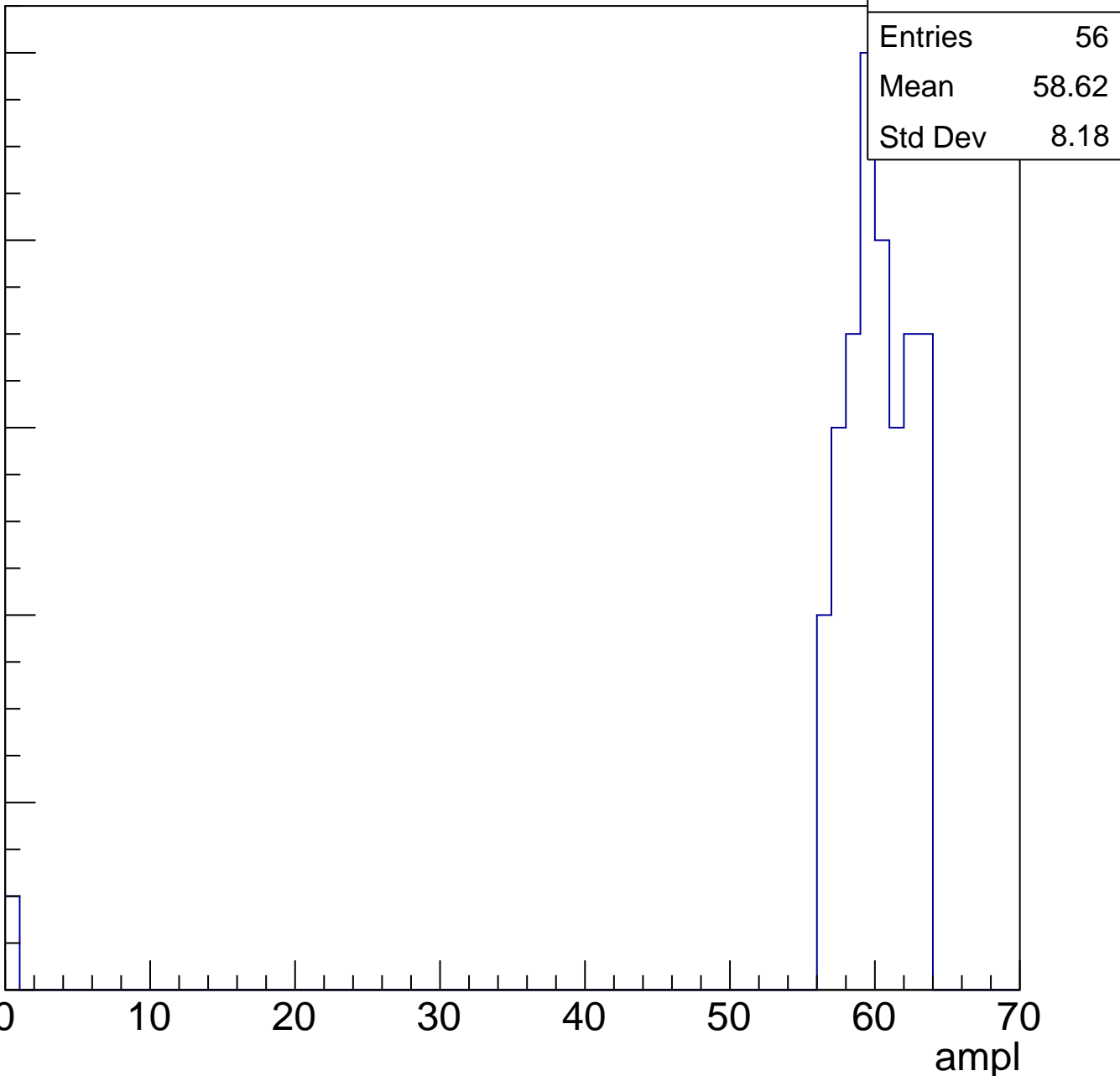
40

50

60

70

ampl



# B1L000S, U24-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

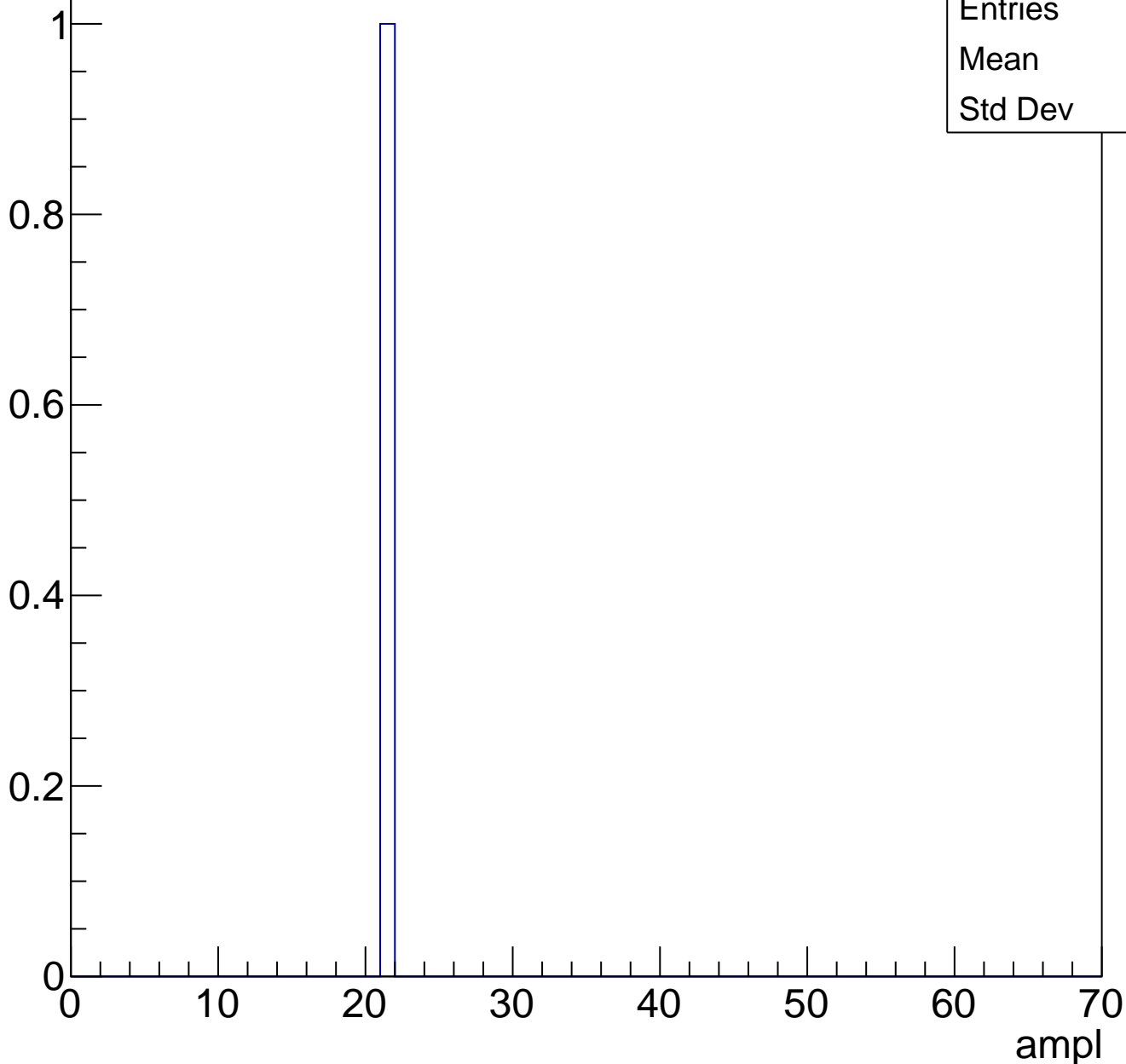




# B1L000S, U24-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch28, adc0

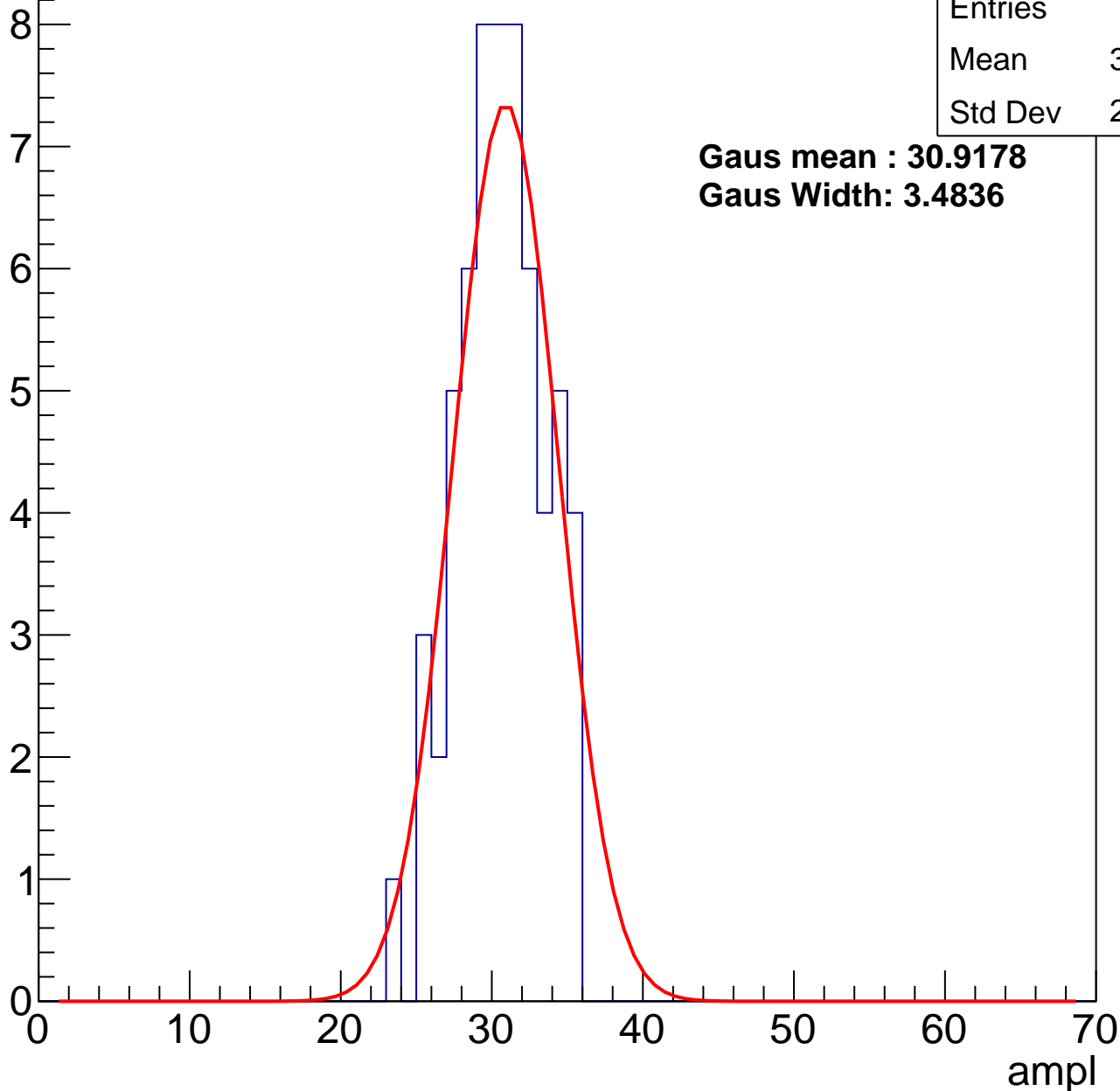
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	30.12
Std Dev	2.829

**Gaus mean : 30.9178**

**Gaus Width: 3.4836**



# B1L000S, U24-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	36.61
Std Dev	3.251

**Gaus mean : 36.9712**  
**Gaus Width: 3.3021**

Entry

10

8

6

4

2

0

0

10

20

30

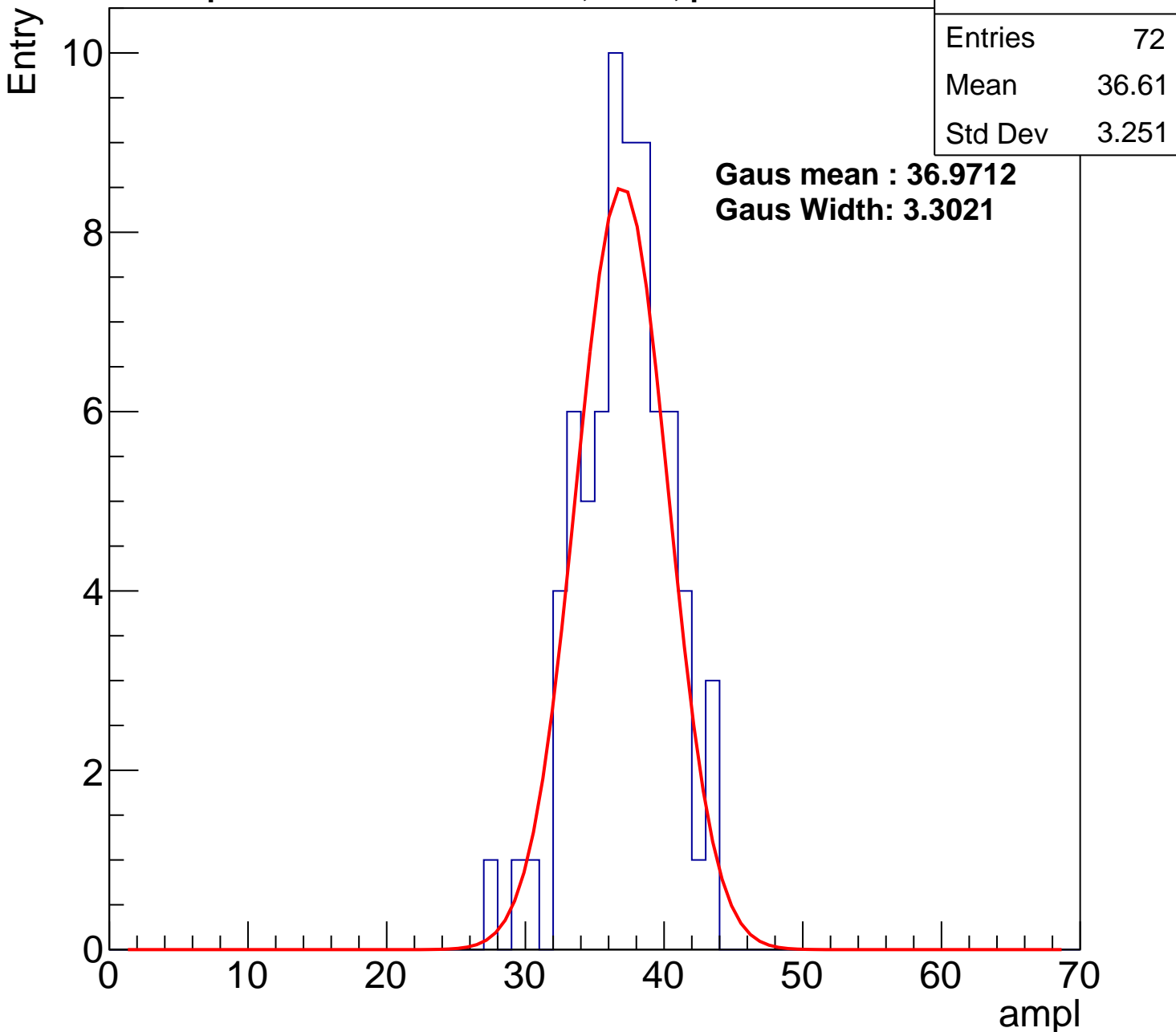
40

50

60

70

ampl



# B1L000S, U24-ch28, adc2

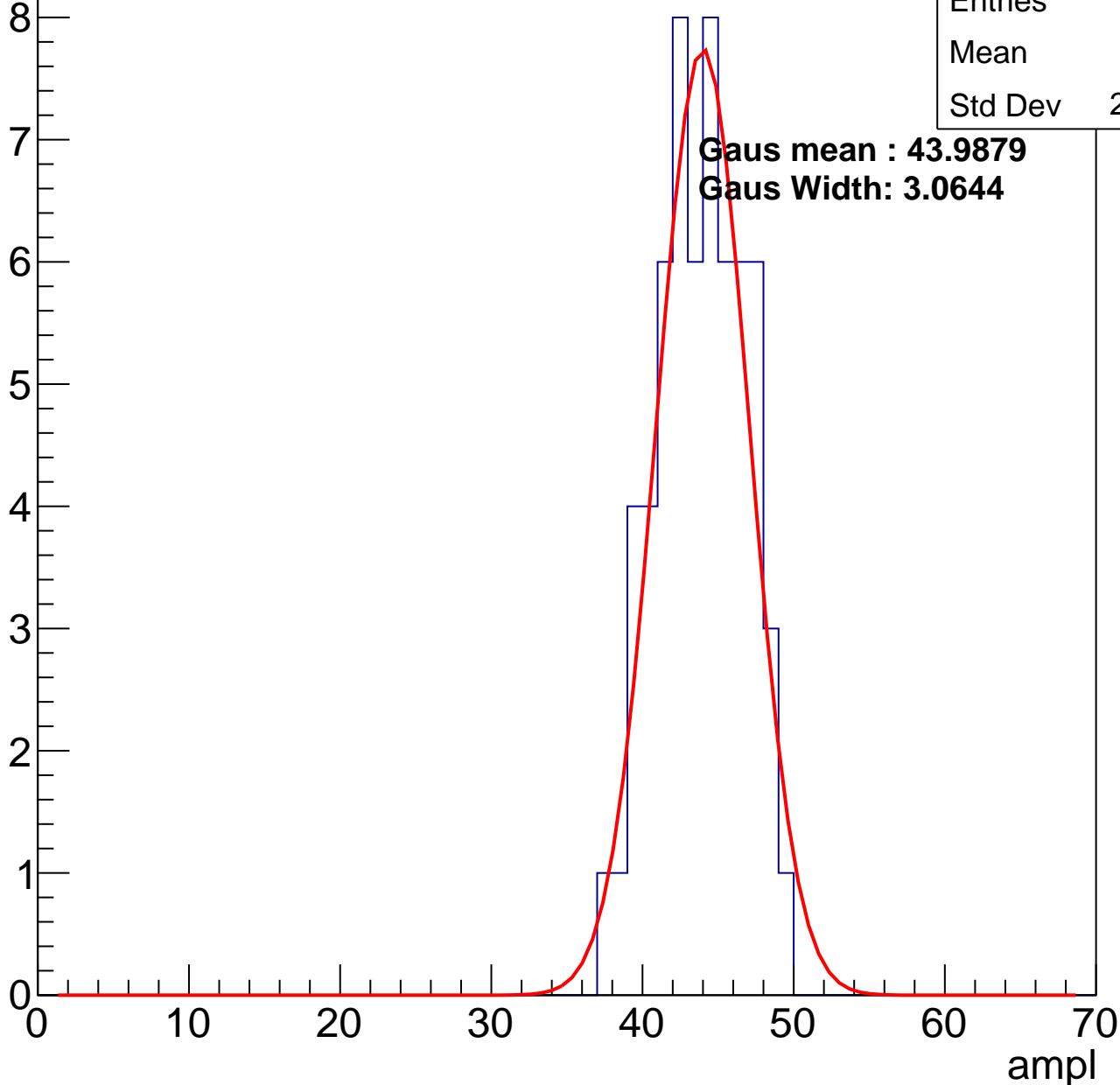
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	43.4
Std Dev	2.818

**Gaus mean : 43.9879**

**Gaus Width: 3.0644**

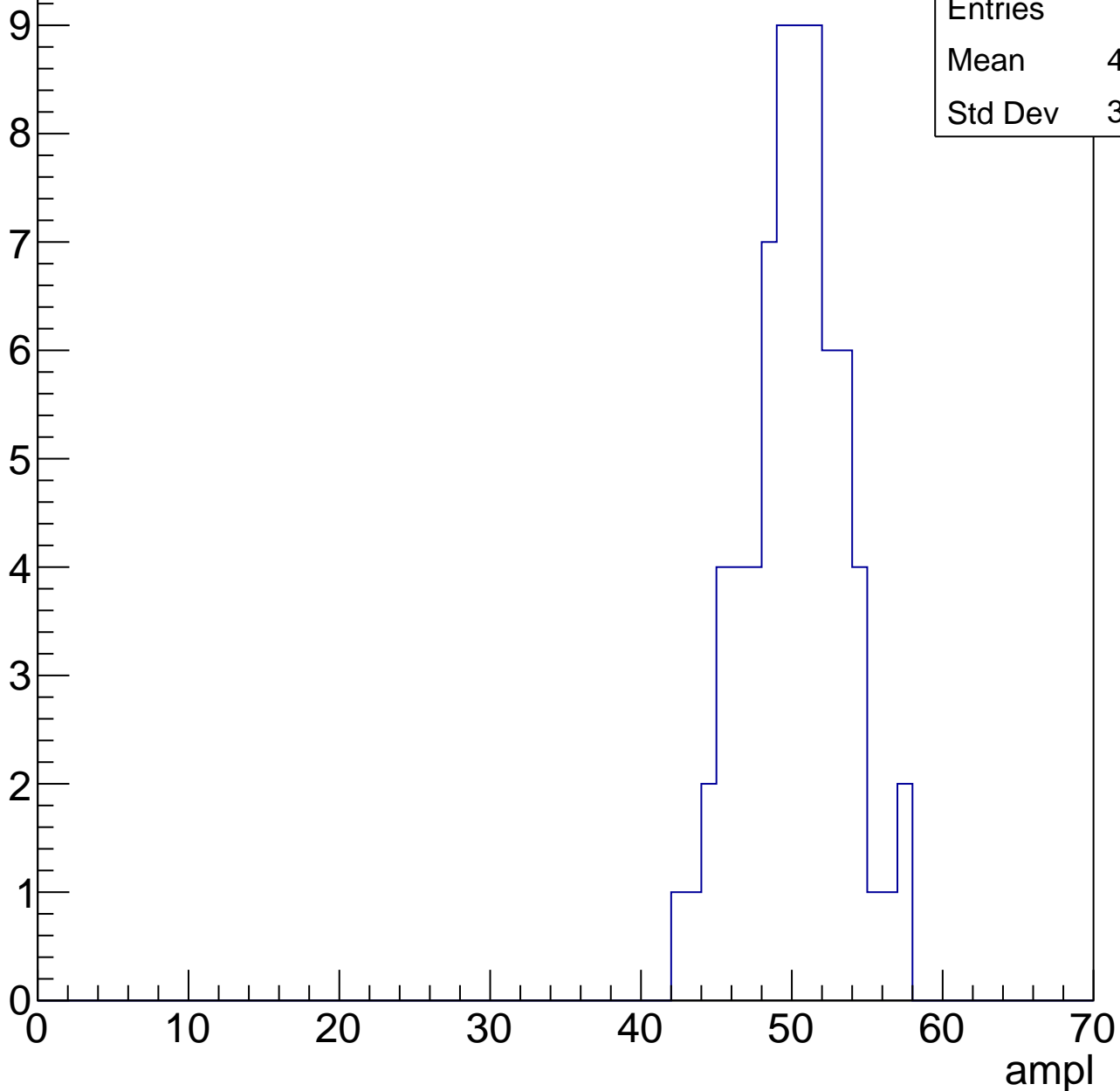


# B1L000S, U24-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

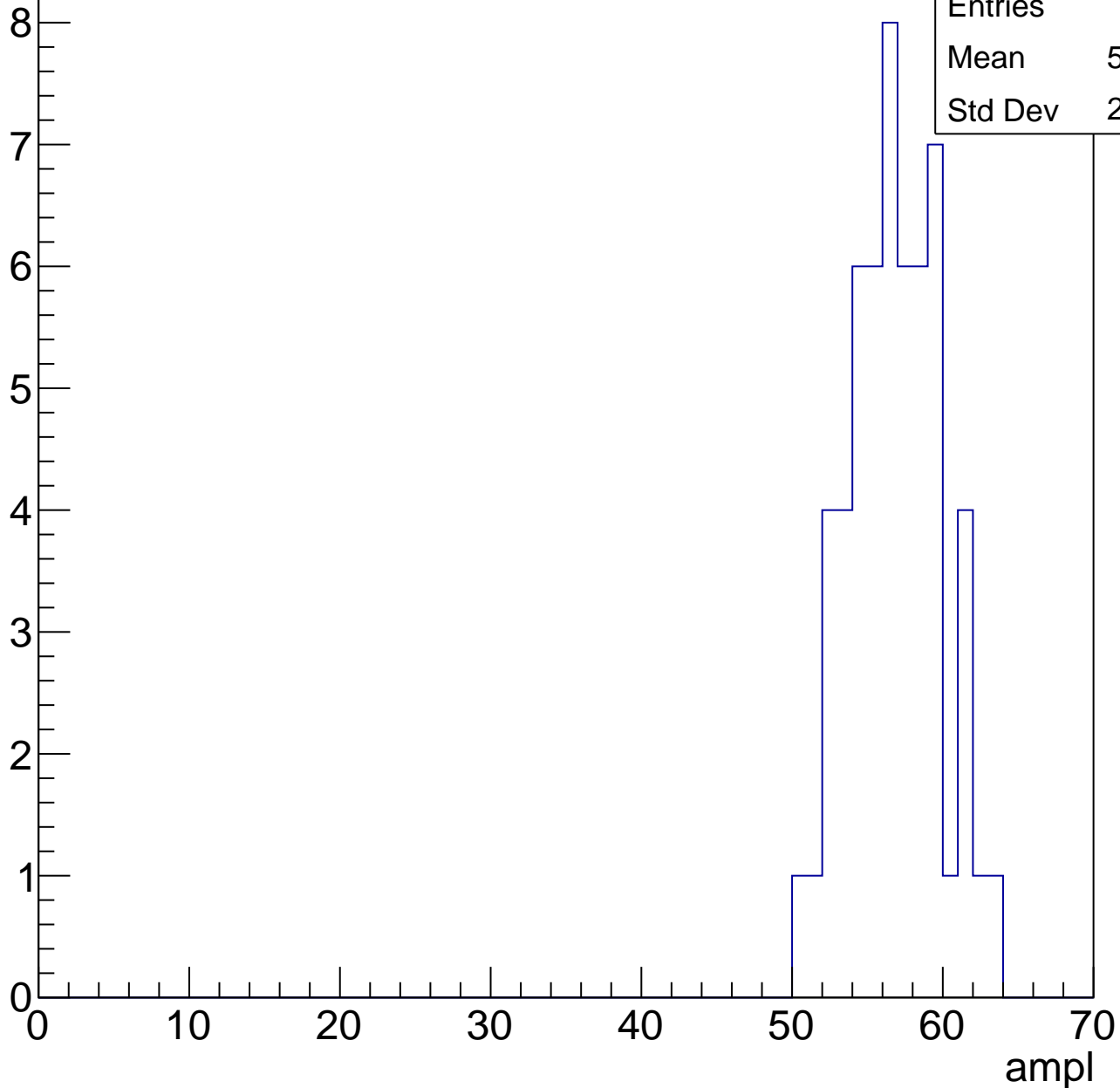
Entries	70
Mean	49.74
Std Dev	3.224



# B1L000S, U24-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

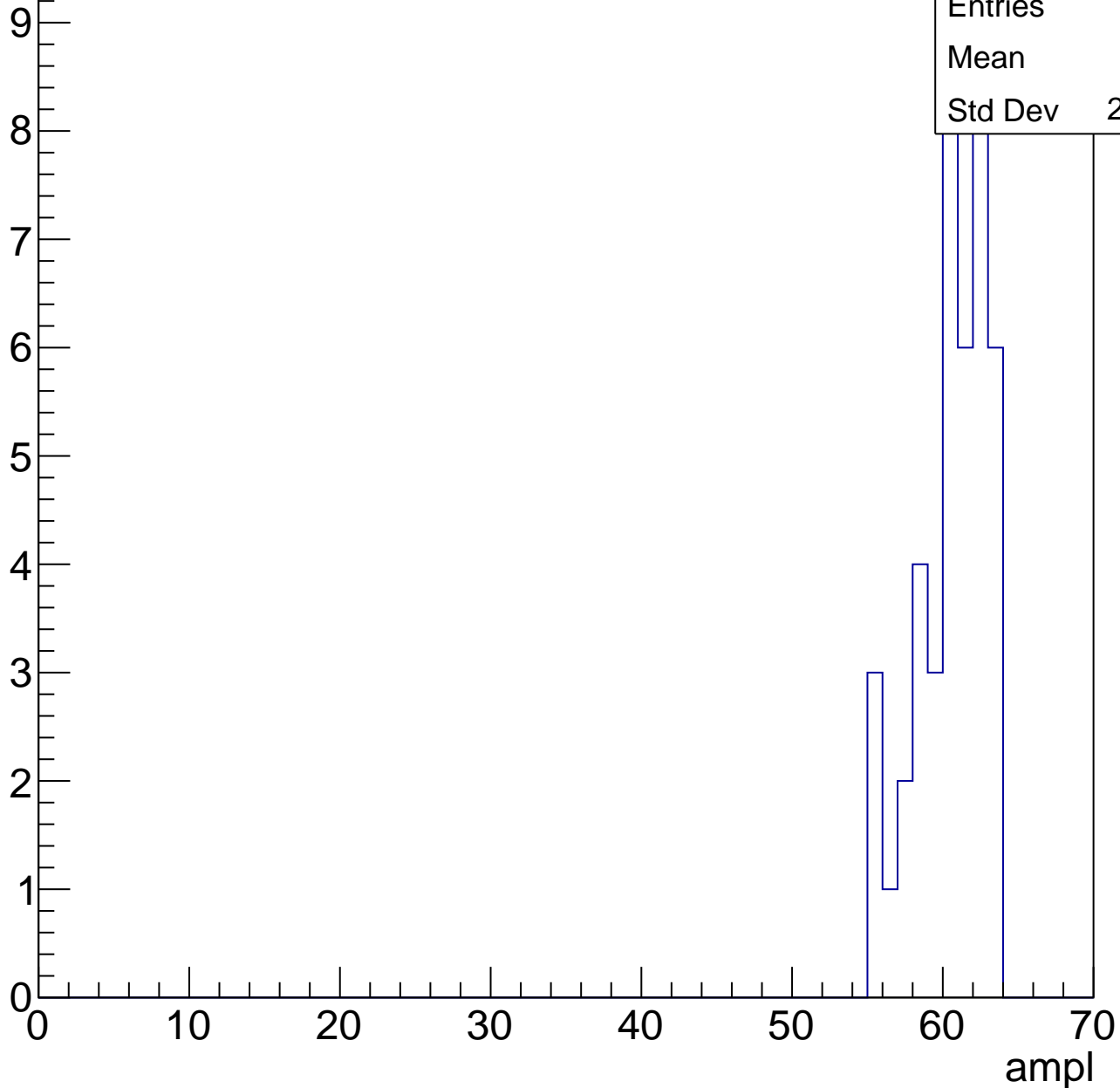
Entry



# B1L000S, U24-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch29, adc0

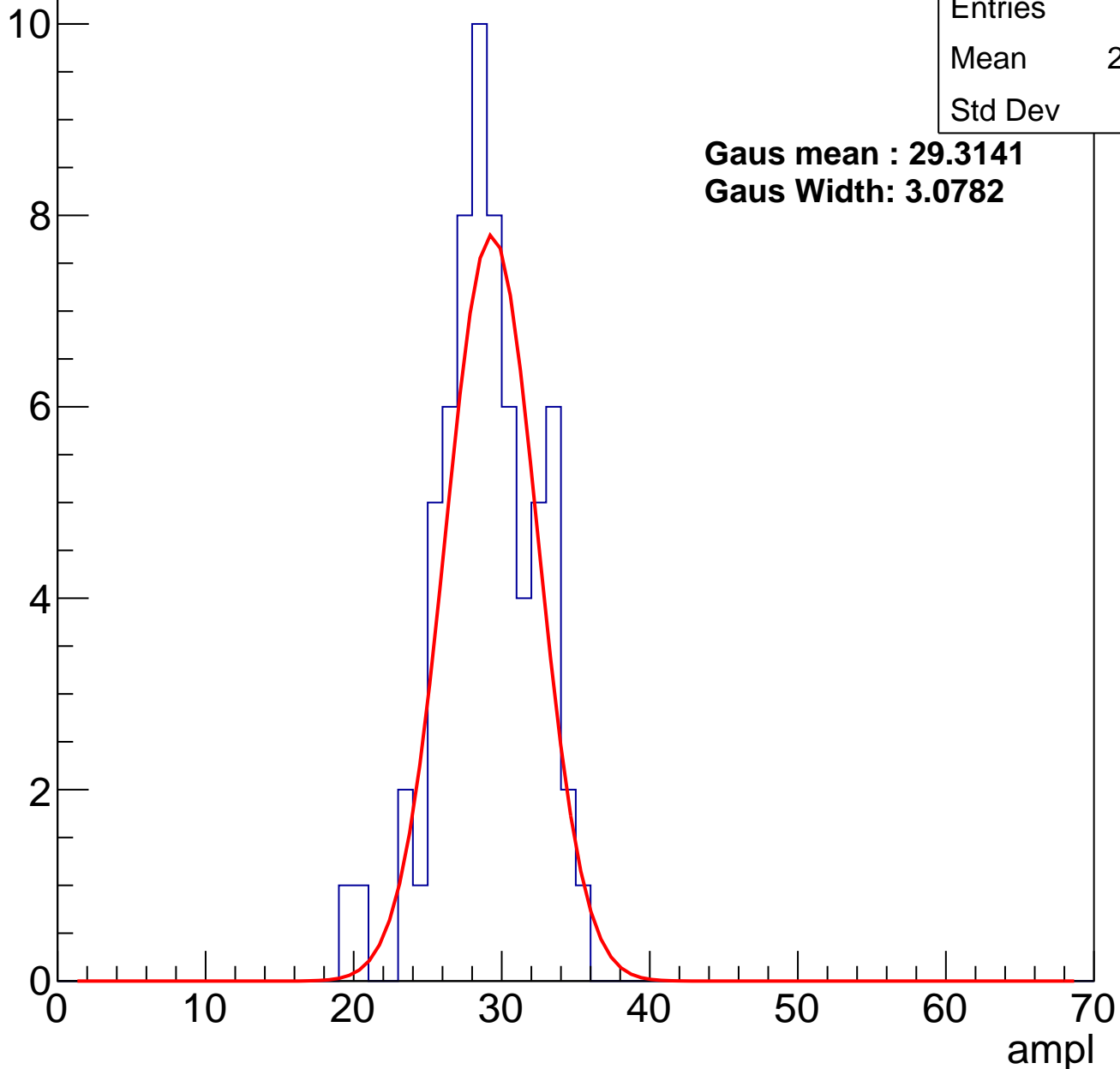
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	28.53
Std Dev	3.23

**Gaus mean : 29.3141**

**Gaus Width: 3.0782**

Entry



# B1L000S, U24-ch29, adc1

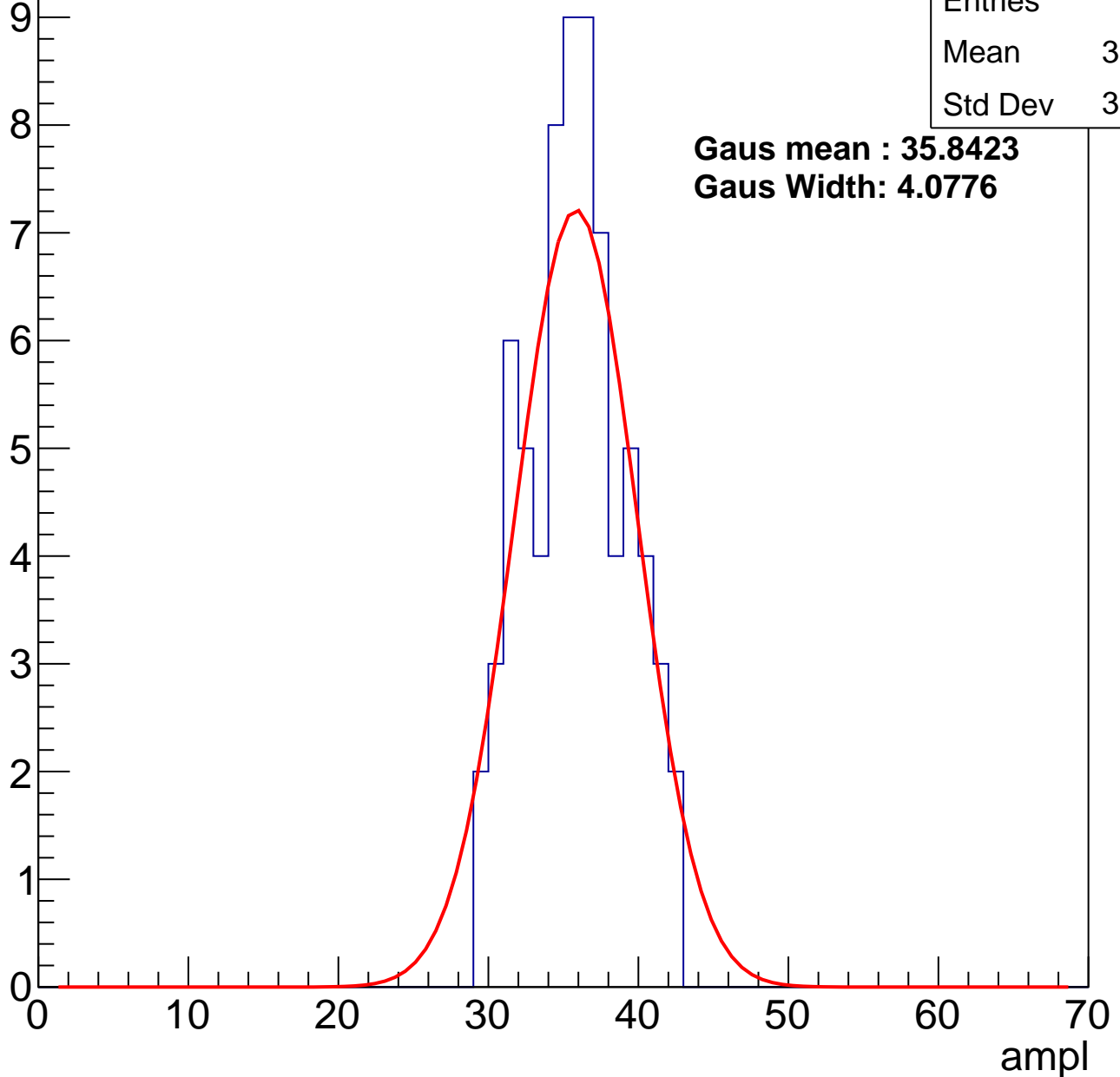
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.35
Std Dev	3.276

**Gaus mean : 35.8423**

**Gaus Width: 4.0776**



# B1L000S, U24-ch29, adc2

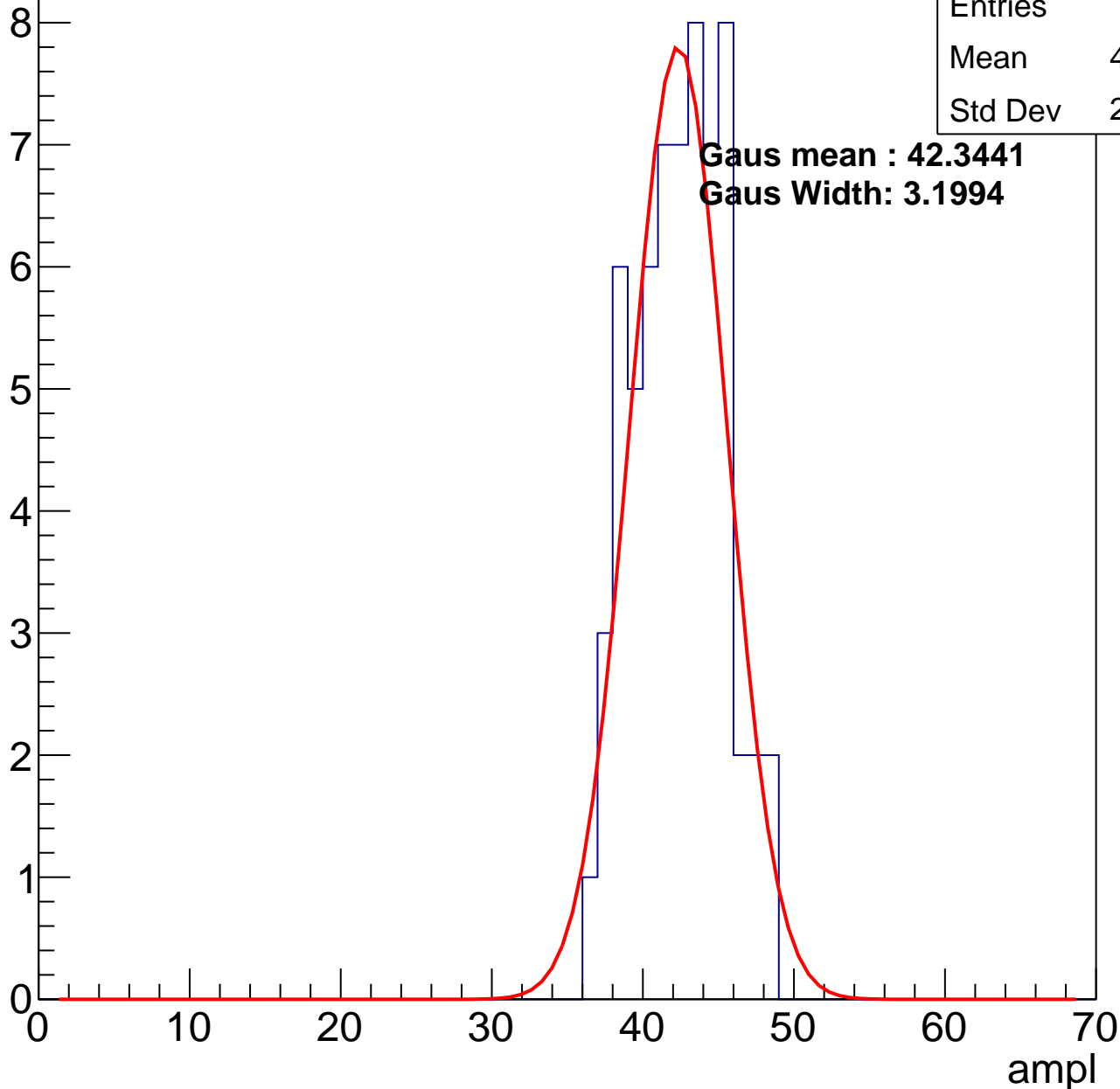
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.95
Std Dev	2.918

**Gaus mean : 42.3441**

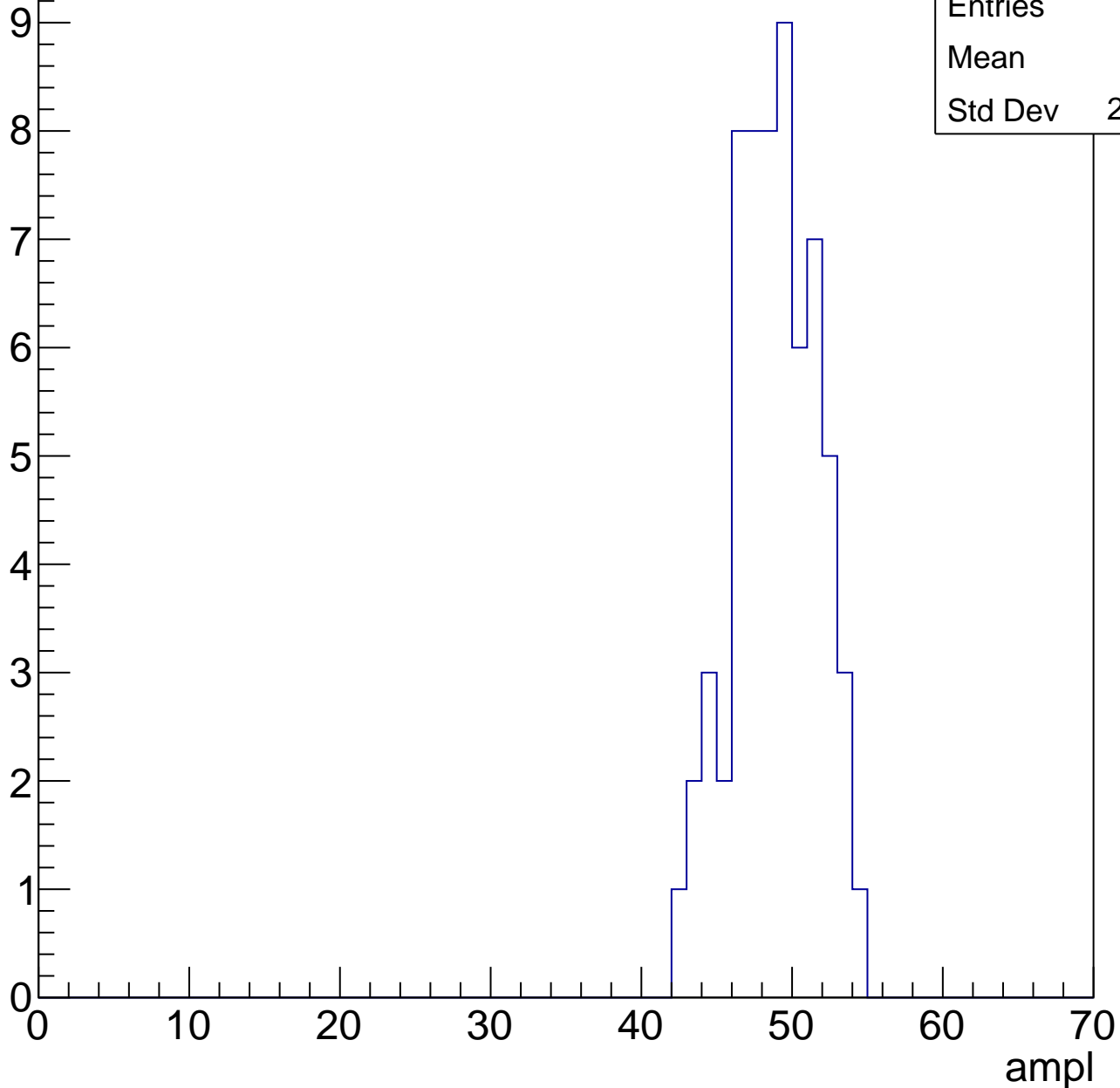
**Gaus Width: 3.1994**



# B1L000S, U24-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

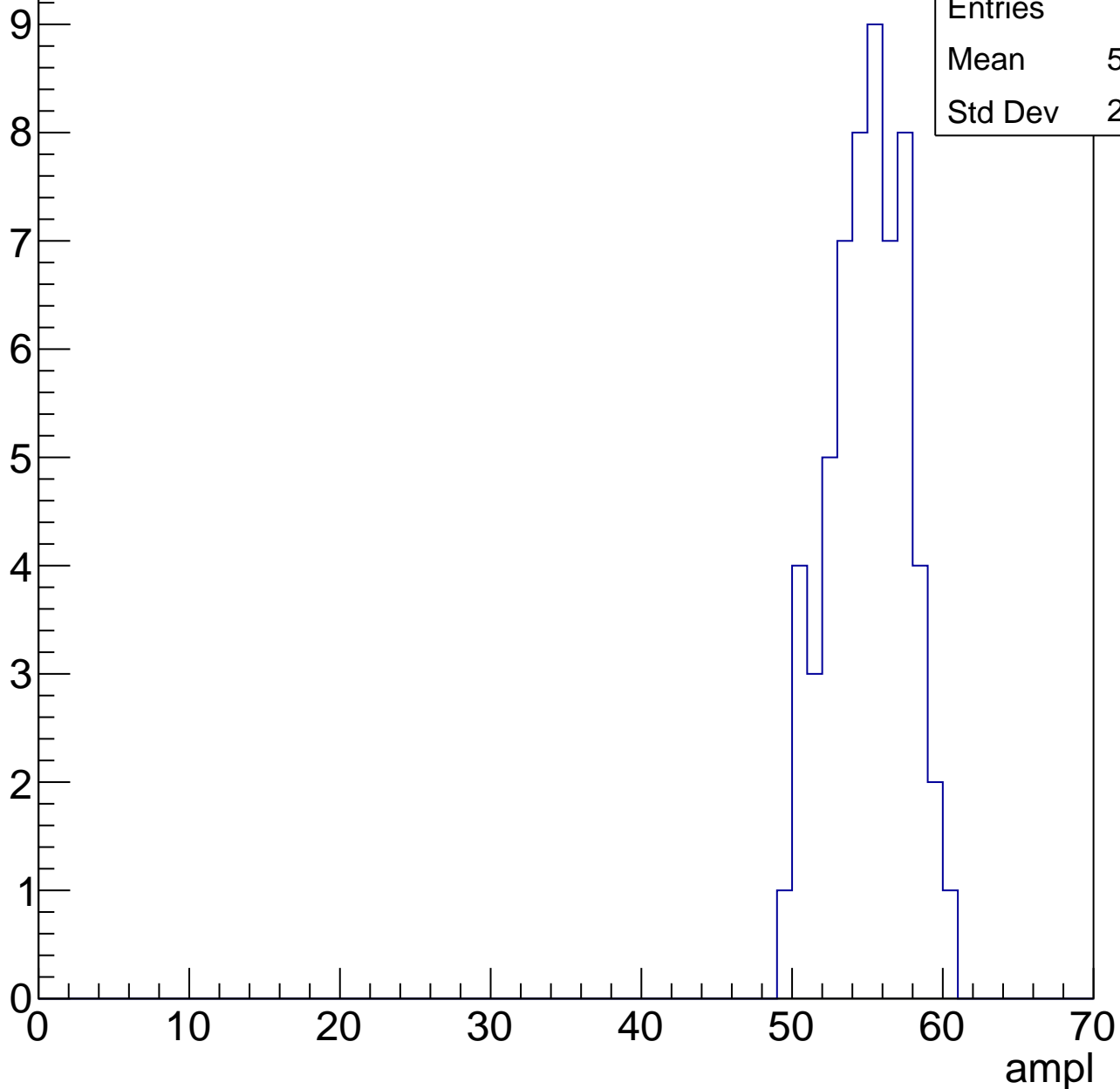
Entry



# B1L000S, U24-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

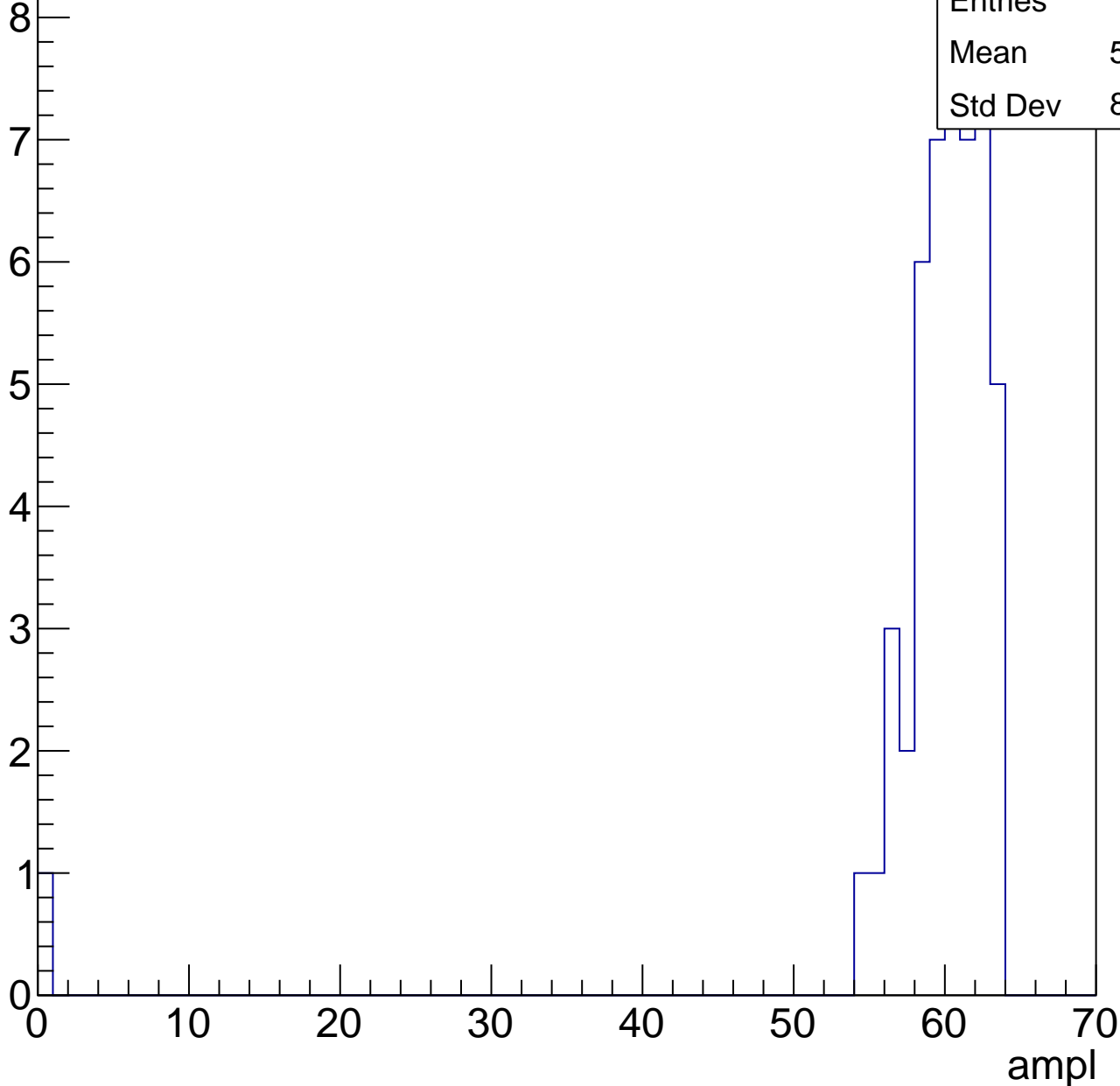


# B1L000S, U24-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

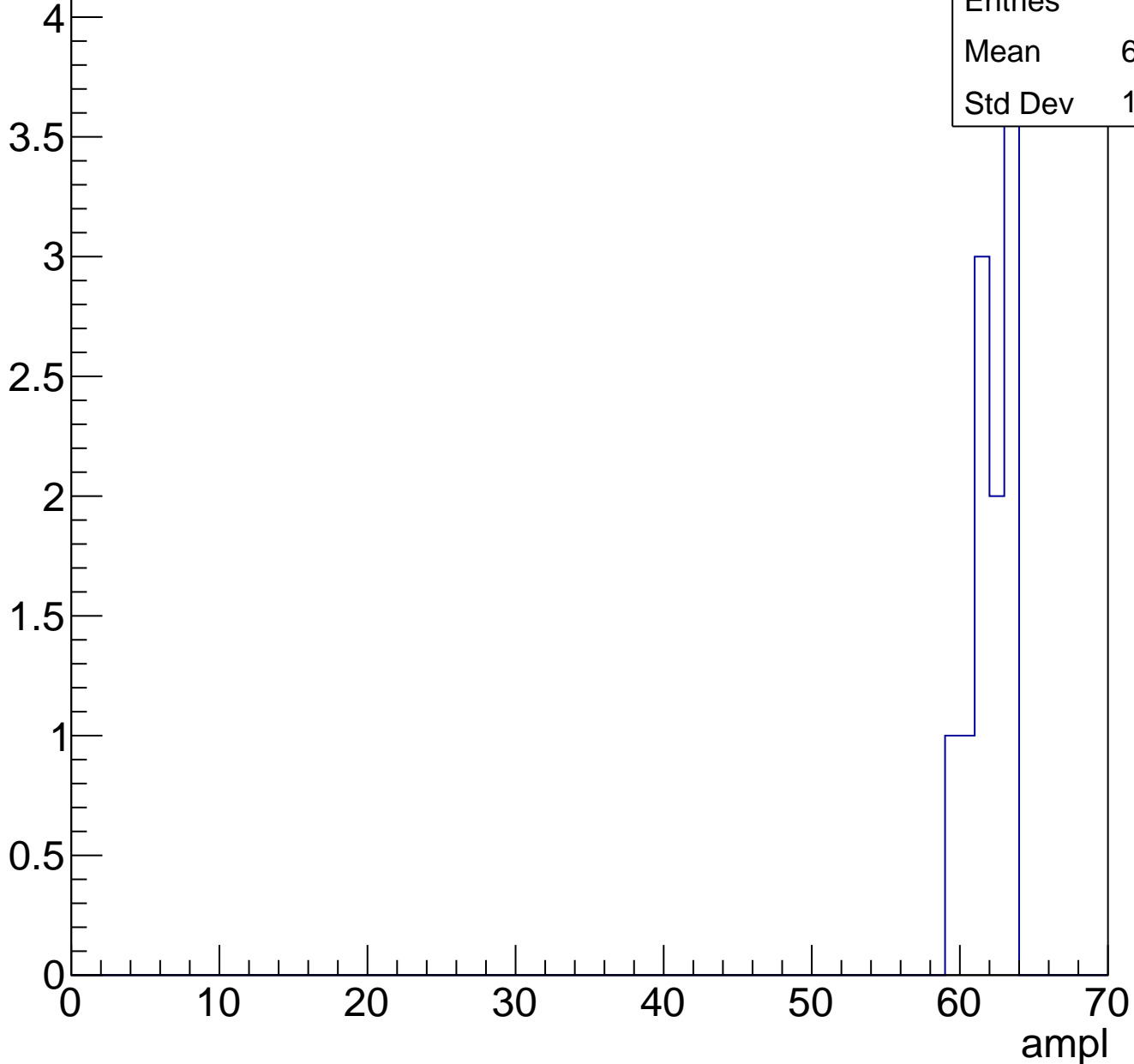
Entries	49
Mean	58.57
Std Dev	8.739



# B1L000S, U24-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch30, adc0

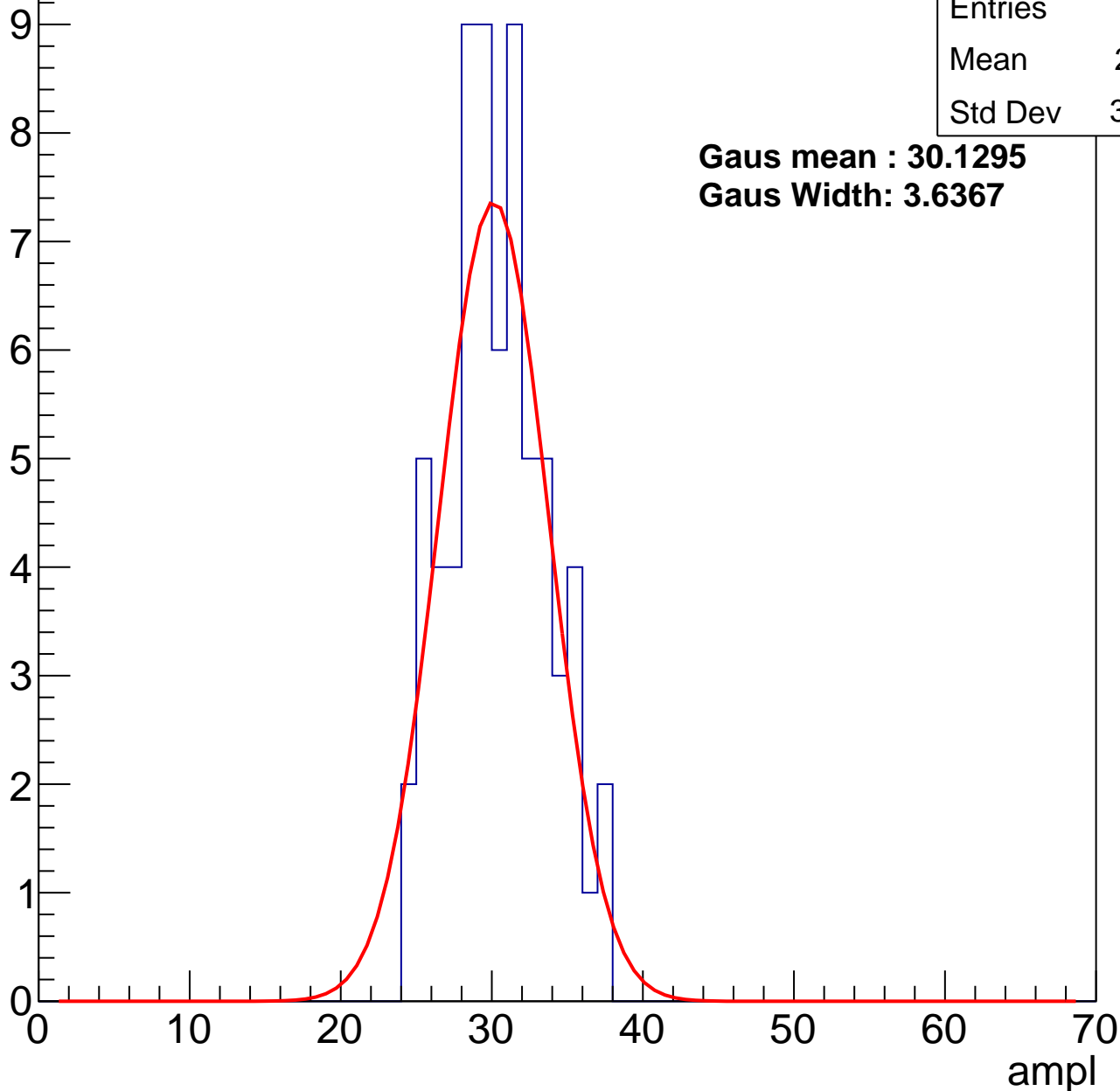
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.91
Std Dev	3.203

**Gaus mean : 30.1295**

**Gaus Width: 3.6367**



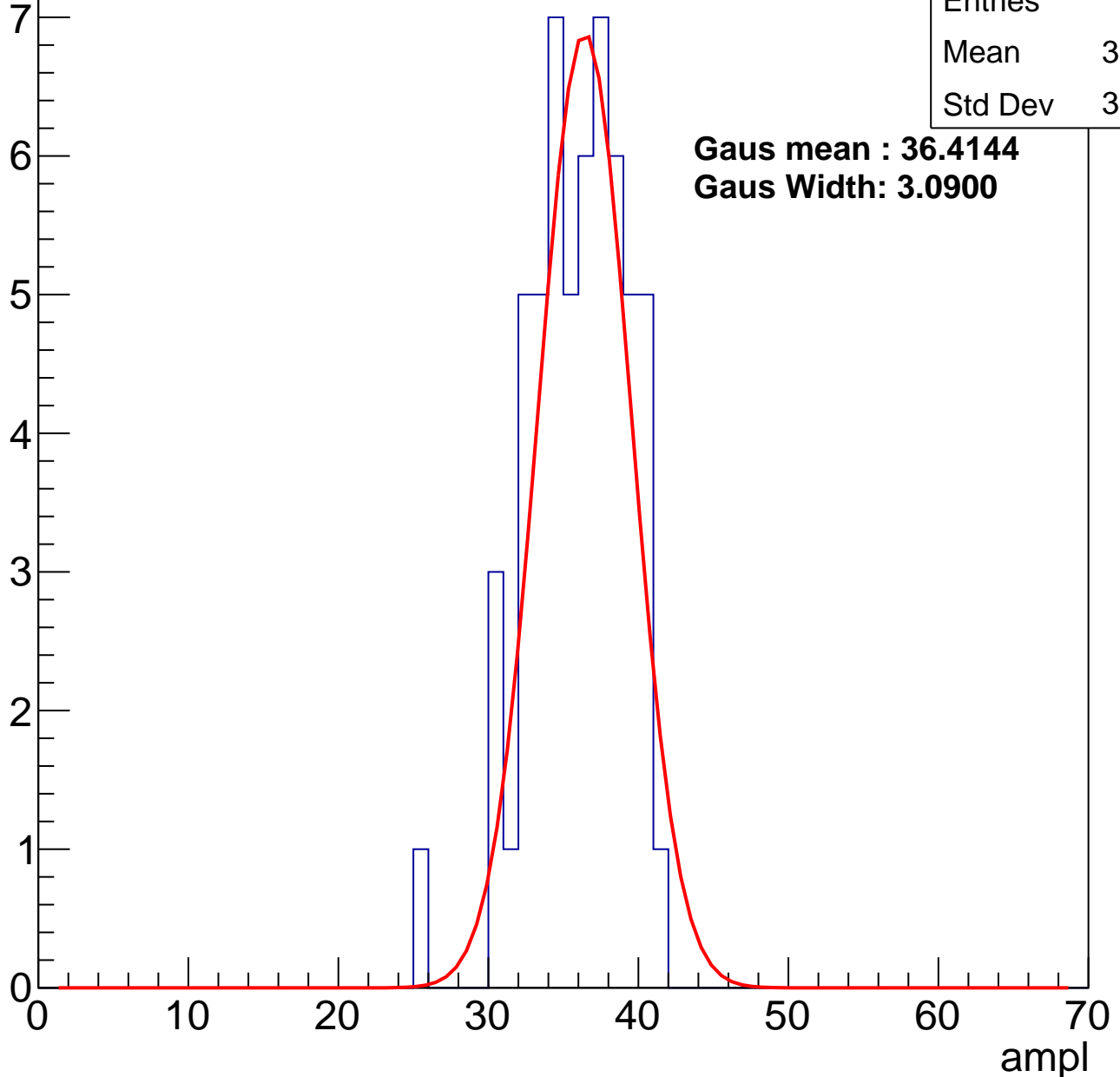
# B1L000S, U24-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.49
Std Dev	3.185

**Gaus mean : 36.4144**  
**Gaus Width: 3.0900**



# B1L000S, U24-ch30, adc2

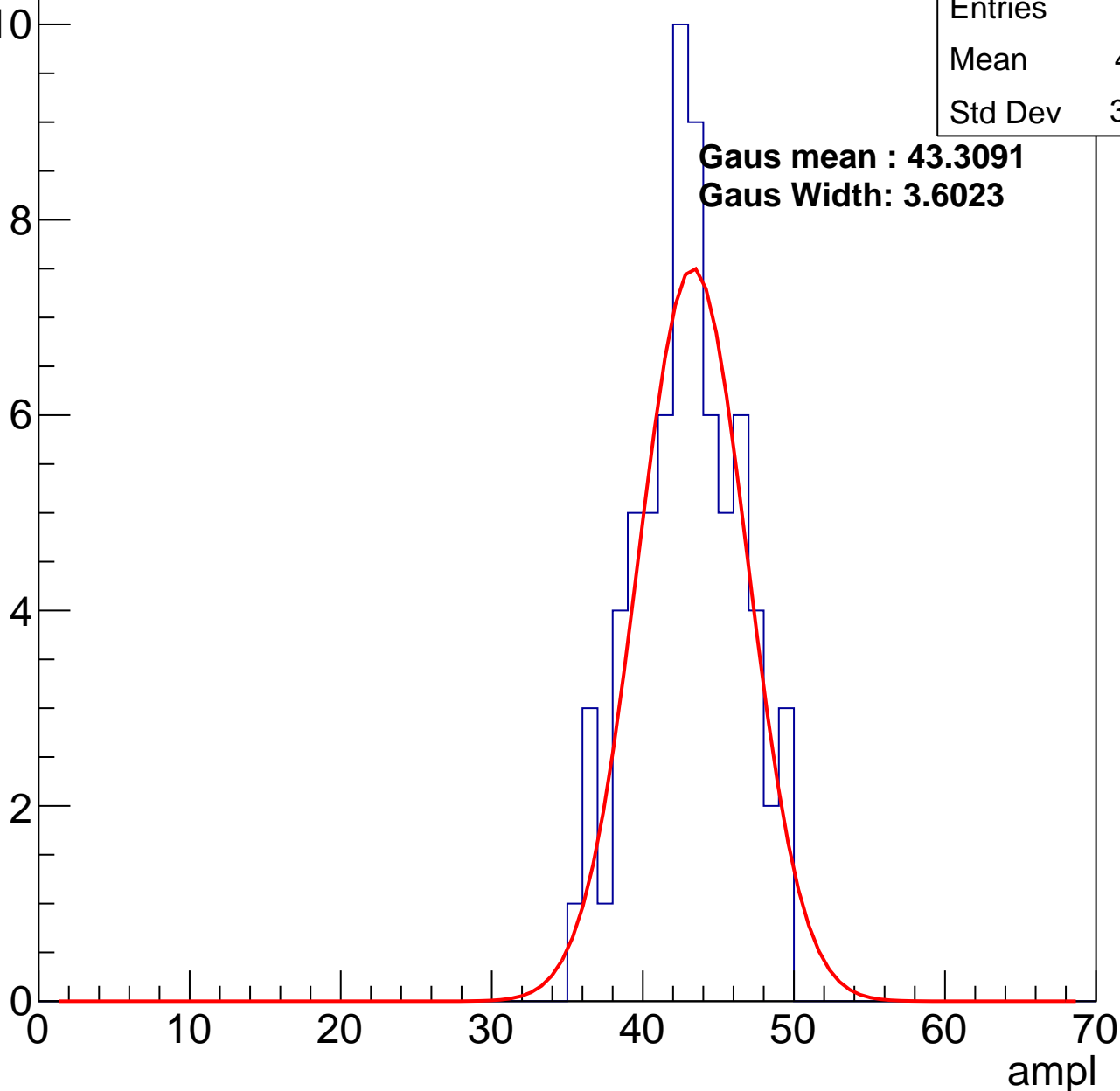
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.51
Std Dev	3.363

**Gaus mean : 43.3091**

**Gaus Width: 3.6023**

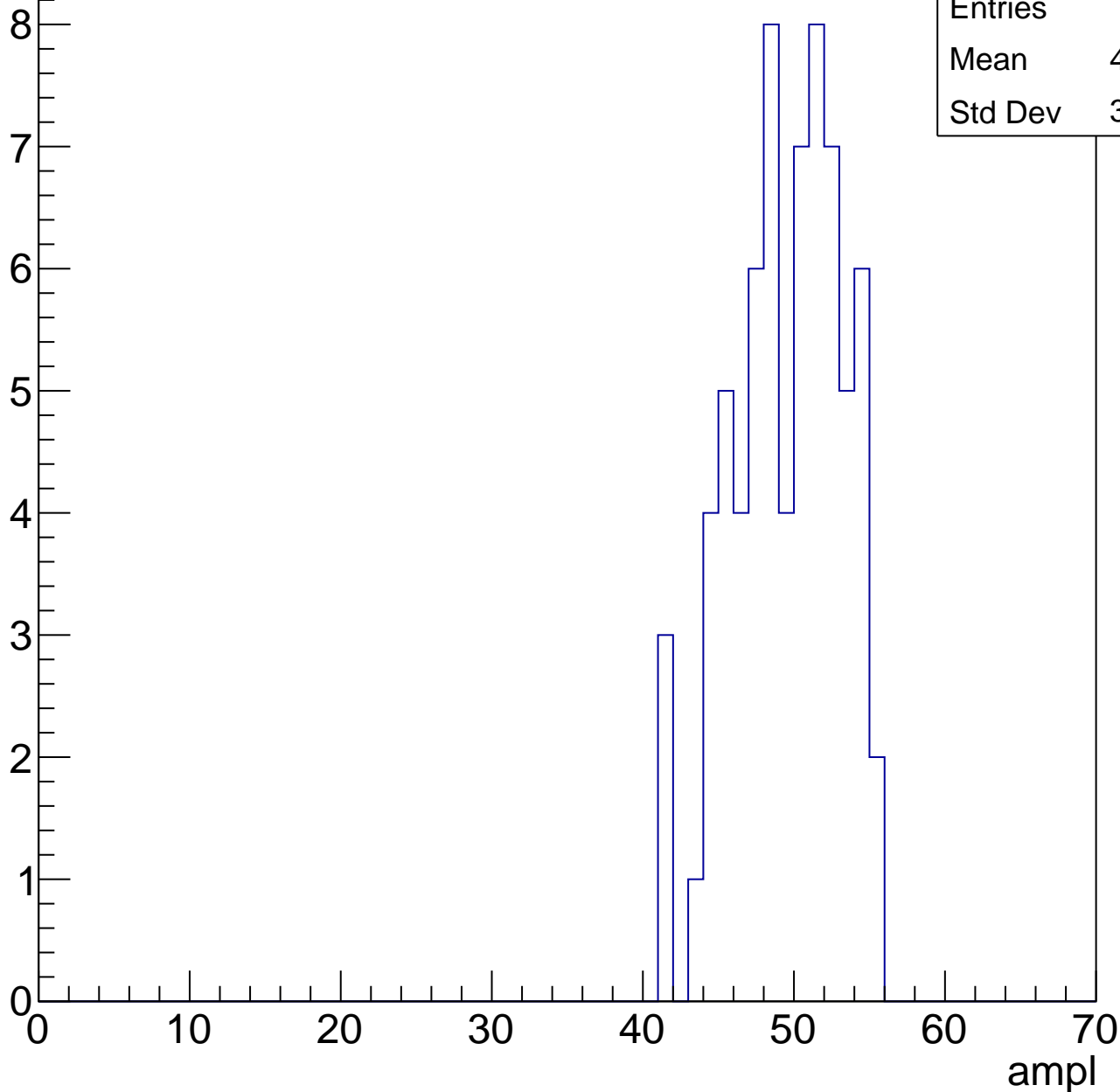


# B1L000S, U24-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	49.06
Std Dev	3.545

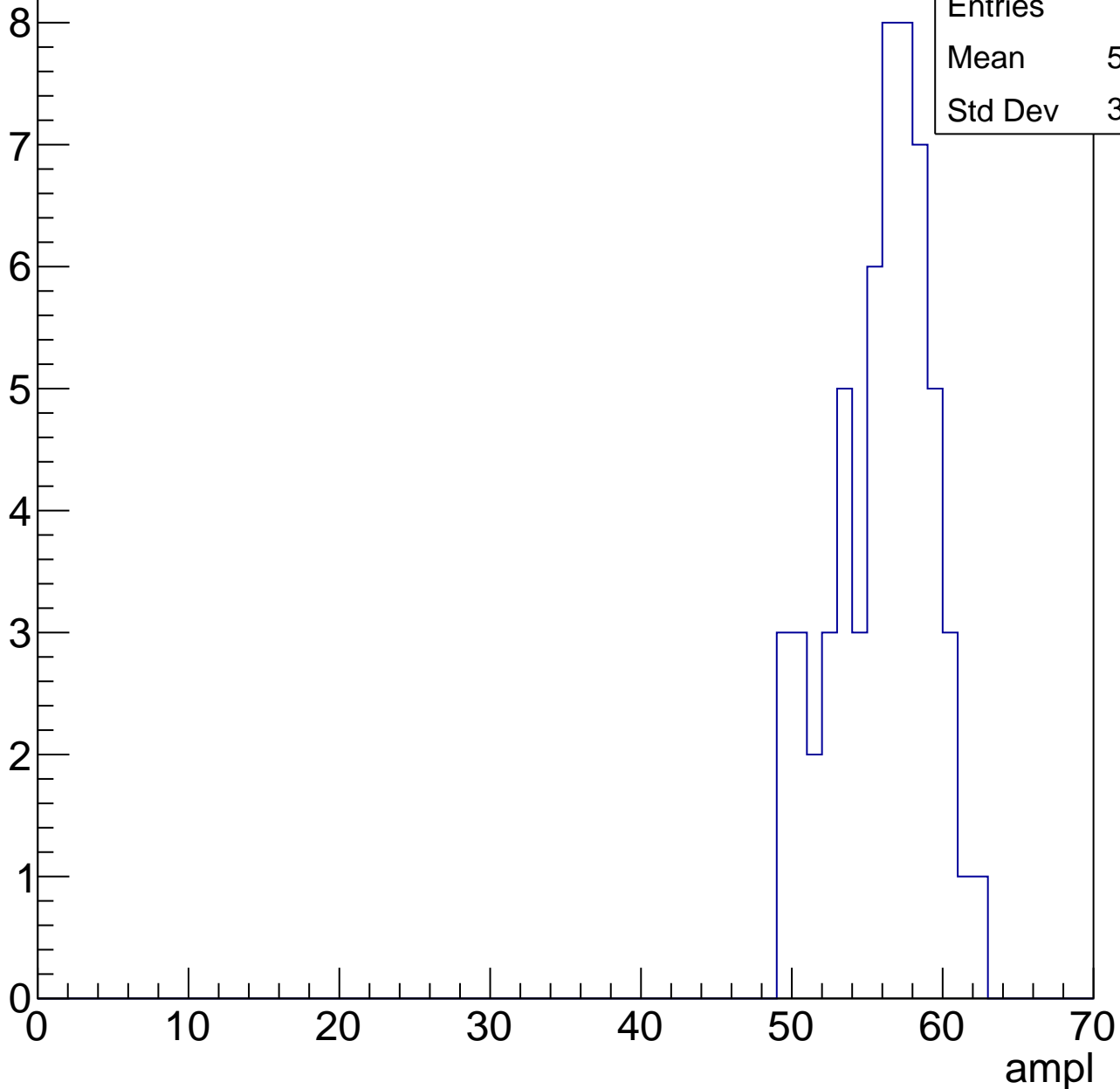


# B1L000S, U24-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

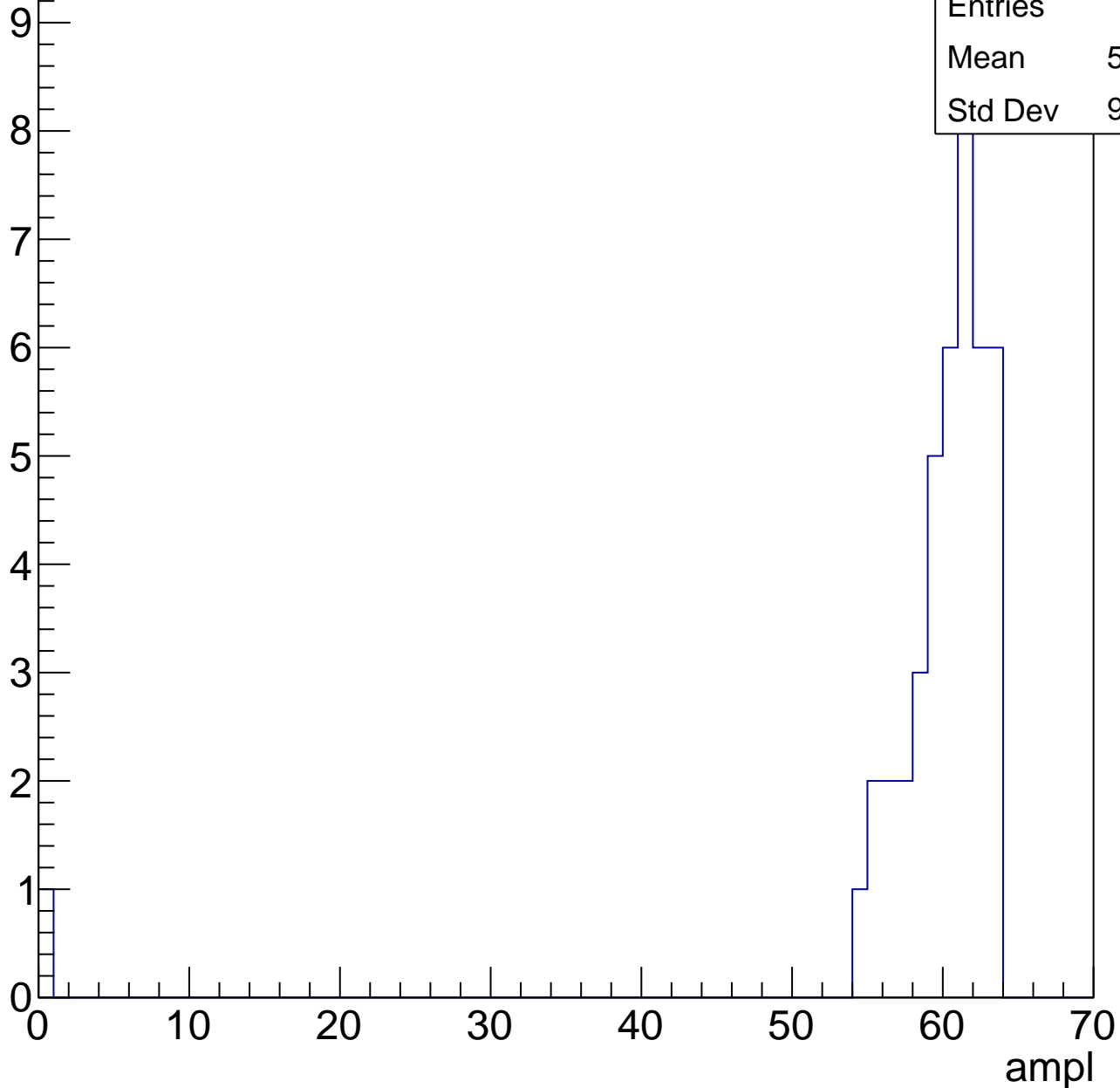
Entries	58
Mean	55.52
Std Dev	3.196



# B1L000S, U24-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

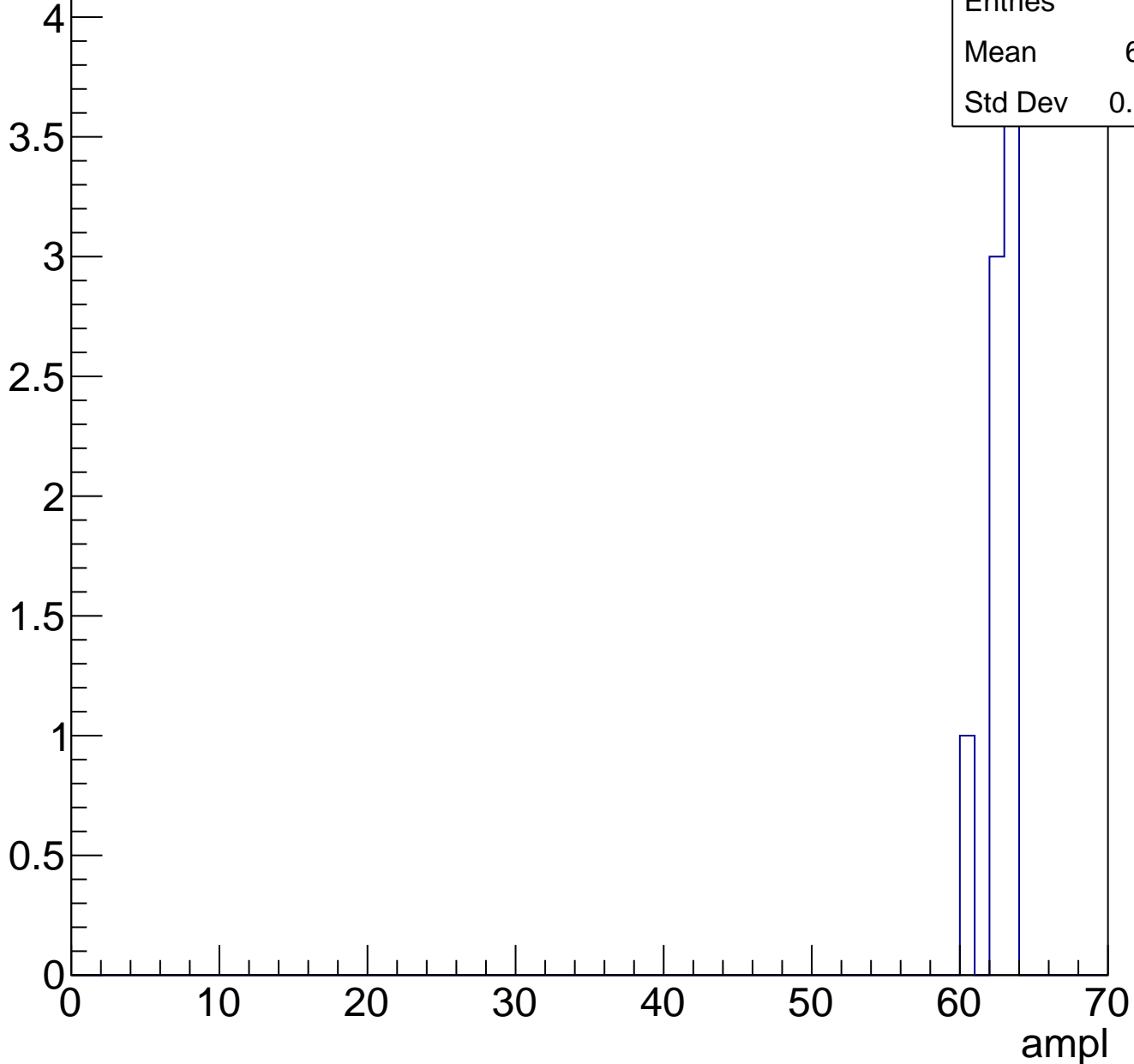
Entry



# B1L000S, U24-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

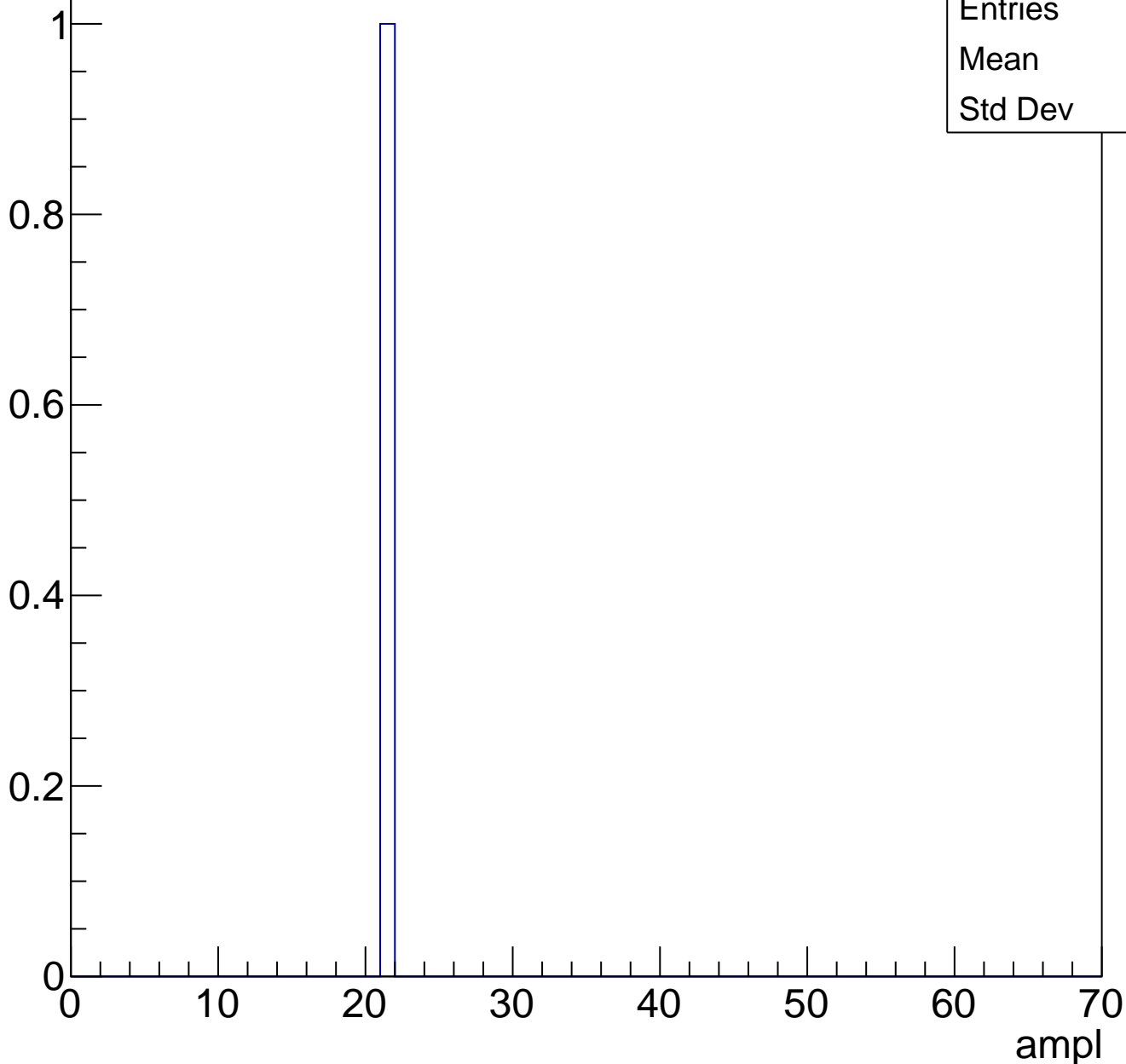




# B1L000S, U24-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch31, adc0

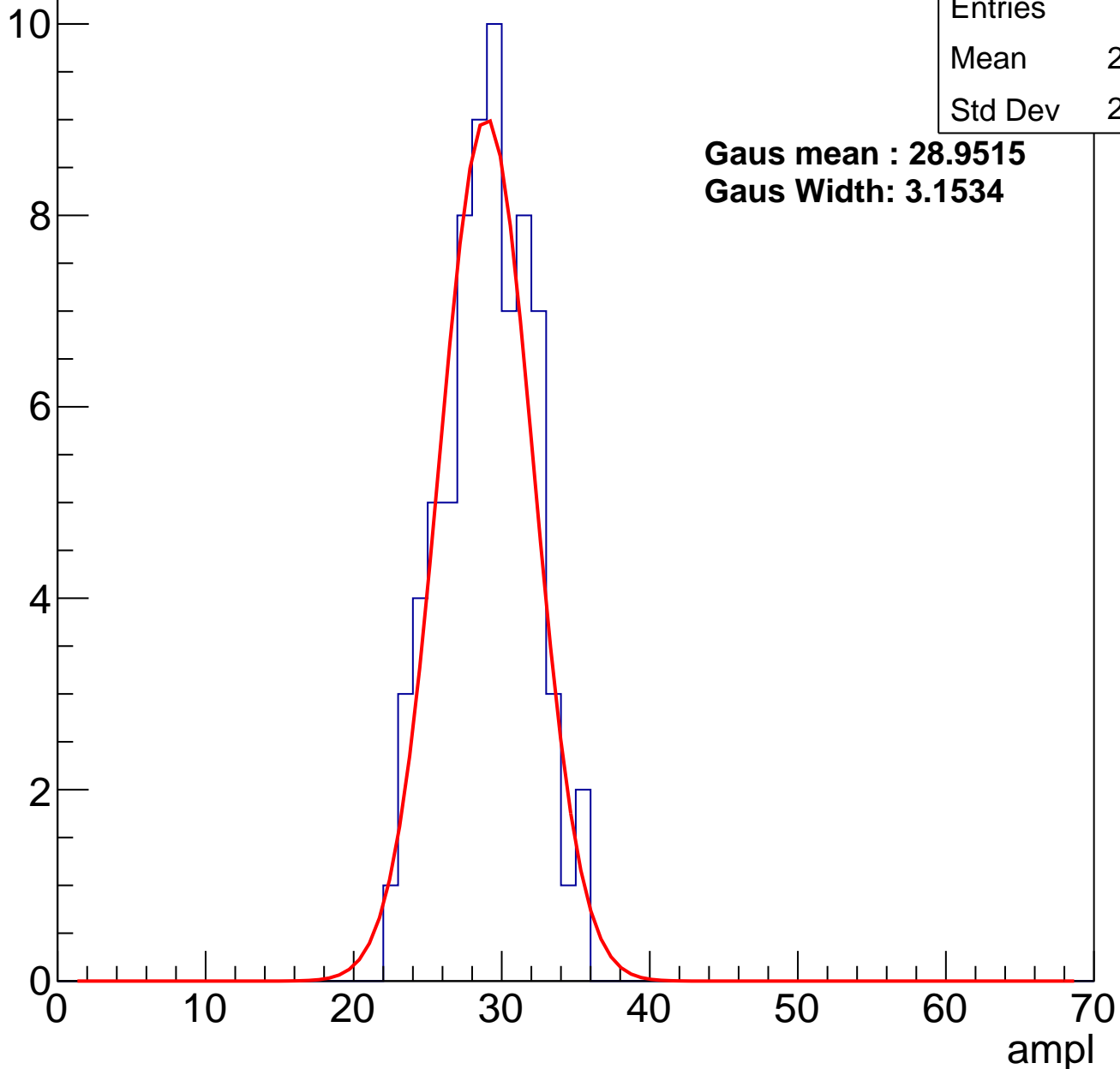
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	28.56
Std Dev	2.998

**Gaus mean : 28.9515**

**Gaus Width: 3.1534**

Entry



# B1L000S, U24-ch31, adc1

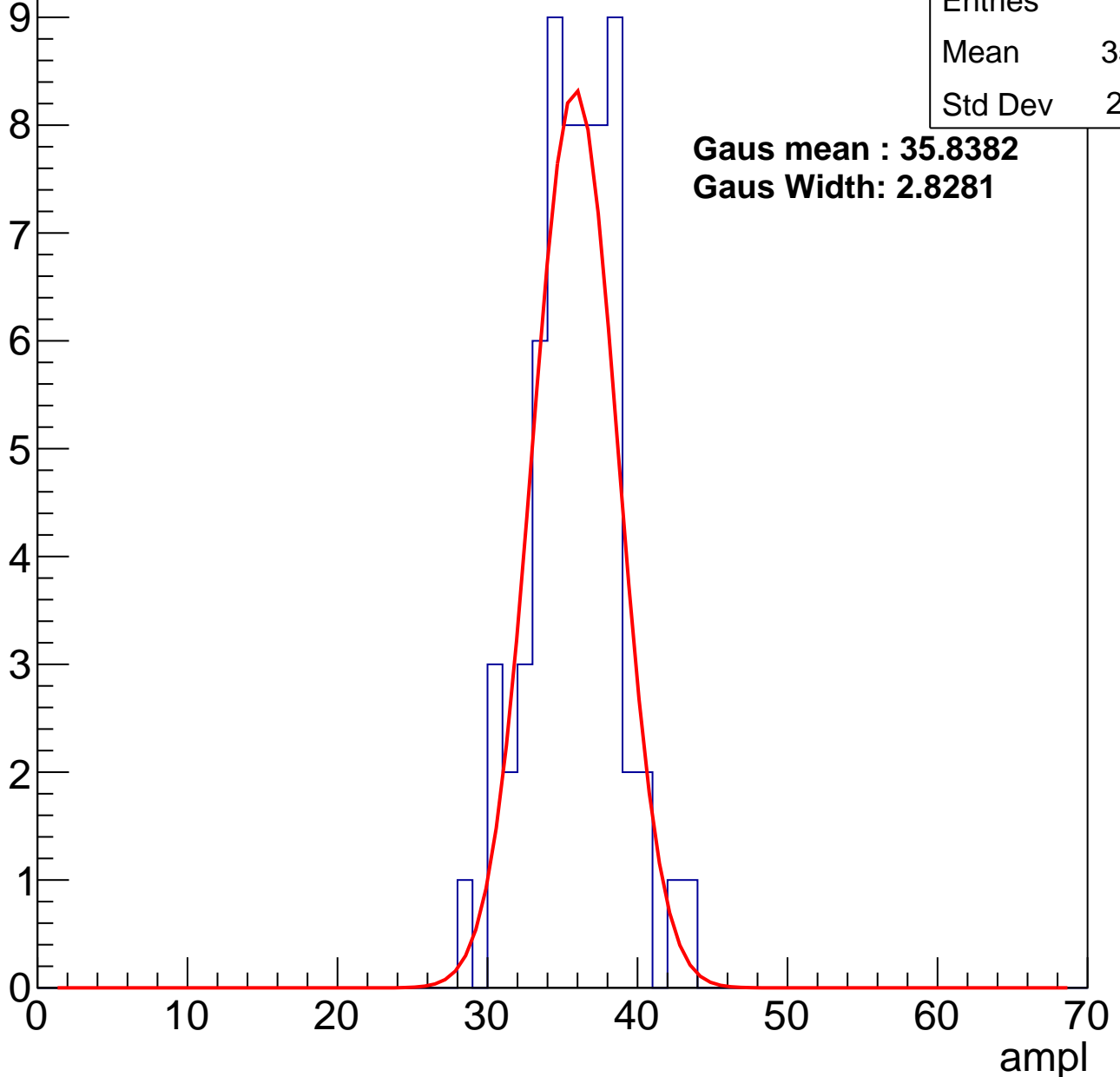
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.38
Std Dev	2.881

**Gaus mean : 35.8382**

**Gaus Width: 2.8281**



# B1L000S, U24-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	42.29
Std Dev	3.279

**Gaus mean : 43.1212**

**Gaus Width: 3.1454**

10

8

6

4

2

0

0

10

20

30

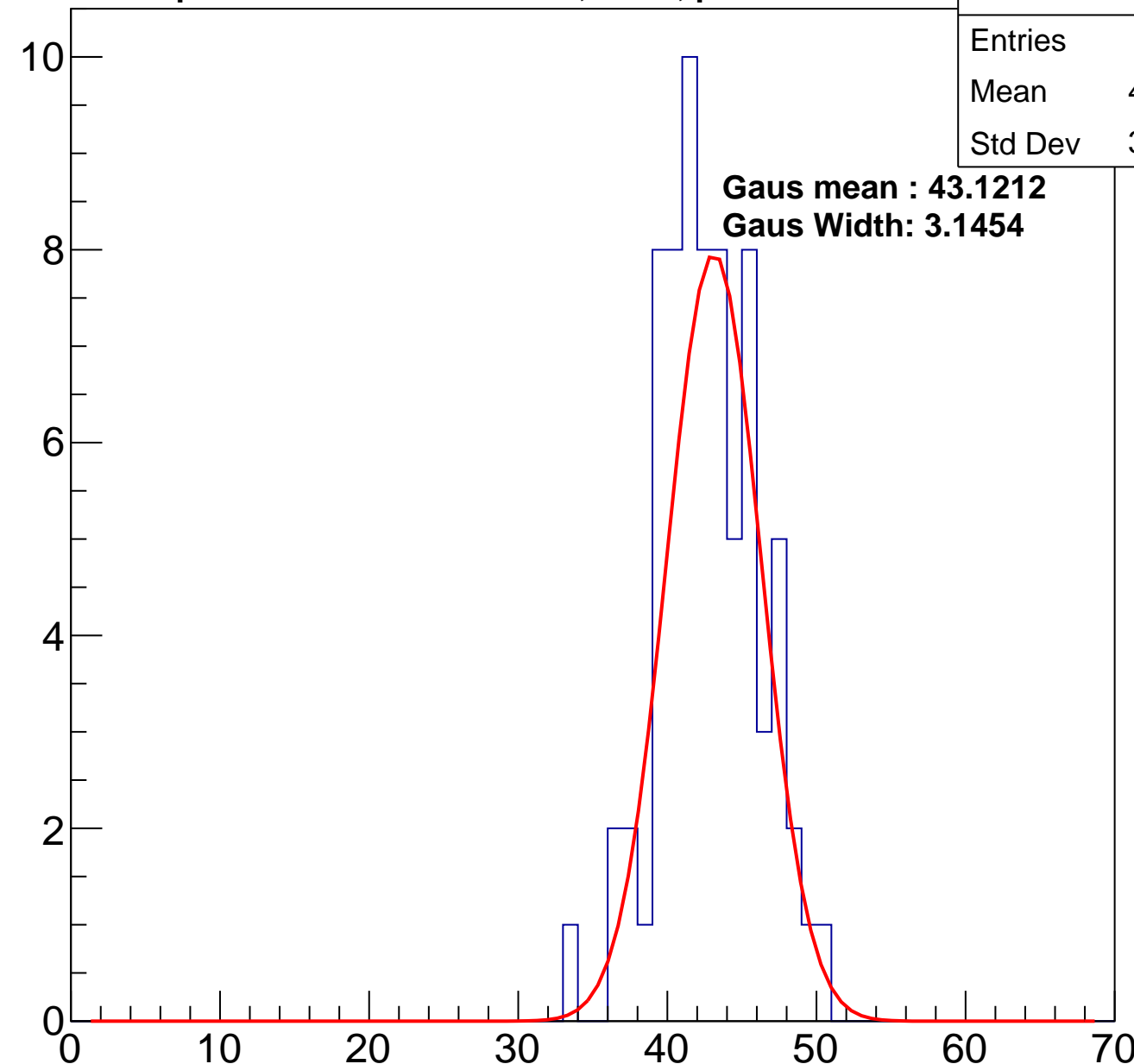
40

50

60

70

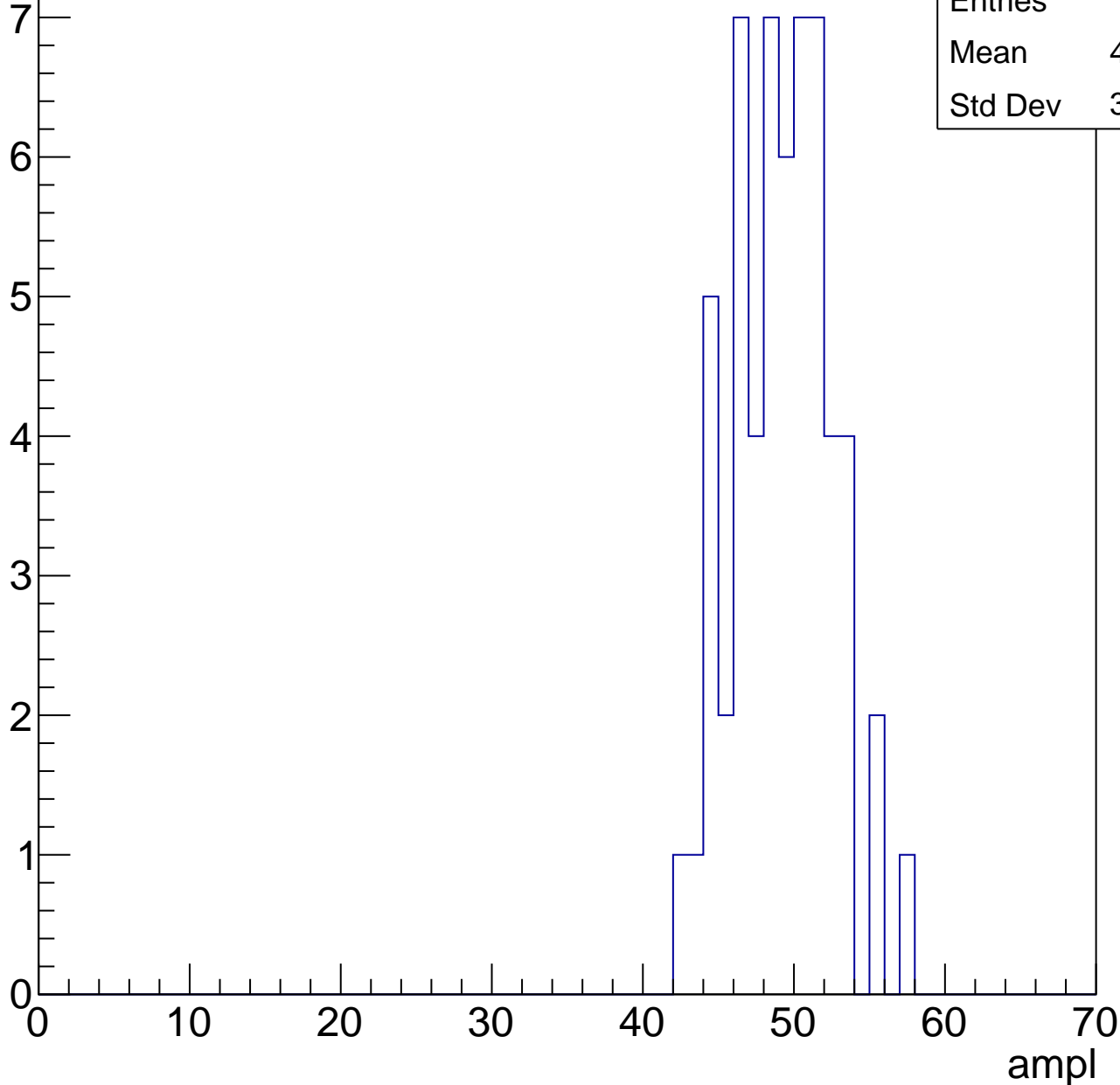
ampl



# B1L000S, U24-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



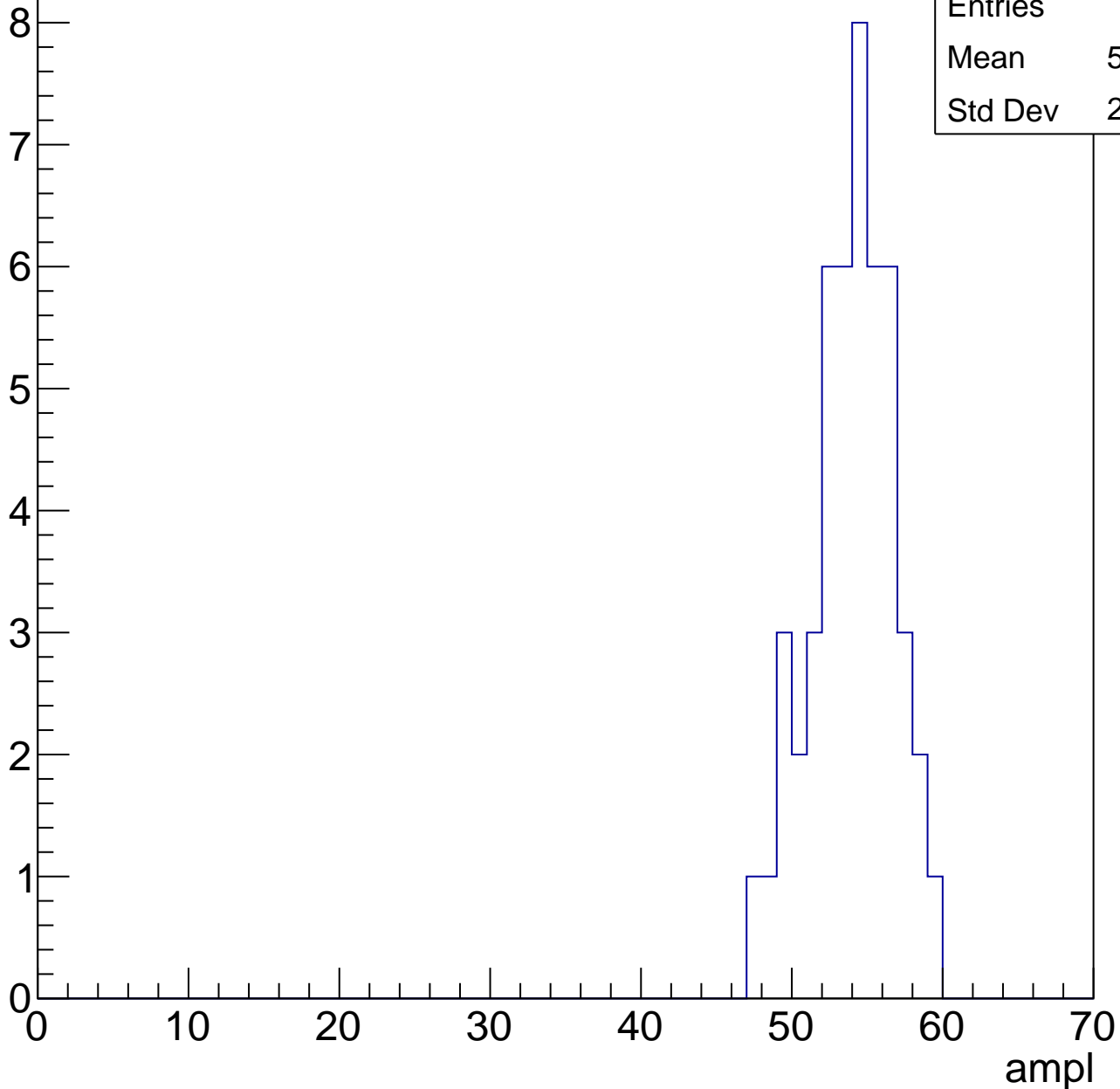
Entries	58
Mean	48.78
Std Dev	3.206

# B1L000S, U24-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	53.52
Std Dev	2.708



# B1L000S, U24-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

71

Mean

58.83

Std Dev

7.43

Entries	71
Mean	58.83
Std Dev	7.43

# B1L000S, U24-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

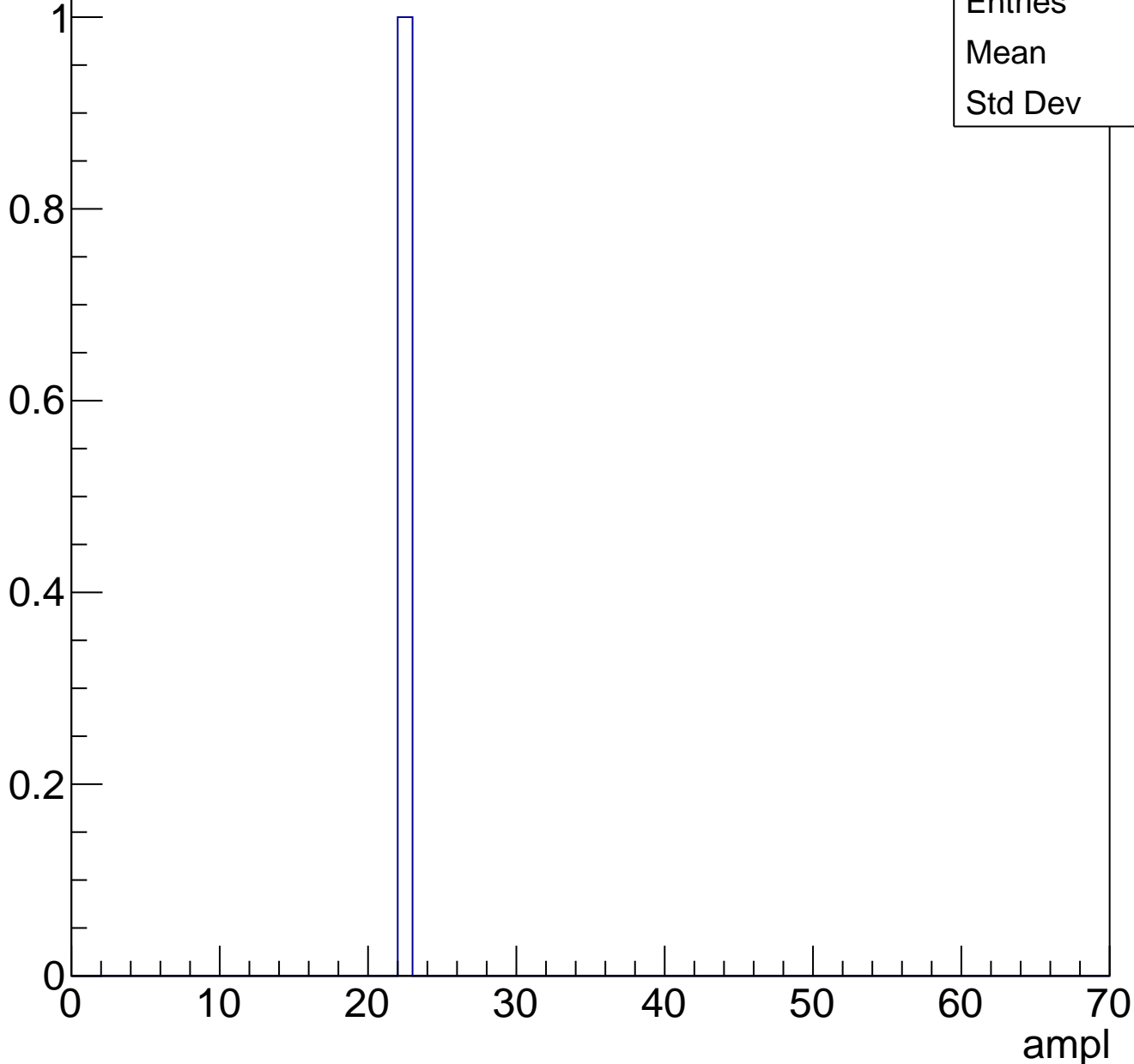




# B1L000S, U24-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U24-ch32, adc0

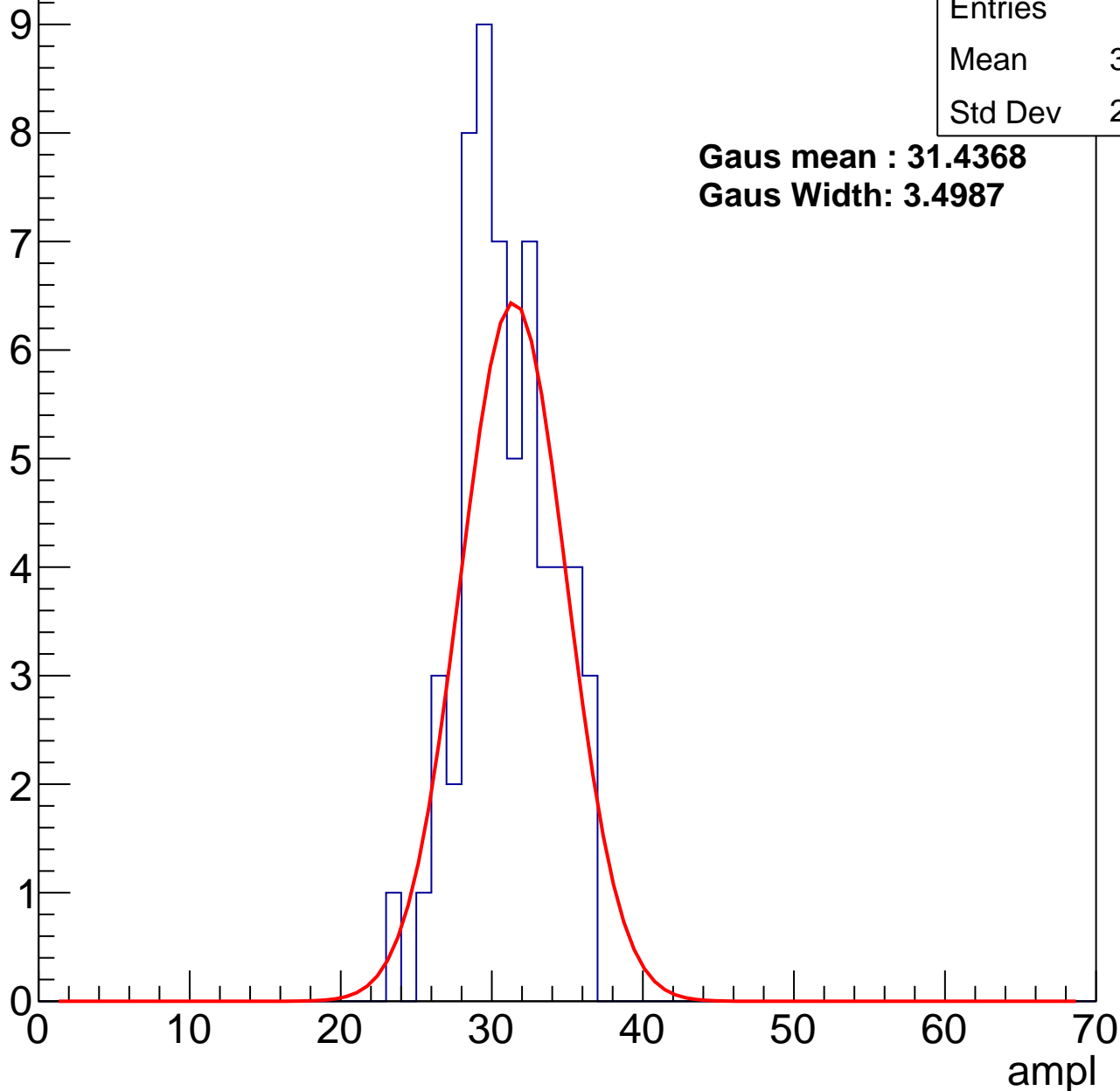
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	30.52
Std Dev	2.955

**Gaus mean : 31.4368**

**Gaus Width: 3.4987**



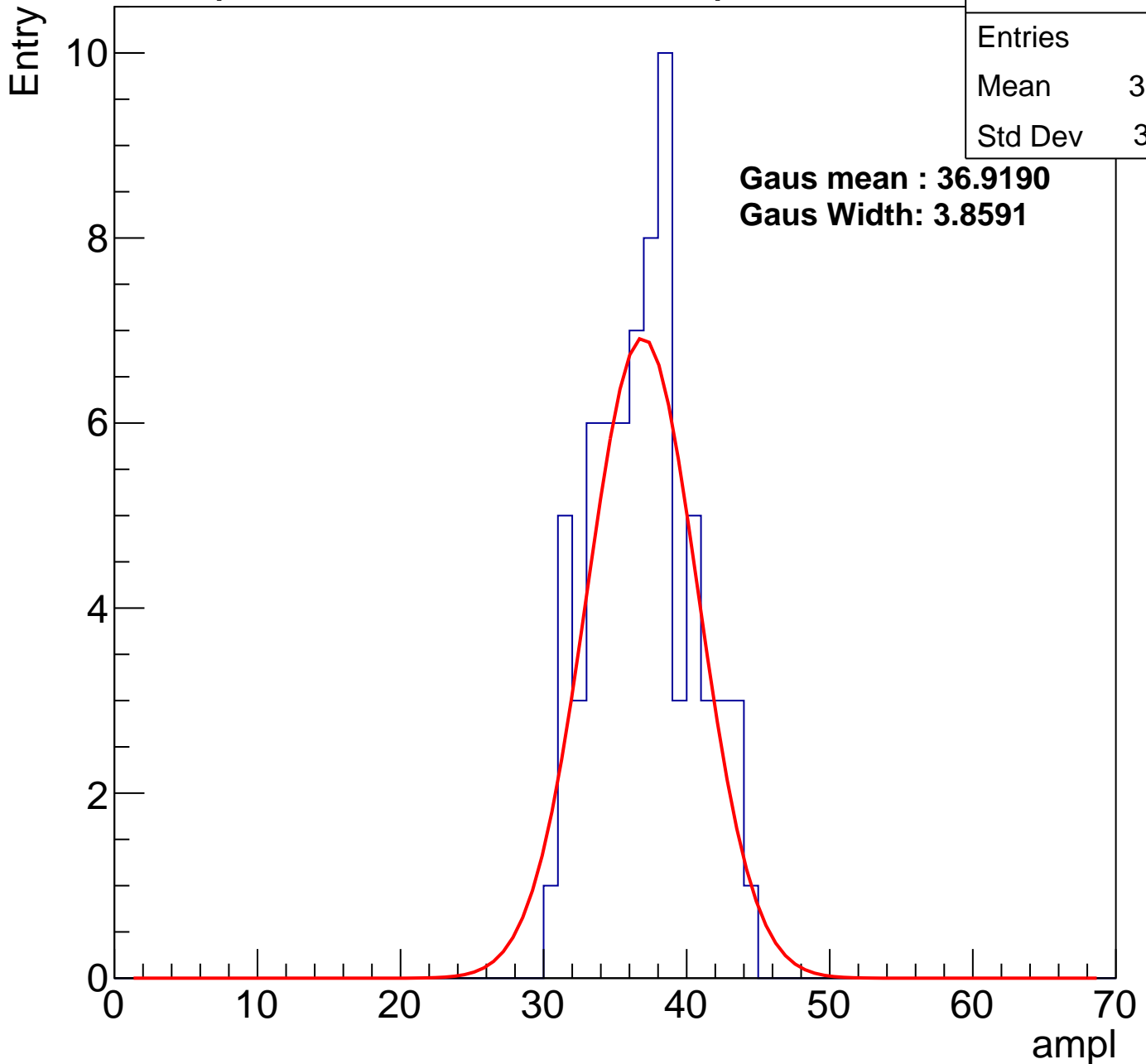
# B1L000S, U24-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	36.57
Std Dev	3.421

**Gaus mean : 36.9190**

**Gaus Width: 3.8591**



# B1L000S, U24-ch32, adc2

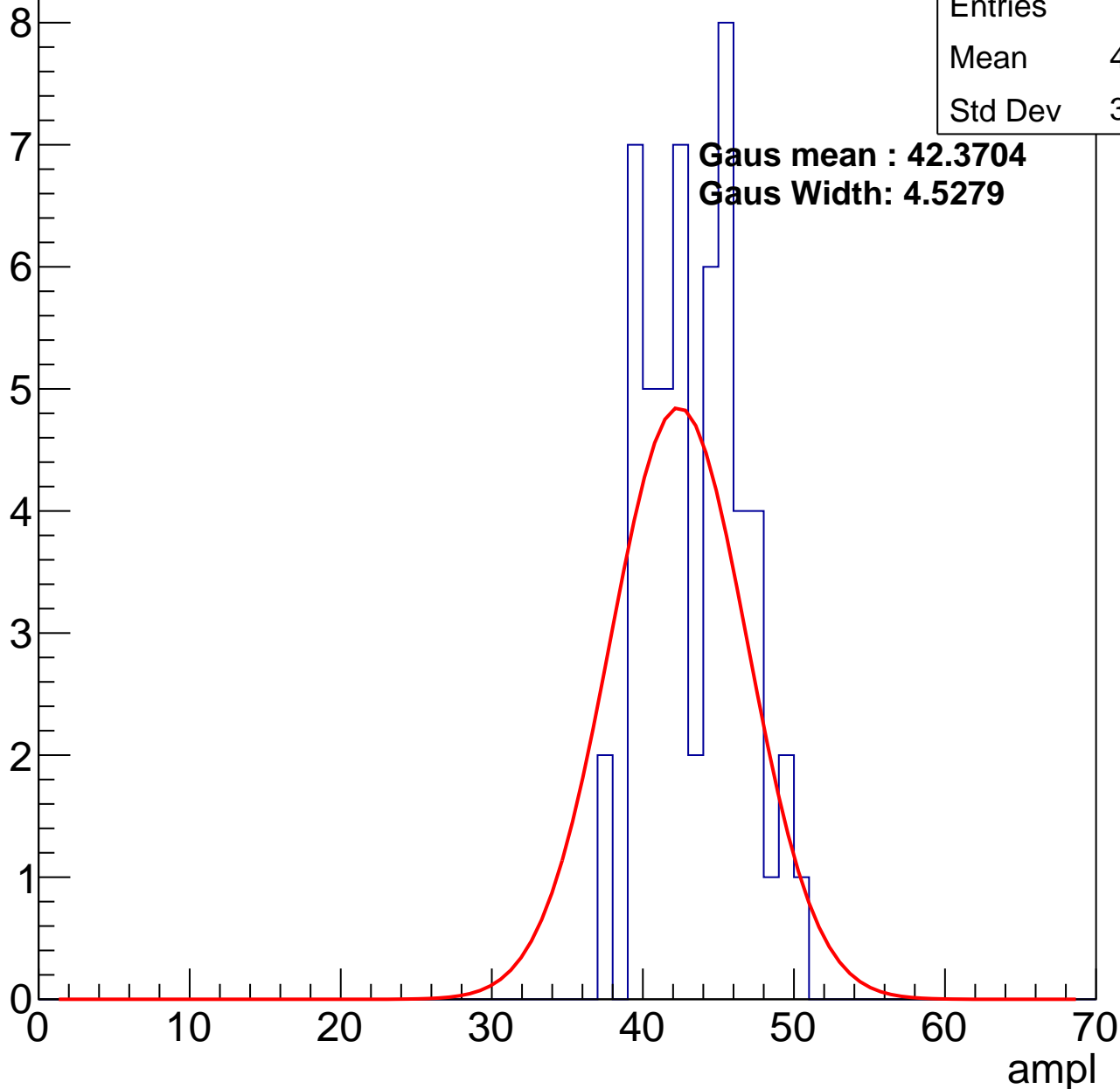
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	43.04
Std Dev	3.162

**Gaus mean : 42.3704**

**Gaus Width: 4.5279**

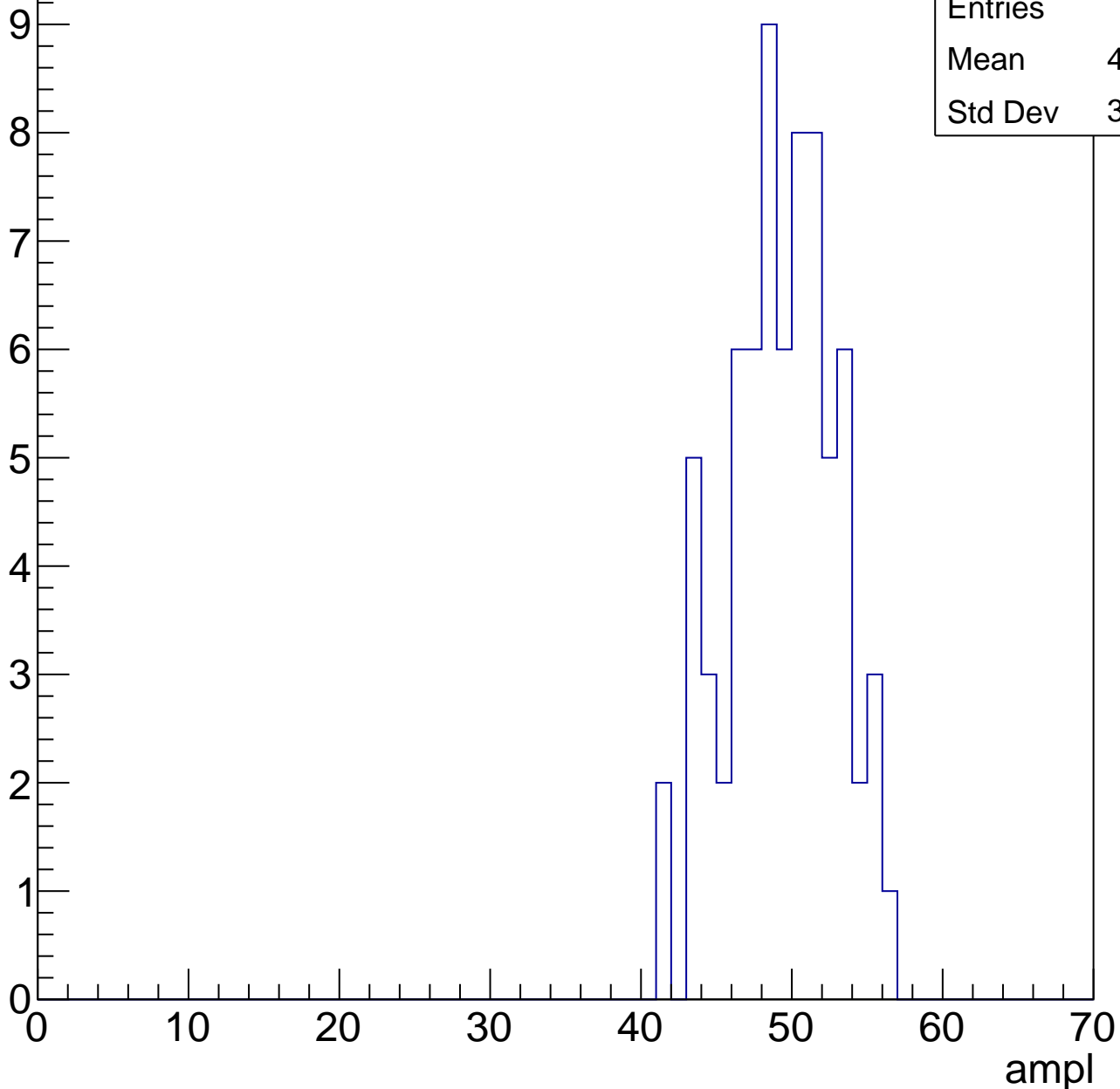


# B1L000S, U24-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

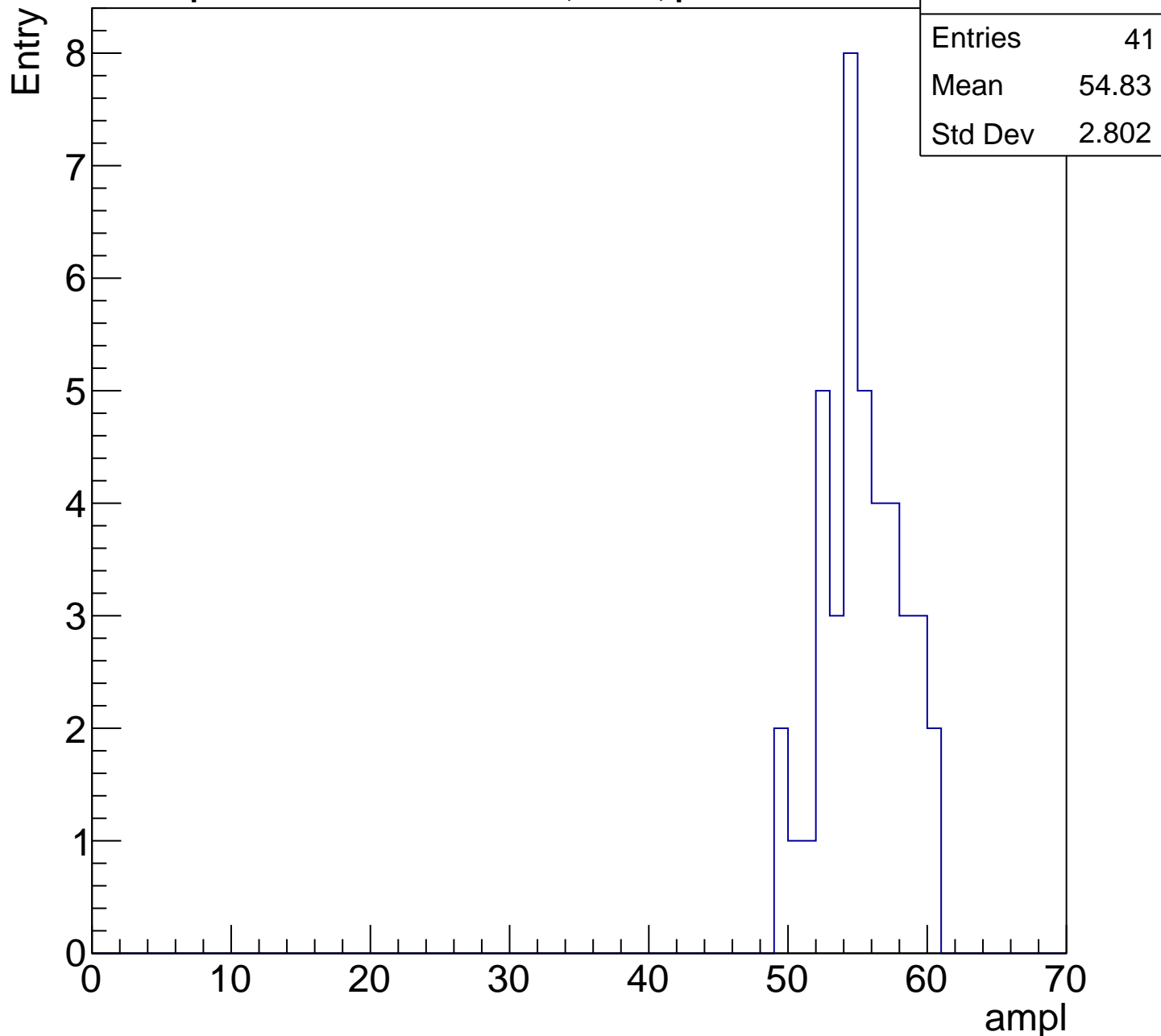
Entry

Entries	72
Mean	48.86
Std Dev	3.513



# B1L000S, U24-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

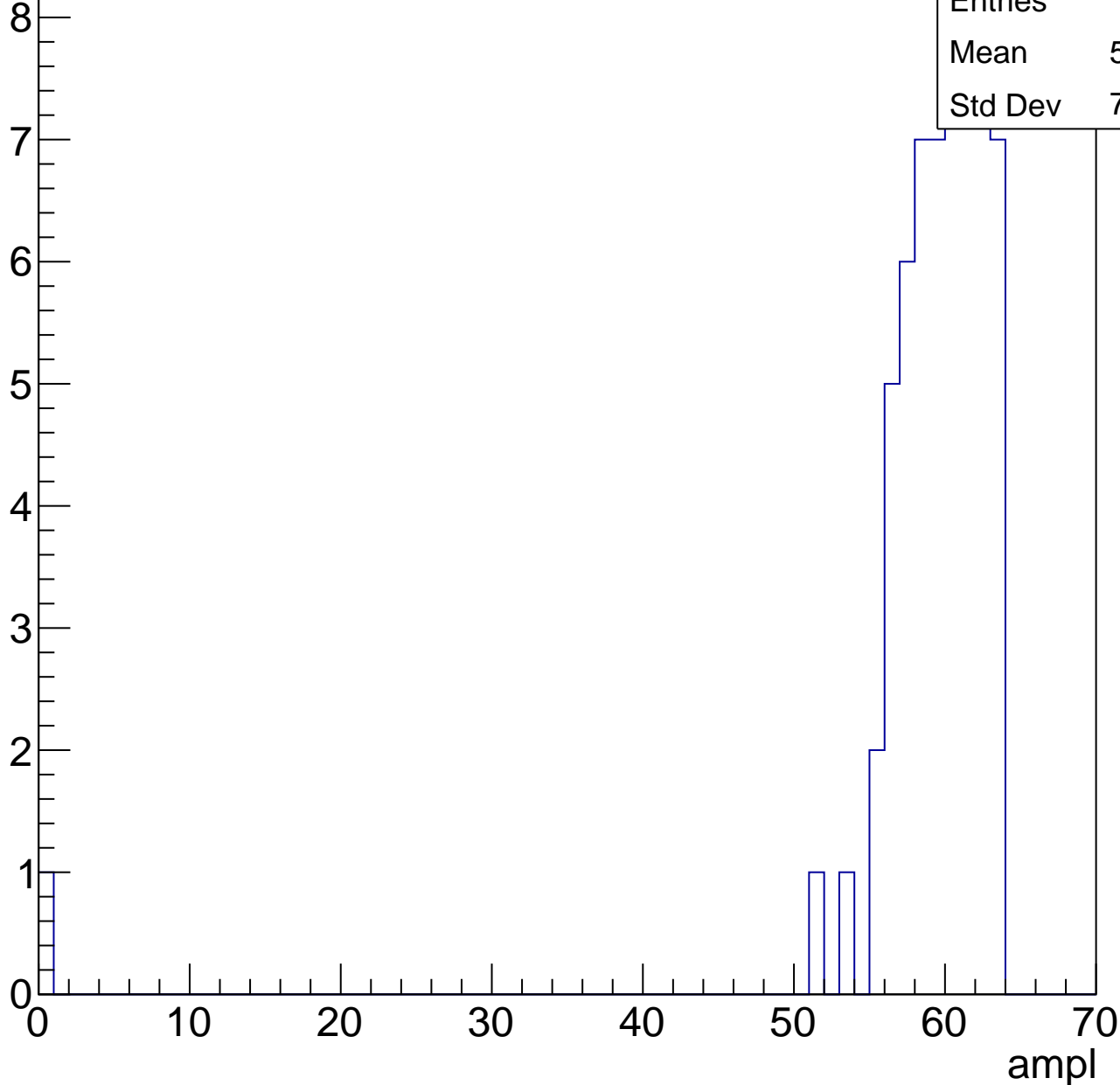


# B1L000S, U24-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.36
Std Dev	7.984



# B1L000S, U24-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch33, adc0

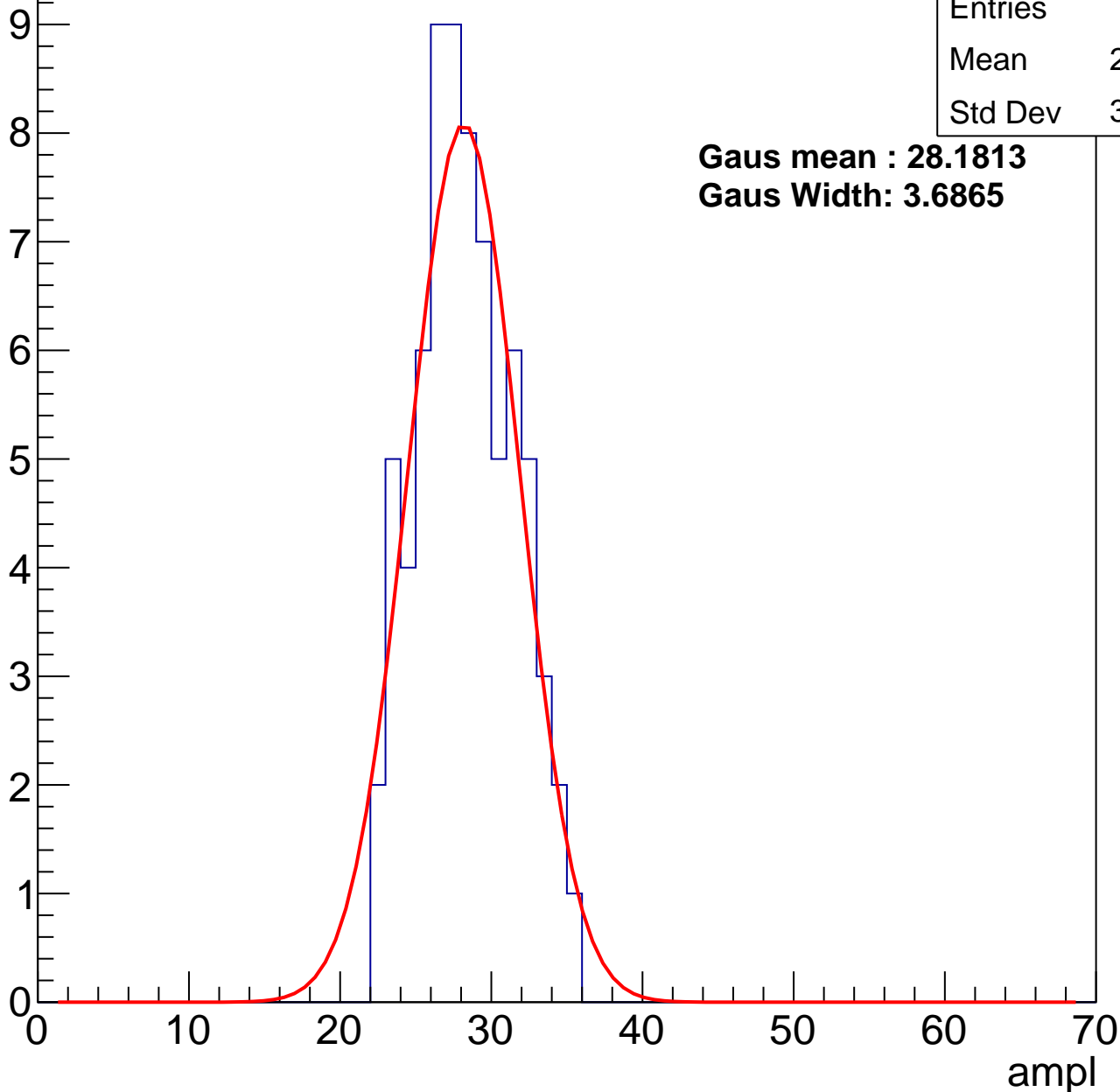
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	27.88
Std Dev	3.153

**Gaus mean : 28.1813**

**Gaus Width: 3.6865**



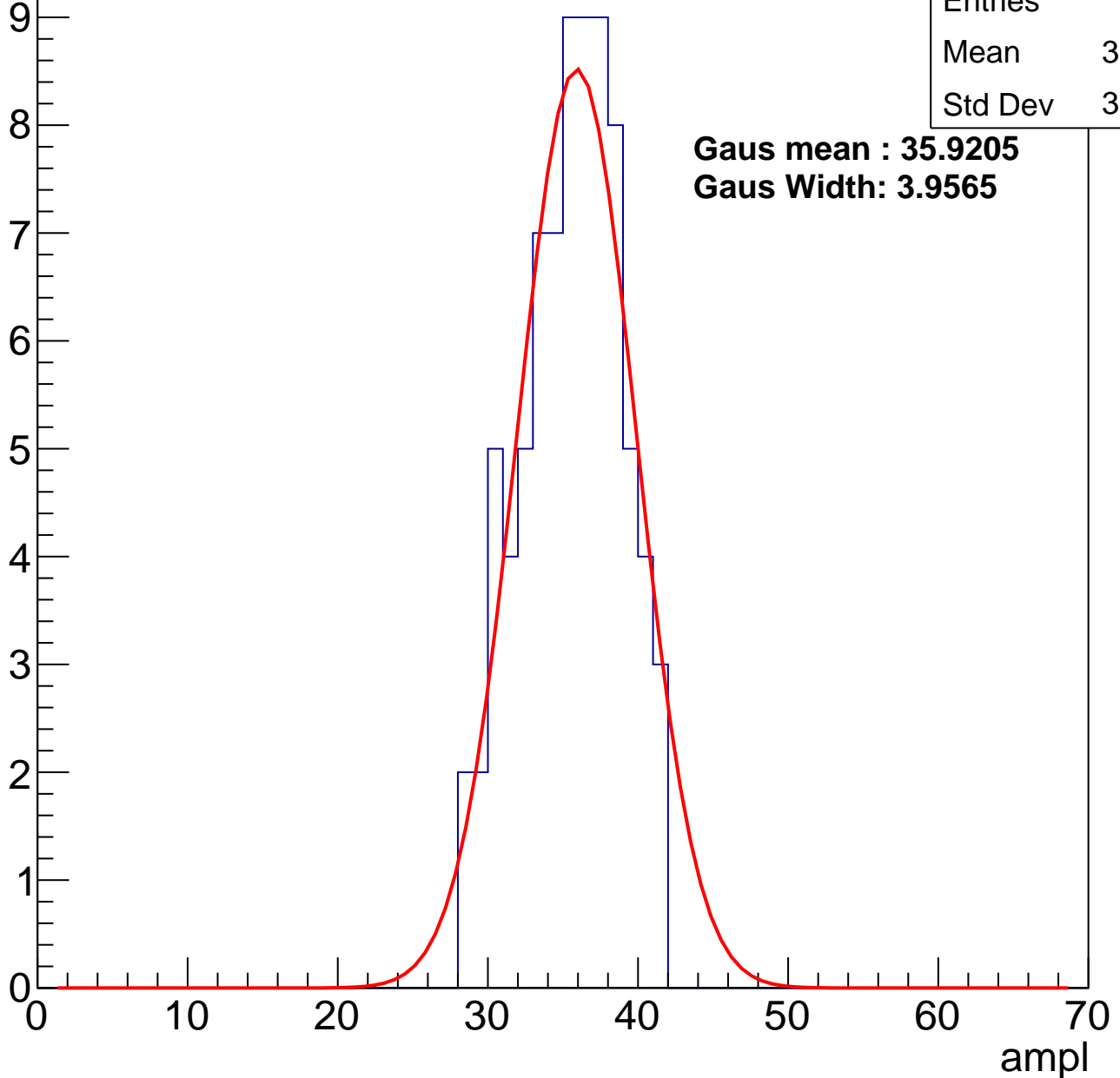
# B1L000S, U24-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	35.08
Std Dev	3.268

**Gaus mean : 35.9205**  
**Gaus Width: 3.9565**



# B1L000S, U24-ch33, adc2

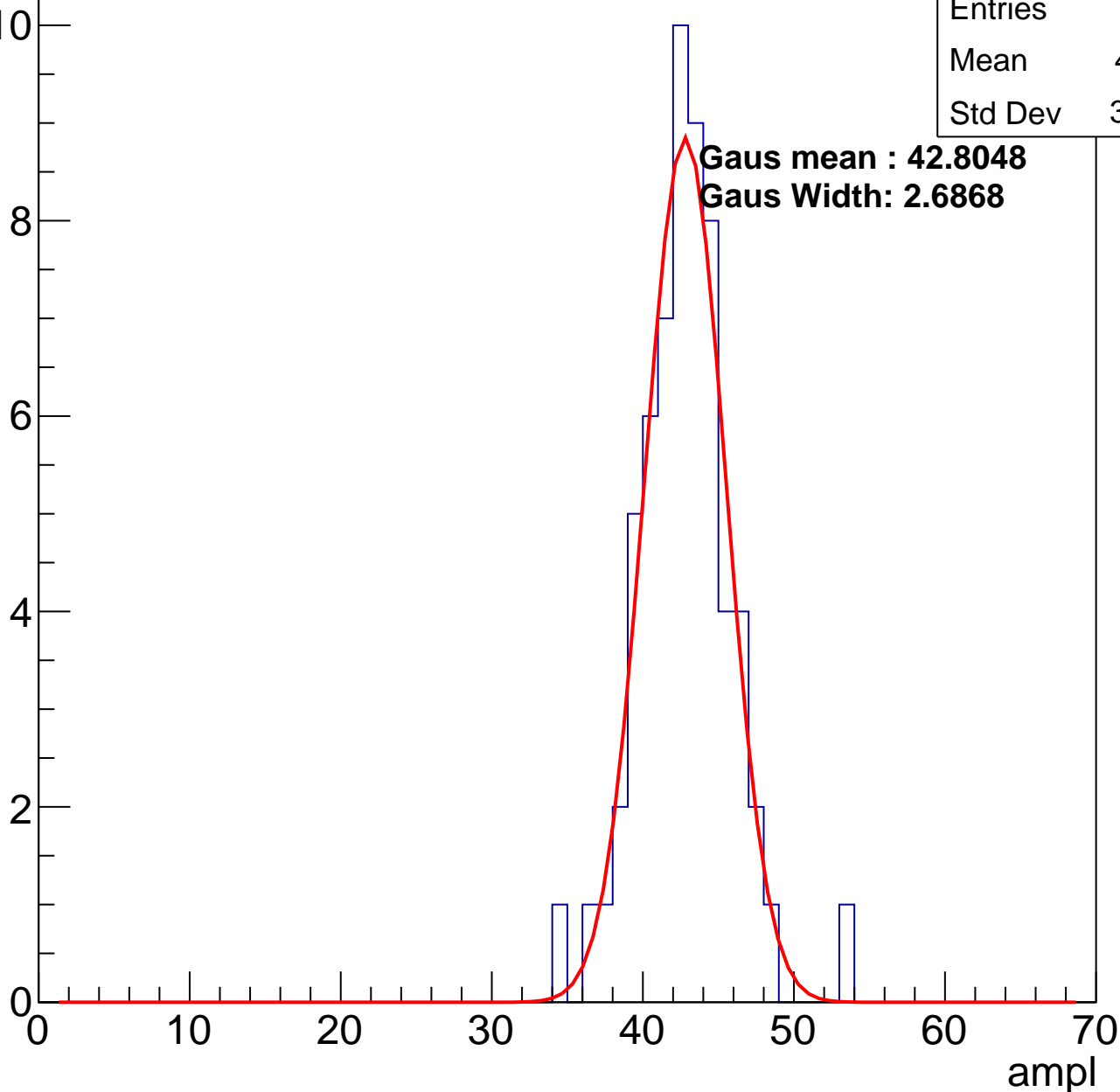
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.31
Std Dev	3.046

**Gaus mean : 42.8048**

**Gaus Width: 2.6868**

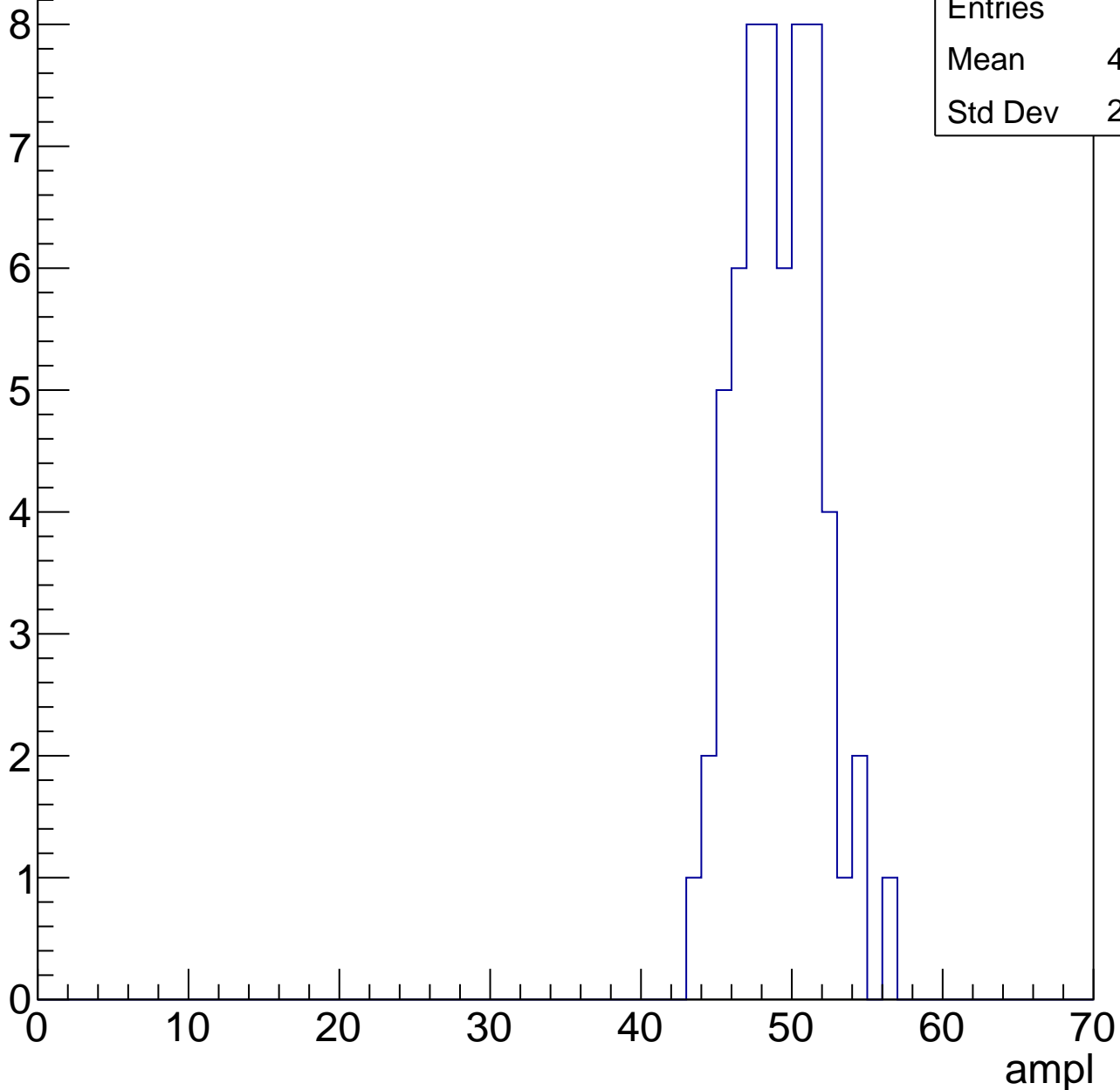


# B1L000S, U24-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	48.65
Std Dev	2.719

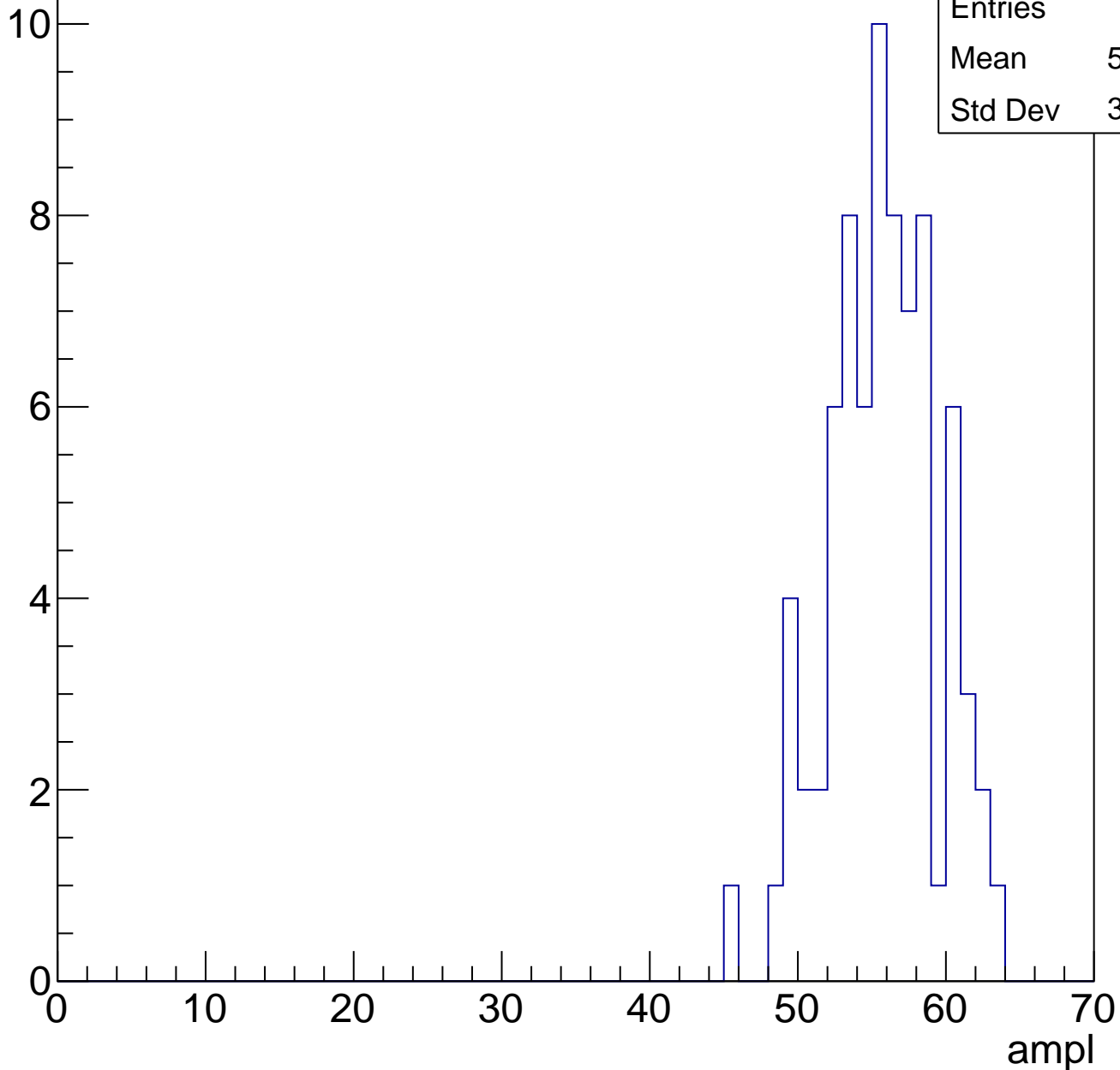


# B1L000S, U24-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	55.28
Std Dev	3.637

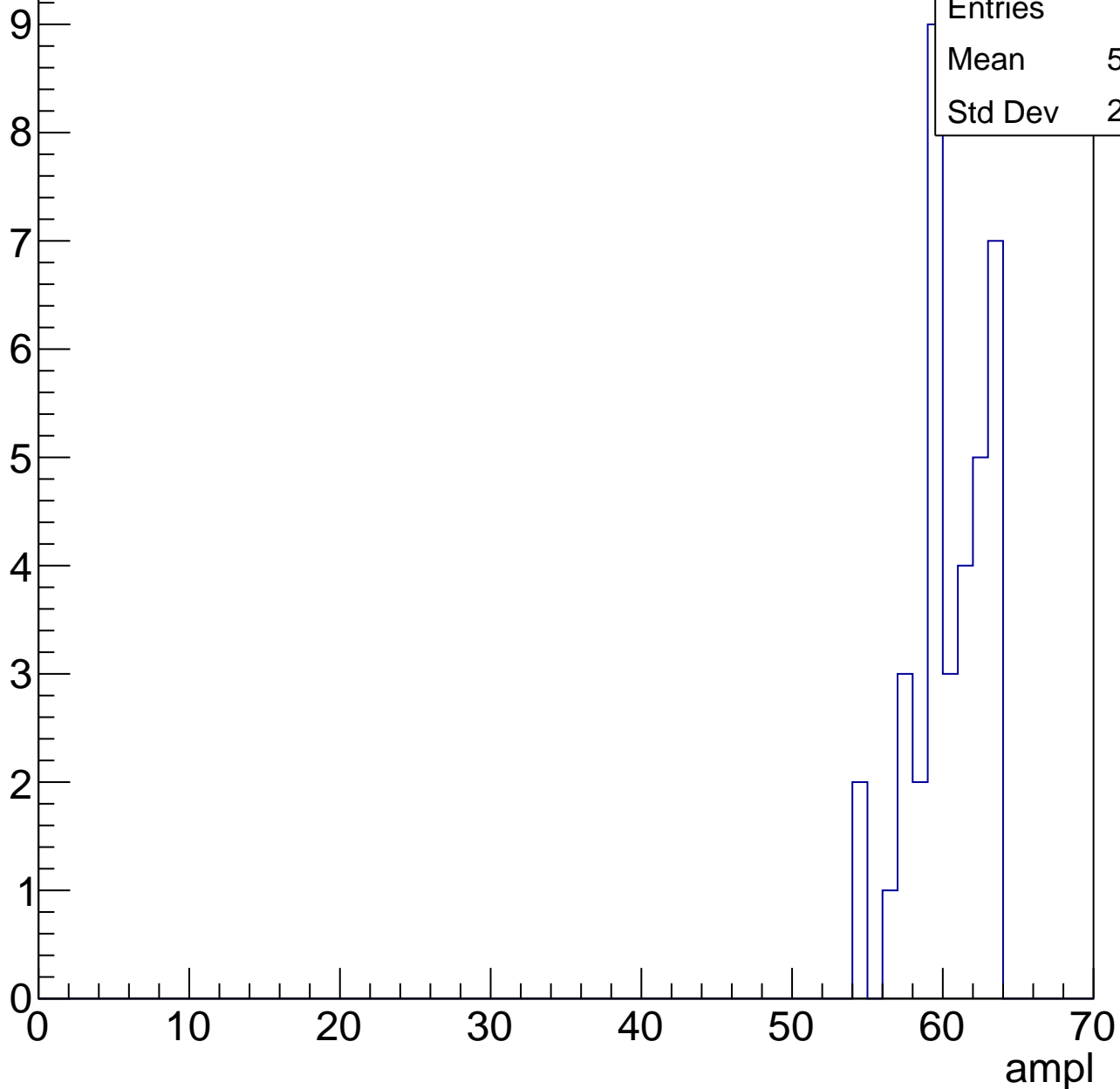
Entry



# B1L000S, U24-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

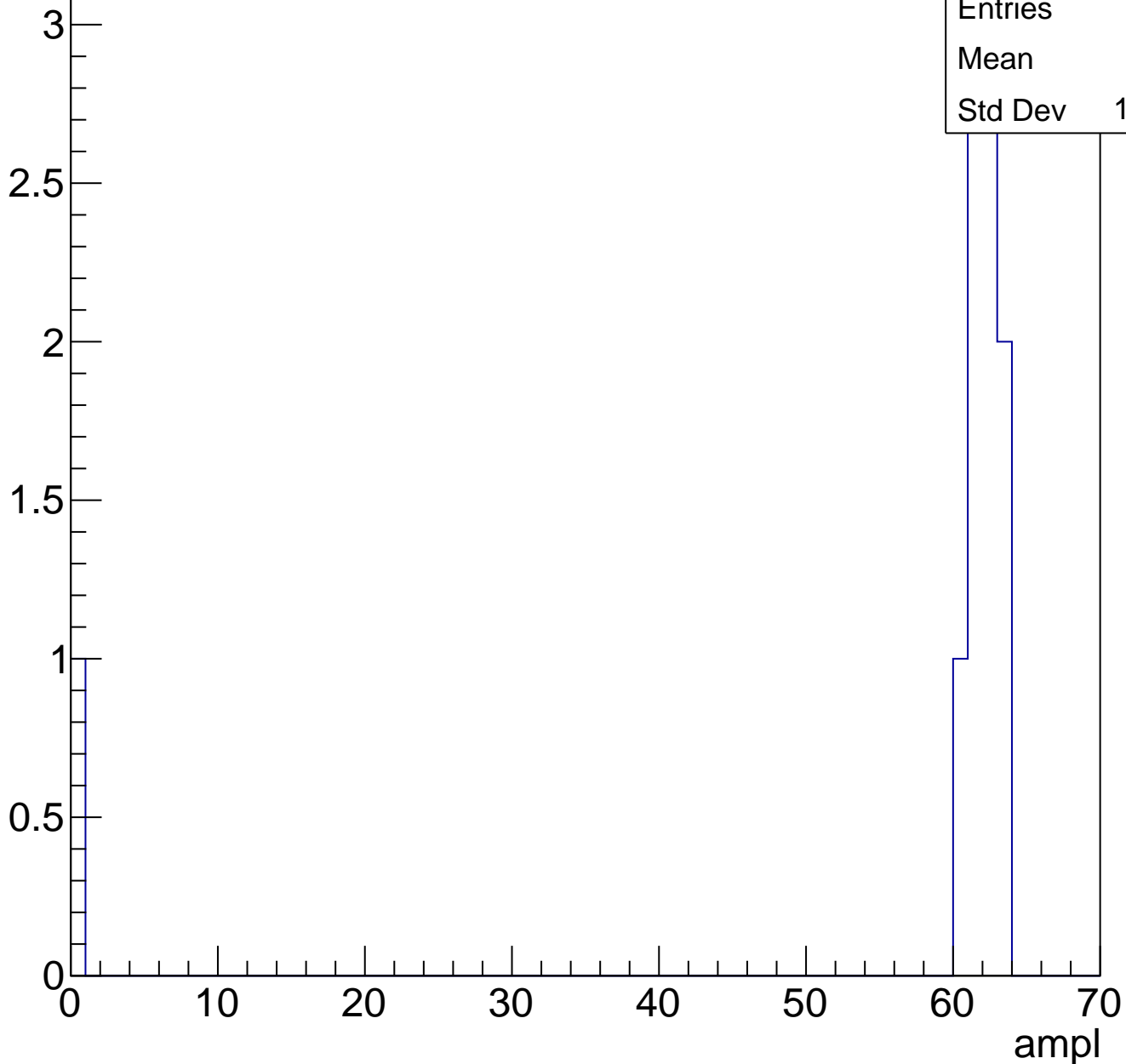
Entry



# B1L000S, U24-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

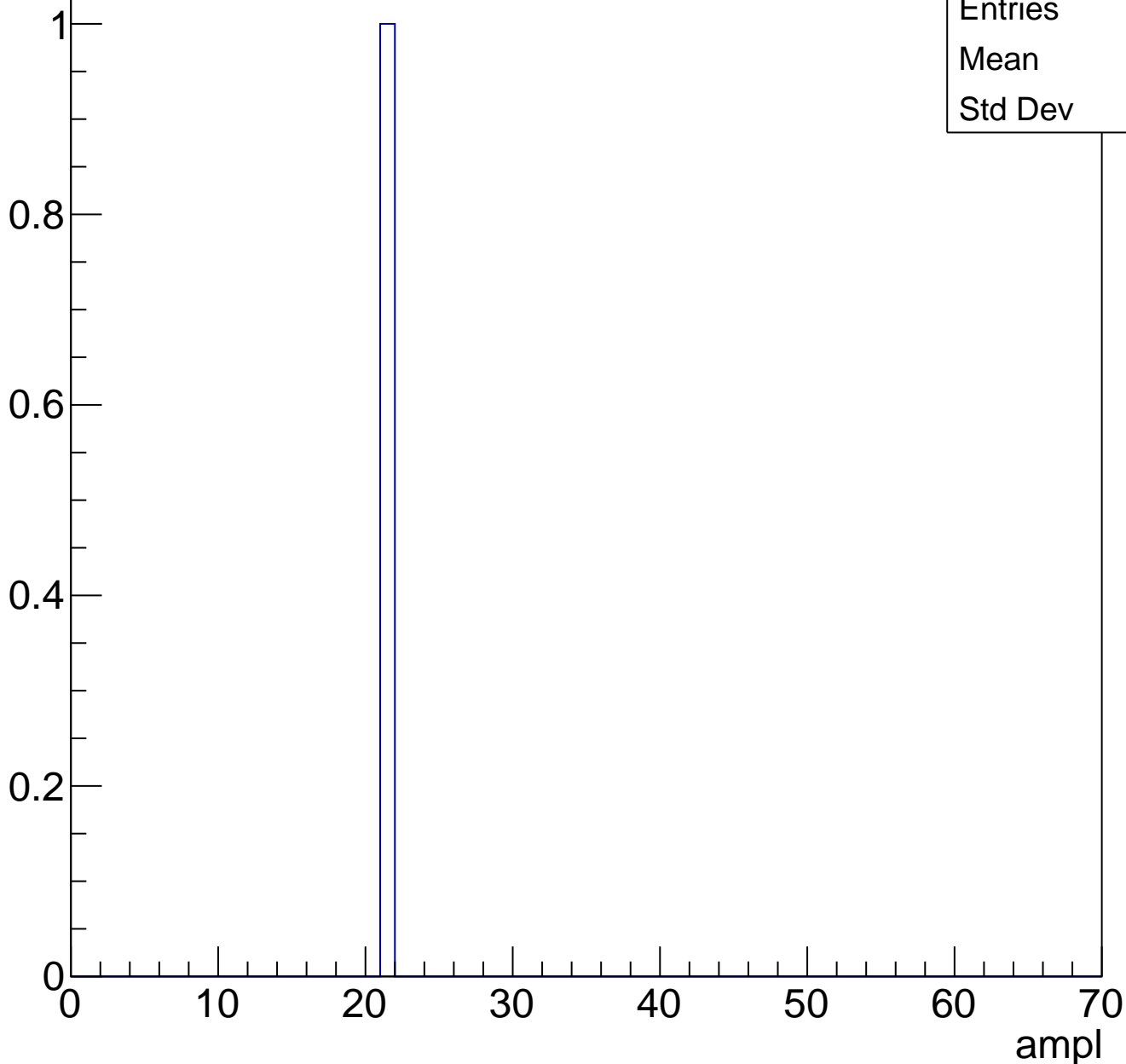




# B1L000S, U24-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	21
Std Dev	0

# B1L000S, U24-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	29.84
Std Dev	3.595

**Gaus mean : 30.6880**

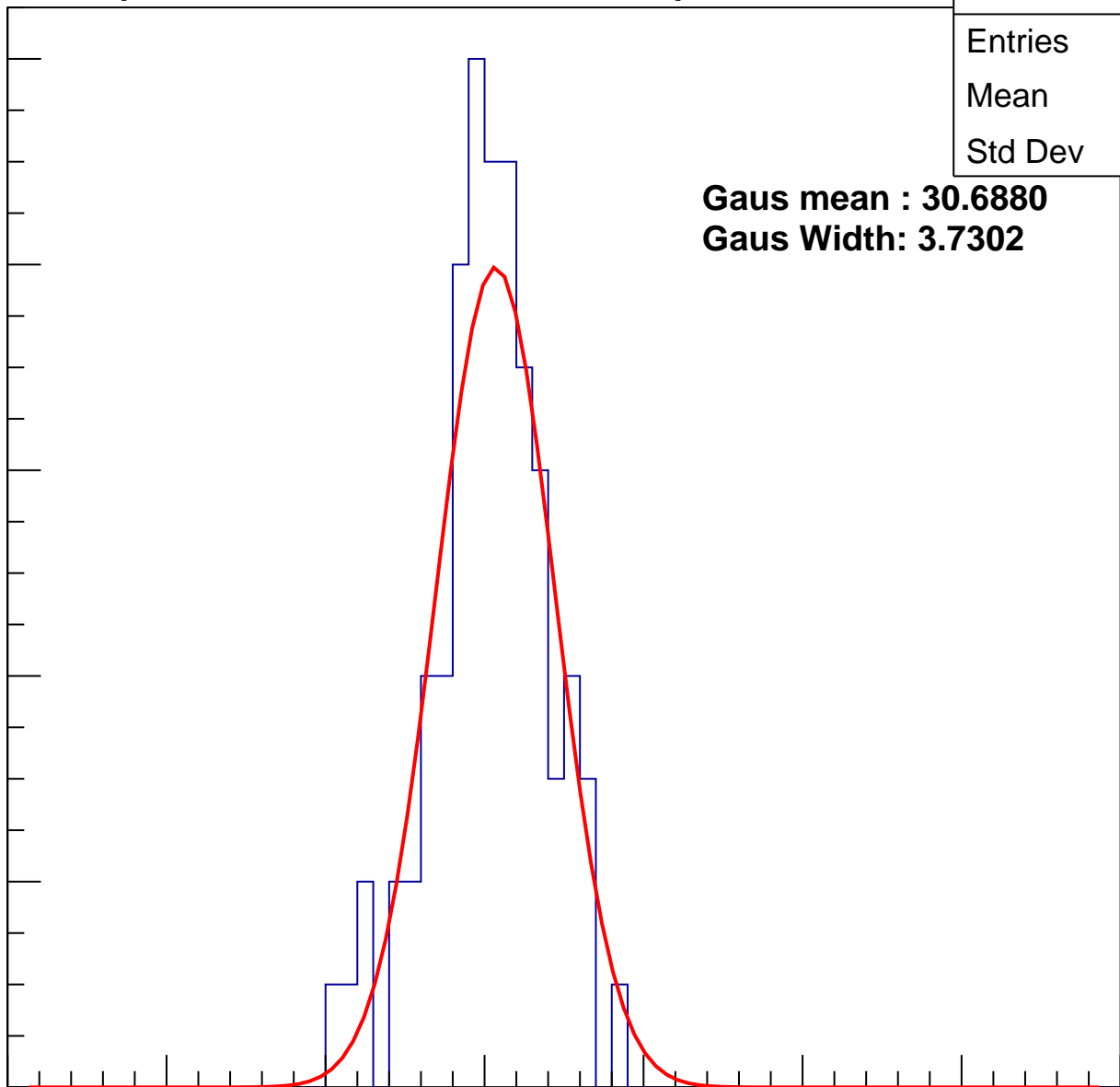
**Gaus Width: 3.7302**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch34, adc1

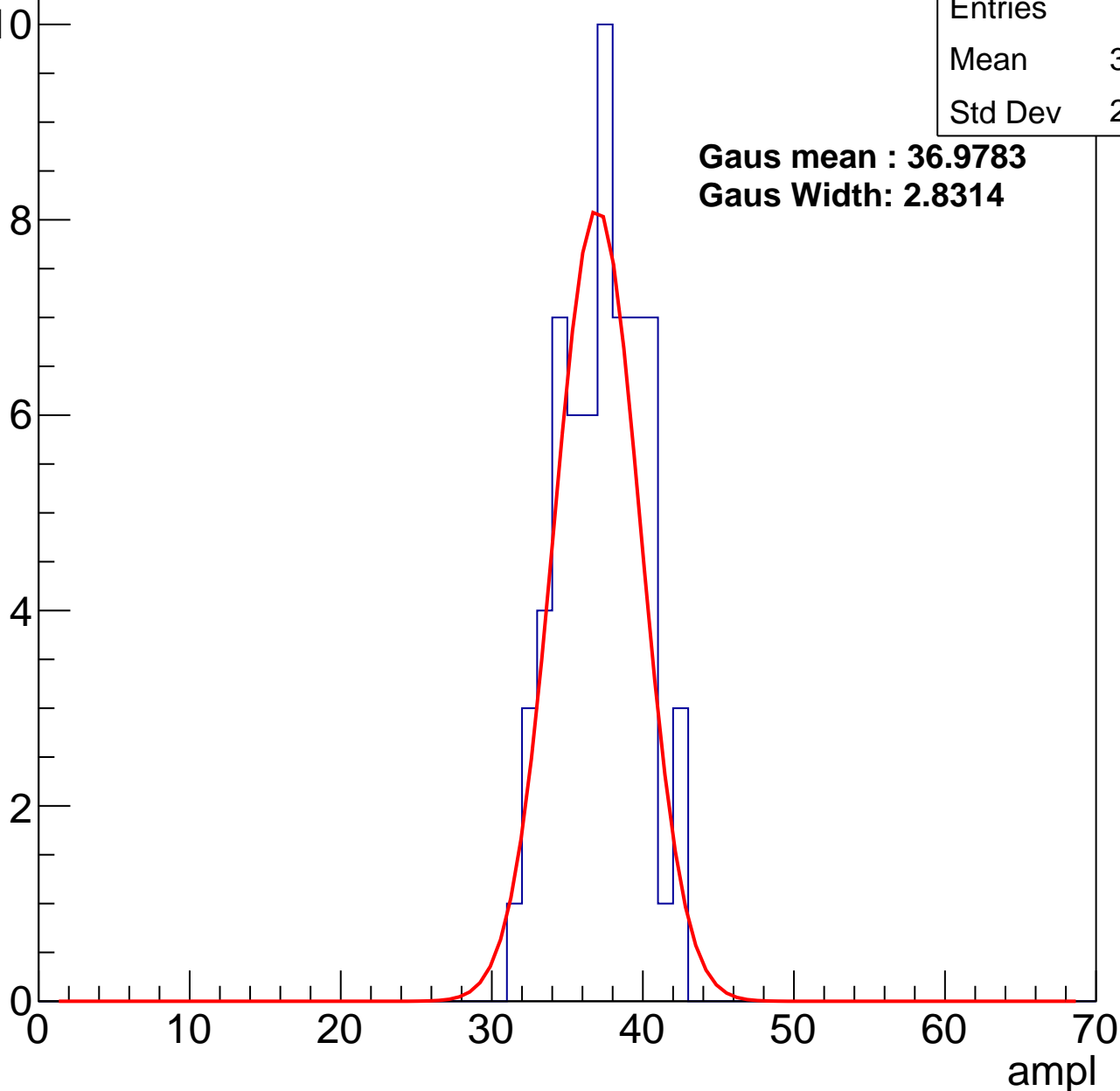
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.76
Std Dev	2.704

**Gaus mean : 36.9783**

**Gaus Width: 2.8314**



# B1L000S, U24-ch34, adc2

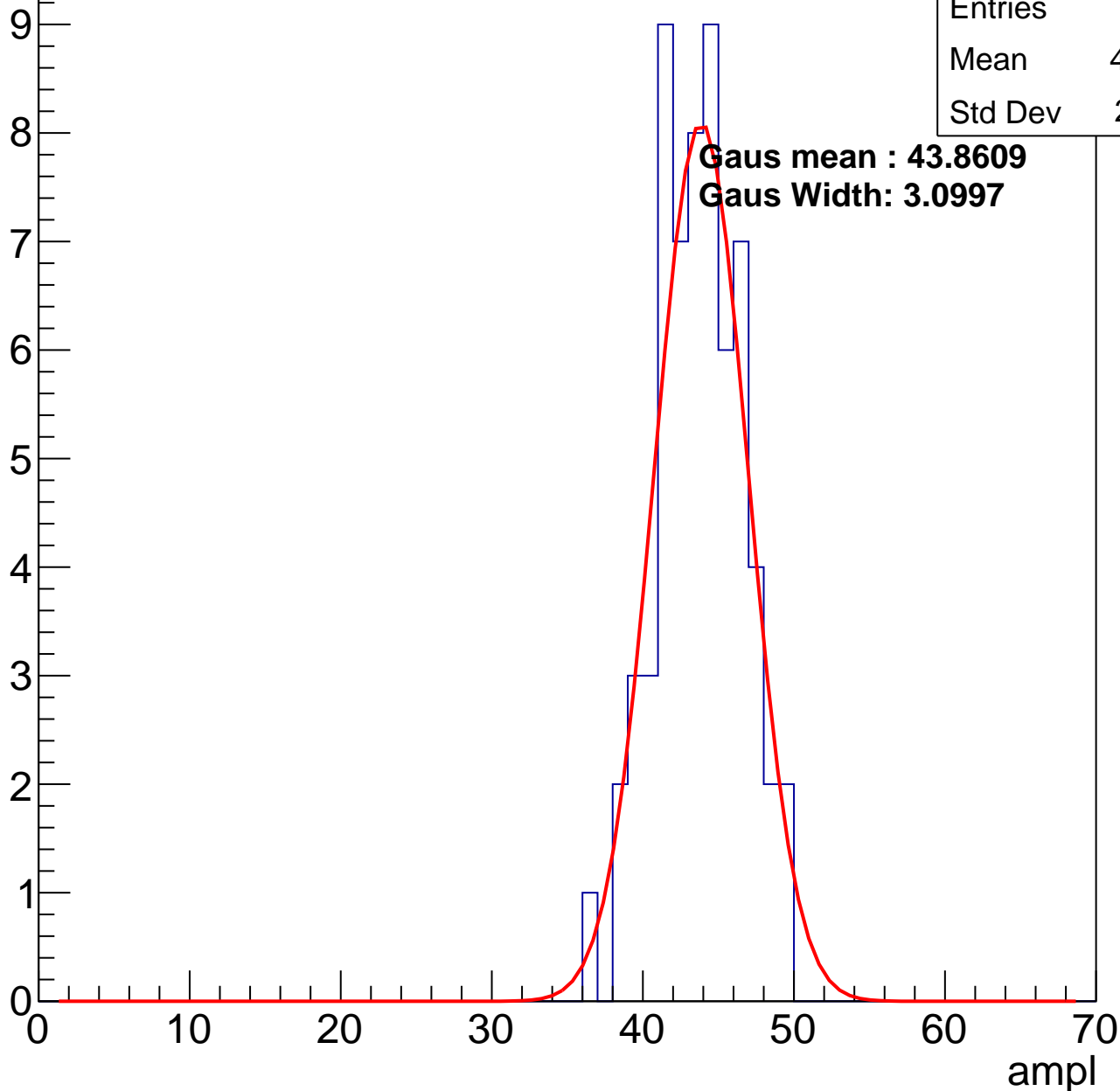
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	43.27
Std Dev	2.801

**Gaus mean : 43.8609**

**Gaus Width: 3.0997**

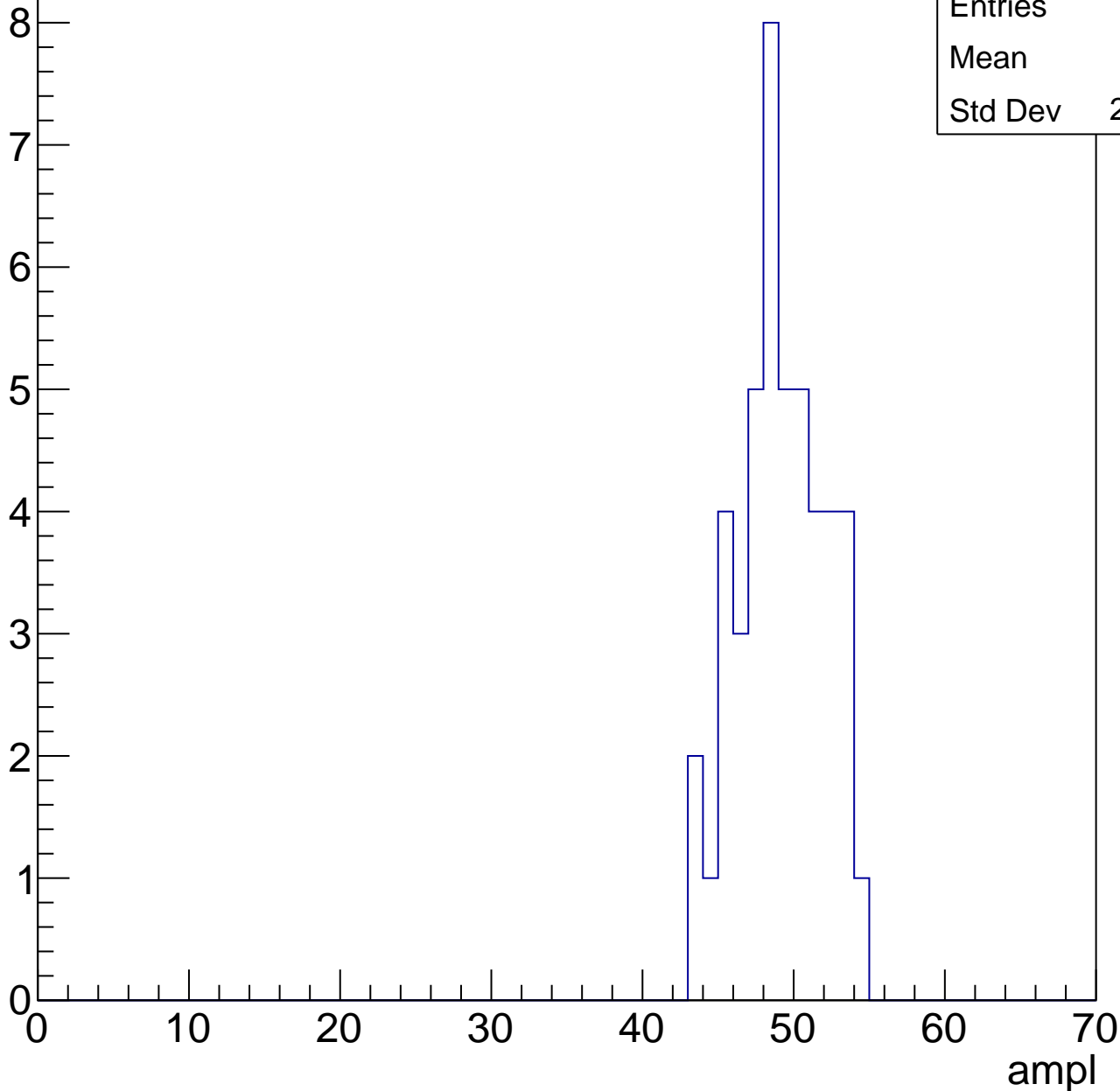


# B1L000S, U24-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	48.7
Std Dev	2.789

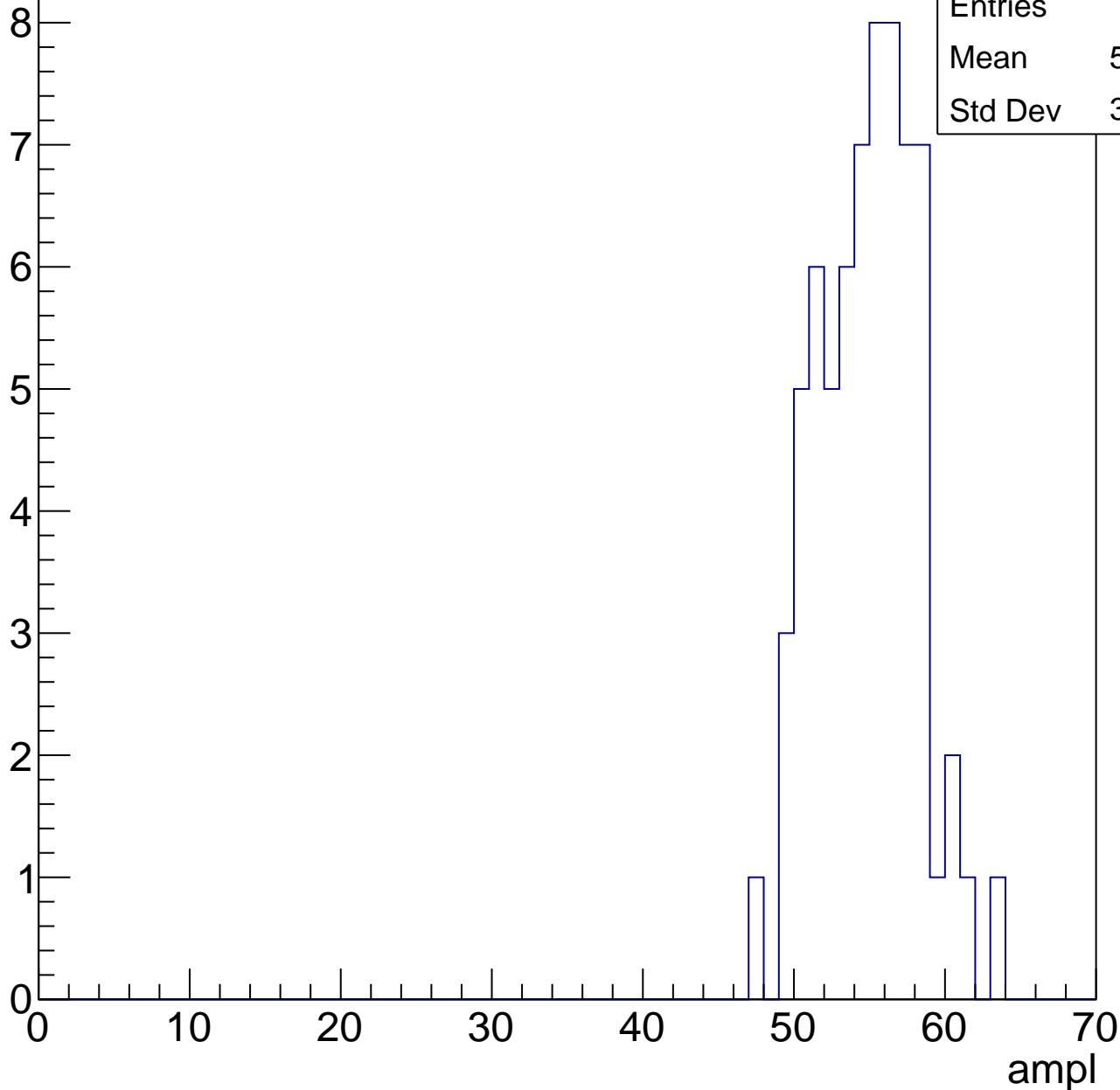


# B1L000S, U24-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	54.44
Std Dev	3.238

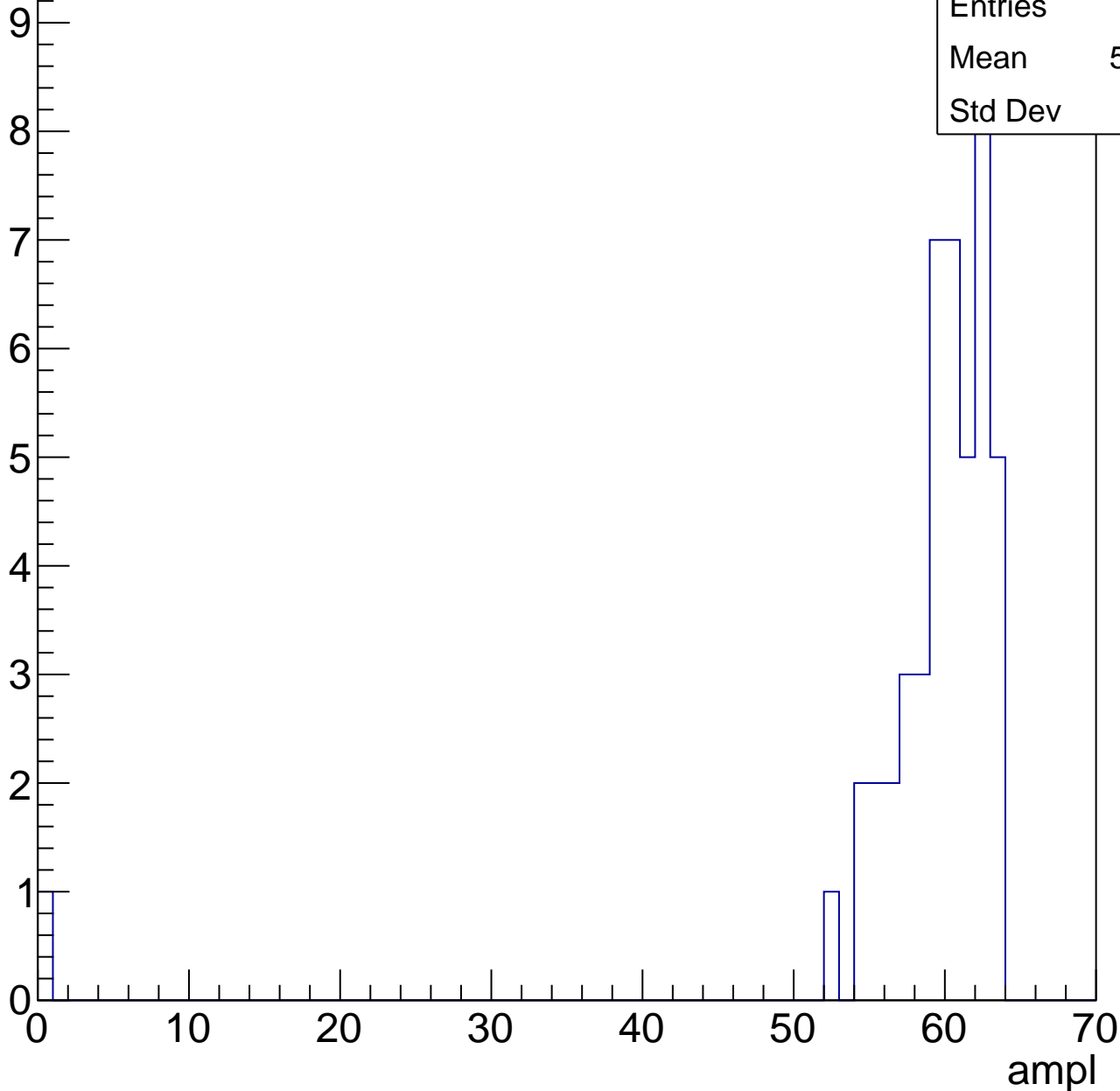


# B1L000S, U24-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

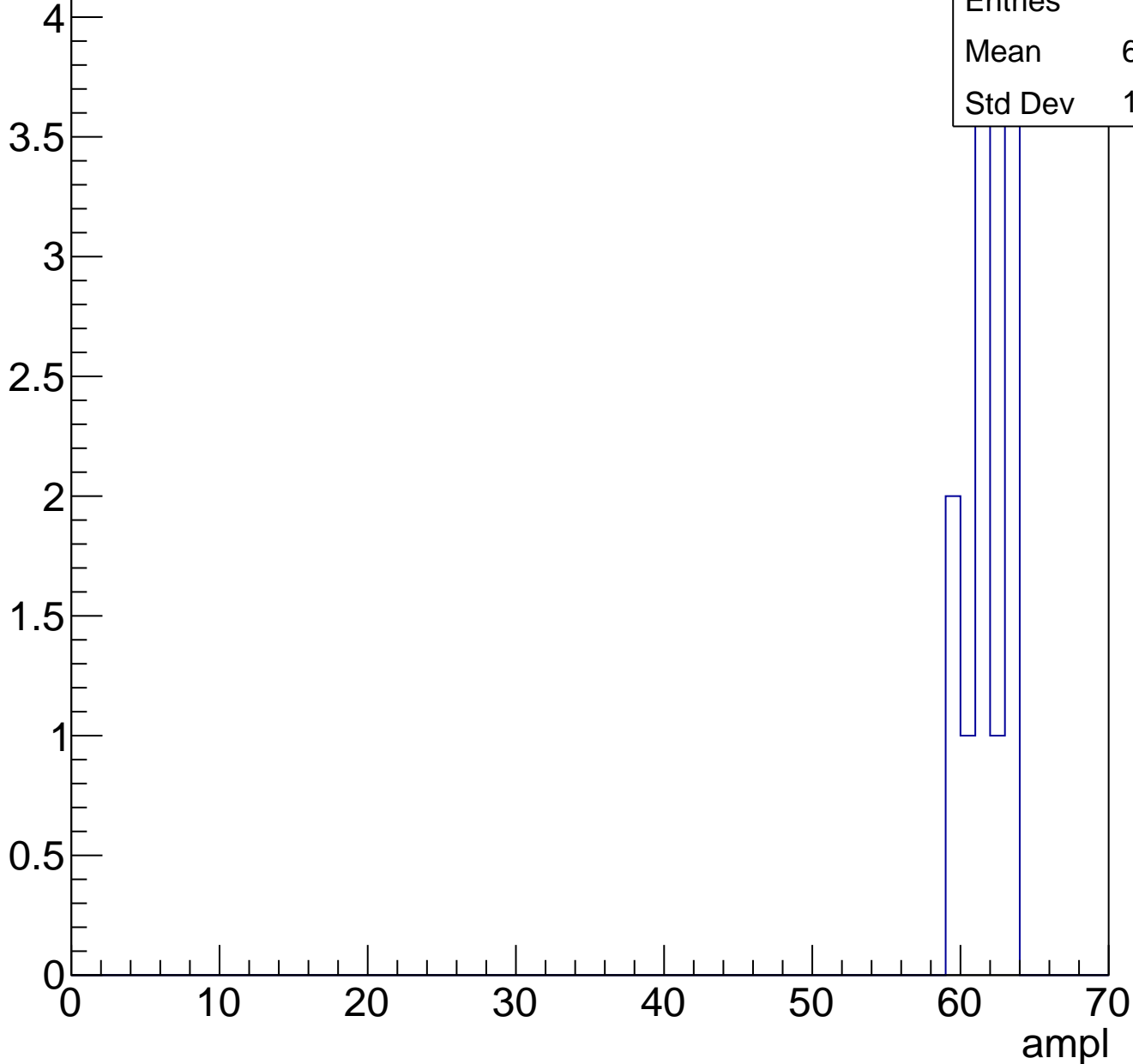
Entries	47
Mean	58.26
Std Dev	9



# B1L000S, U24-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L000S, U24-ch35, adc0

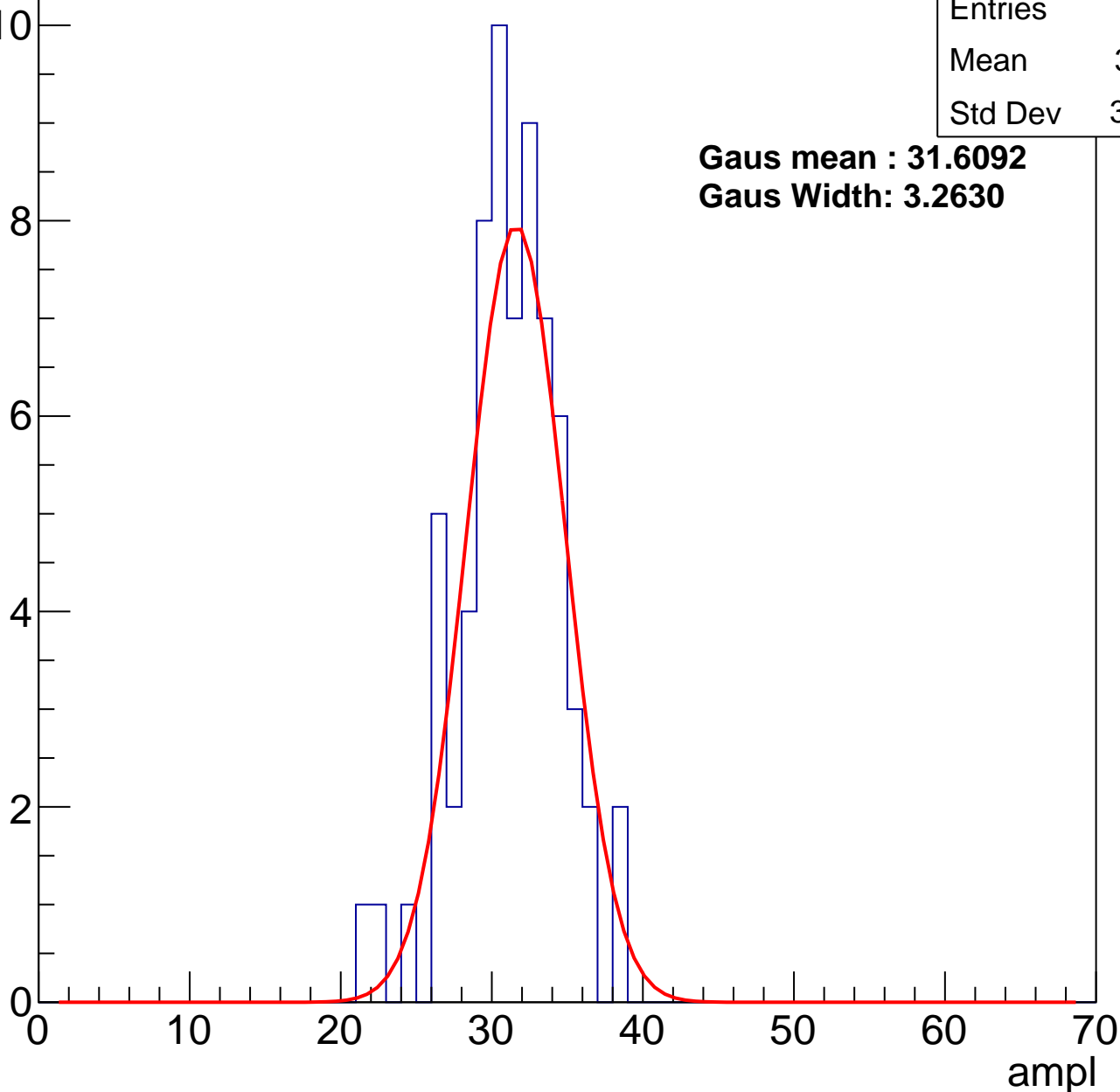
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.71
Std Dev	3.312

**Gaus mean : 31.6092**

**Gaus Width: 3.2630**



# B1L000S, U24-ch35, adc1

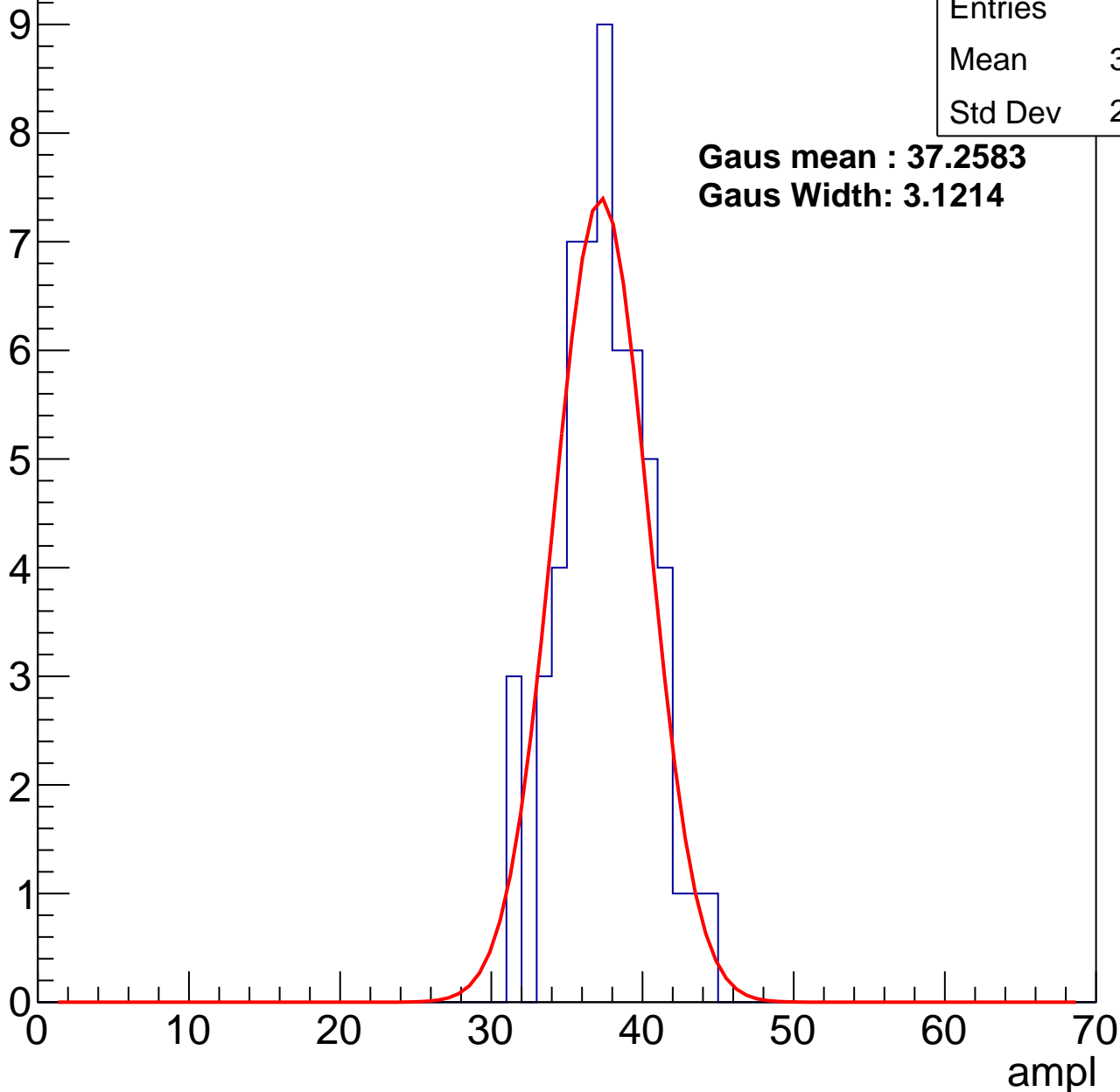
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	37.07
Std Dev	2.889

**Gaus mean : 37.2583**

**Gaus Width: 3.1214**



# B1L000S, U24-ch35, adc2

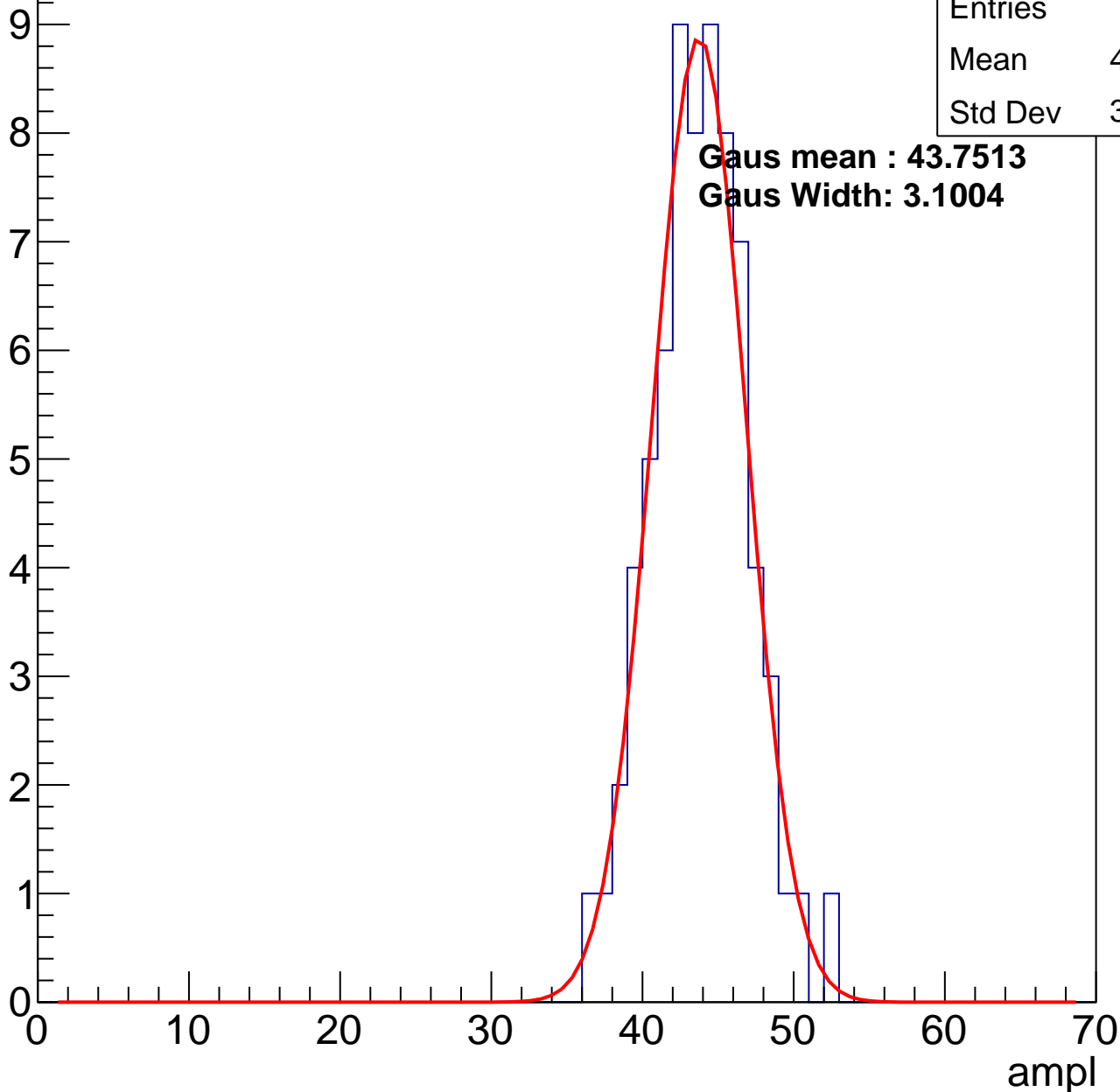
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	43.34
Std Dev	3.112

**Gaus mean : 43.7513**

**Gaus Width: 3.1004**

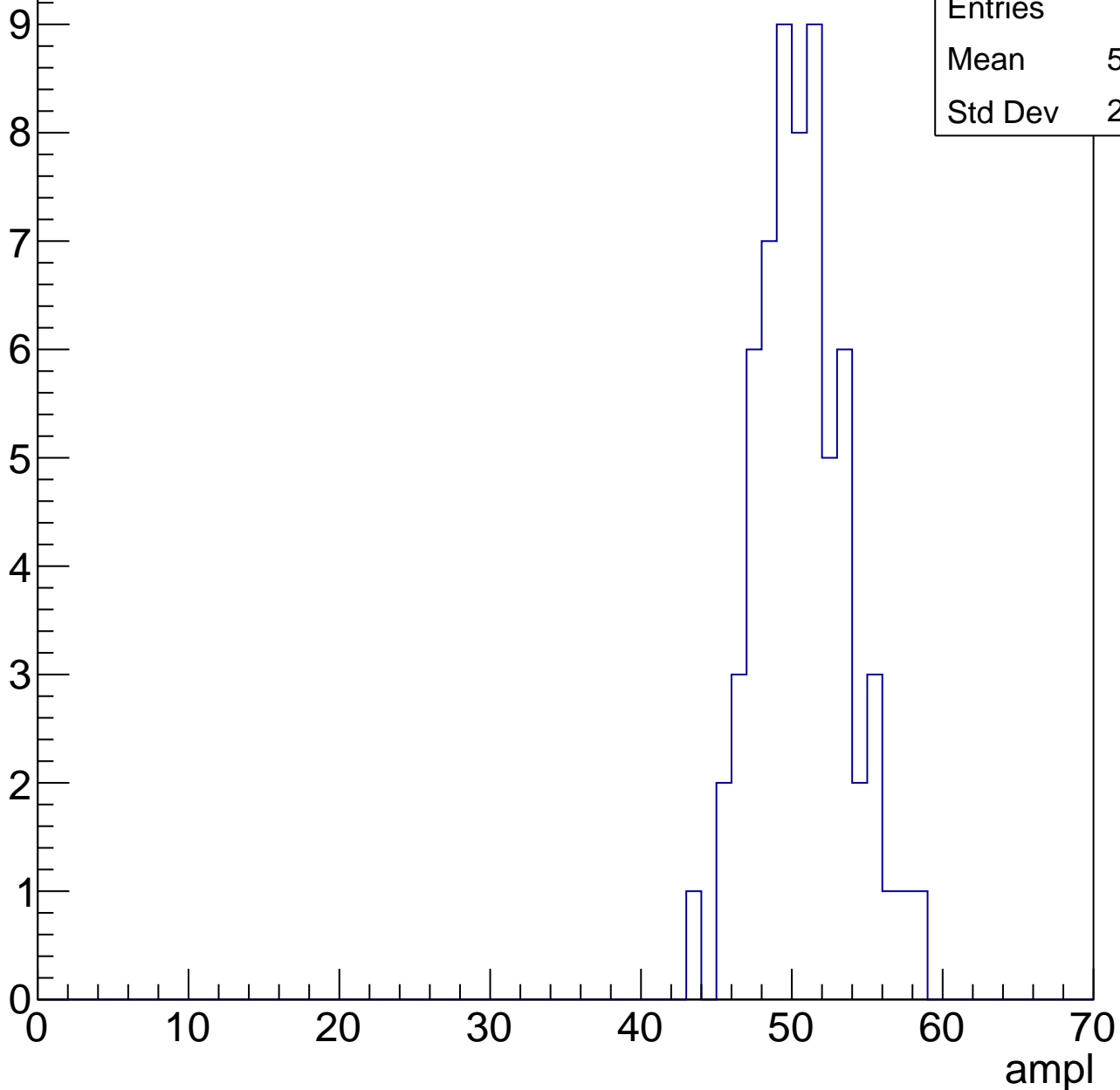


# B1L000S, U24-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	50.17
Std Dev	2.998

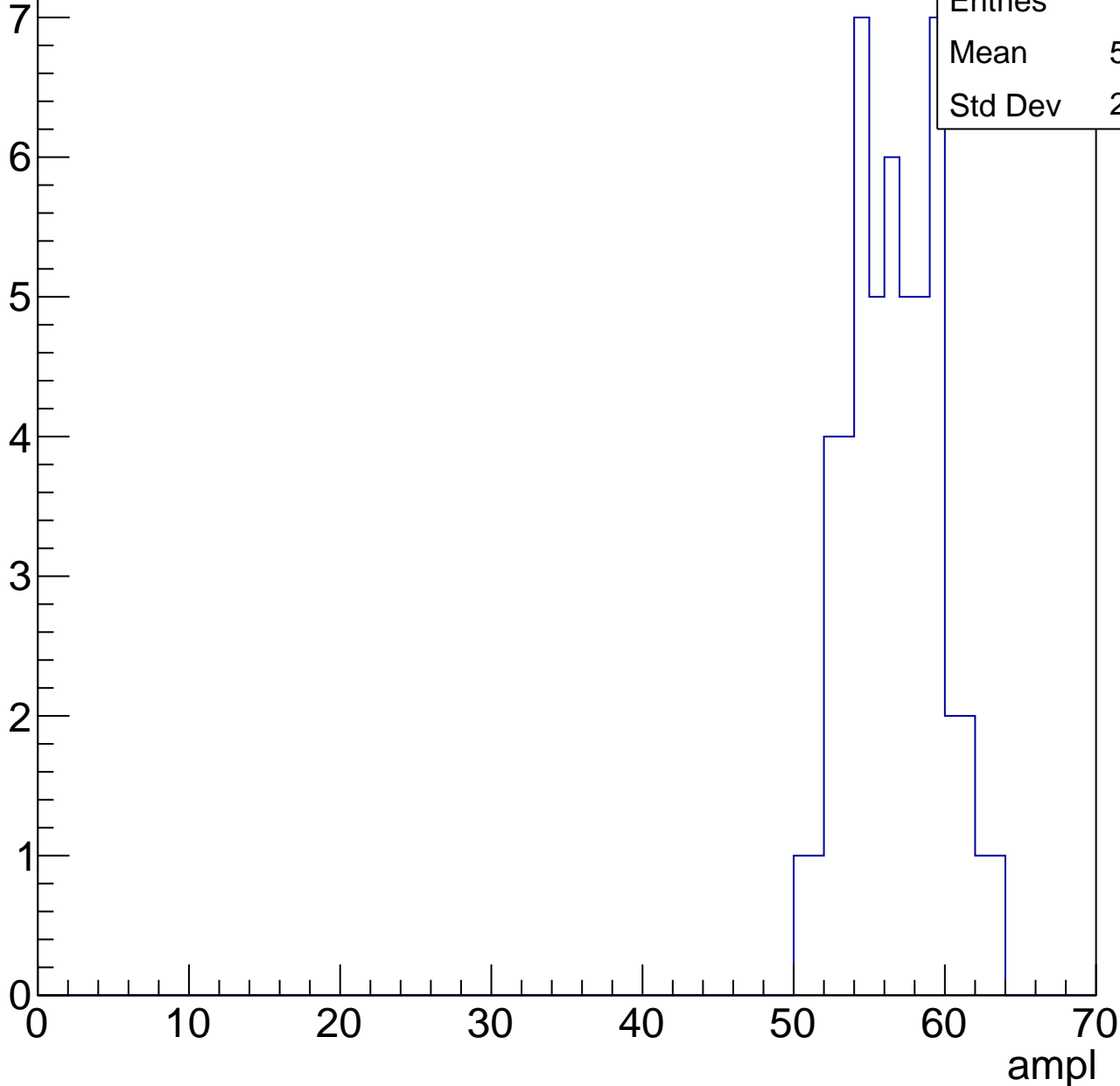


# B1L000S, U24-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	56.18
Std Dev	2.962

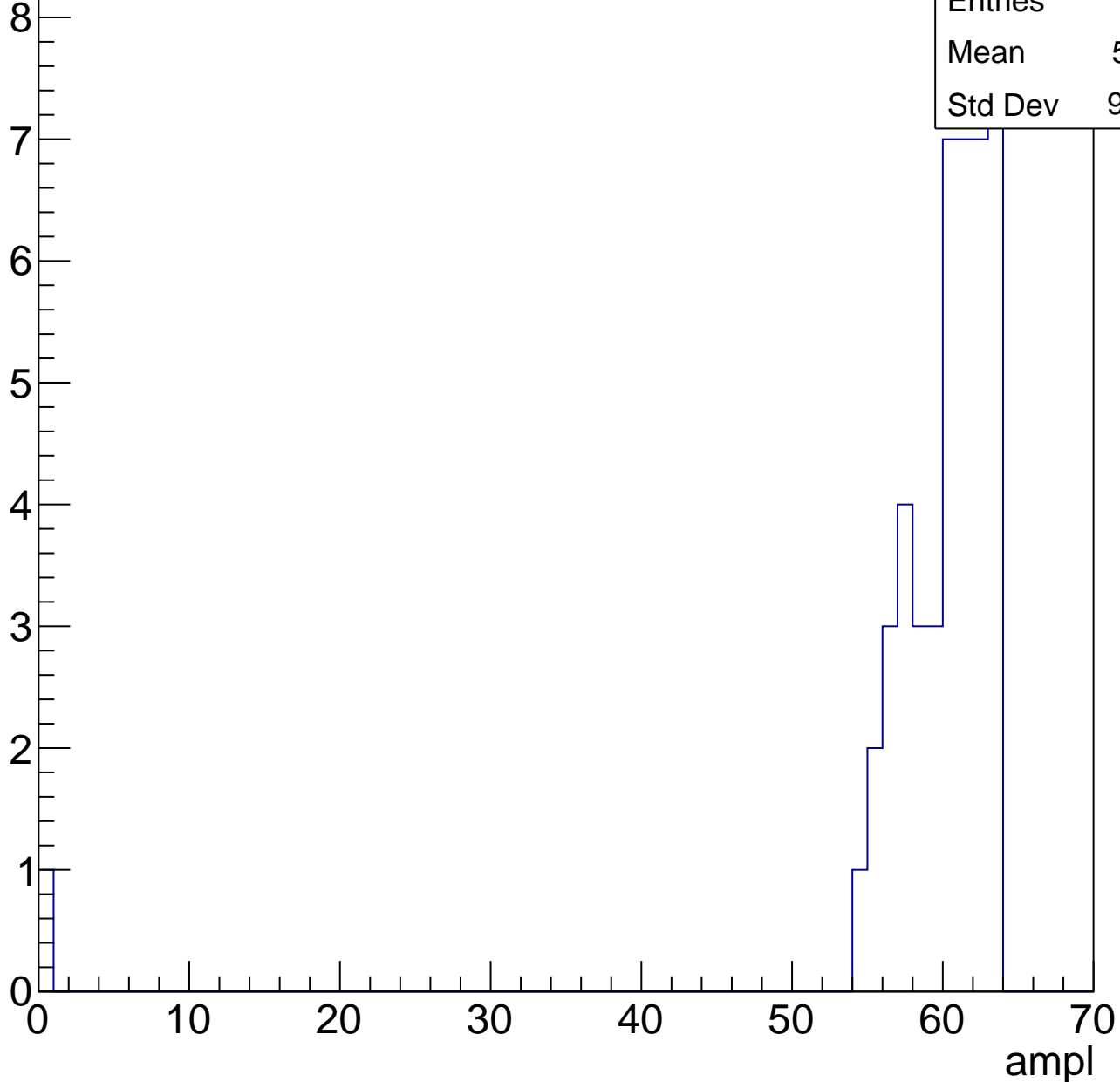


# B1L000S, U24-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

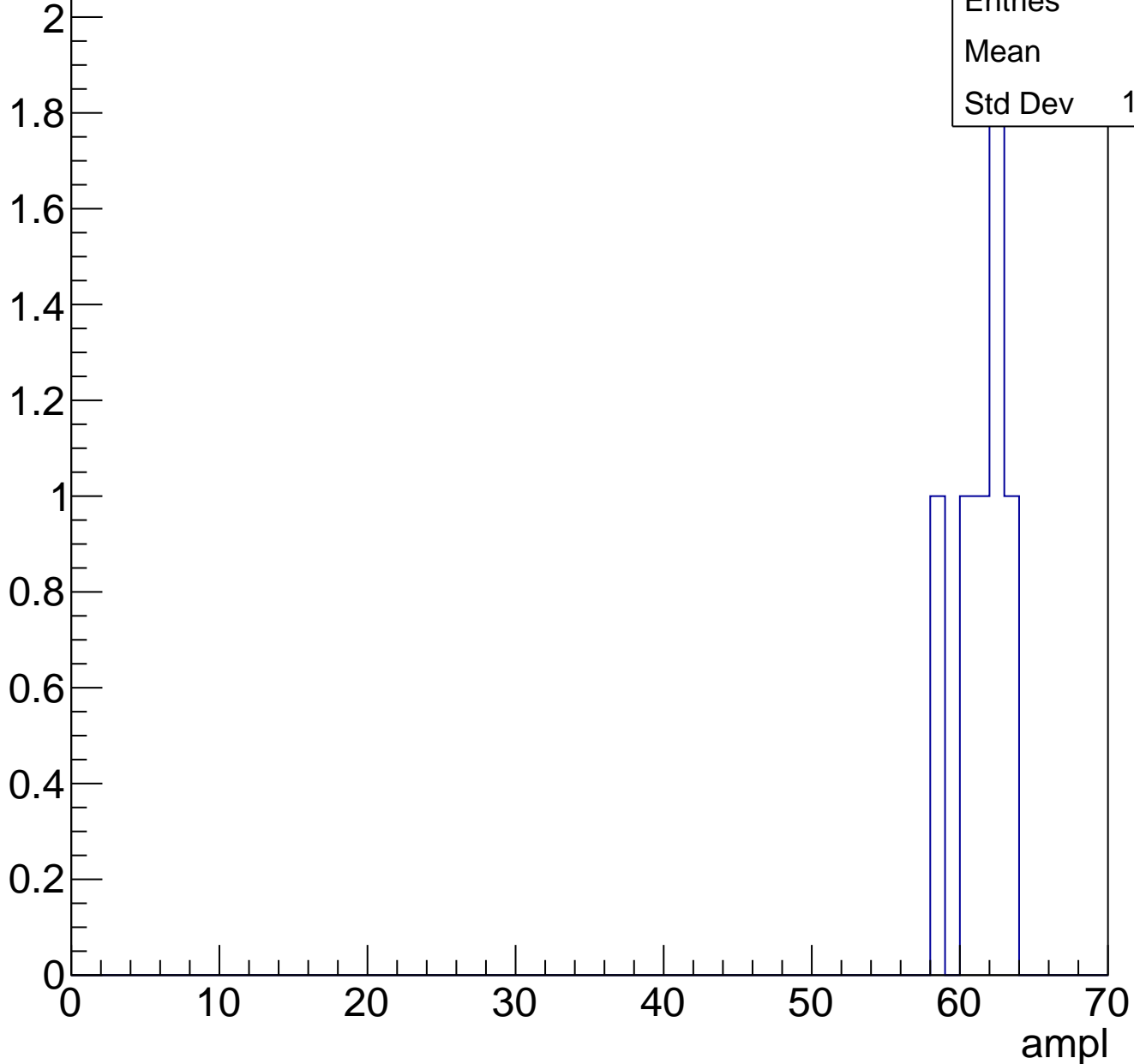
Entries	46
Mean	58.61
Std Dev	9.092



# B1L000S, U24-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch36, adc0

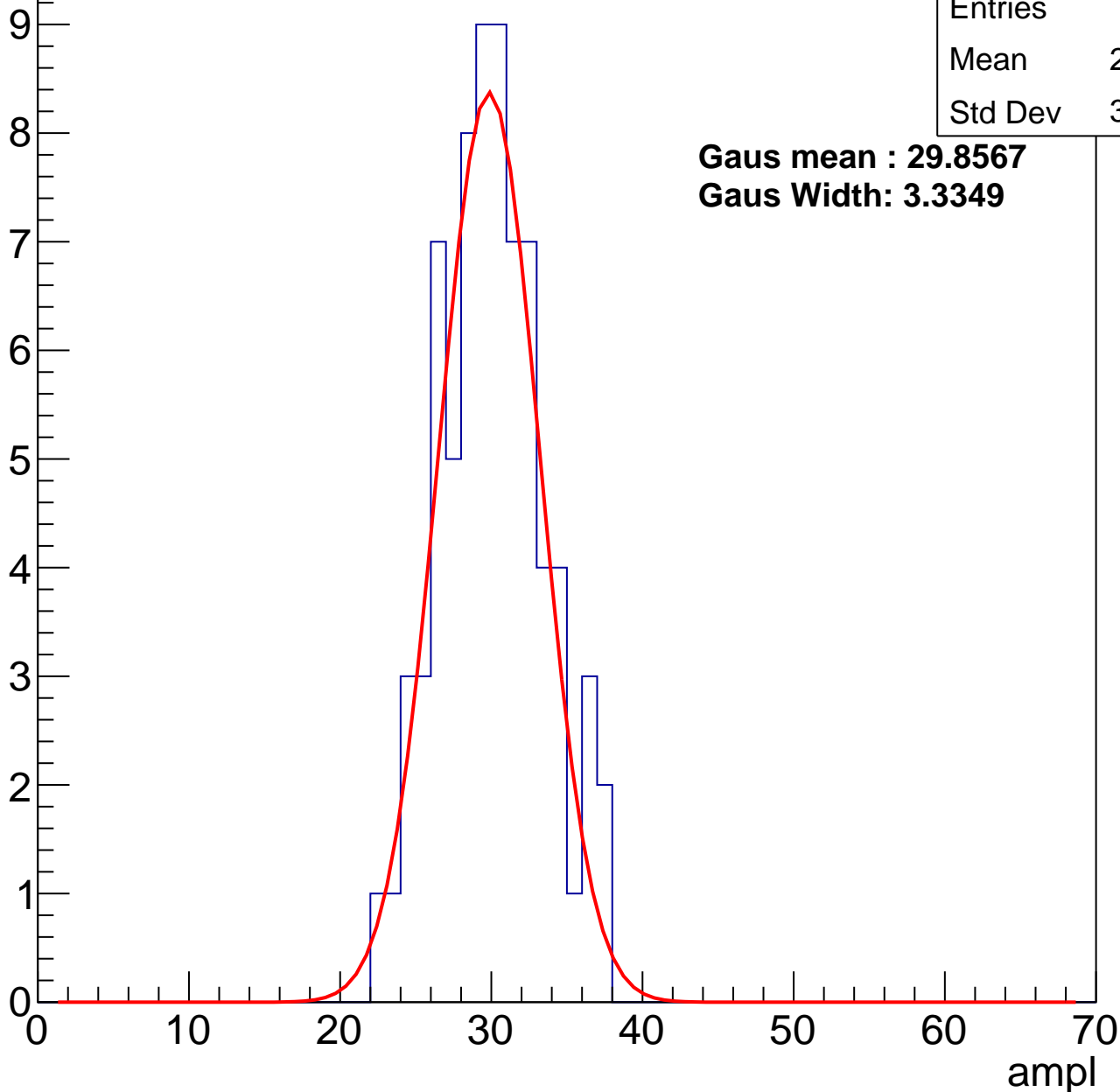
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29.59
Std Dev	3.385

**Gaus mean : 29.8567**

**Gaus Width: 3.3349**



# B1L000S, U24-ch36, adc1

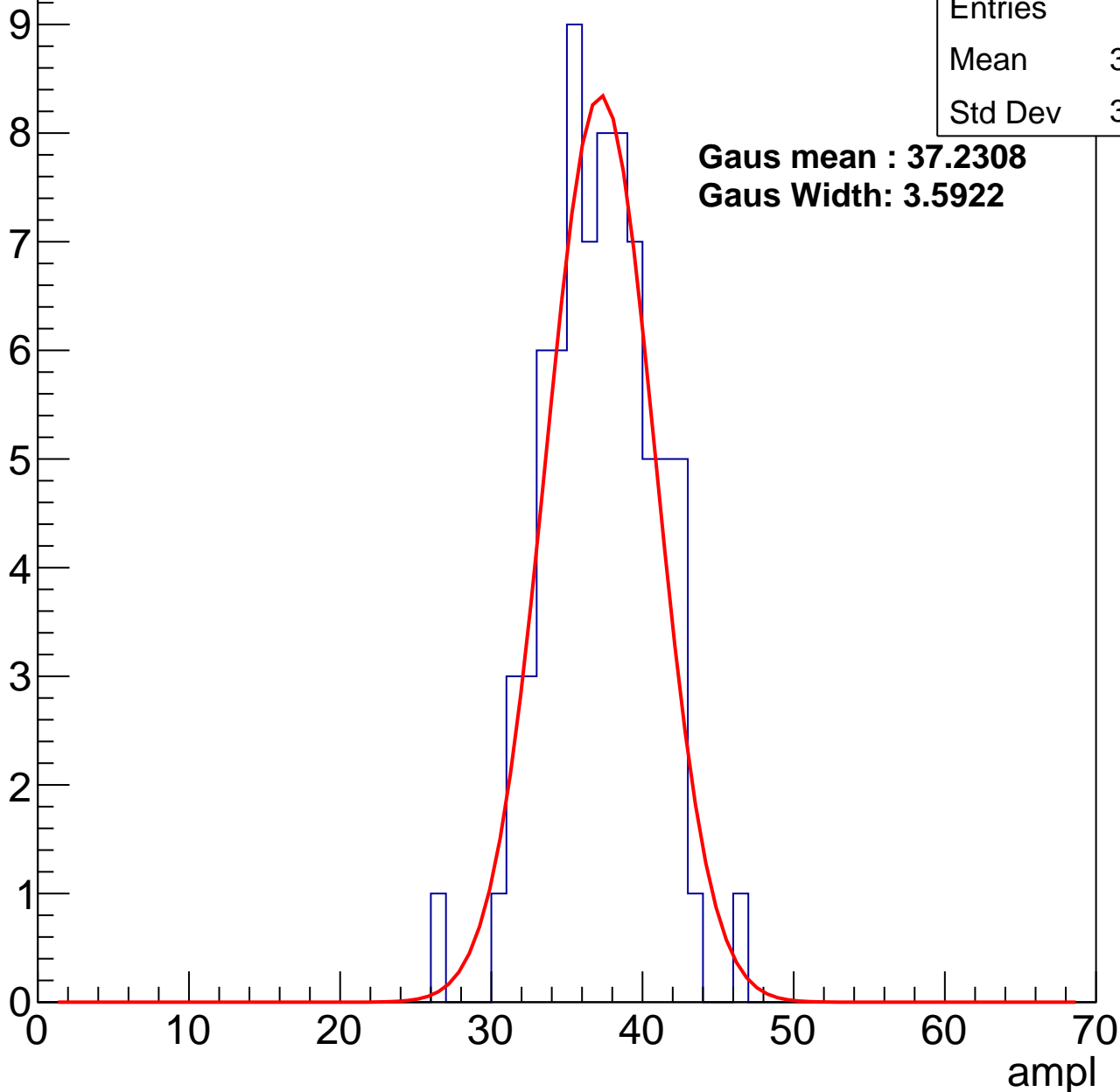
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	36.72
Std Dev	3.523

**Gaus mean : 37.2308**

**Gaus Width: 3.5922**



# B1L000S, U24-ch36, adc2

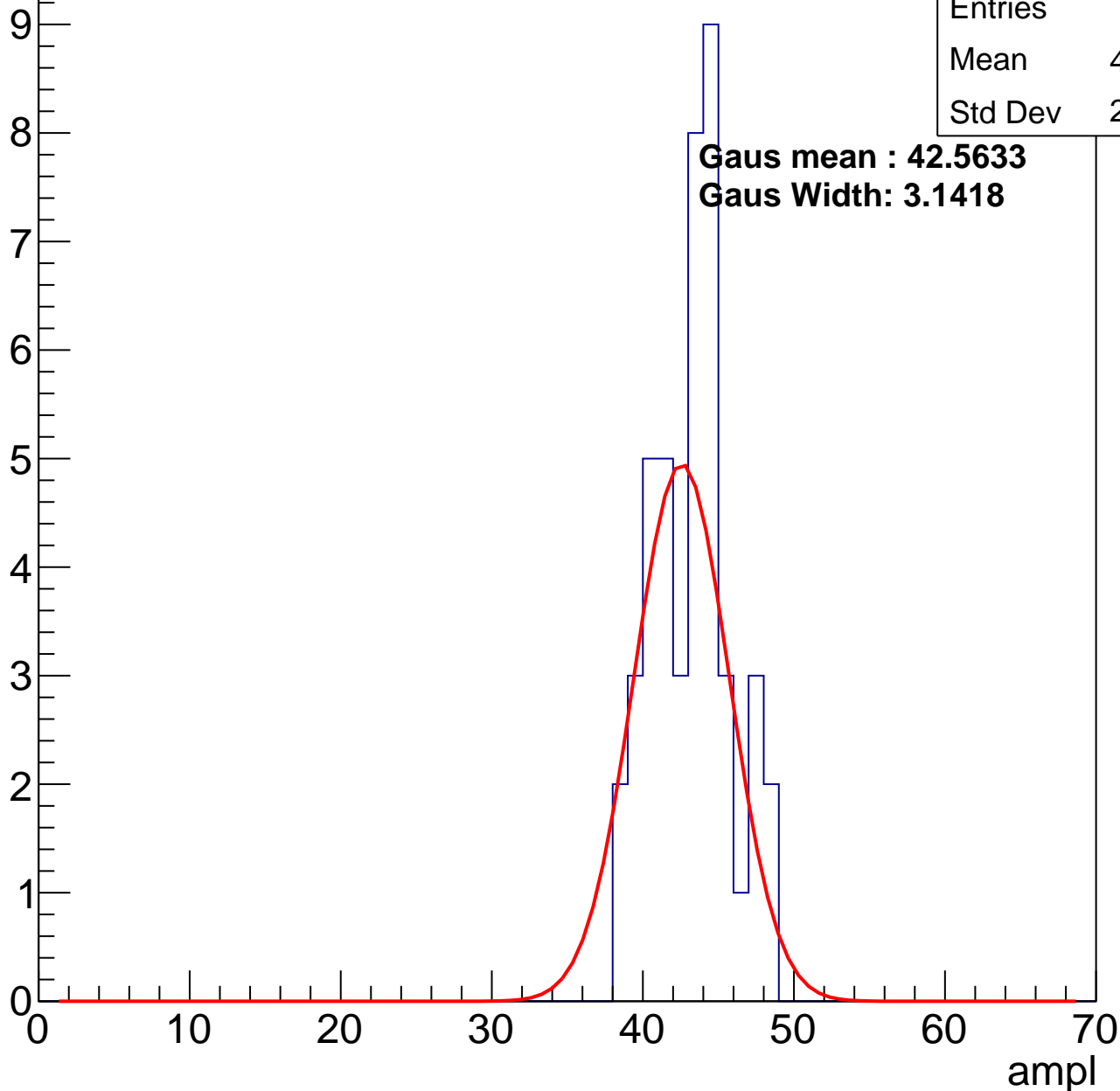
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	42.77
Std Dev	2.575

**Gaus mean : 42.5633**

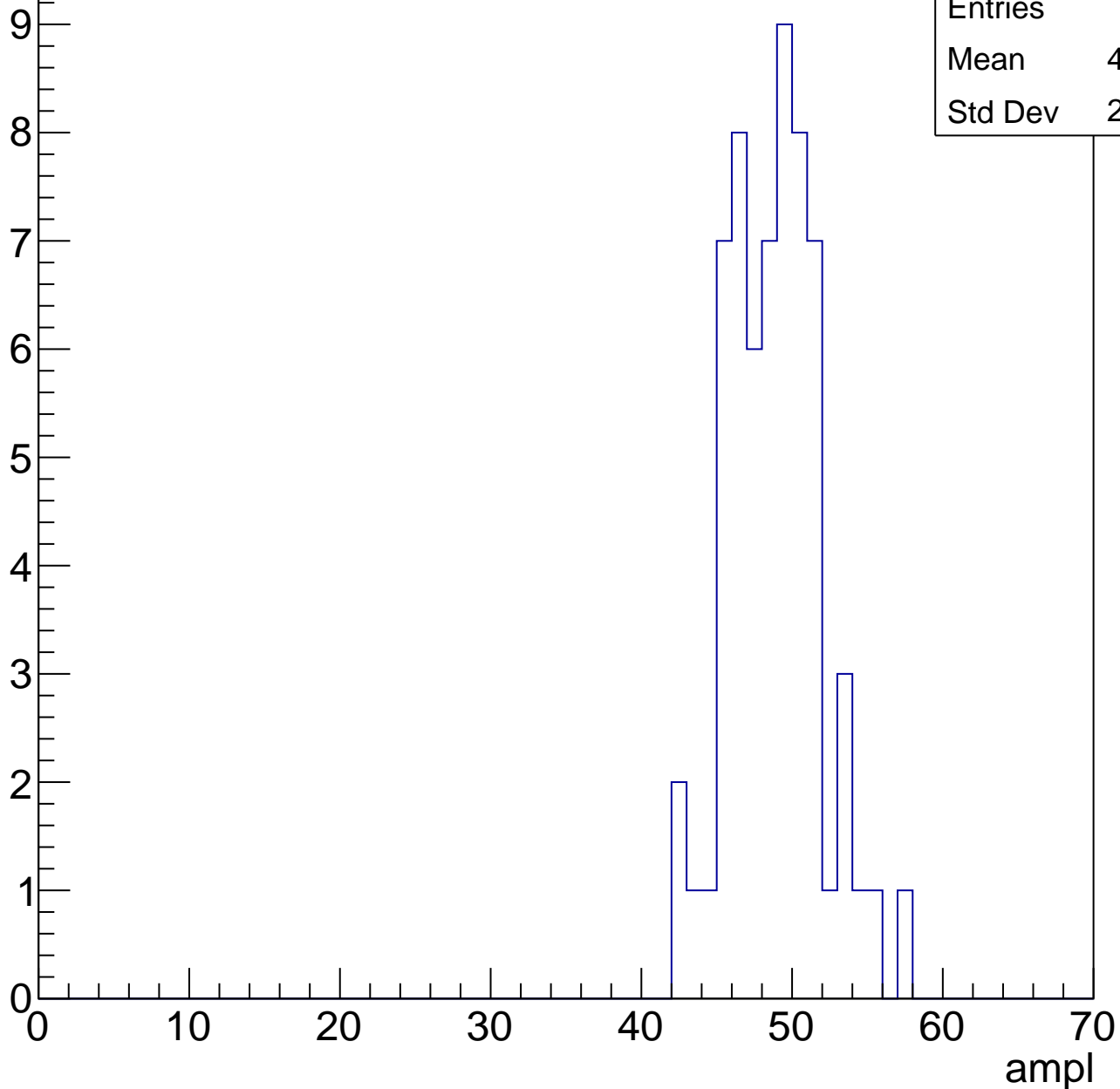
**Gaus Width: 3.1418**



# B1L000S, U24-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

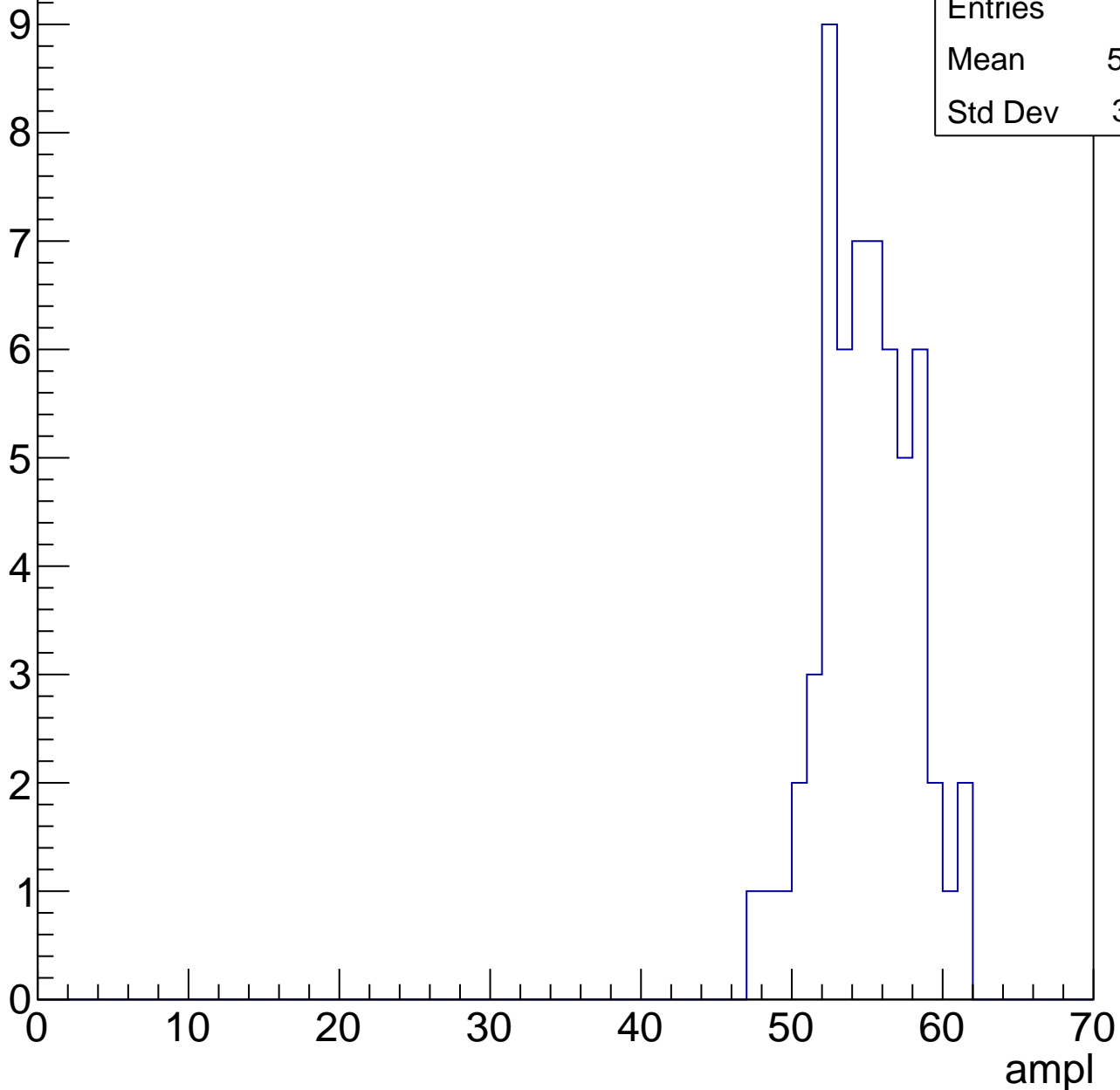


# B1L000S, U24-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

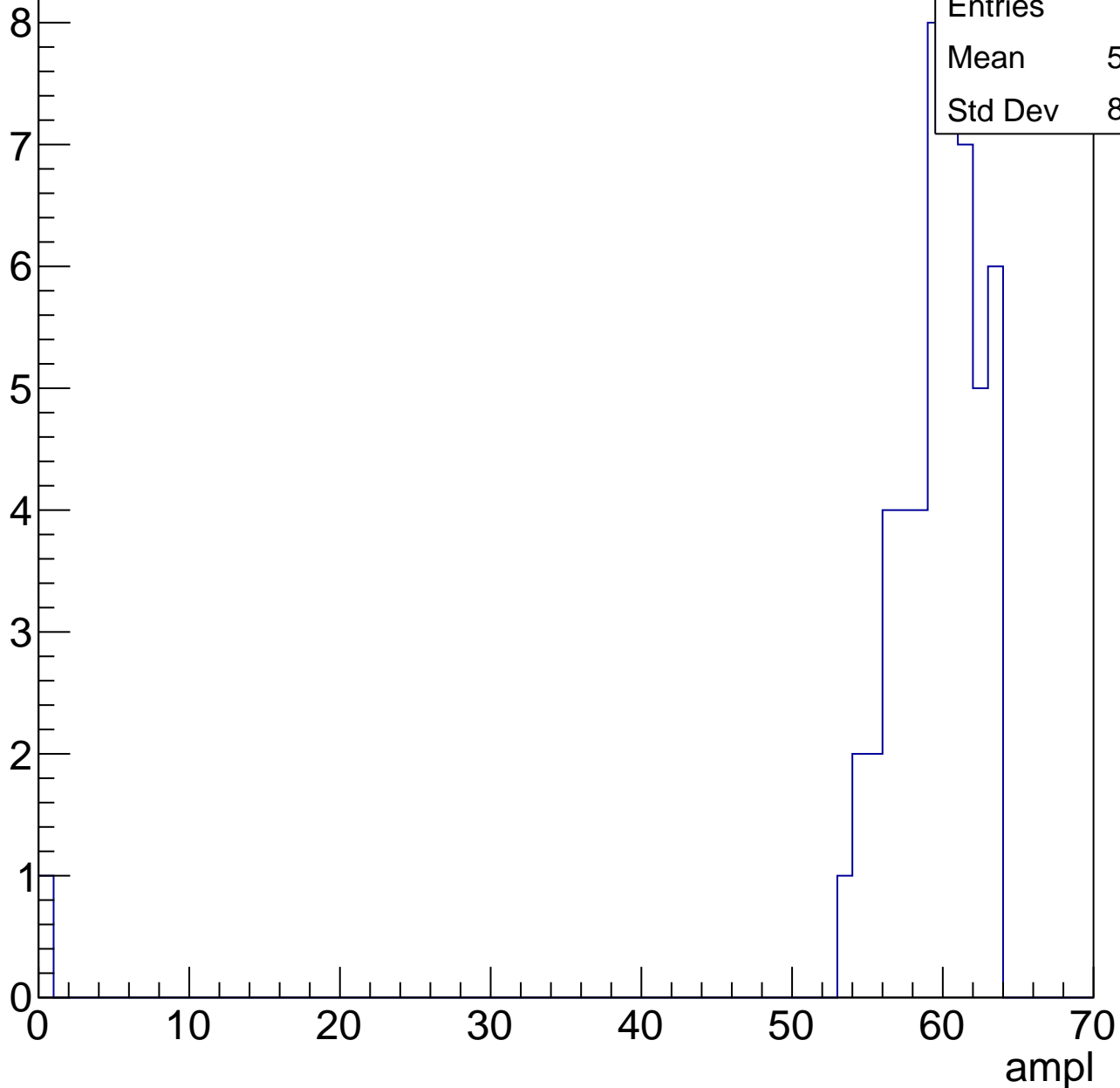
Entries	59
Mean	54.49
Std Dev	3.061



# B1L000S, U24-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

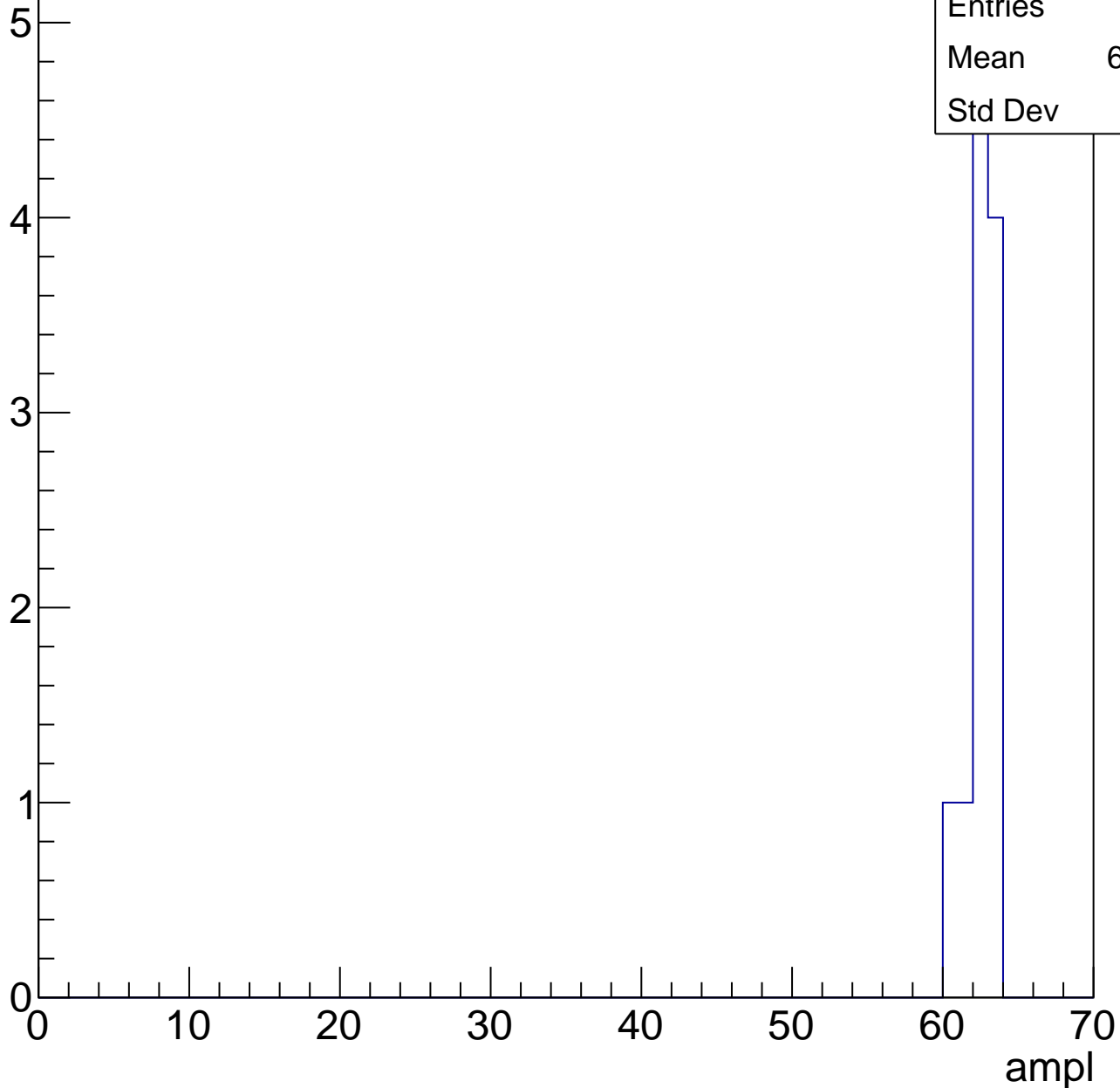


# B1L000S, U24-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	62.09
Std Dev	0.9





# B1L000S, U24-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch37, adc0

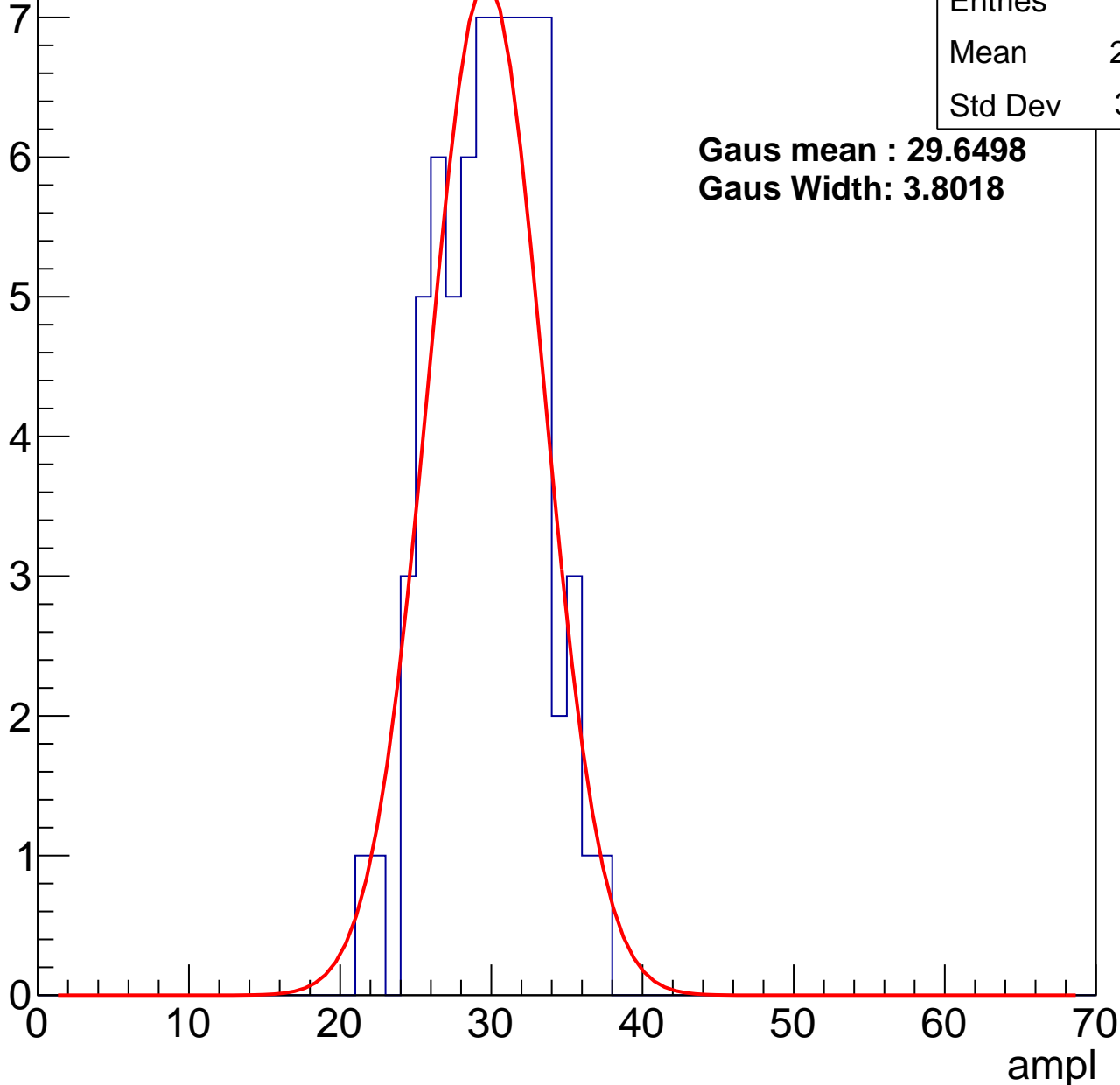
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.42
Std Dev	3.441

**Gaus mean : 29.6498**

**Gaus Width: 3.8018**



# B1L000S, U24-ch37, adc1

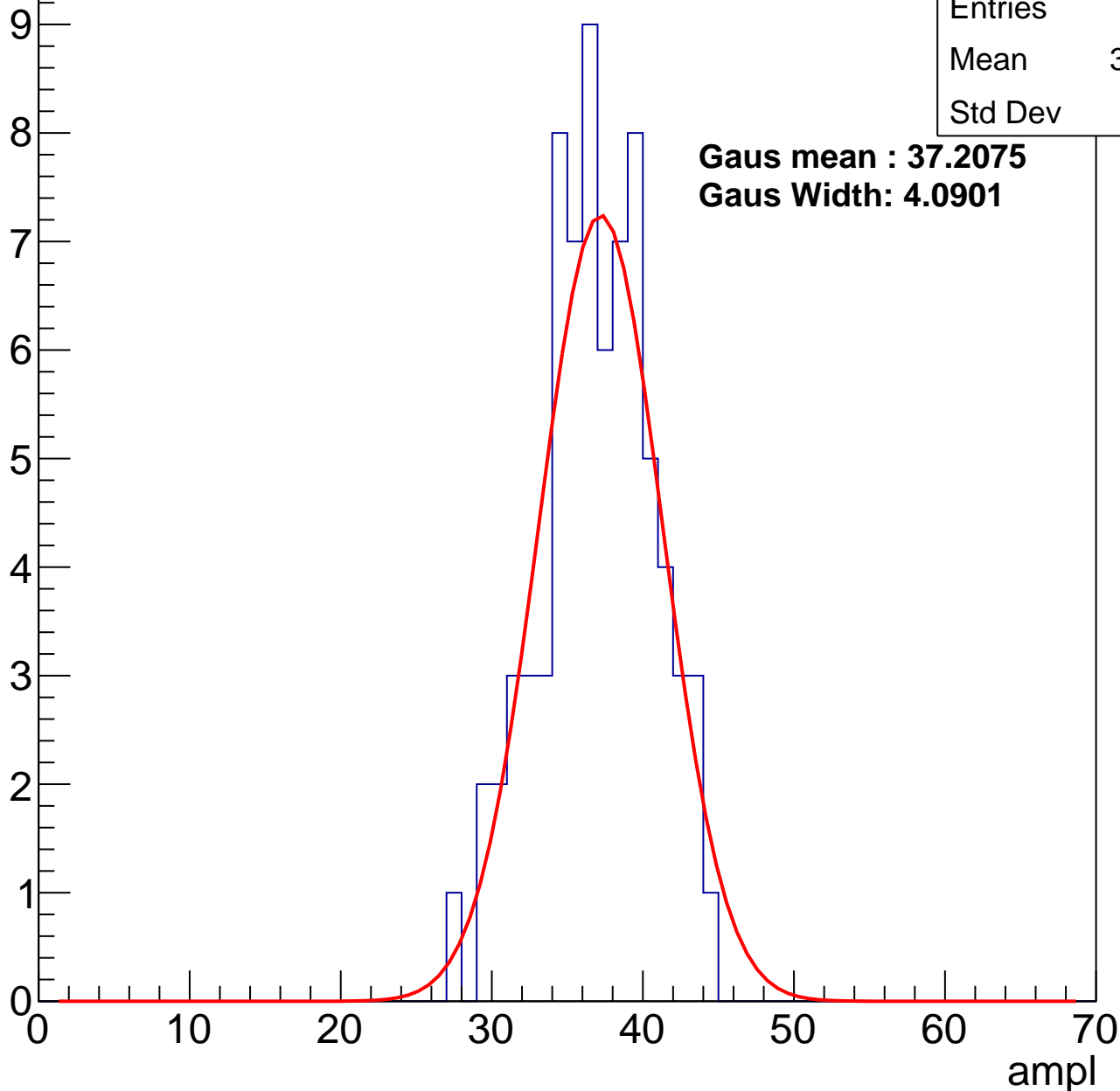
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	36.49
Std Dev	3.7

**Gaus mean : 37.2075**

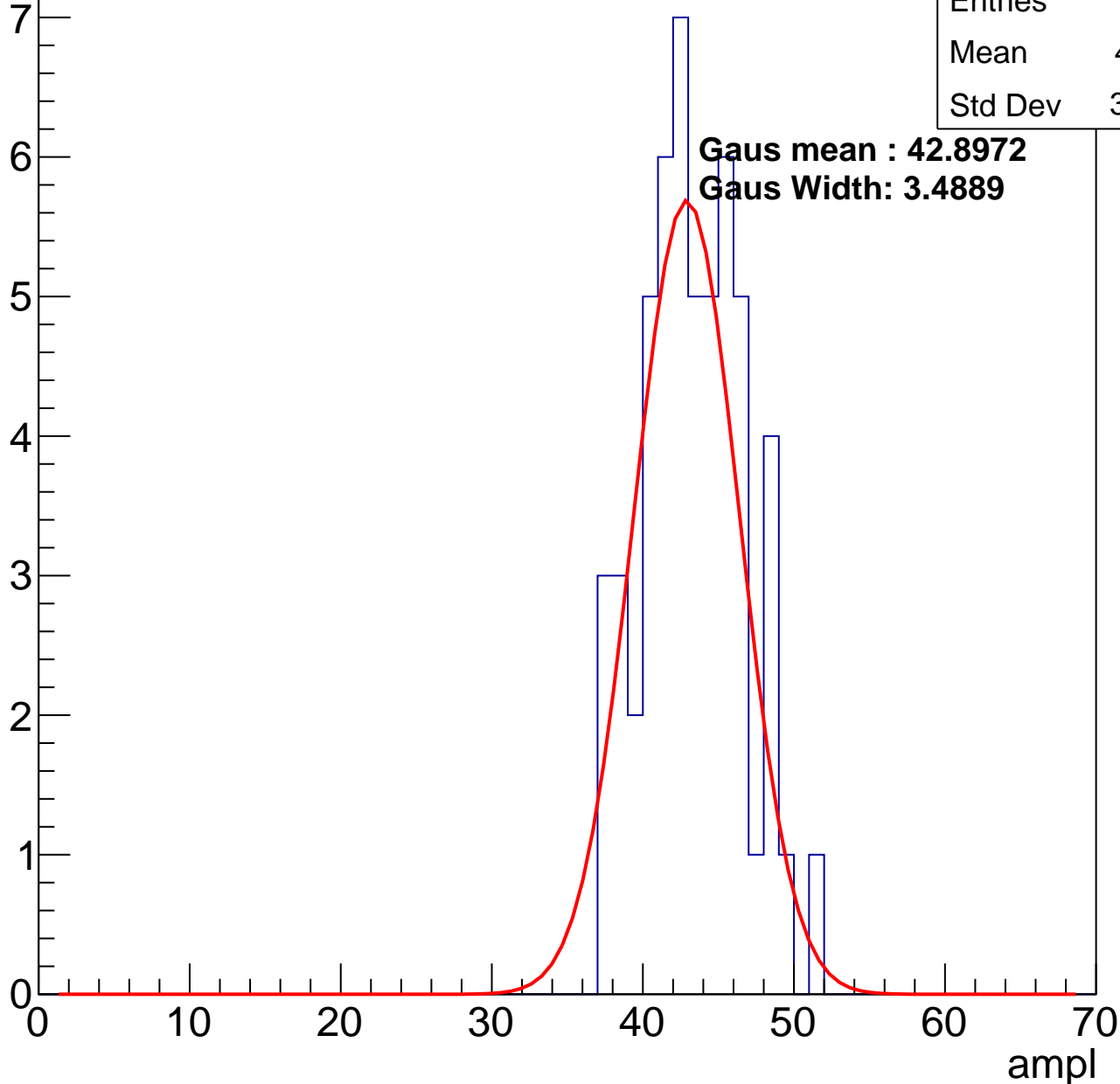
**Gaus Width: 4.0901**



# B1L000S, U24-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

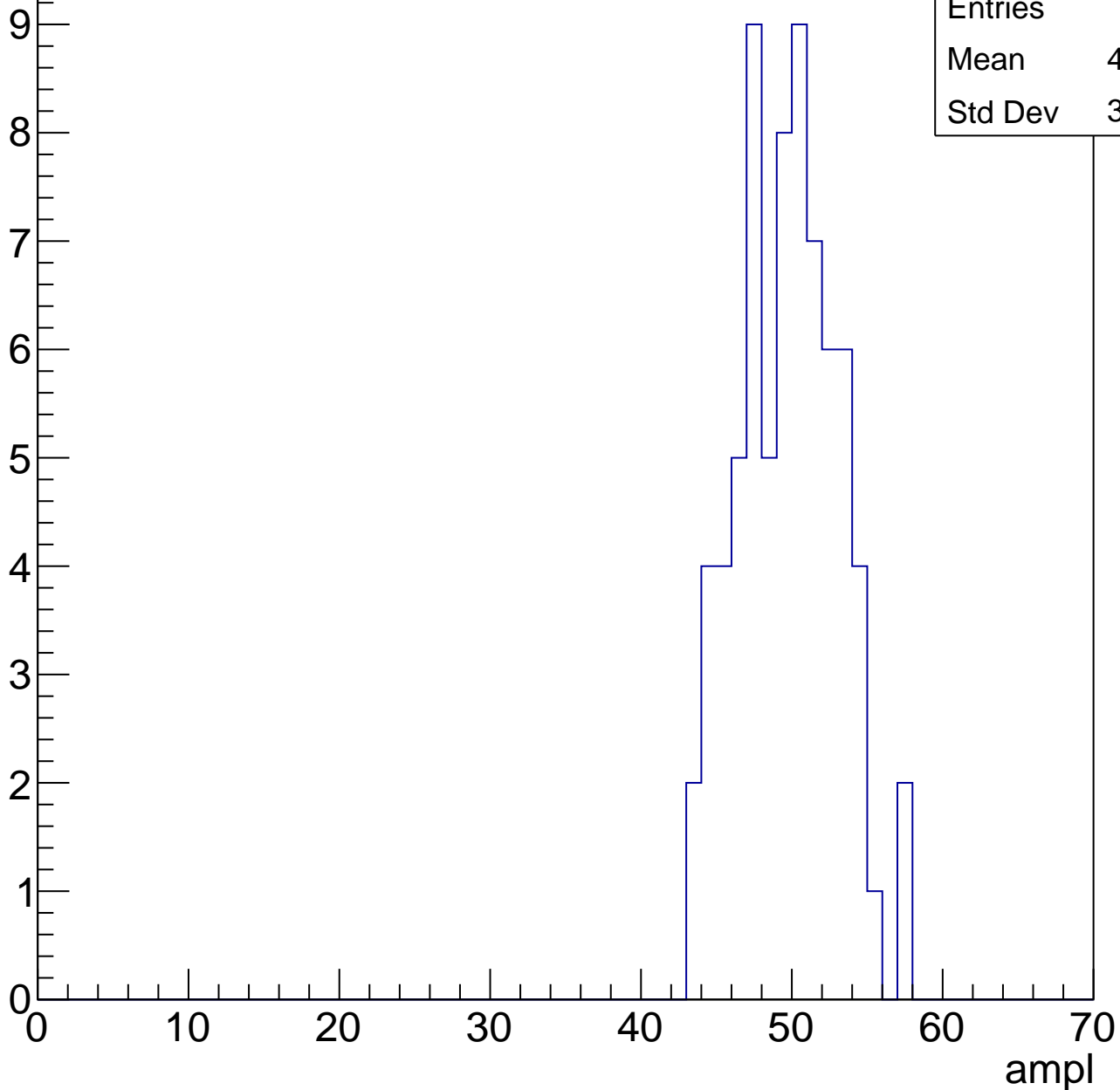
Entry



# B1L000S, U24-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

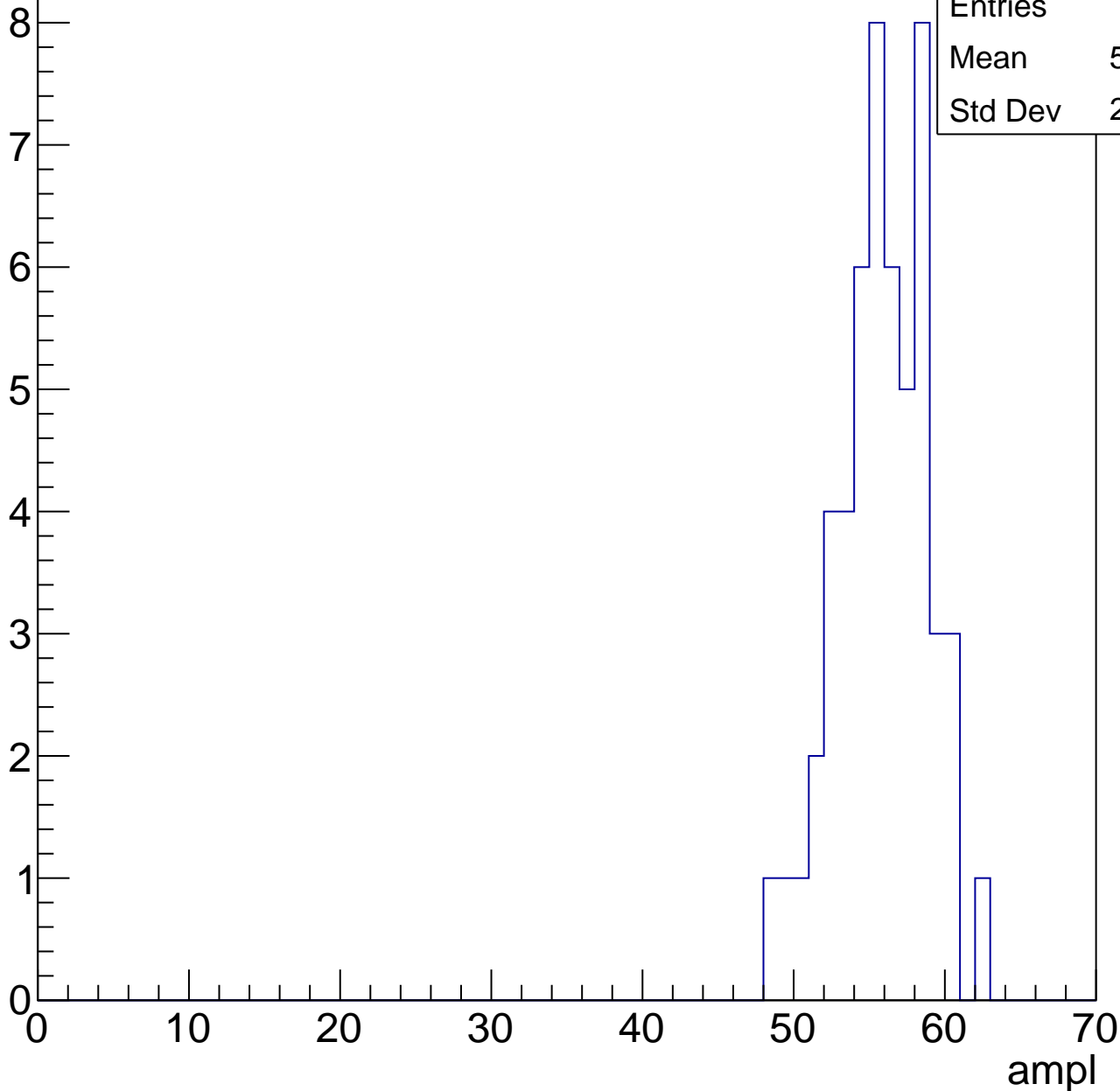


# B1L000S, U24-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	55.42
Std Dev	2.949



# B1L000S, U24-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

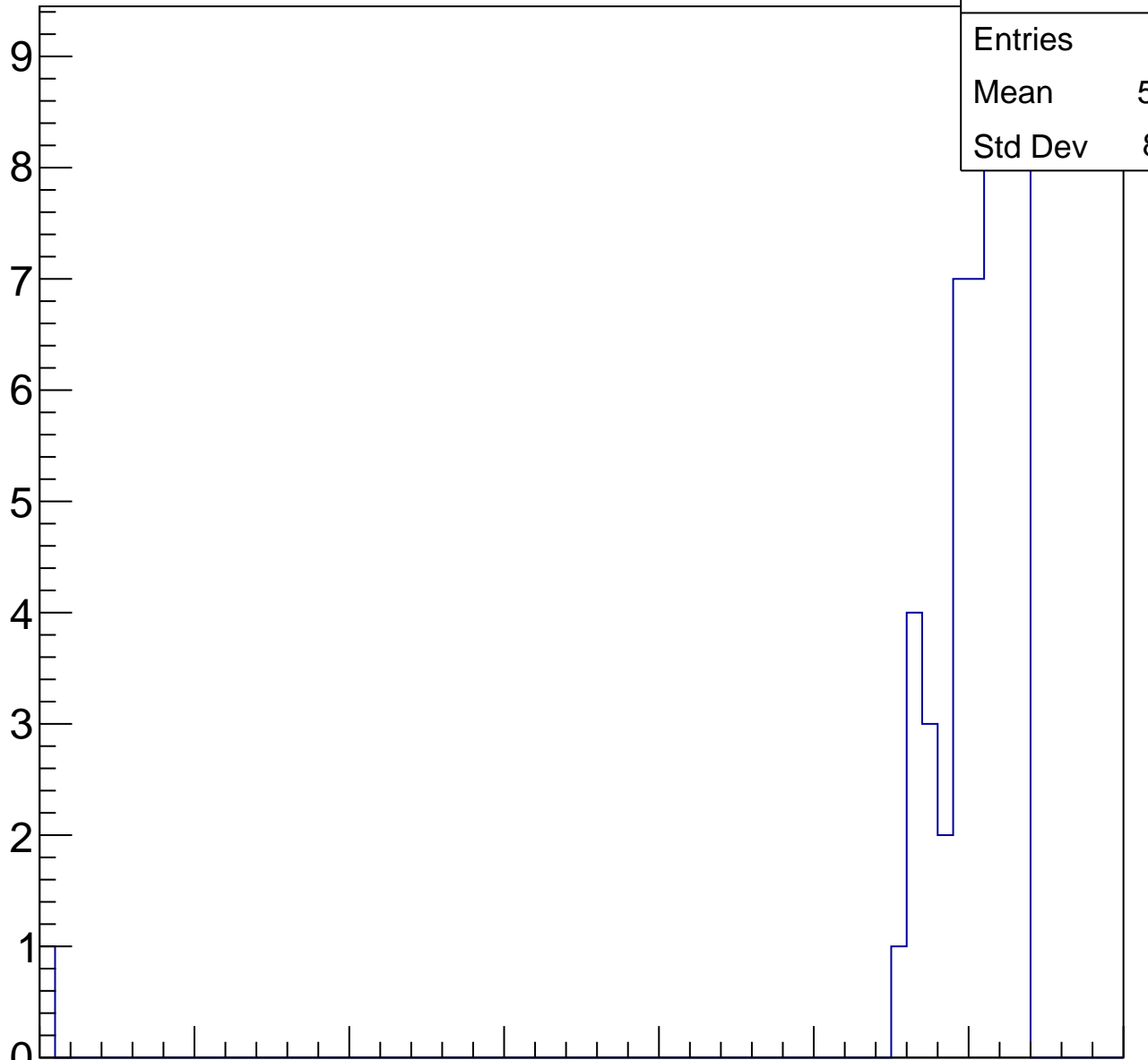
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.02
Std Dev	8.631

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B1L000S, U24-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch38, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	29.65
Std Dev	2.286

**Gaus mean : 30.2279**

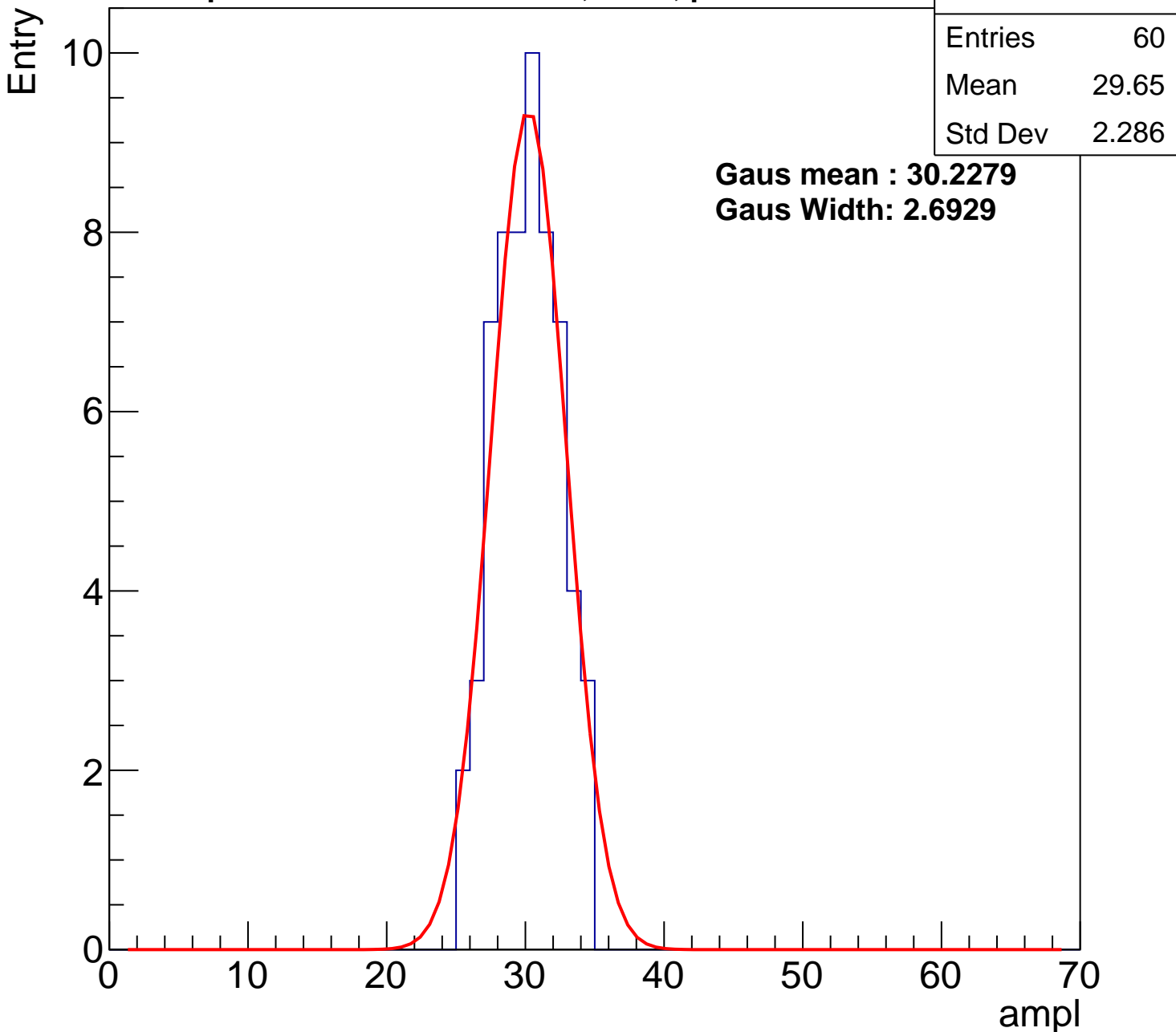
**Gaus Width: 2.6929**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch38, adc1

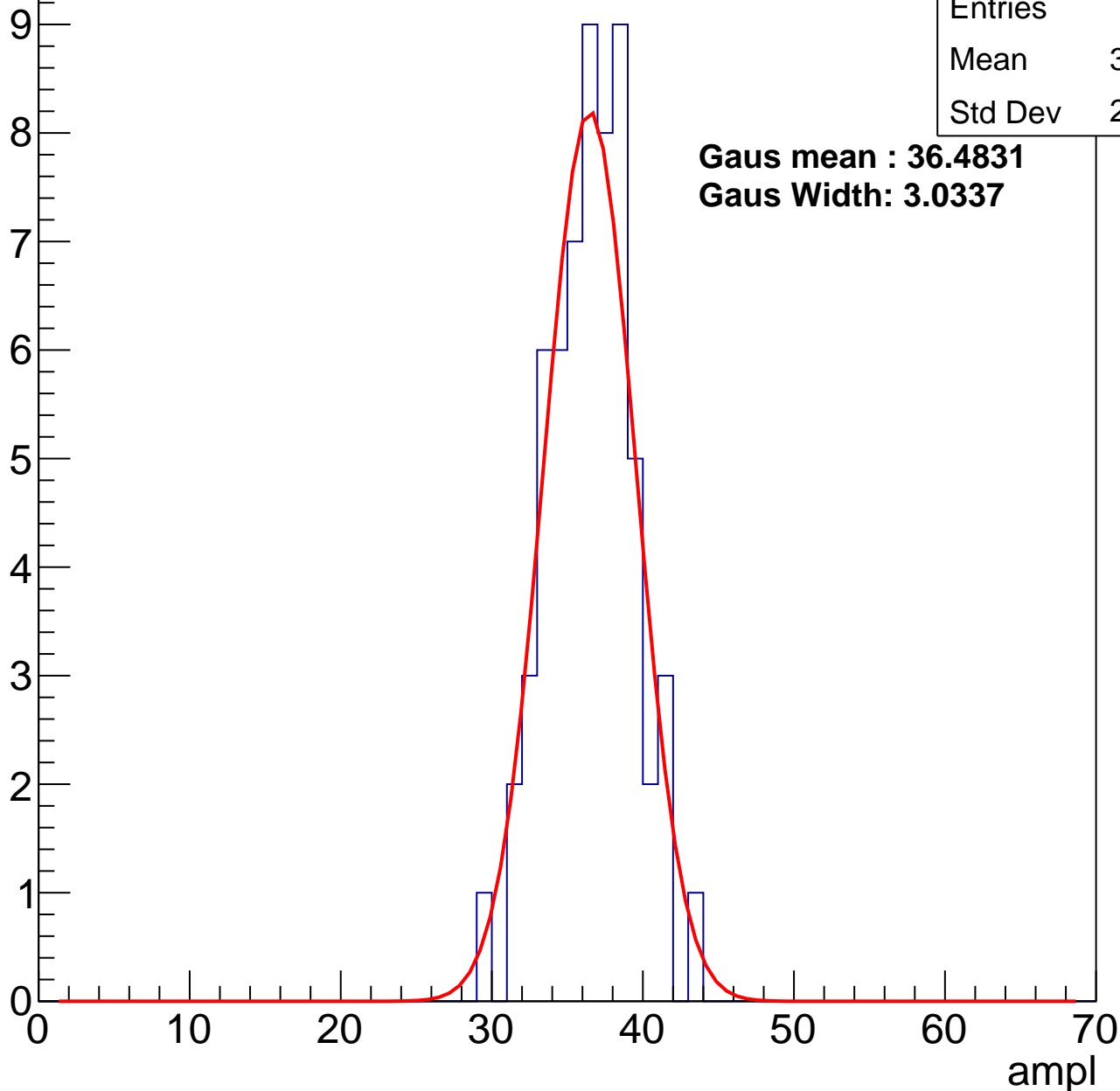
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.08
Std Dev	2.773

**Gaus mean : 36.4831**

**Gaus Width: 3.0337**



# B1L000S, U24-ch38, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.45
Std Dev	3.484

**Gaus mean : 43.4391**

**Gaus Width: 3.1672**

10

8

6

4

2

0

0

10

20

30

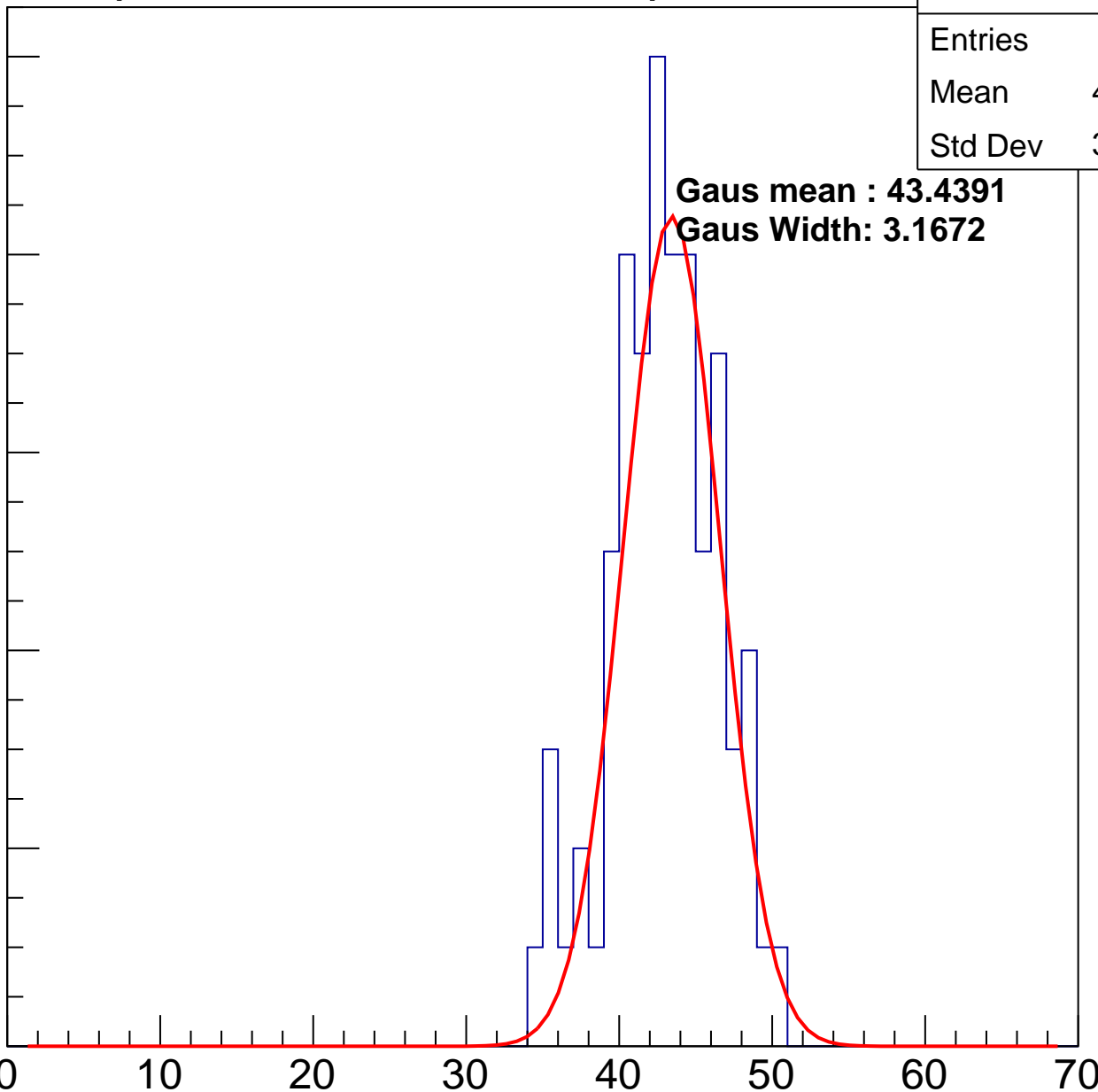
40

50

60

70

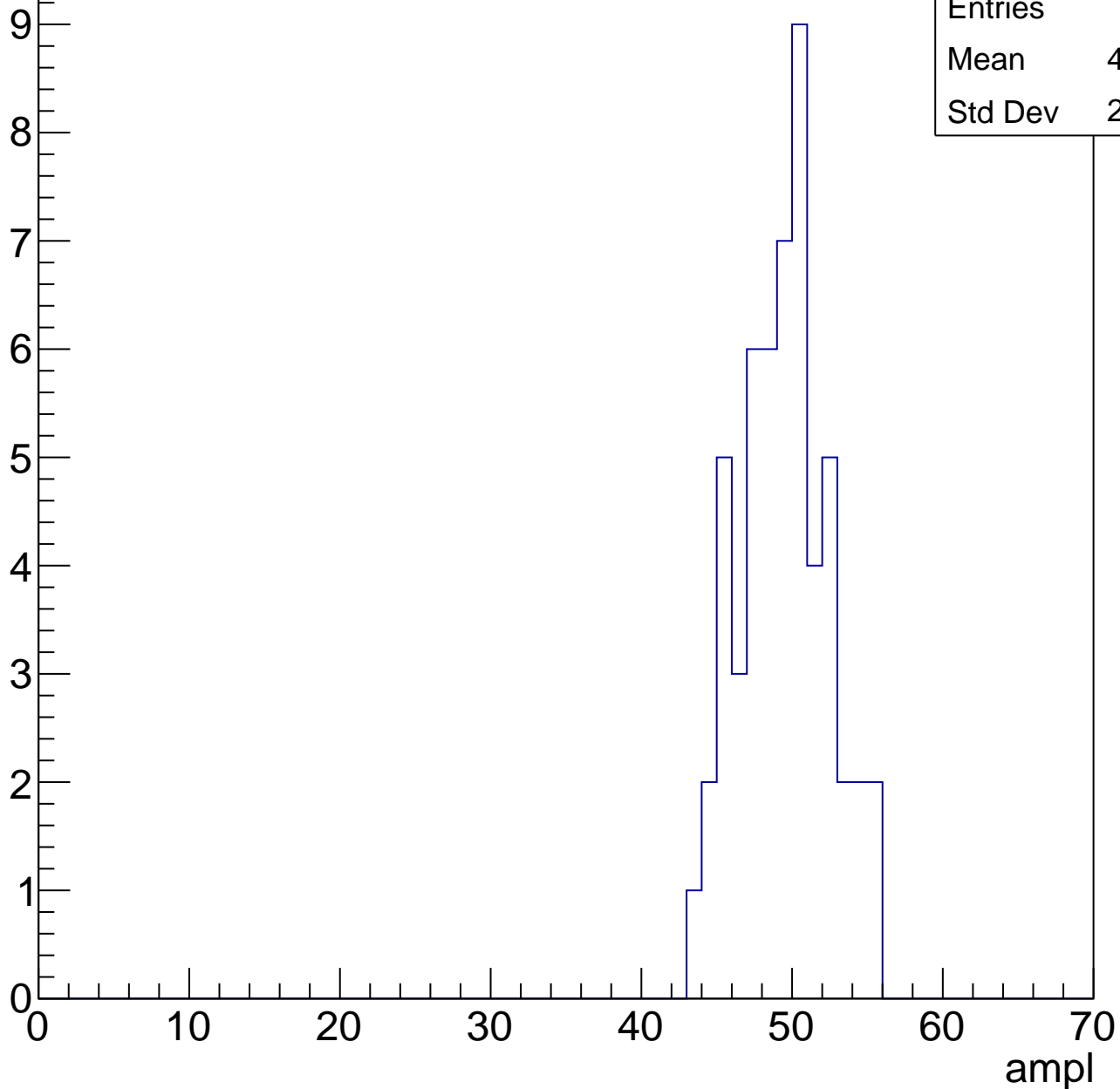
ampl



# B1L000S, U24-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



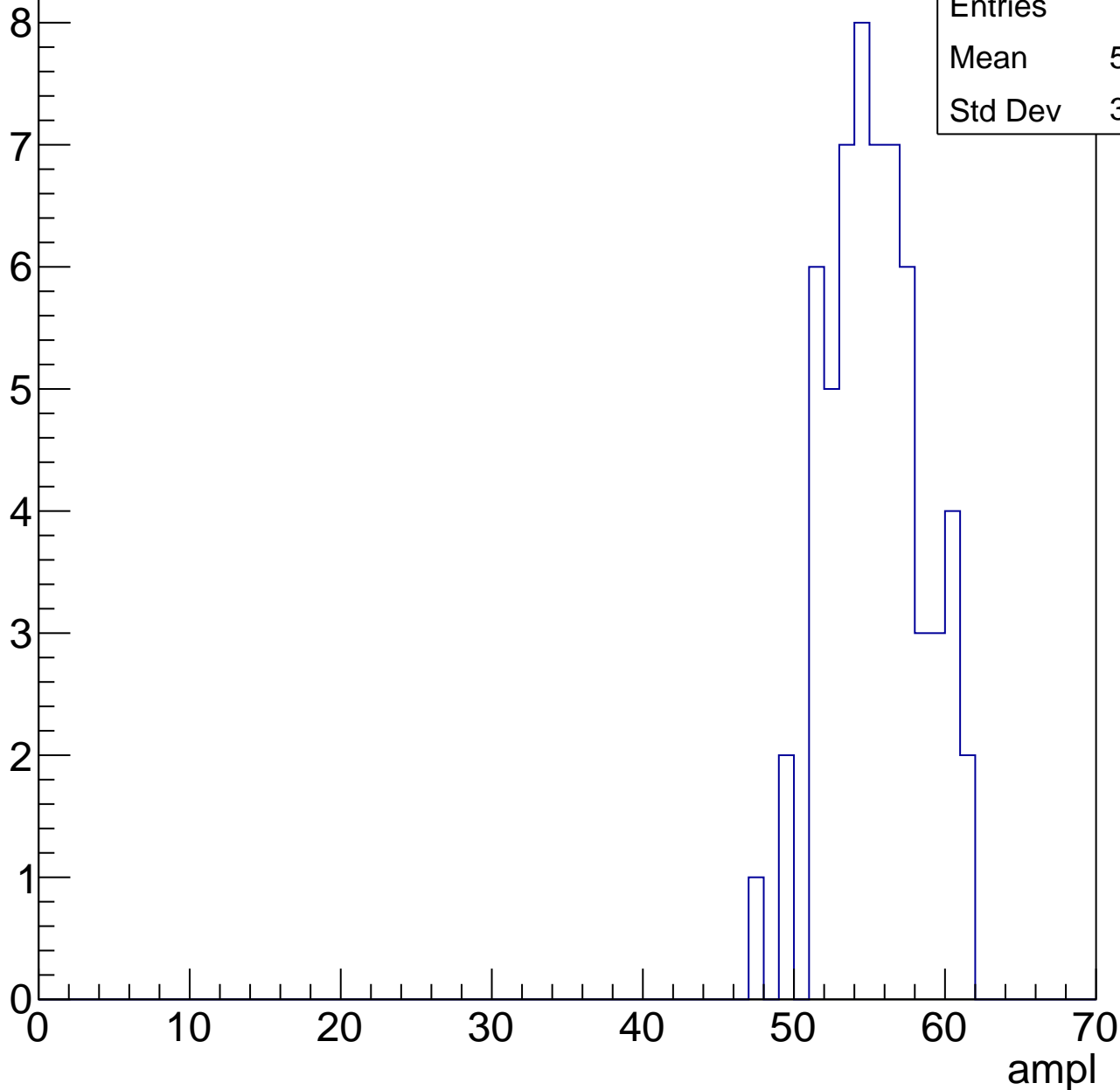
Entries	54
Mean	48.98
Std Dev	2.877

# B1L000S, U24-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	54.85
Std Dev	3.109

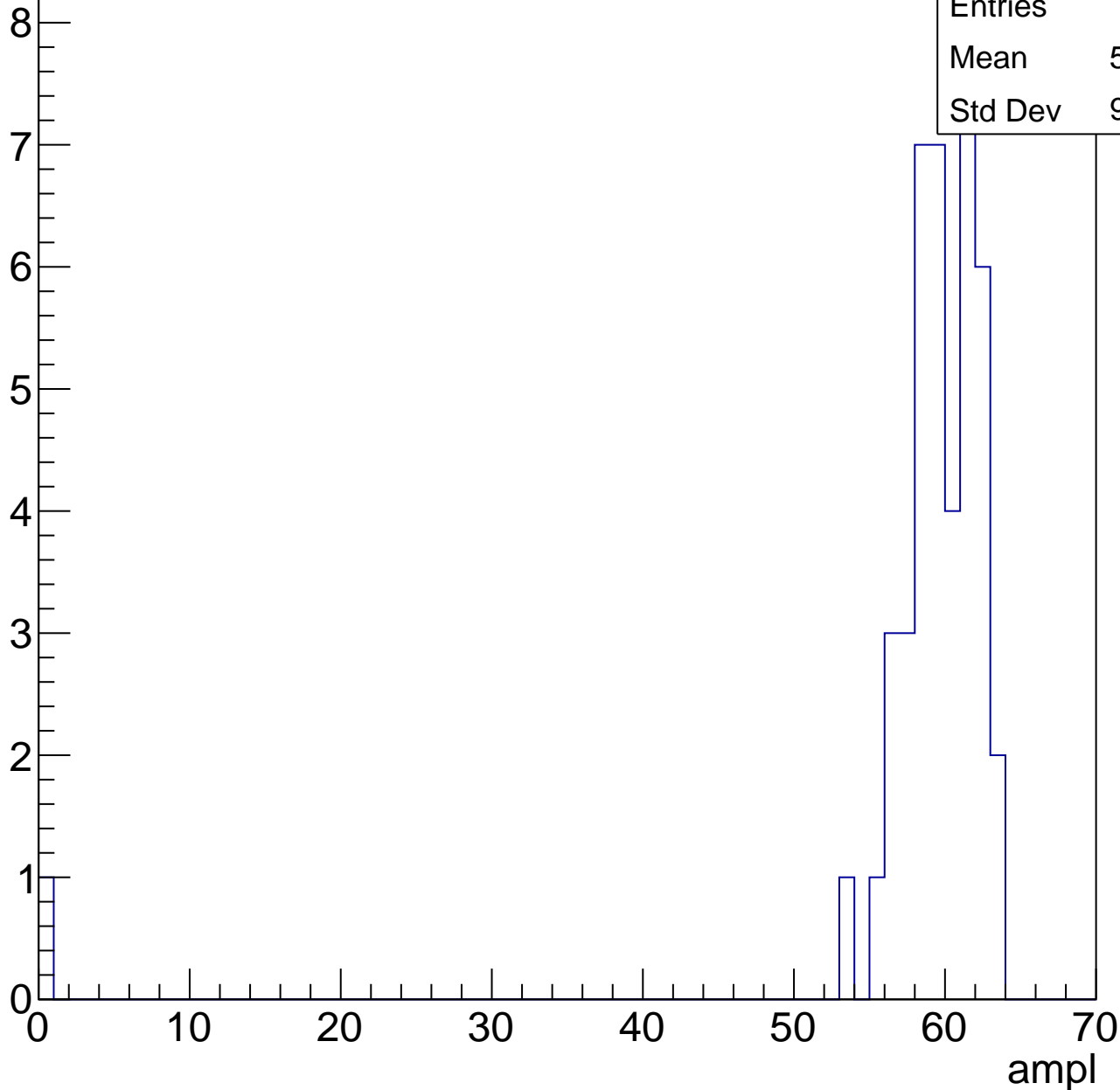


# B1L000S, U24-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	57.95
Std Dev	9.218

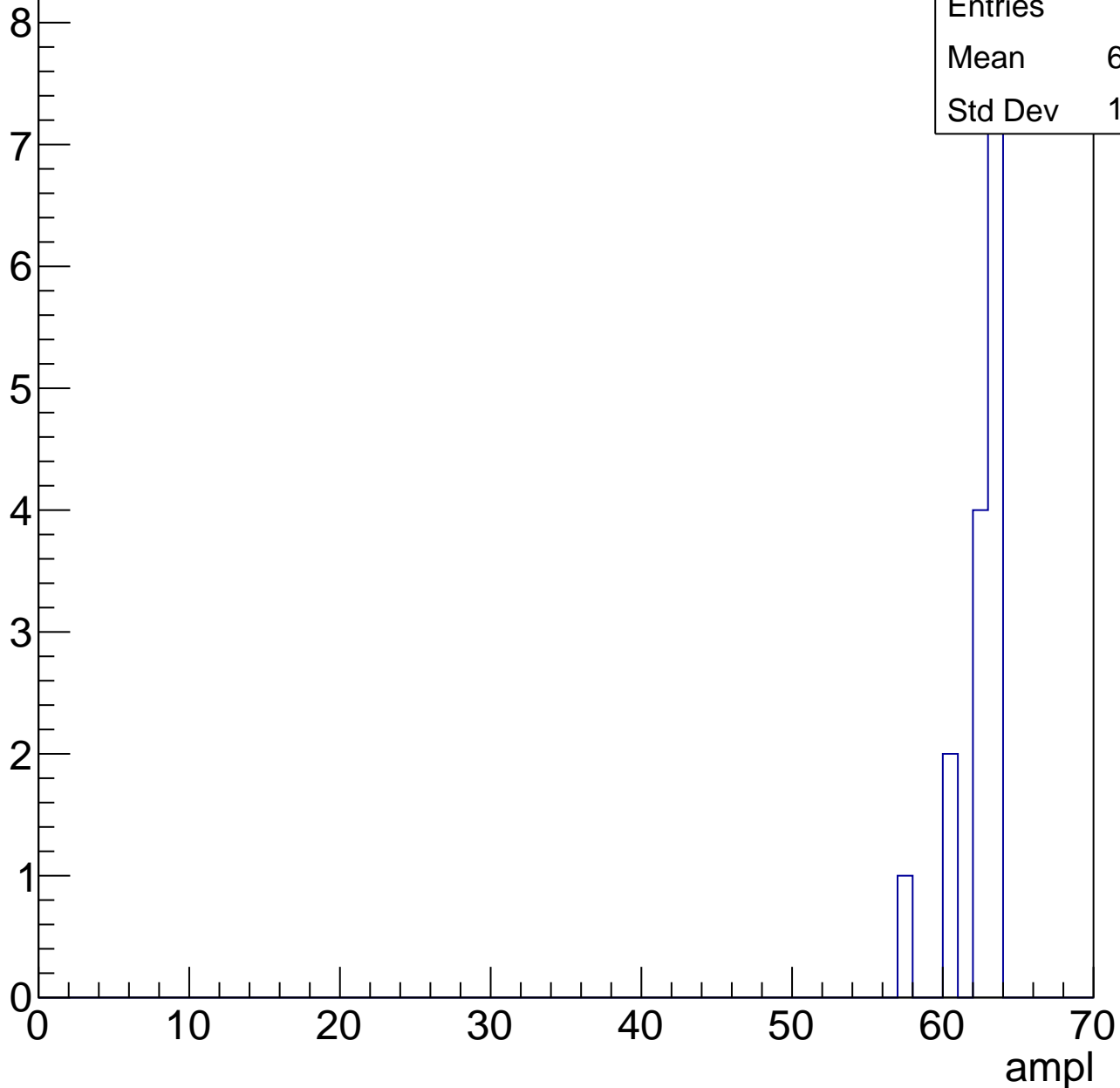


# B1L000S, U24-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.93
Std Dev	1.652





# B1L000S, U24-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	29.51
Std Dev	3.211

**Gaus mean : 30.2830**

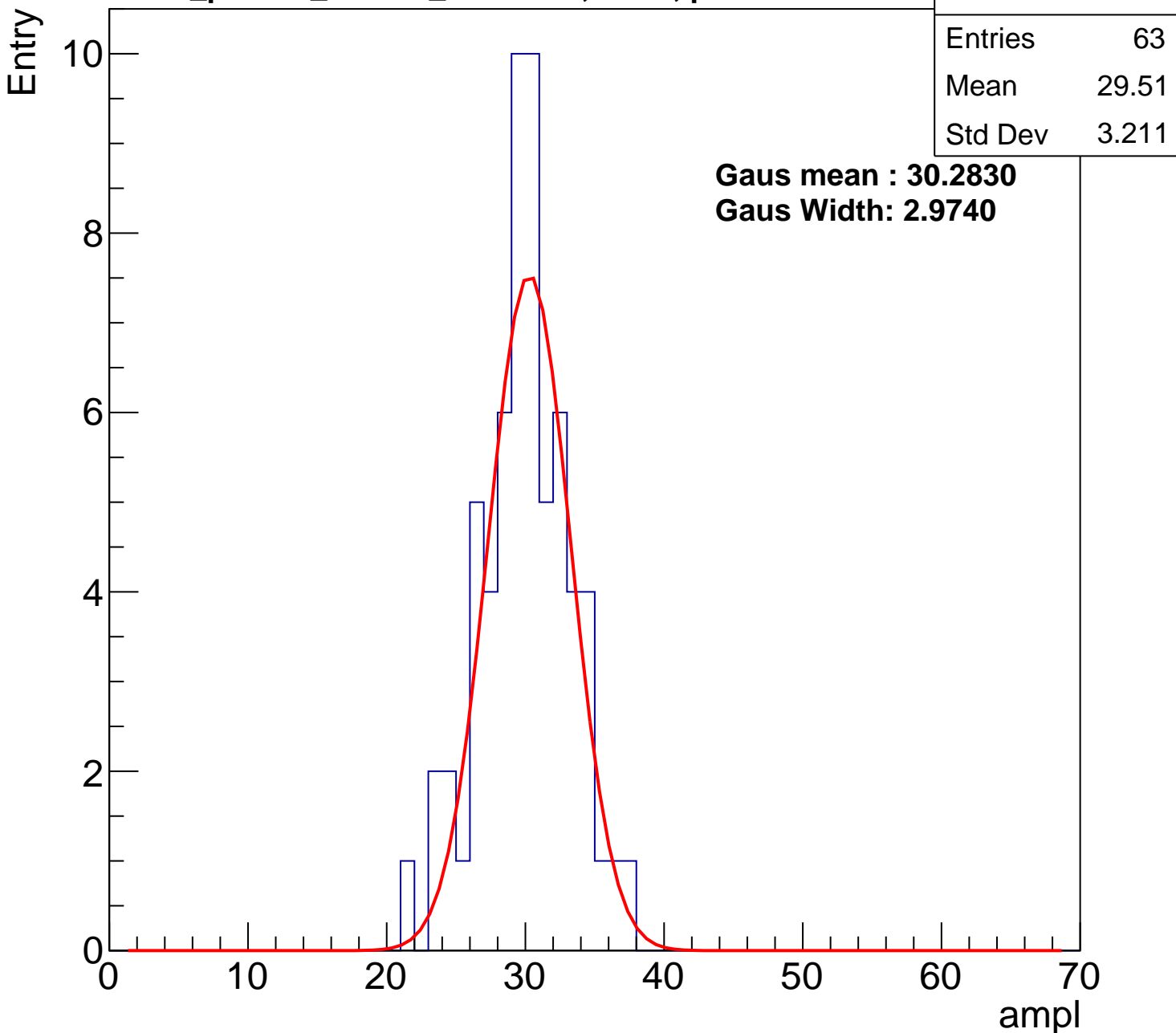
**Gaus Width: 2.9740**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch39, adc1

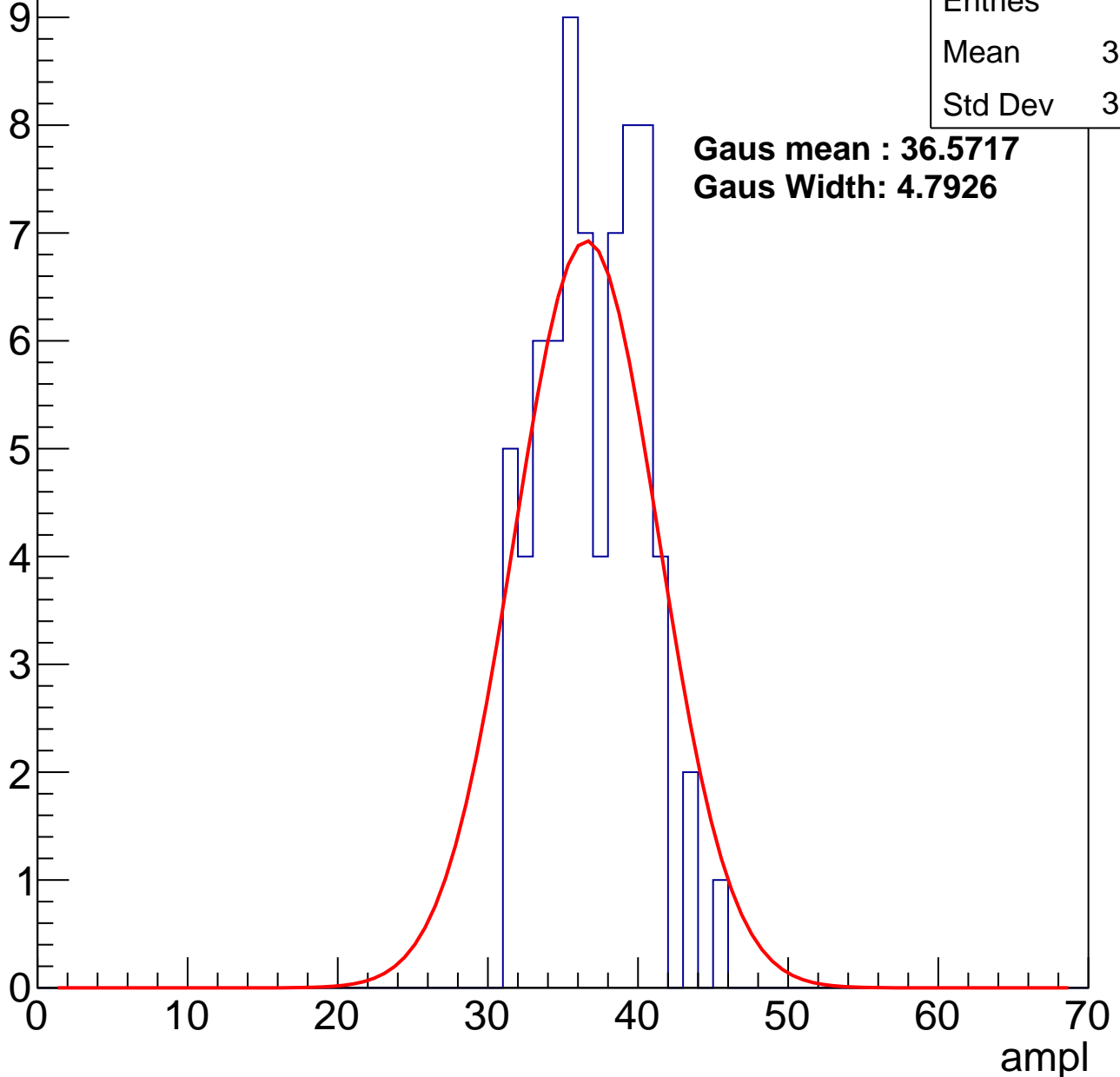
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	36.52
Std Dev	3.288

**Gaus mean : 36.5717**

**Gaus Width: 4.7926**



# B1L000S, U24-ch39, adc2

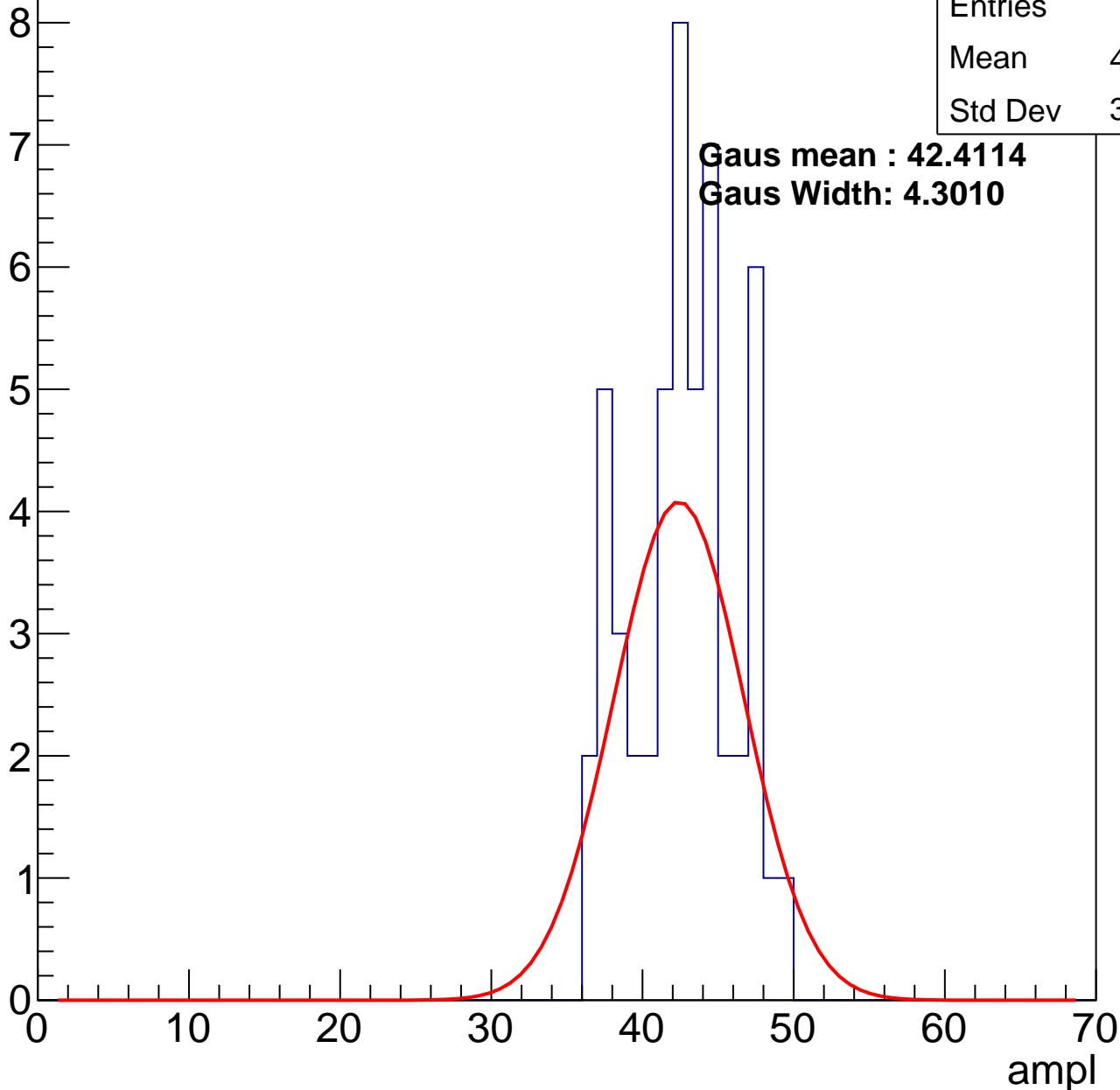
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	42.24
Std Dev	3.405

**Gaus mean : 42.4114**

**Gaus Width: 4.3010**



# B1L000S, U24-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

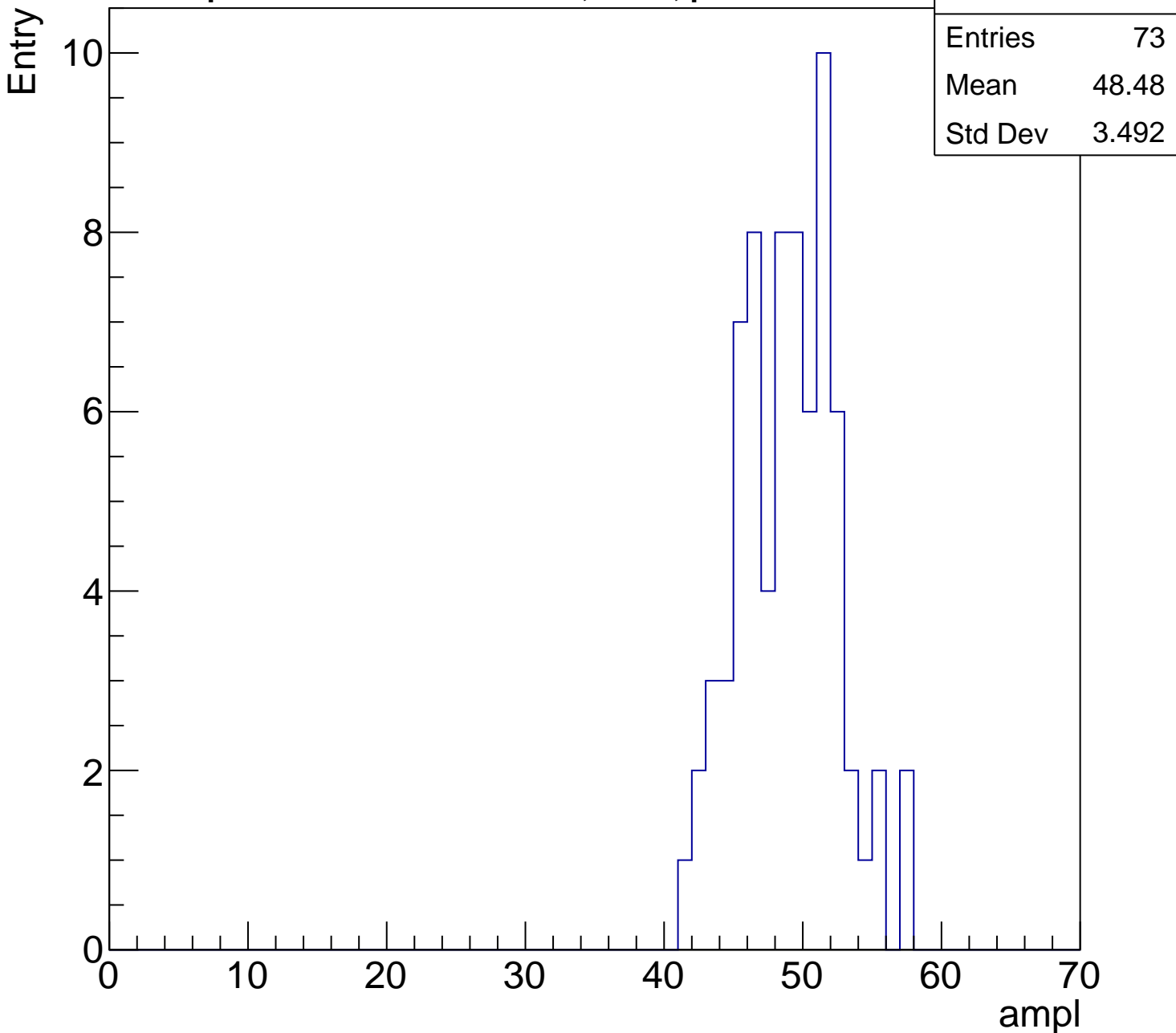
Entries	73
Mean	48.48
Std Dev	3.492

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

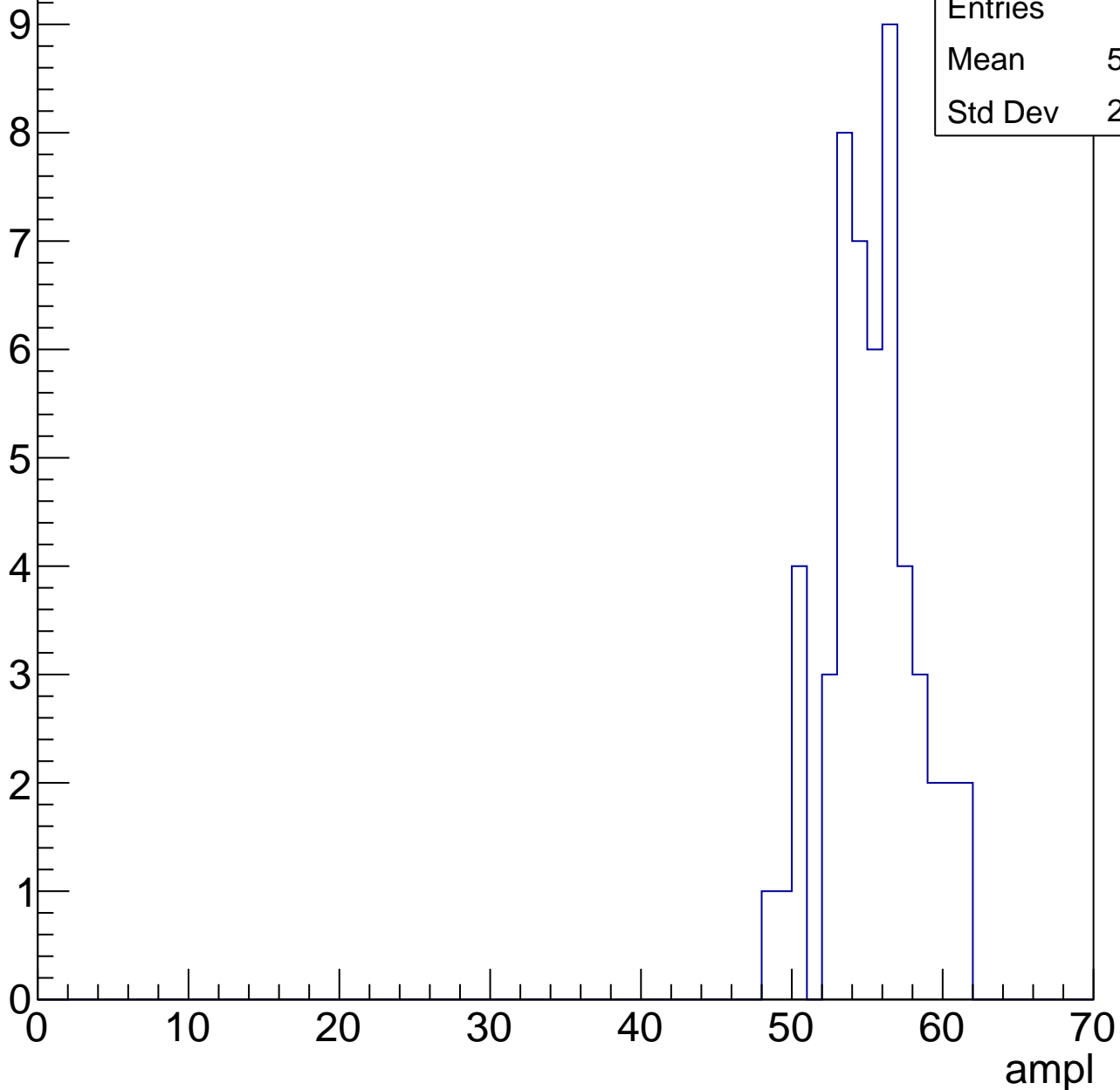


# B1L000S, U24-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	54.83
Std Dev	2.959

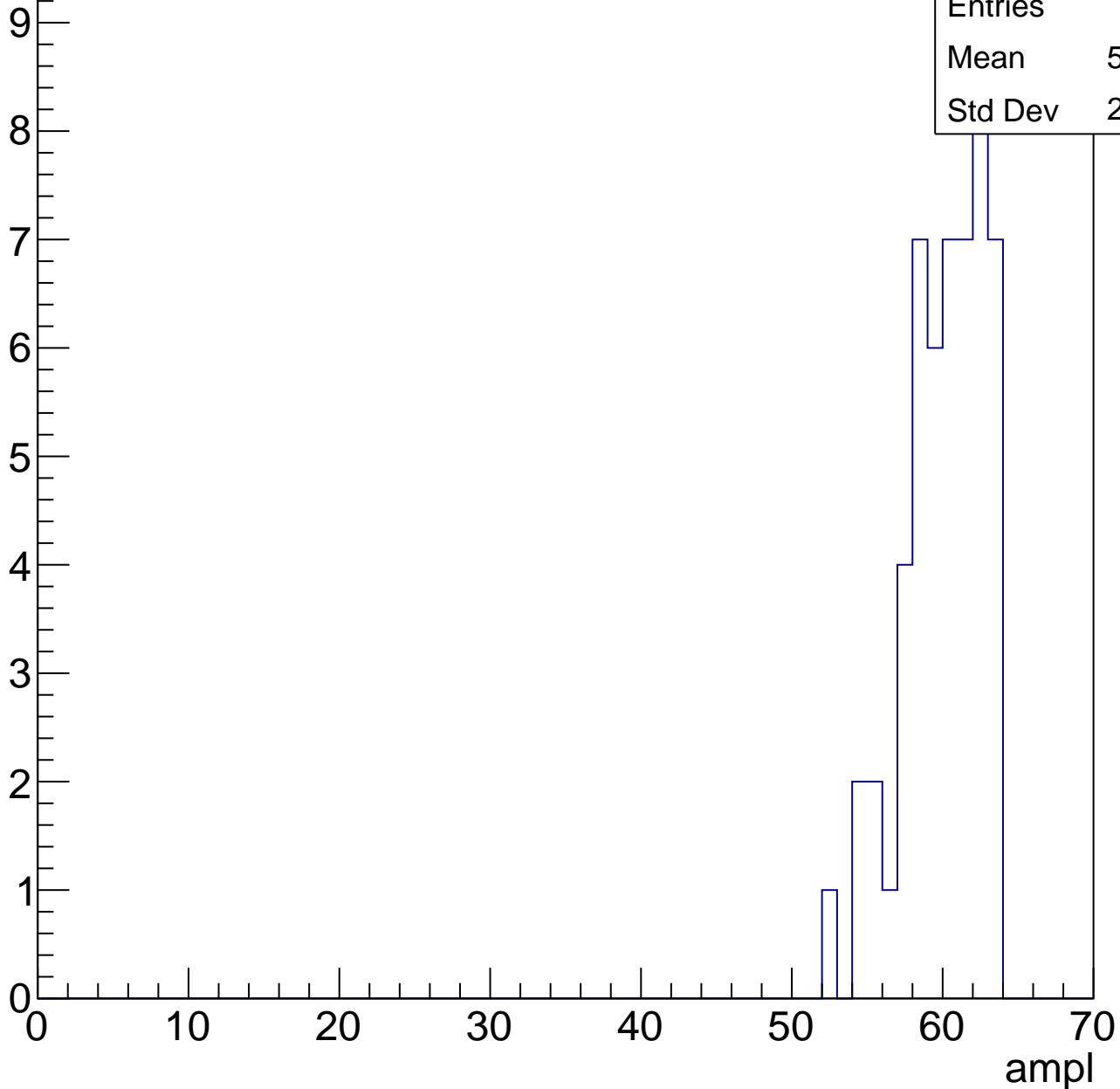


# B1L000S, U24-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

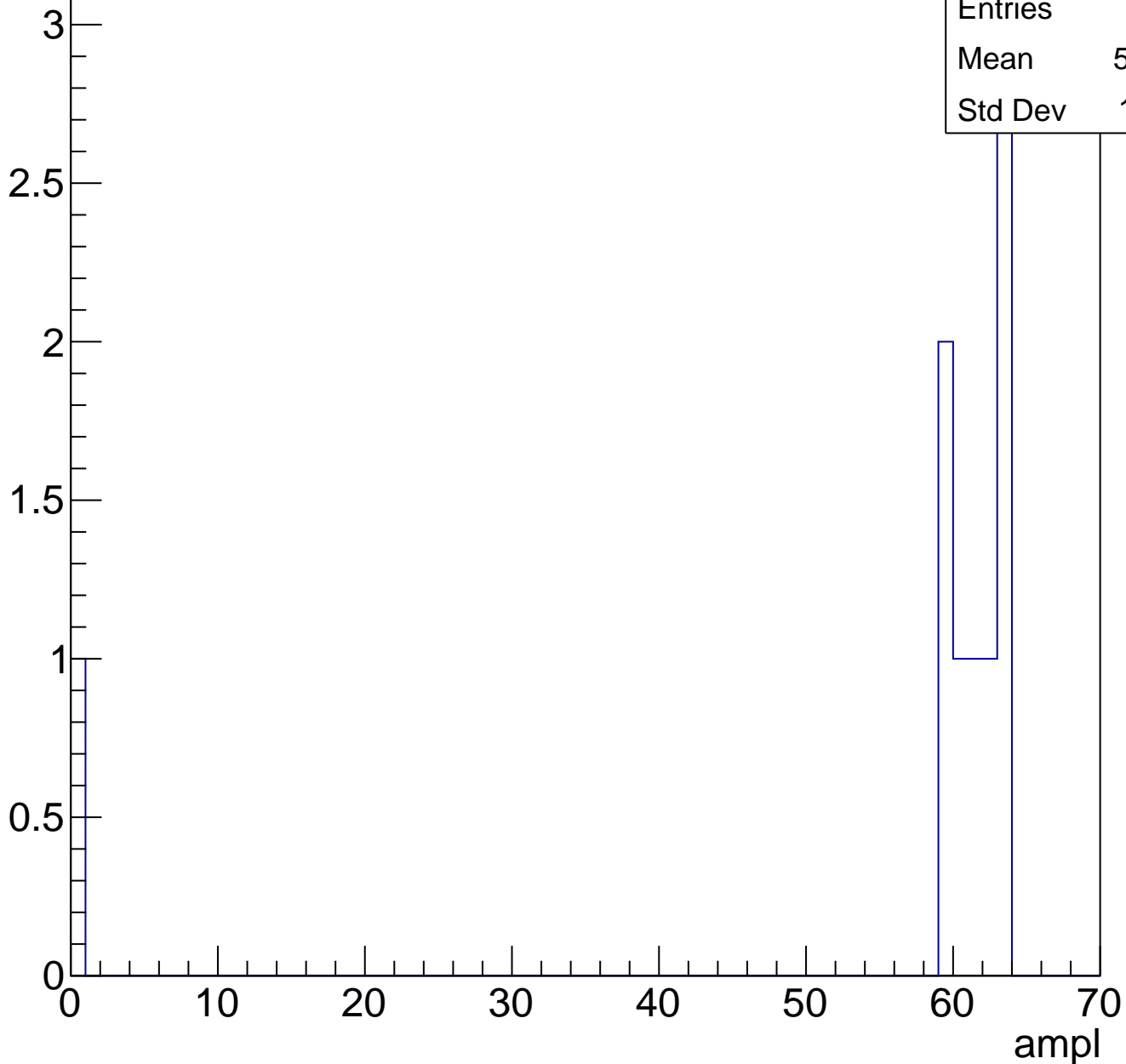
Entries	53
Mean	59.62
Std Dev	2.644



# B1L000S, U24-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch40, adc0

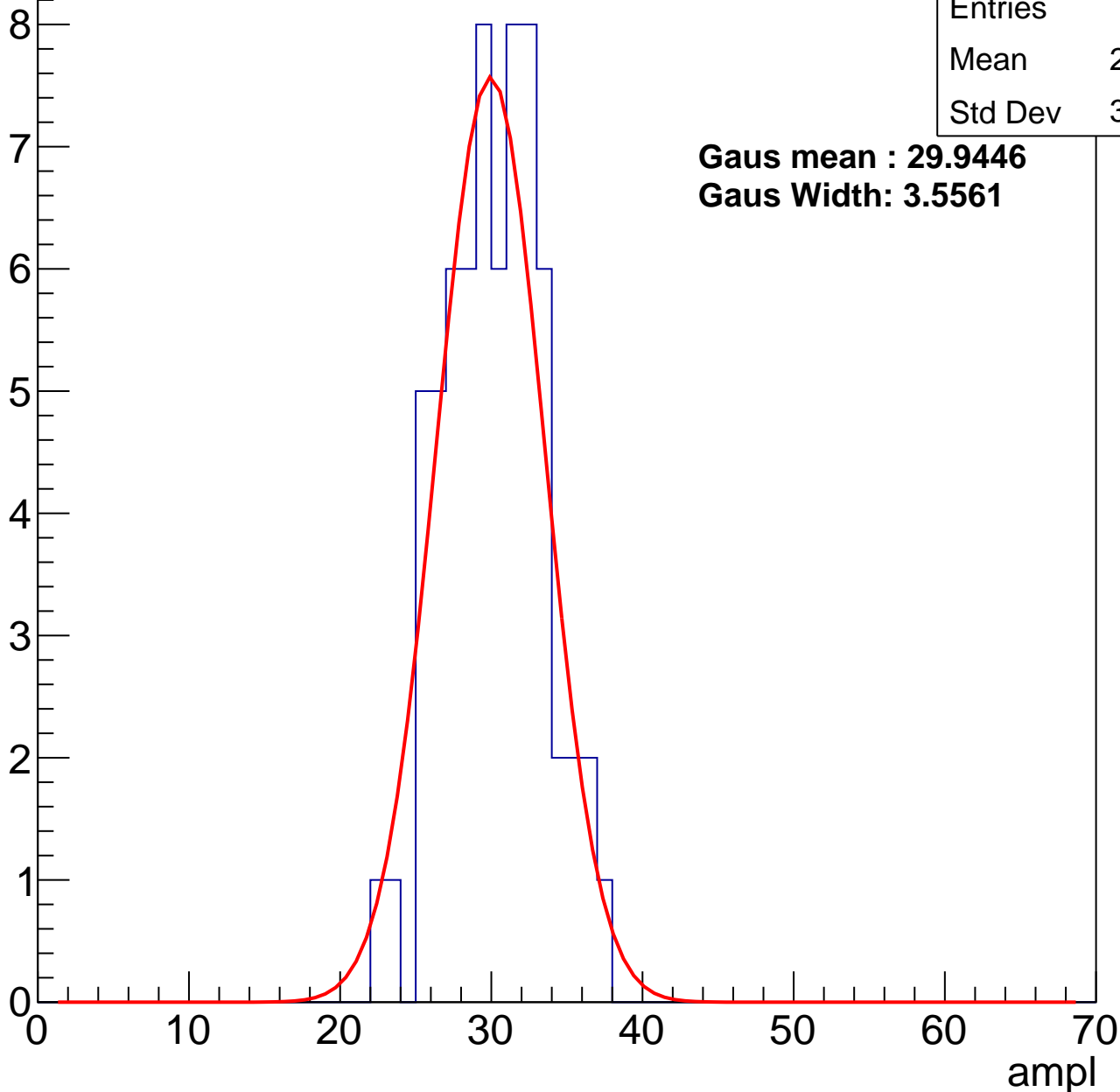
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.72
Std Dev	3.222

**Gaus mean : 29.9446**

**Gaus Width: 3.5561**



# B1L000S, U24-ch40, adc1

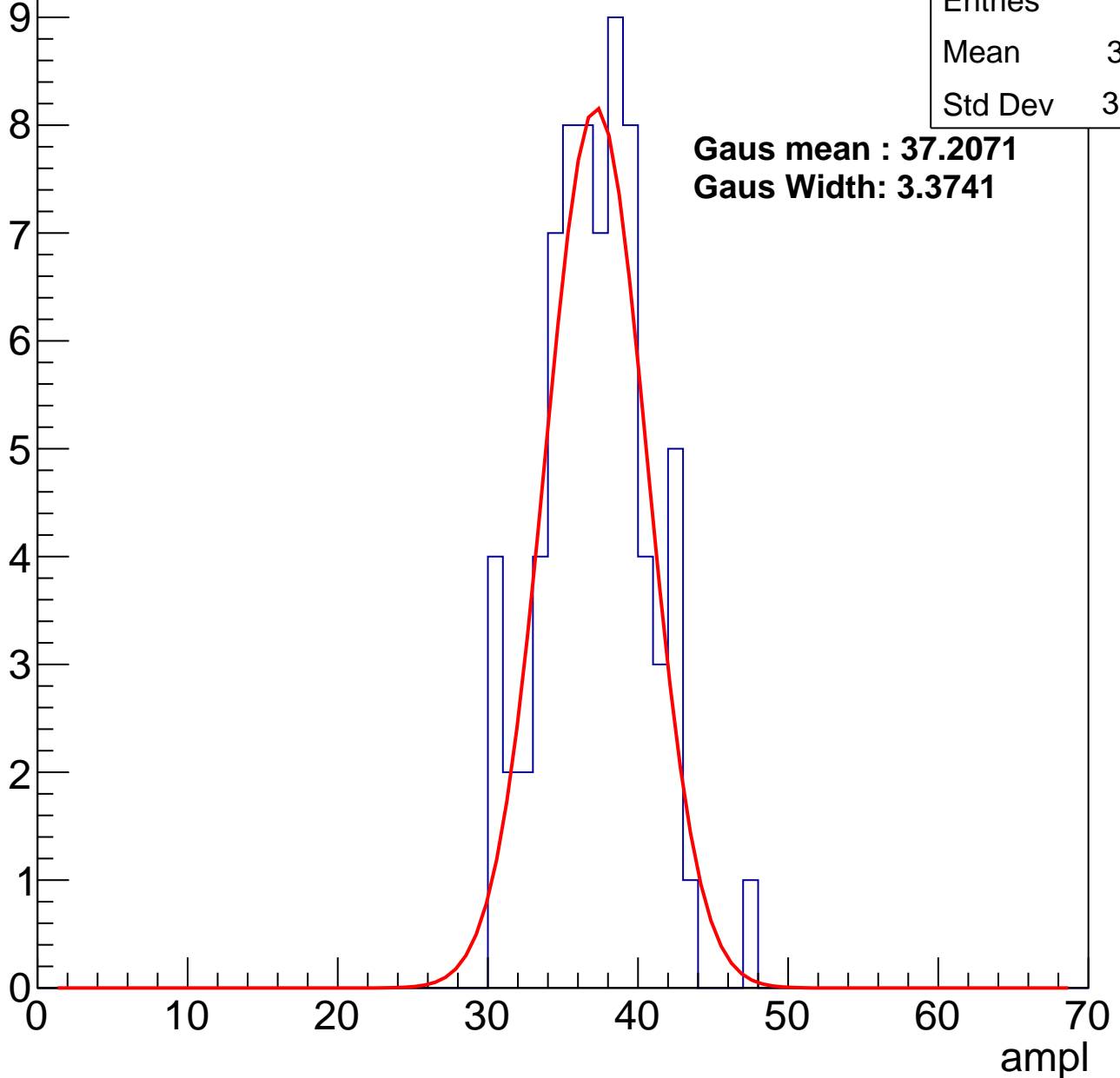
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	36.71
Std Dev	3.442

**Gaus mean : 37.2071**

**Gaus Width: 3.3741**



# B1L000S, U24-ch40, adc2

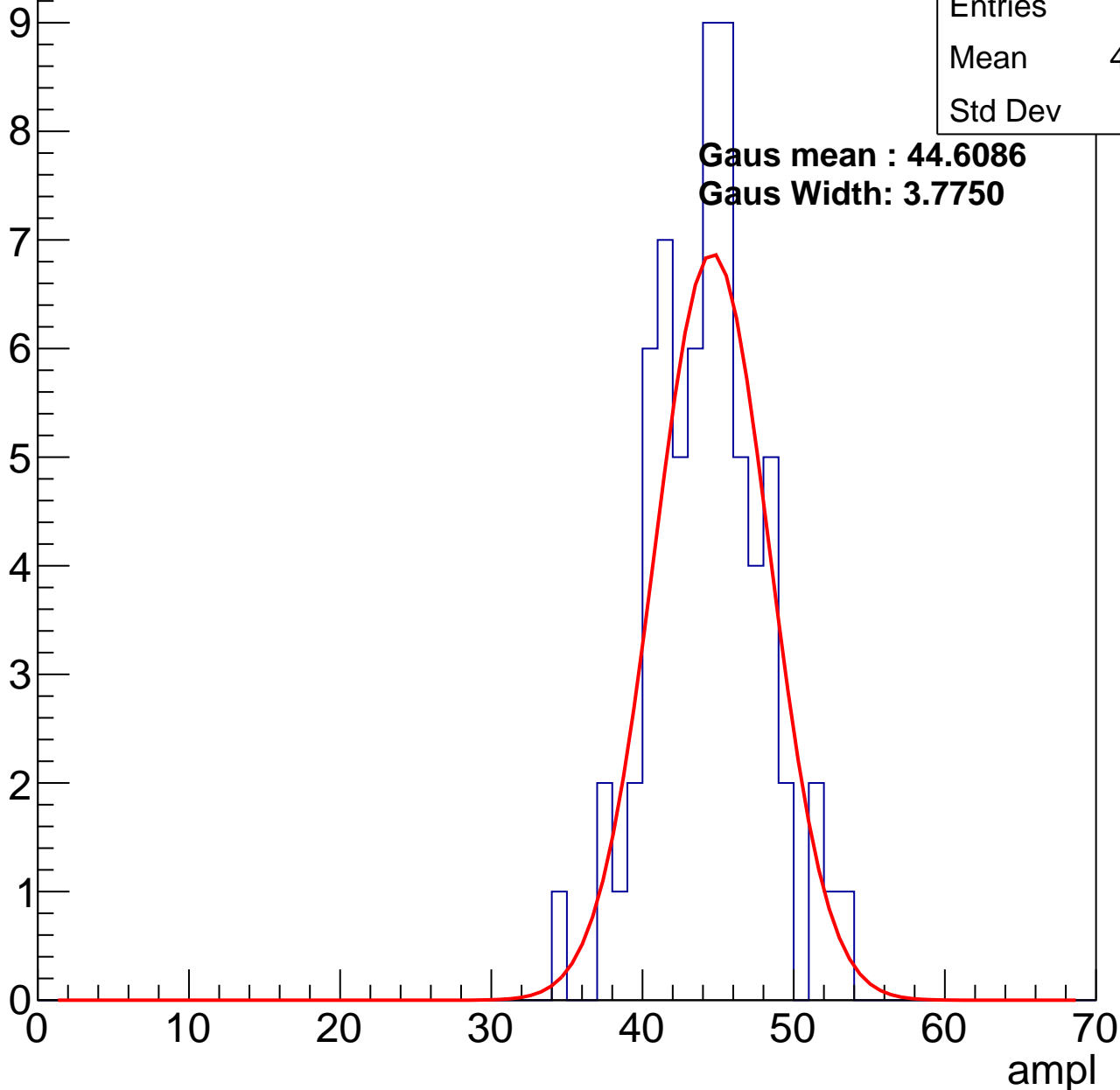
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	43.87
Std Dev	3.65

**Gaus mean : 44.6086**

**Gaus Width: 3.7750**



# B1L000S, U24-ch40, adc3

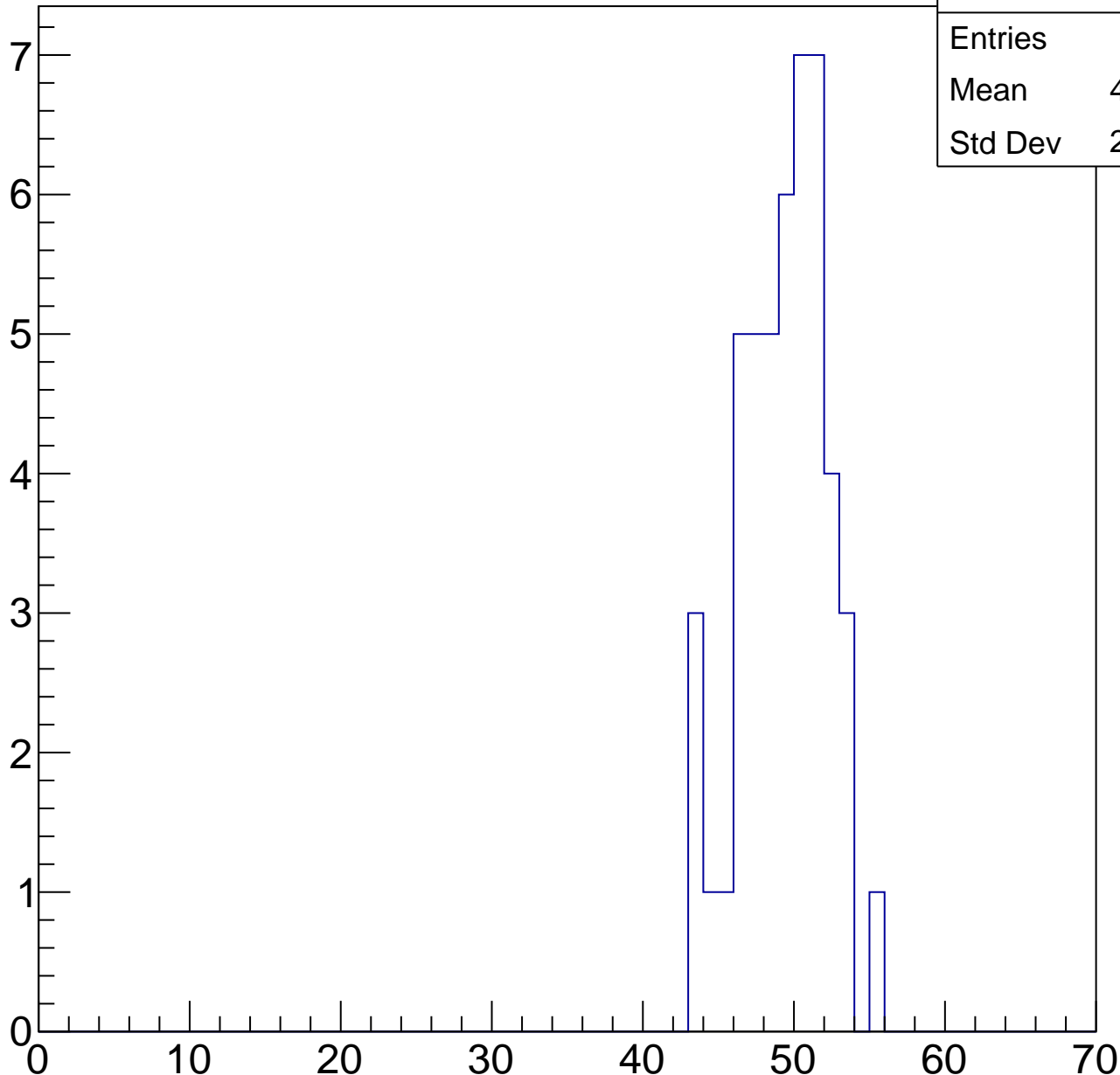
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	48.88
Std Dev	2.789

ampl

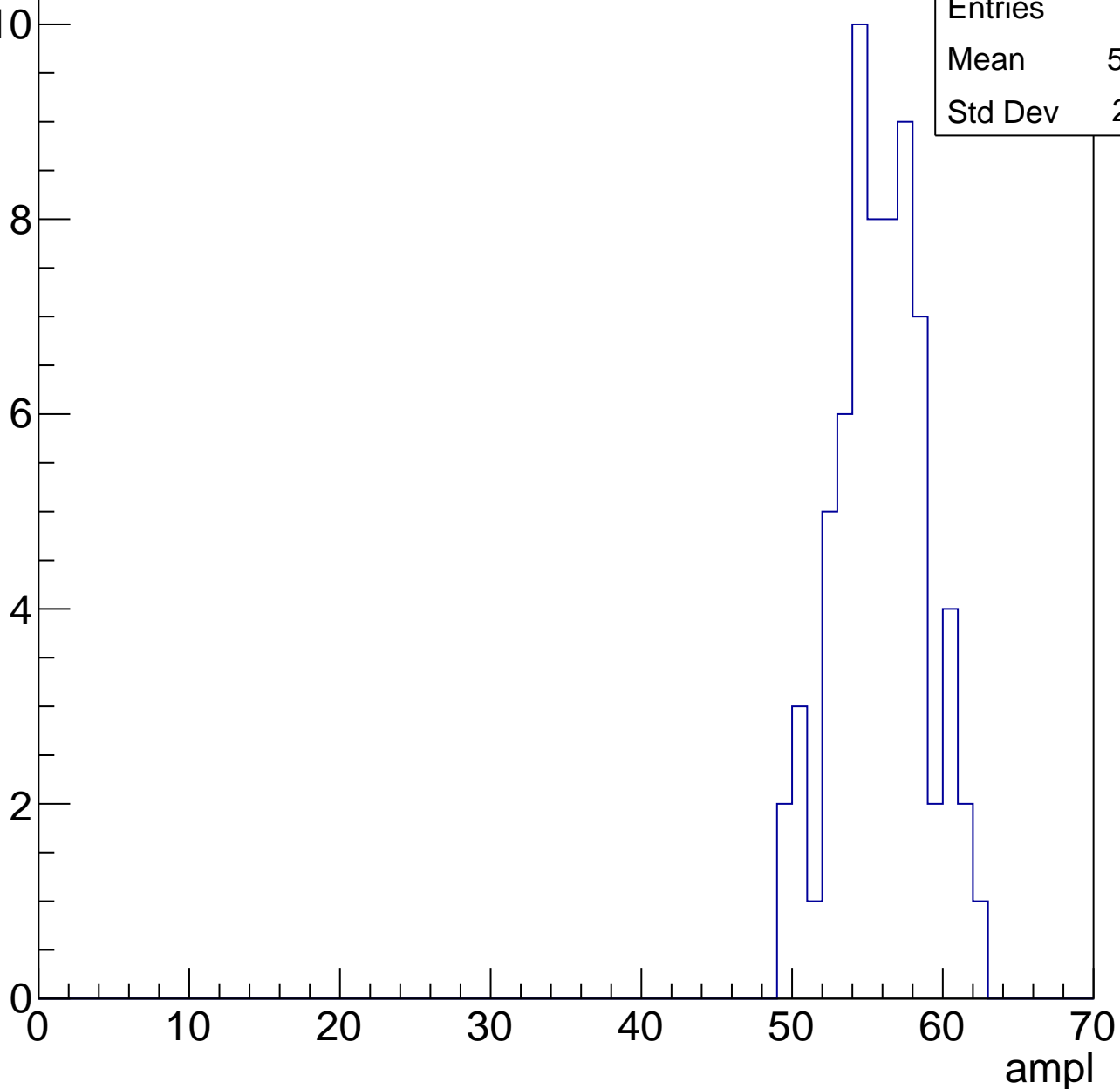


# B1L000S, U24-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	55.38
Std Dev	2.951



# B1L000S, U24-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

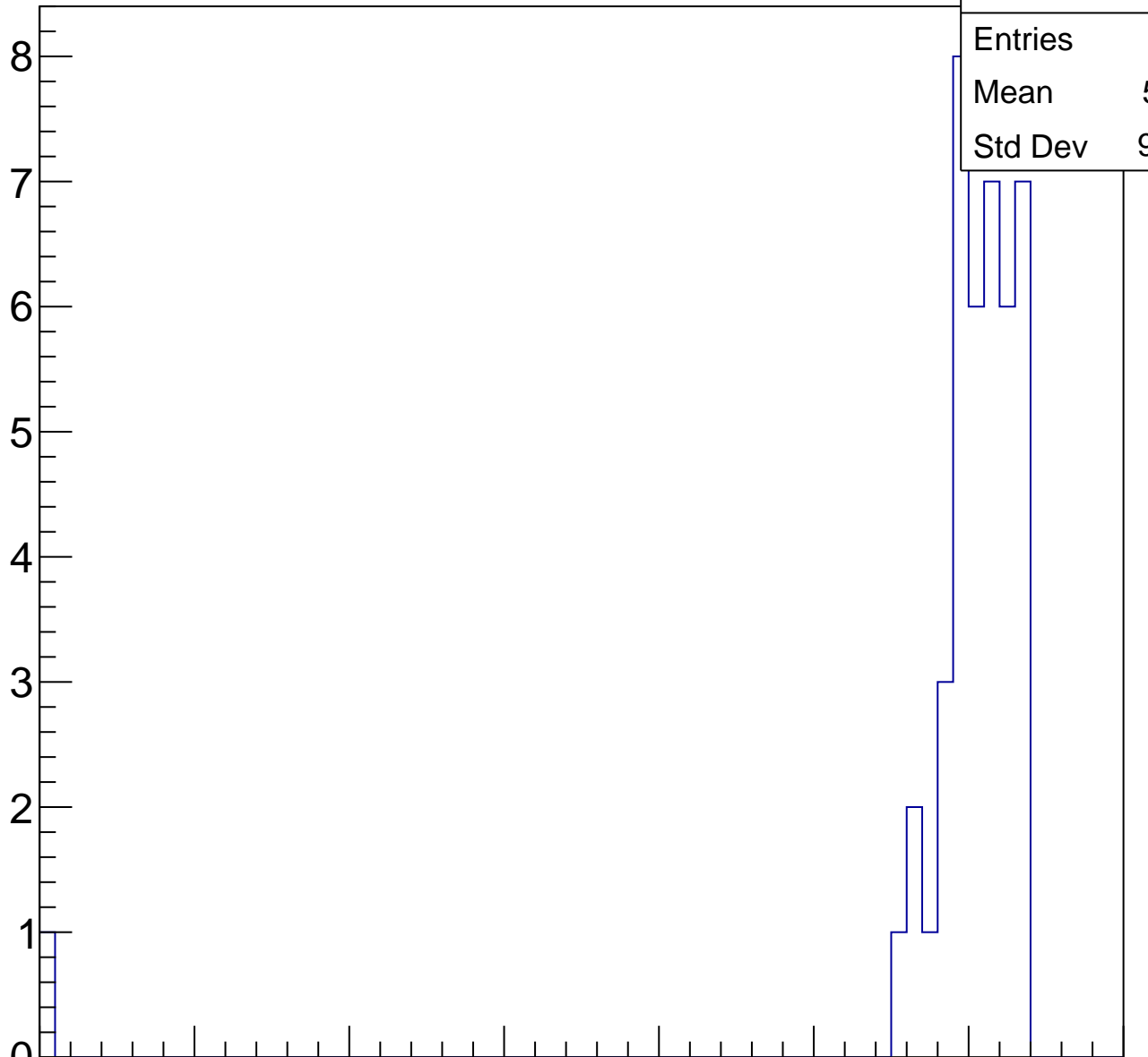
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	58.81
Std Dev	9.412

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch41, adc0

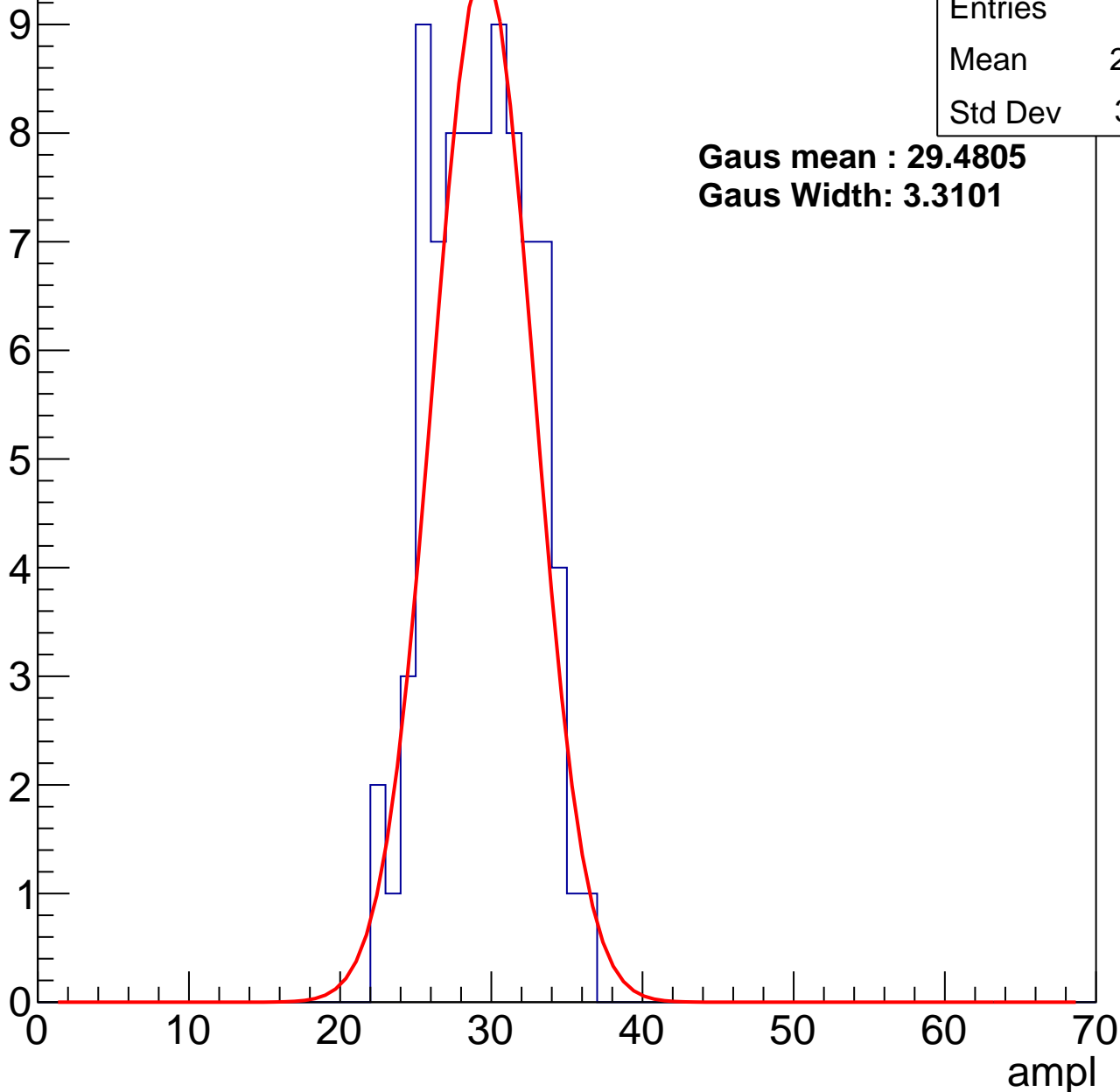
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	28.89
Std Dev	3.211

**Gaus mean : 29.4805**

**Gaus Width: 3.3101**



# B1L000S, U24-ch41, adc1

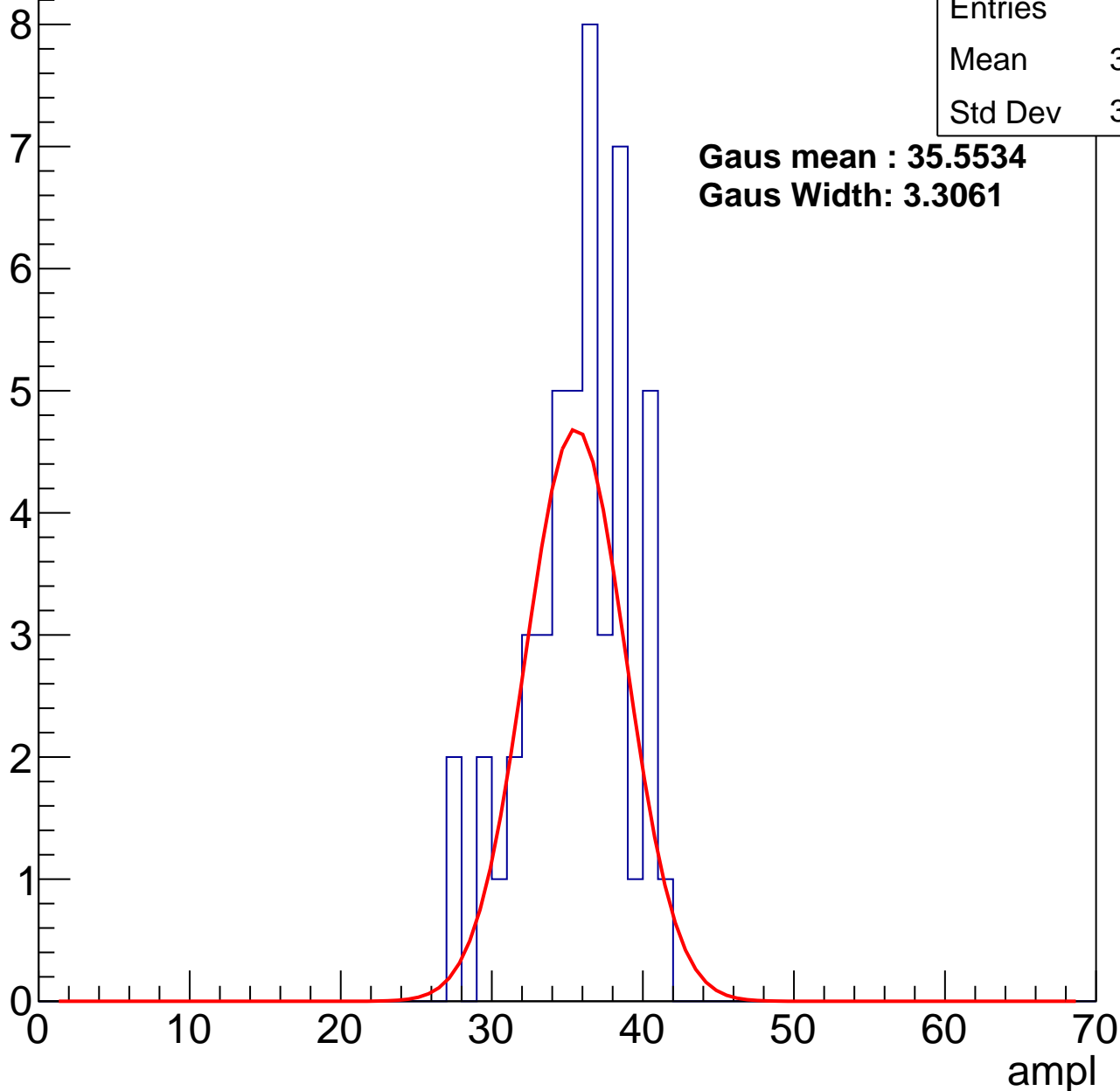
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	35.19
Std Dev	3.414

**Gaus mean : 35.5534**

**Gaus Width: 3.3061**



# B1L000S, U24-ch41, adc2

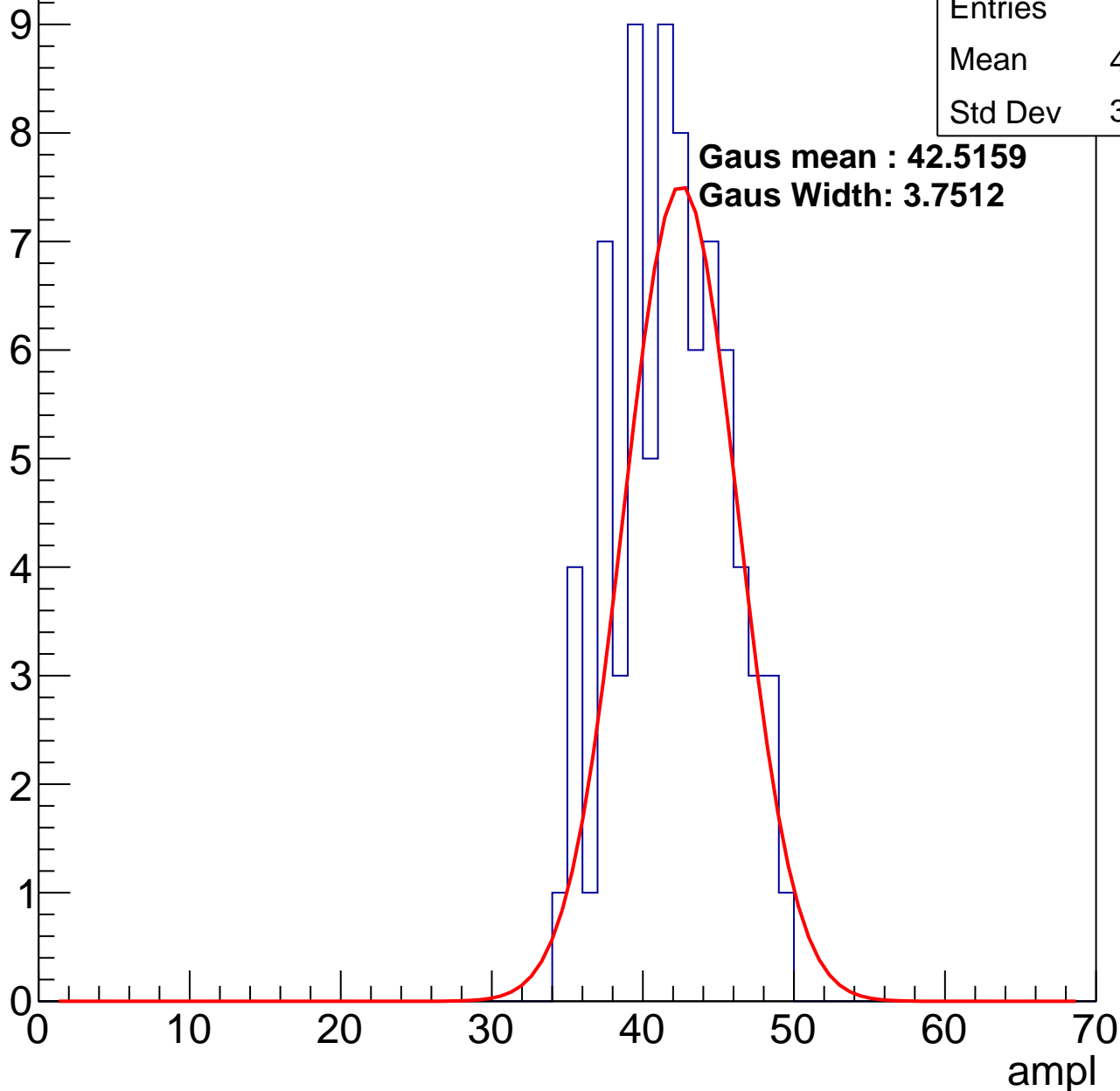
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	41.47
Std Dev	3.588

**Gaus mean : 42.5159**

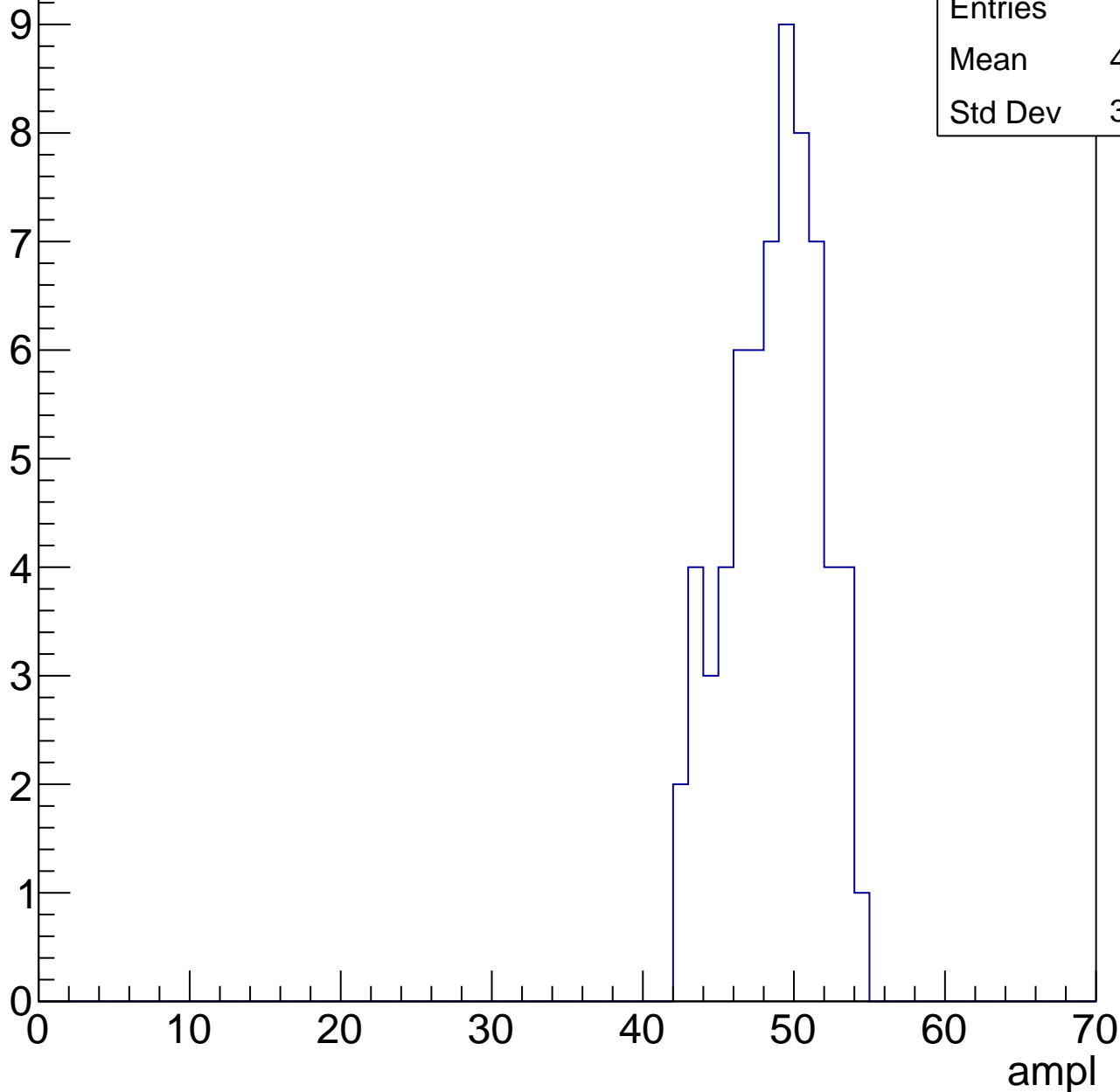
**Gaus Width: 3.7512**



# B1L000S, U24-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



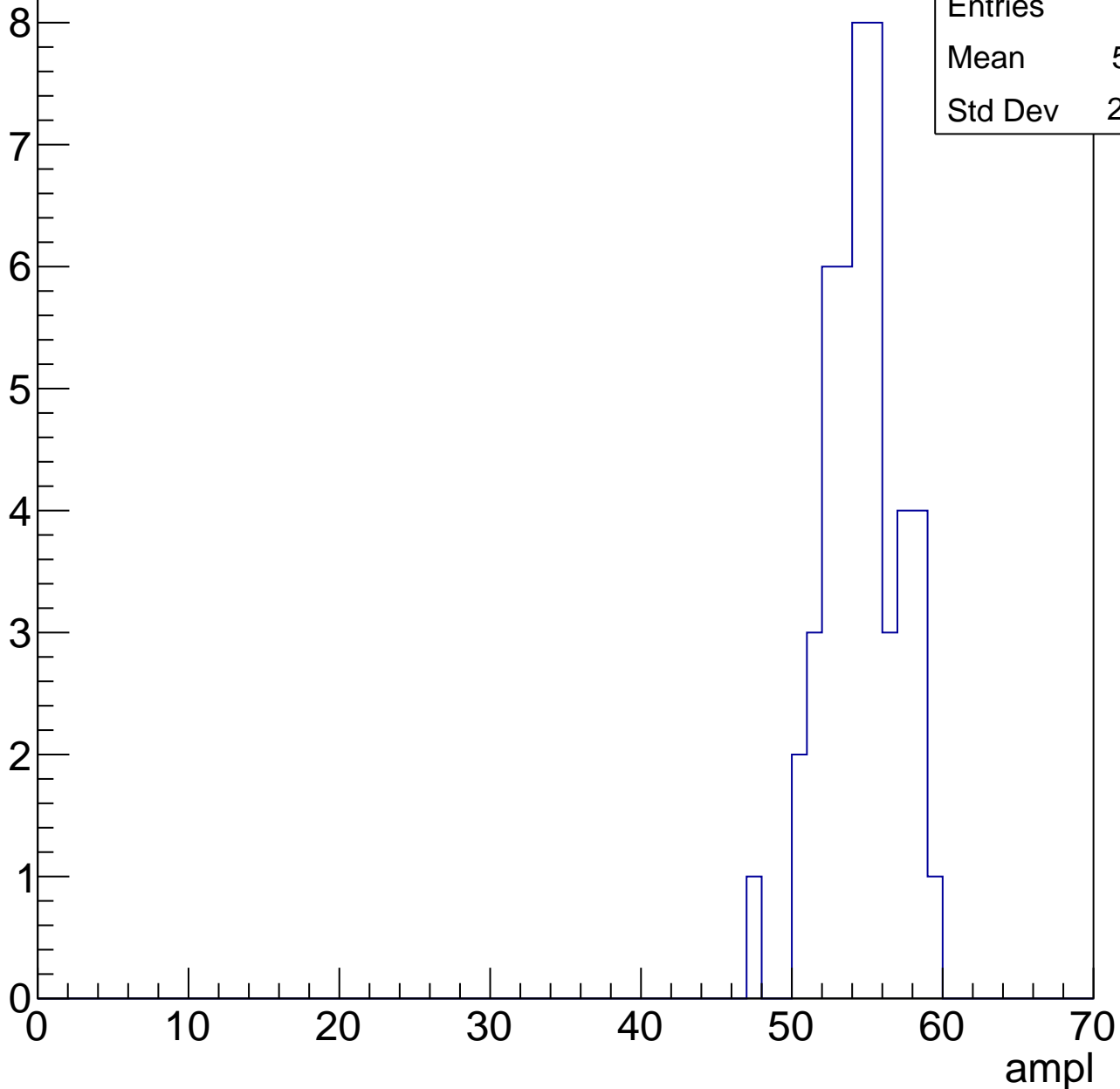
Entries	65
Mean	48.22
Std Dev	3.005

# B1L000S, U24-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	54.11
Std Dev	2.478



# B1L000S, U24-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 56

Mean 59

Std Dev 2.244

8

6

4

2

0

0

10

20

30

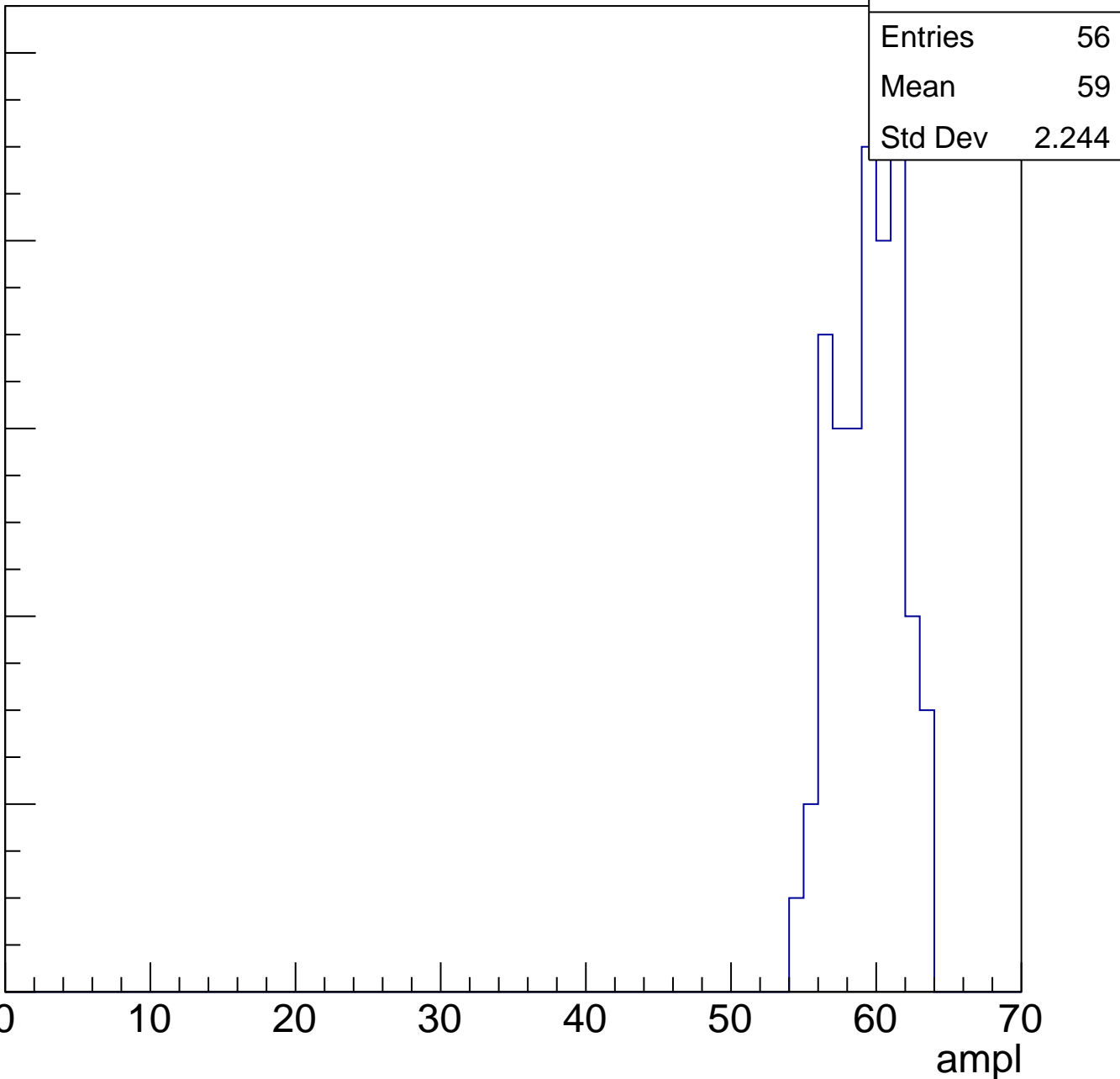
40

50

60

70

ampl

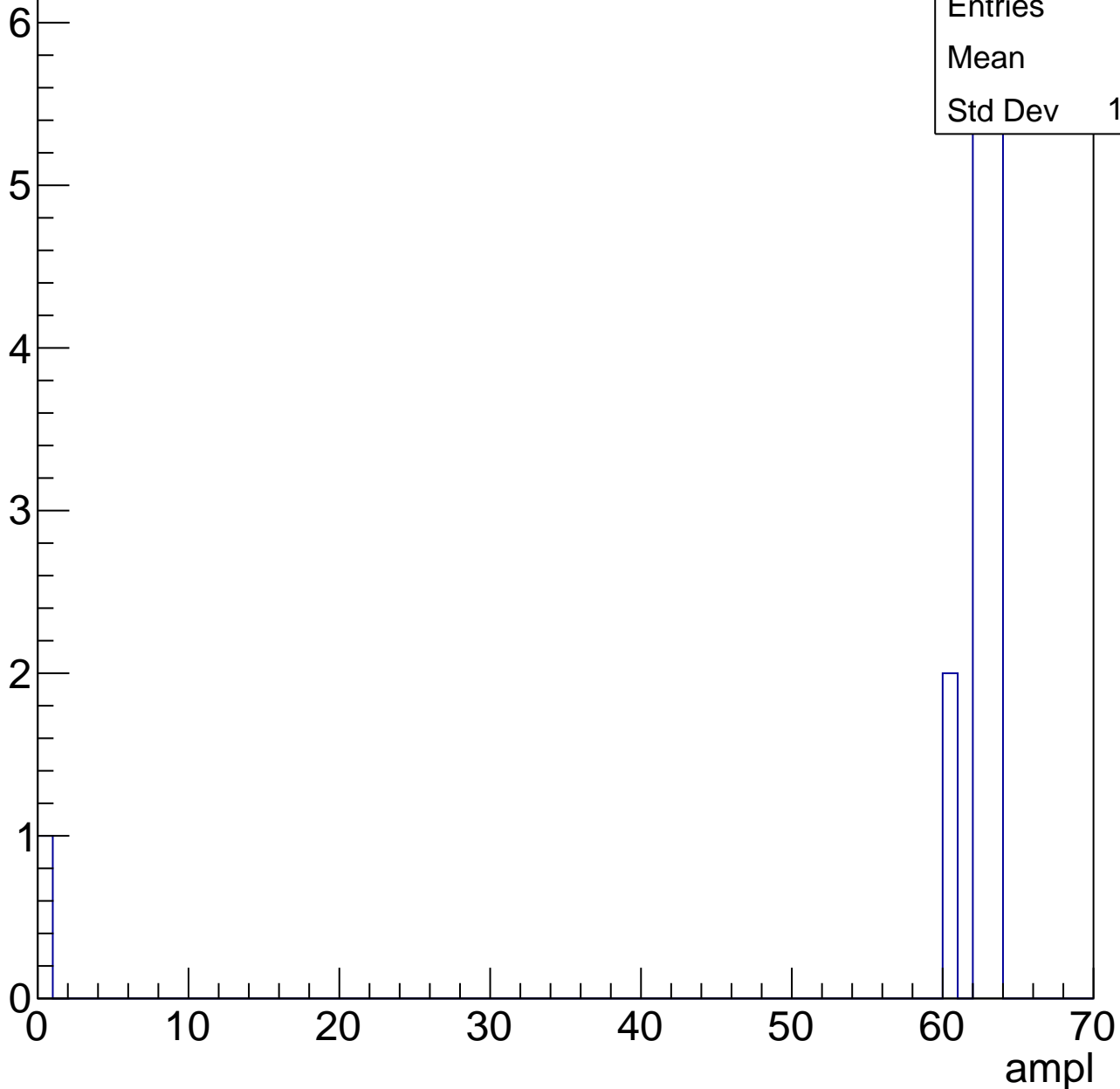


# B1L000S, U24-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	58
Std Dev	15.53





# B1L000S, U24-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

# B1L000S, U24-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	29.75
Std Dev	4.727

**Gaus mean : 30.4168**

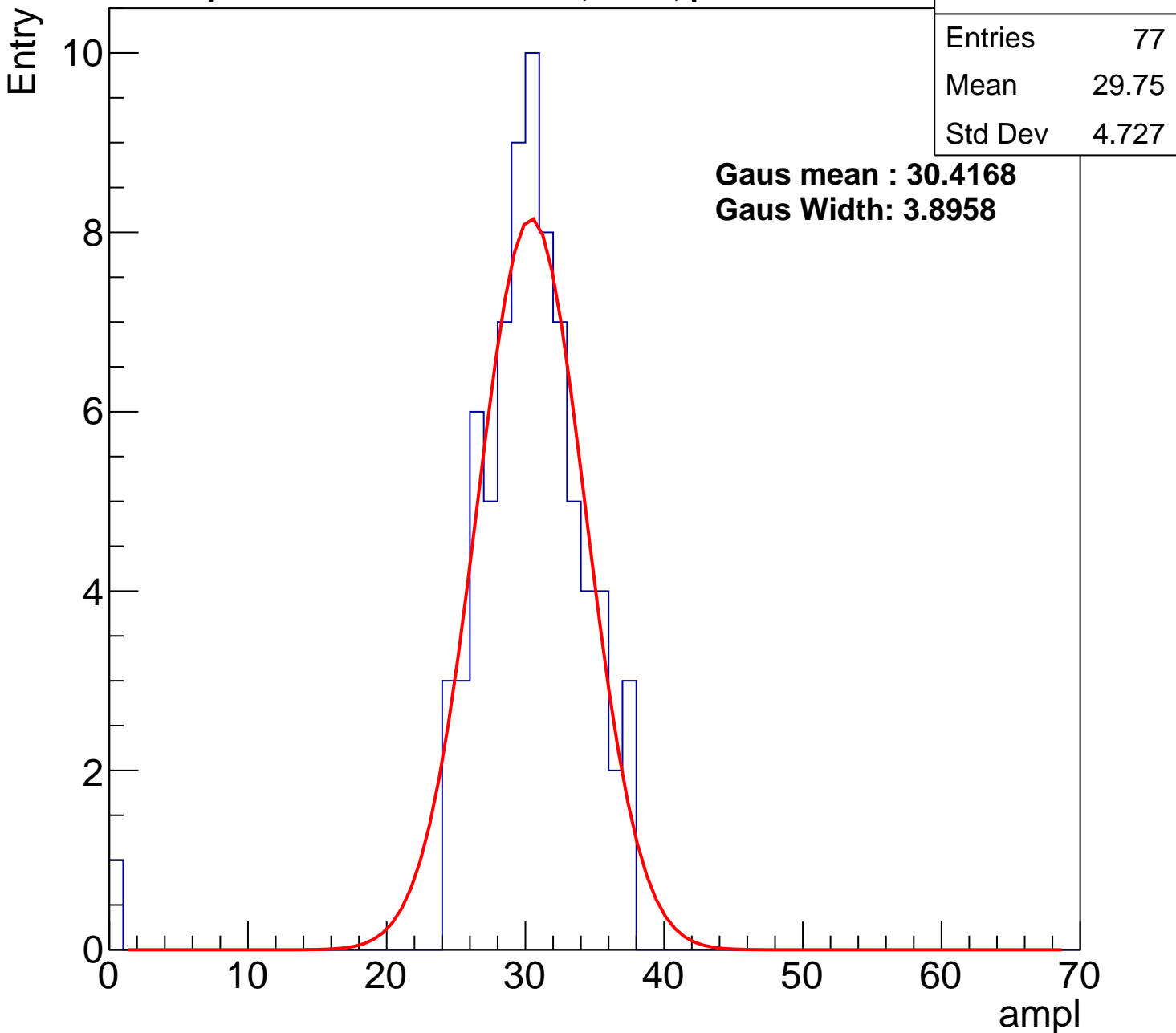
**Gaus Width: 3.8958**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



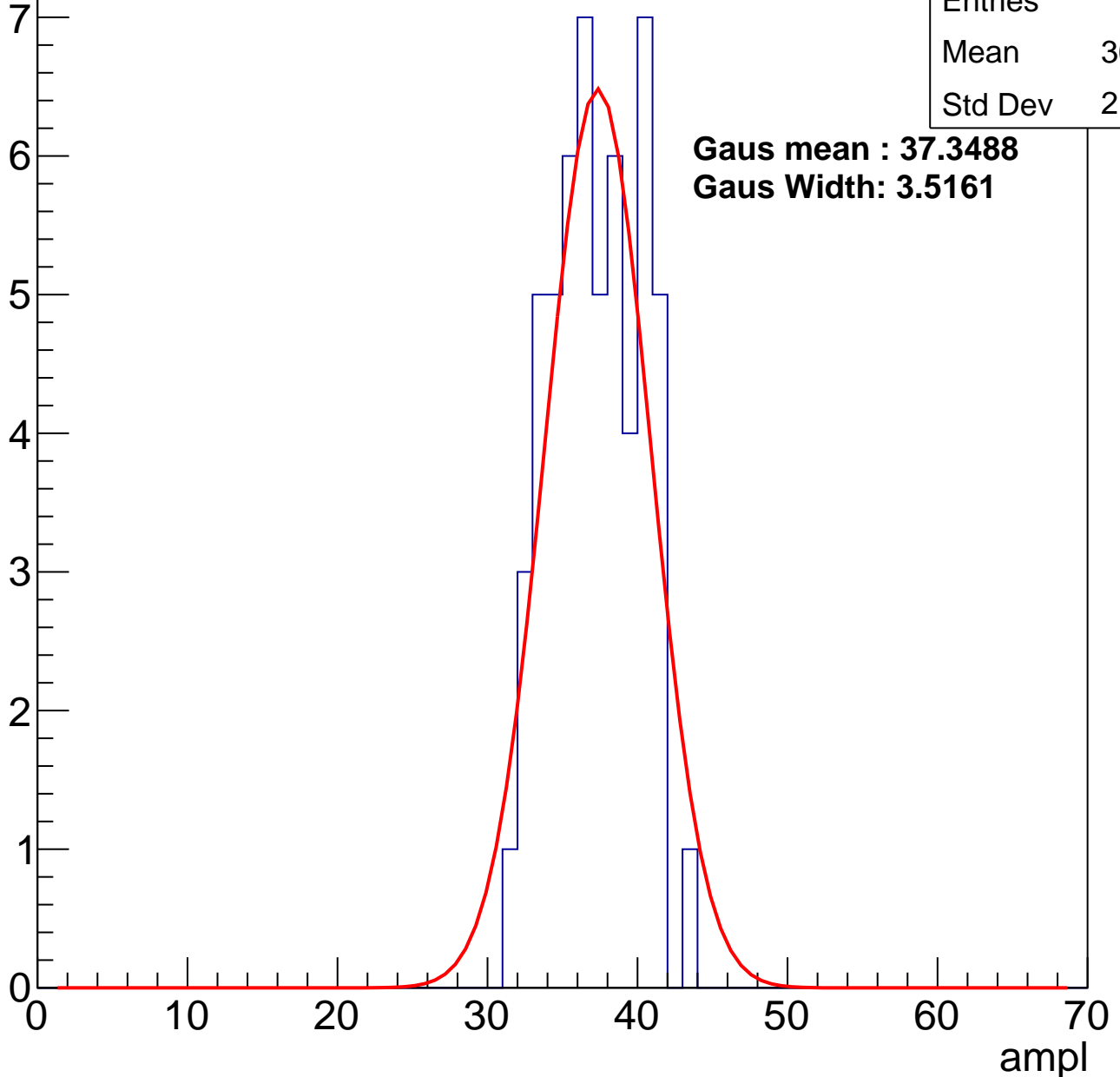
# B1L000S, U24-ch42, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	36.75
Std Dev	2.906

**Gaus mean : 37.3488**  
**Gaus Width: 3.5161**



# B1L000S, U24-ch42, adc2

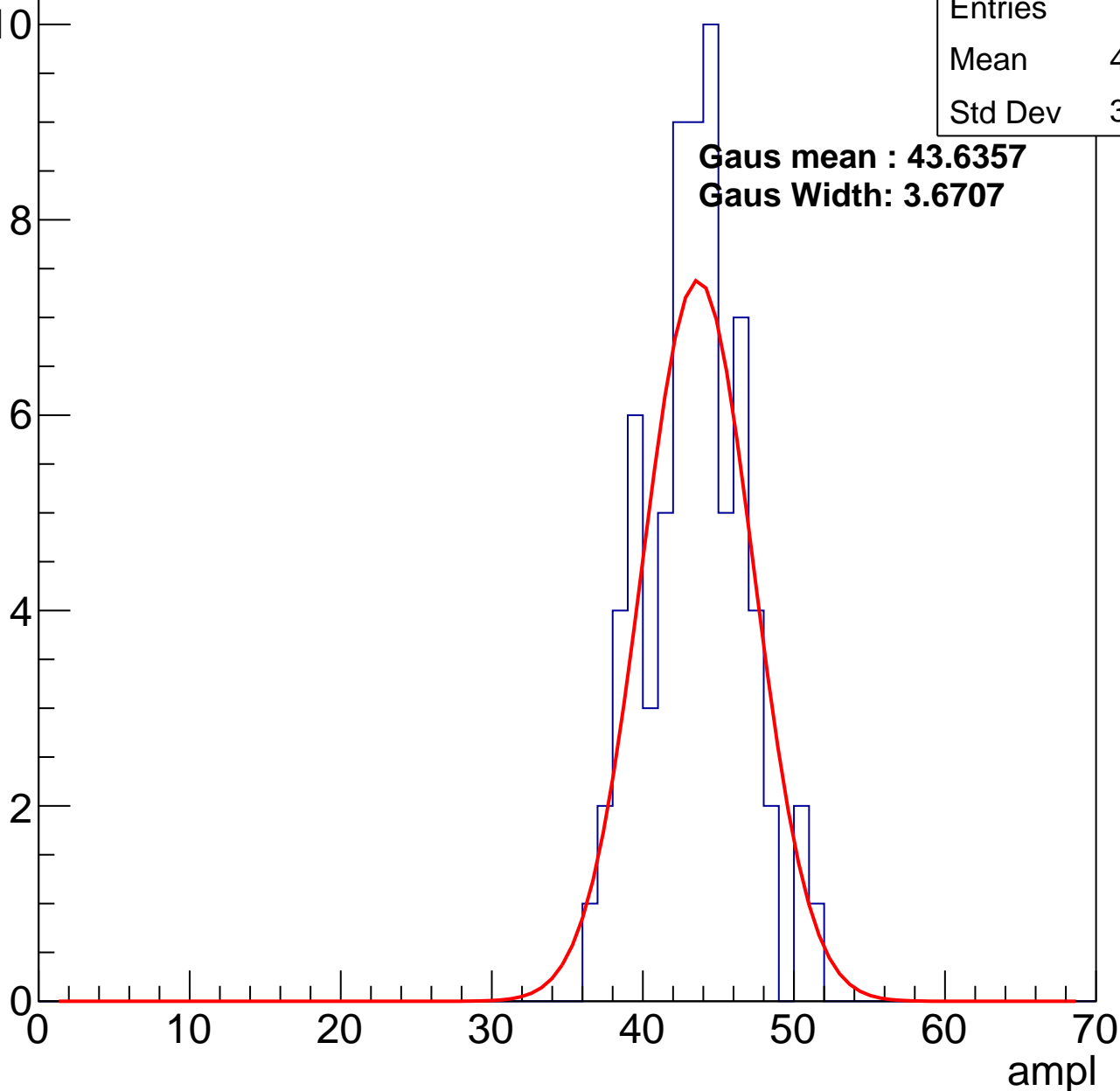
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.97
Std Dev	3.256

**Gaus mean : 43.6357**

**Gaus Width: 3.6707**

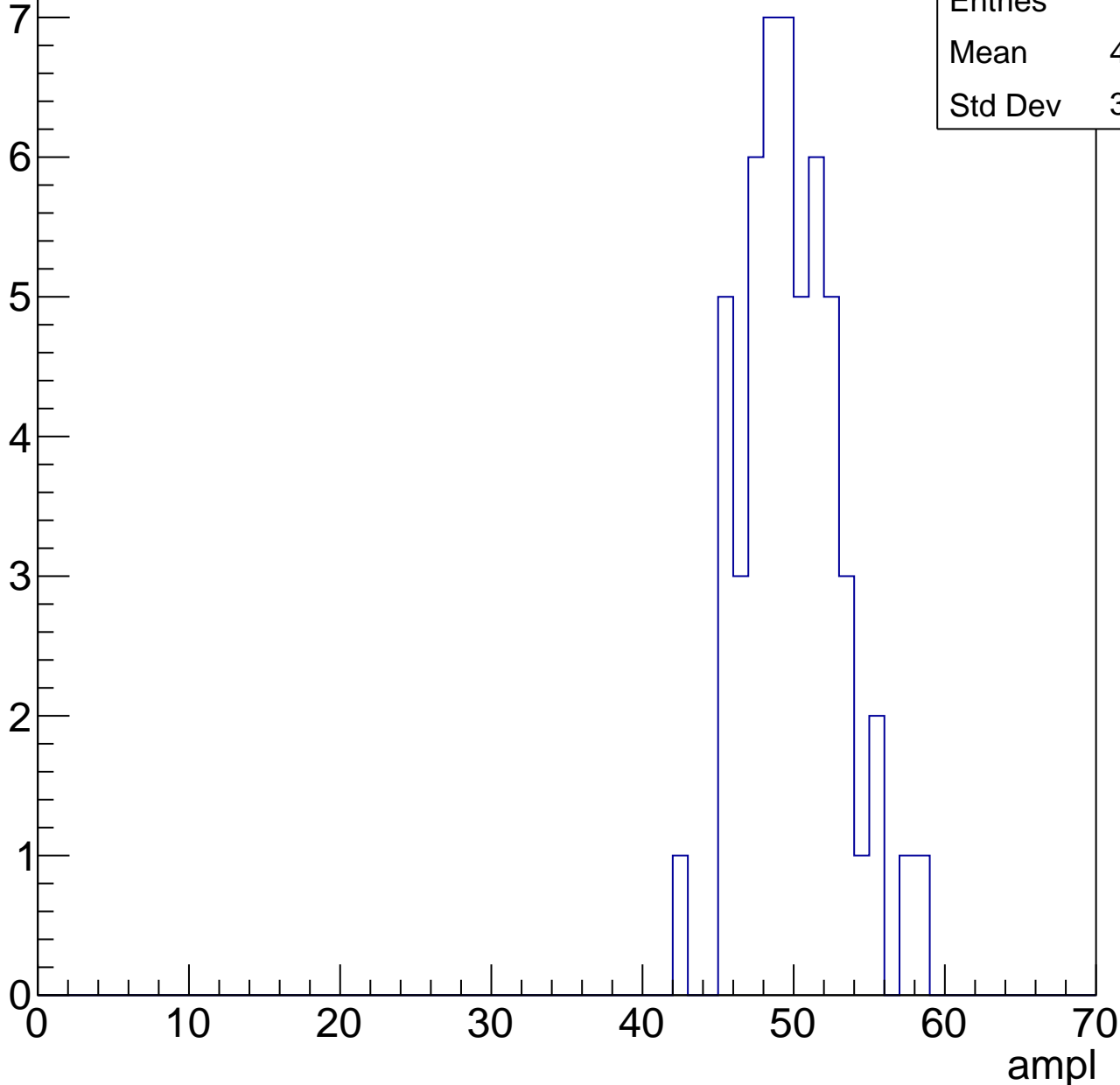


# B1L000S, U24-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	49.43
Std Dev	3.195

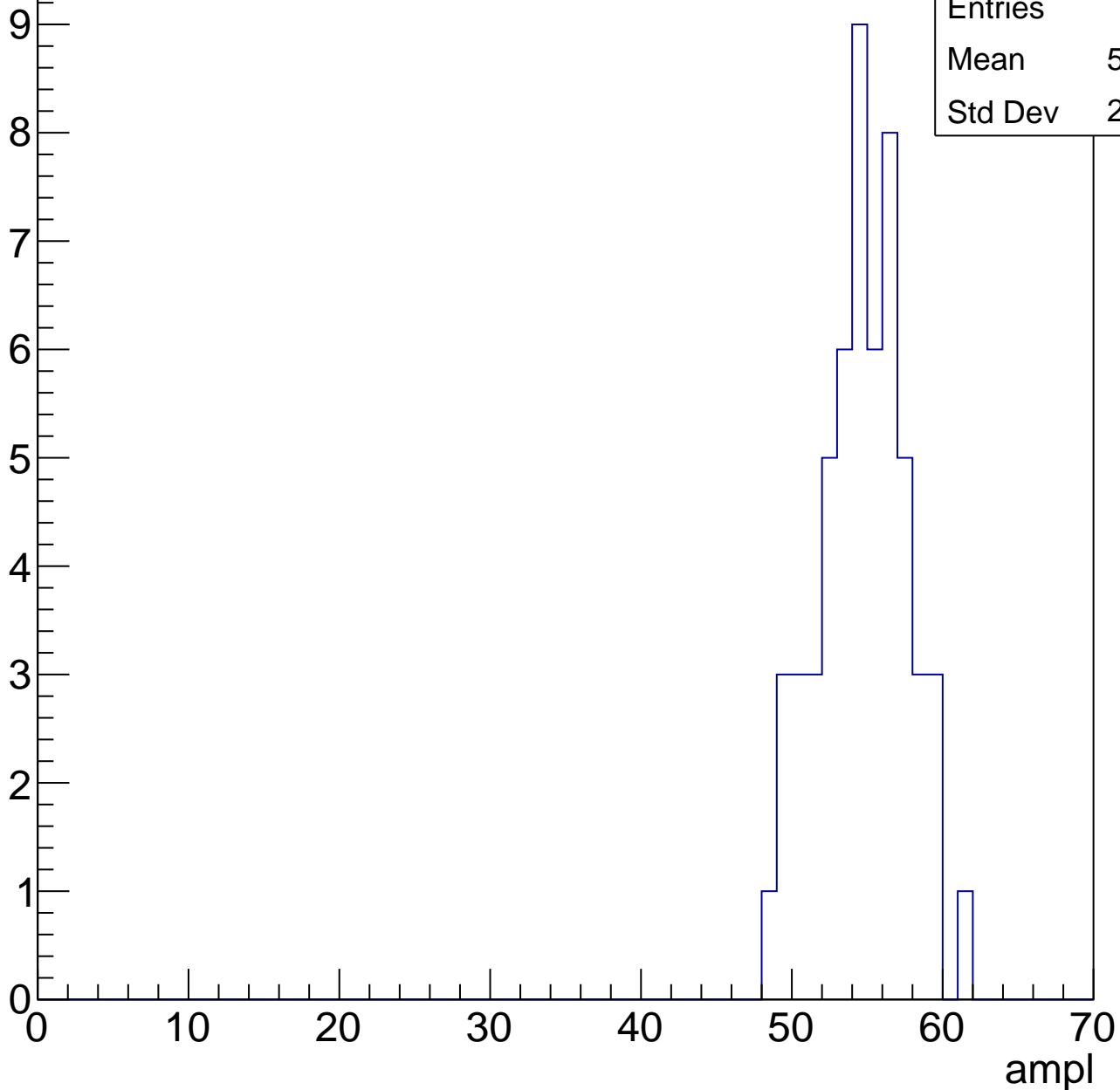


# B1L000S, U24-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	54.23
Std Dev	2.878



# B1L000S, U24-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

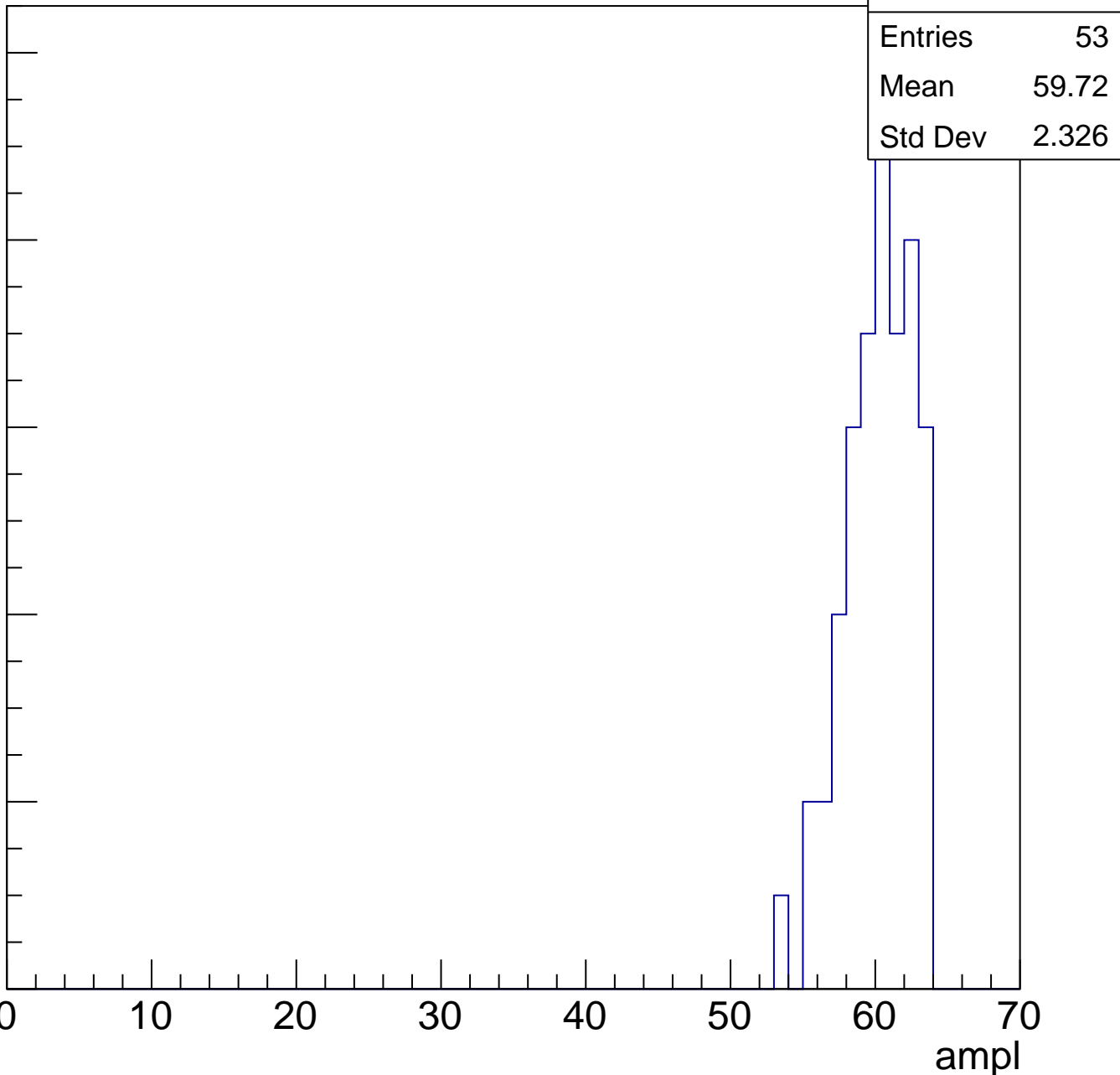
40

50

60

ampl

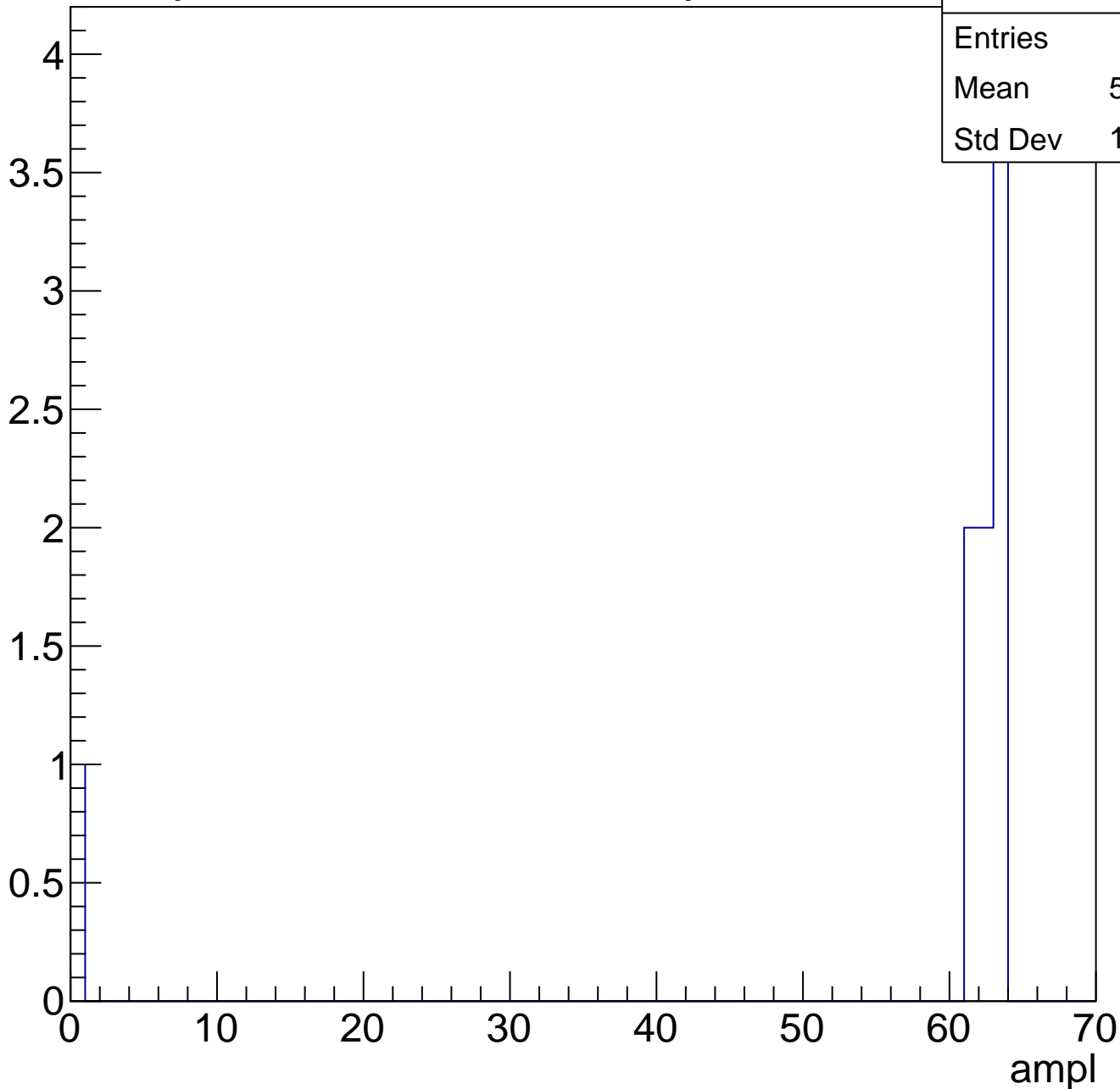
Entries	53
Mean	59.72
Std Dev	2.326



# B1L000S, U24-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch43, adc0

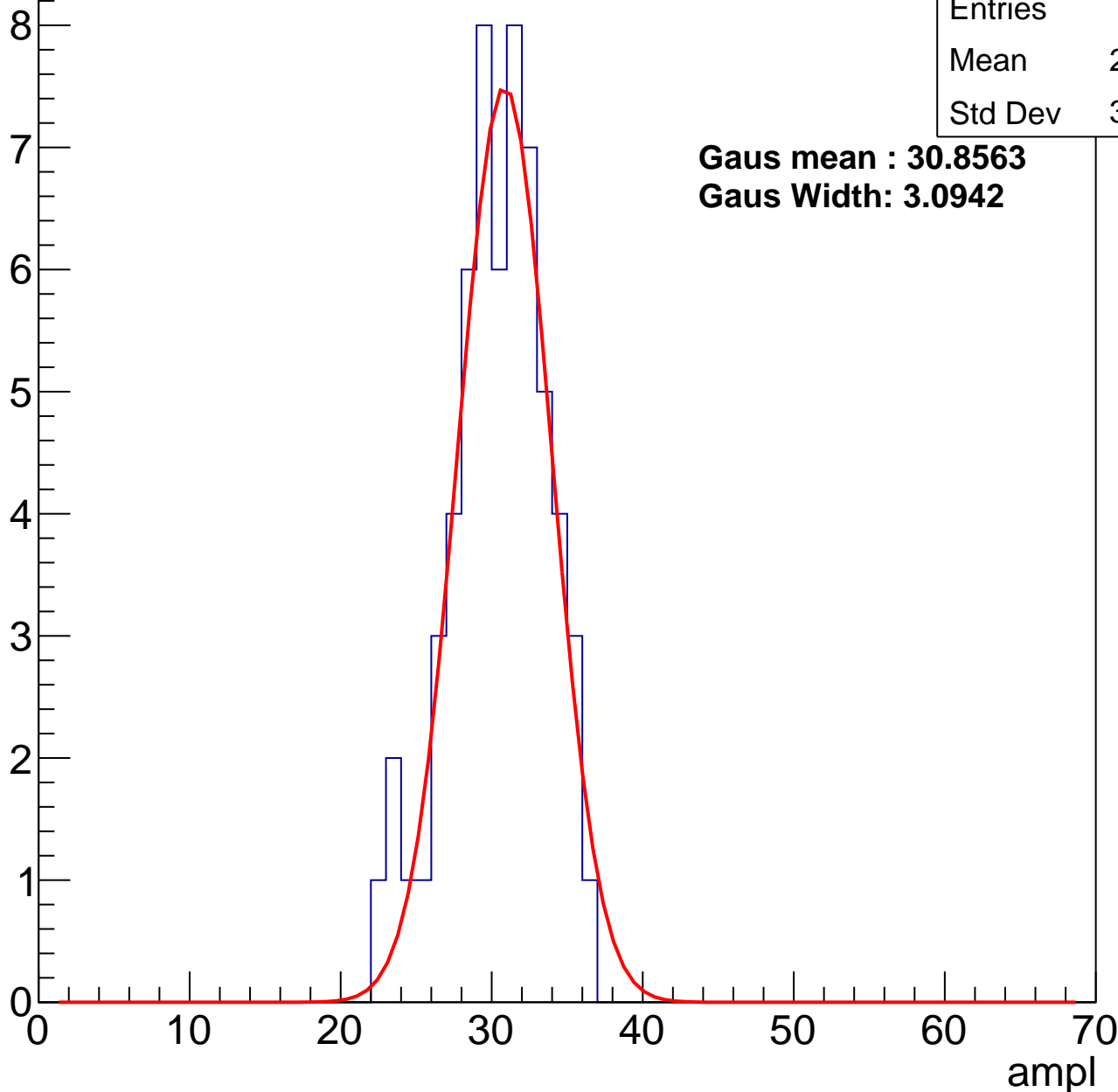
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	29.95
Std Dev	3.149

**Gaus mean : 30.8563**

**Gaus Width: 3.0942**

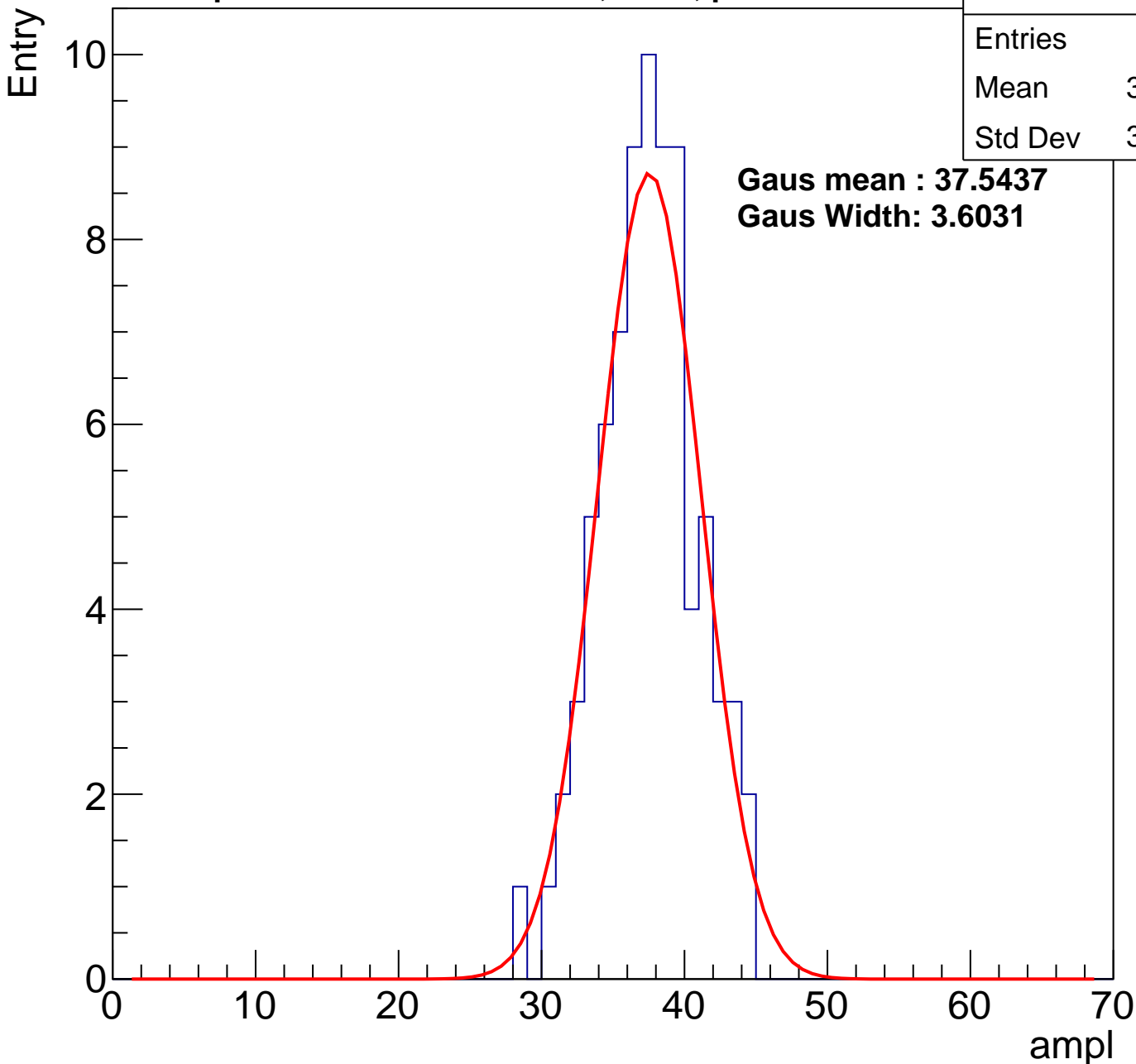


# B1L000S, U24-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	37.03
Std Dev	3.356

**Gaus mean : 37.5437**  
**Gaus Width: 3.6031**



# B1L000S, U24-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	44.11
Std Dev	3.014

**Gaus mean : 44.5118**

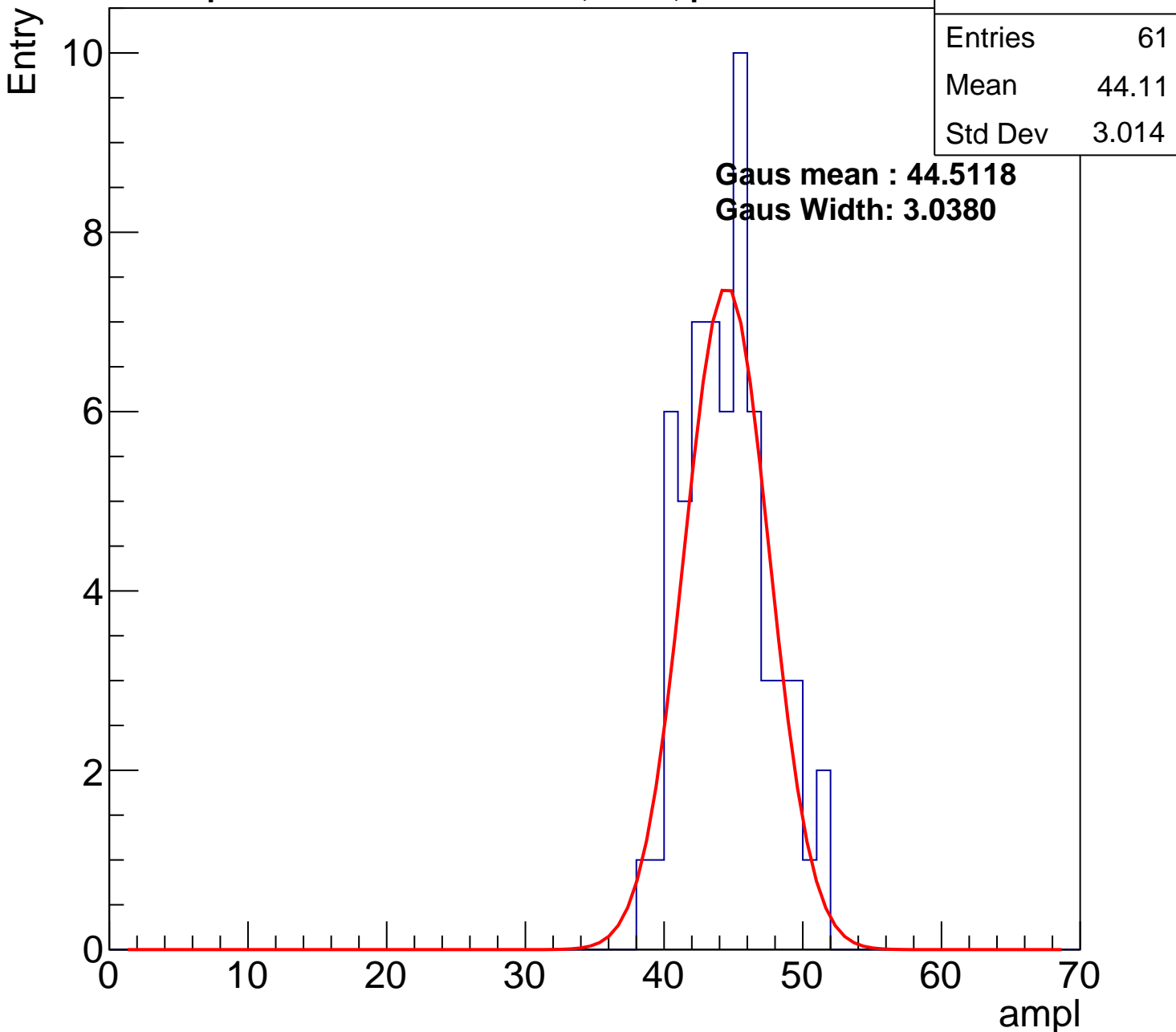
**Gaus Width: 3.0380**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

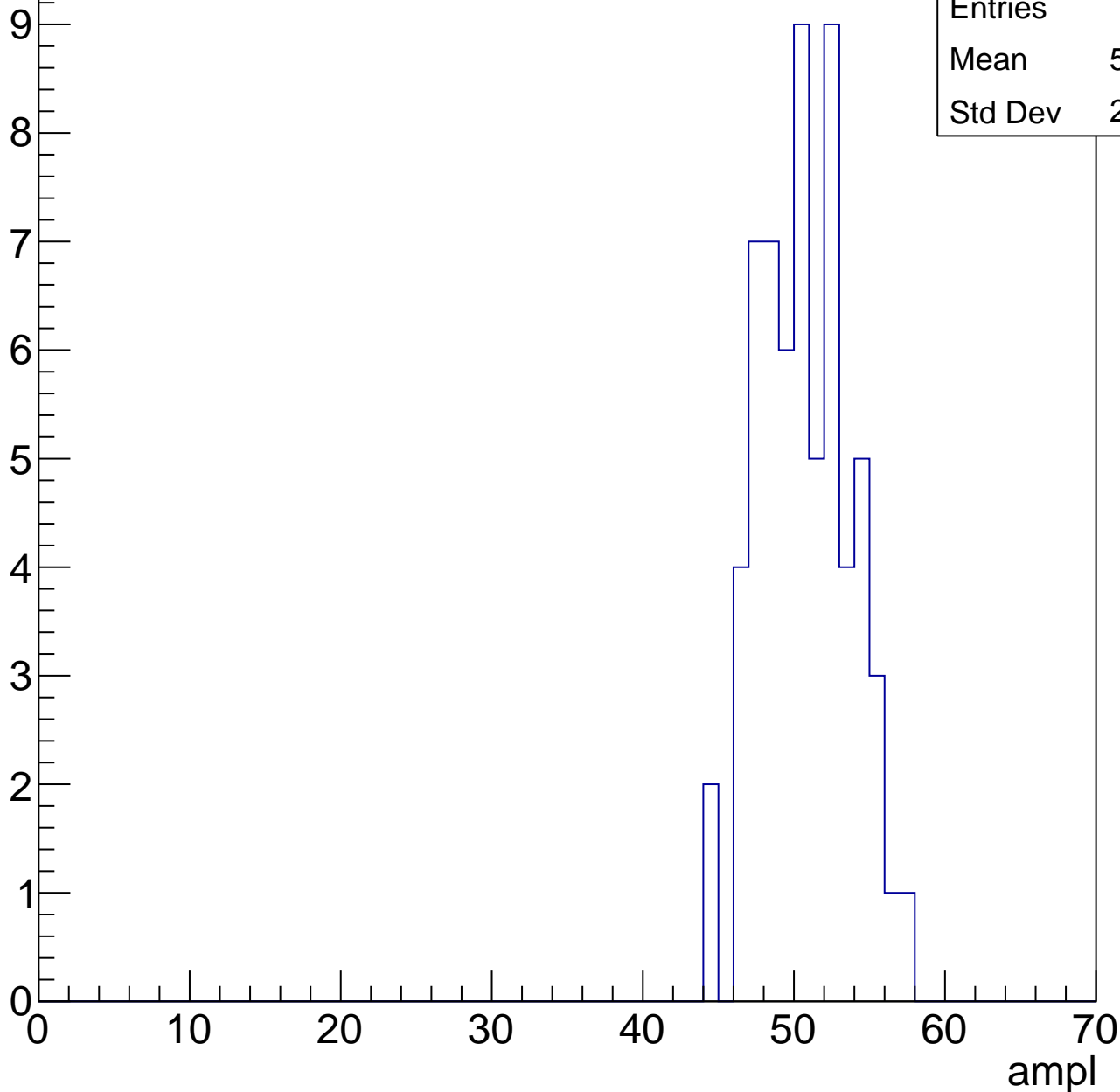


# B1L000S, U24-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	50.22
Std Dev	2.946

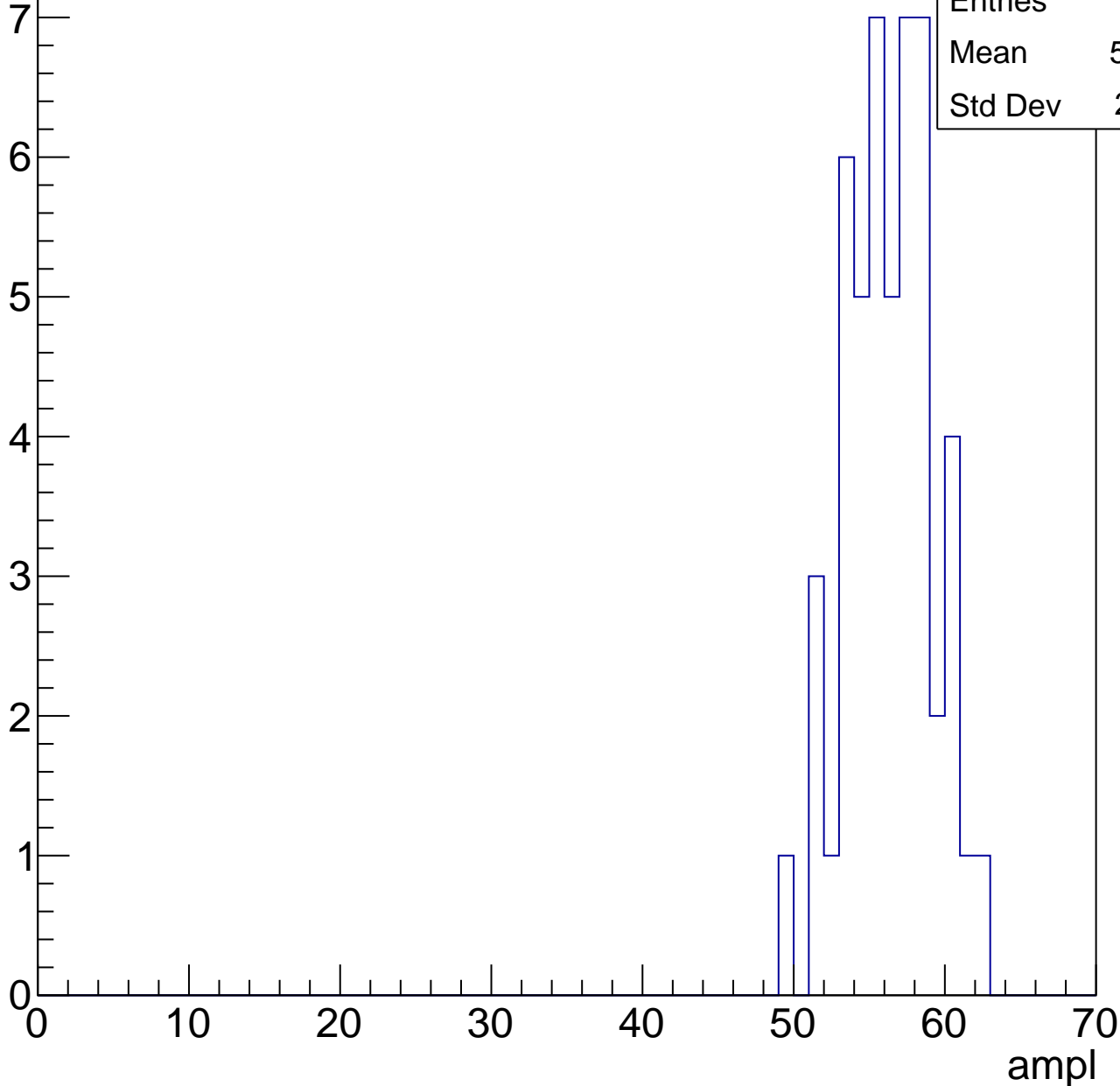


# B1L000S, U24-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

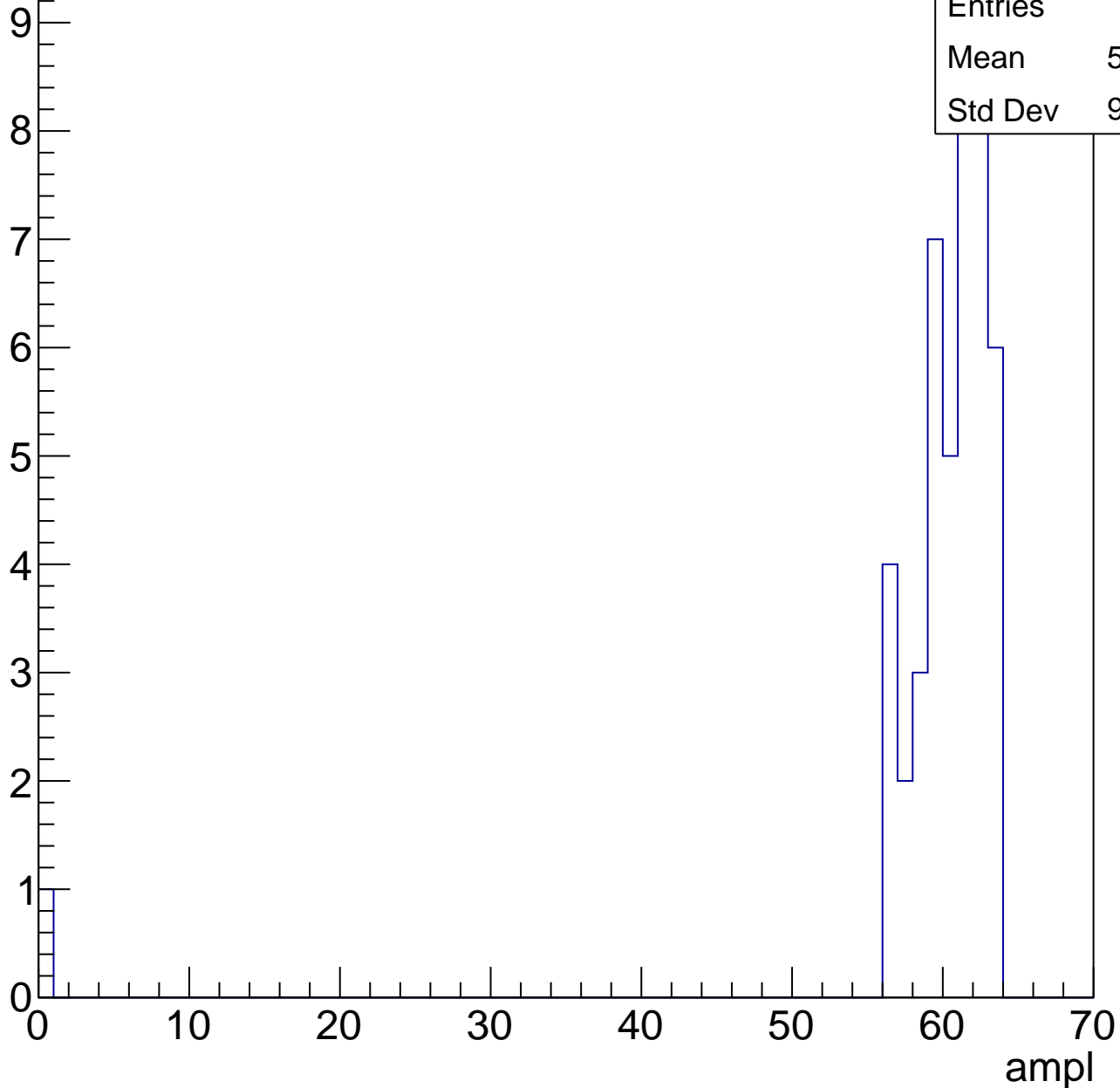
Entries	50
Mean	55.86
Std Dev	2.821



# B1L000S, U24-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

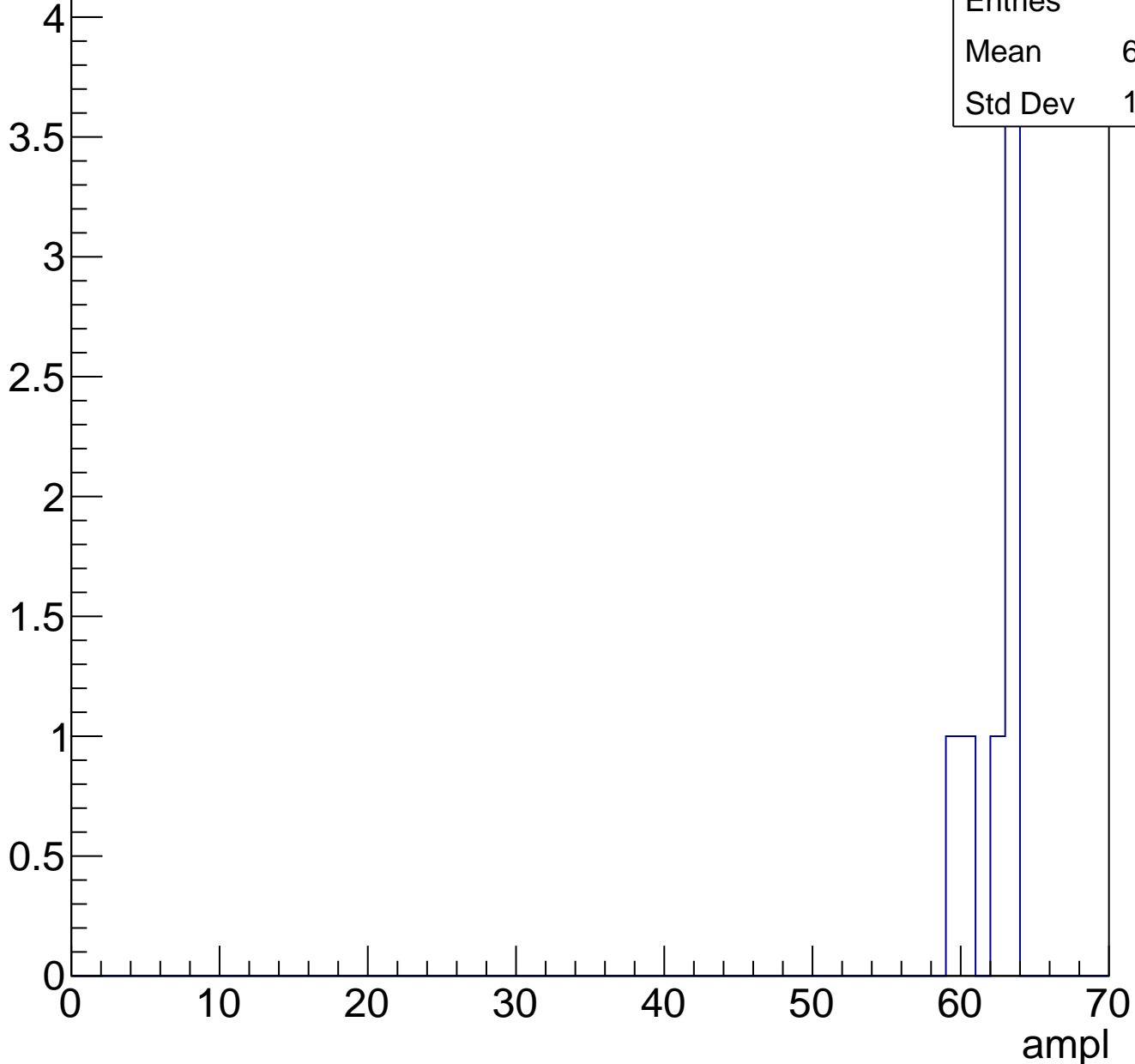
Entry



# B1L000S, U24-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

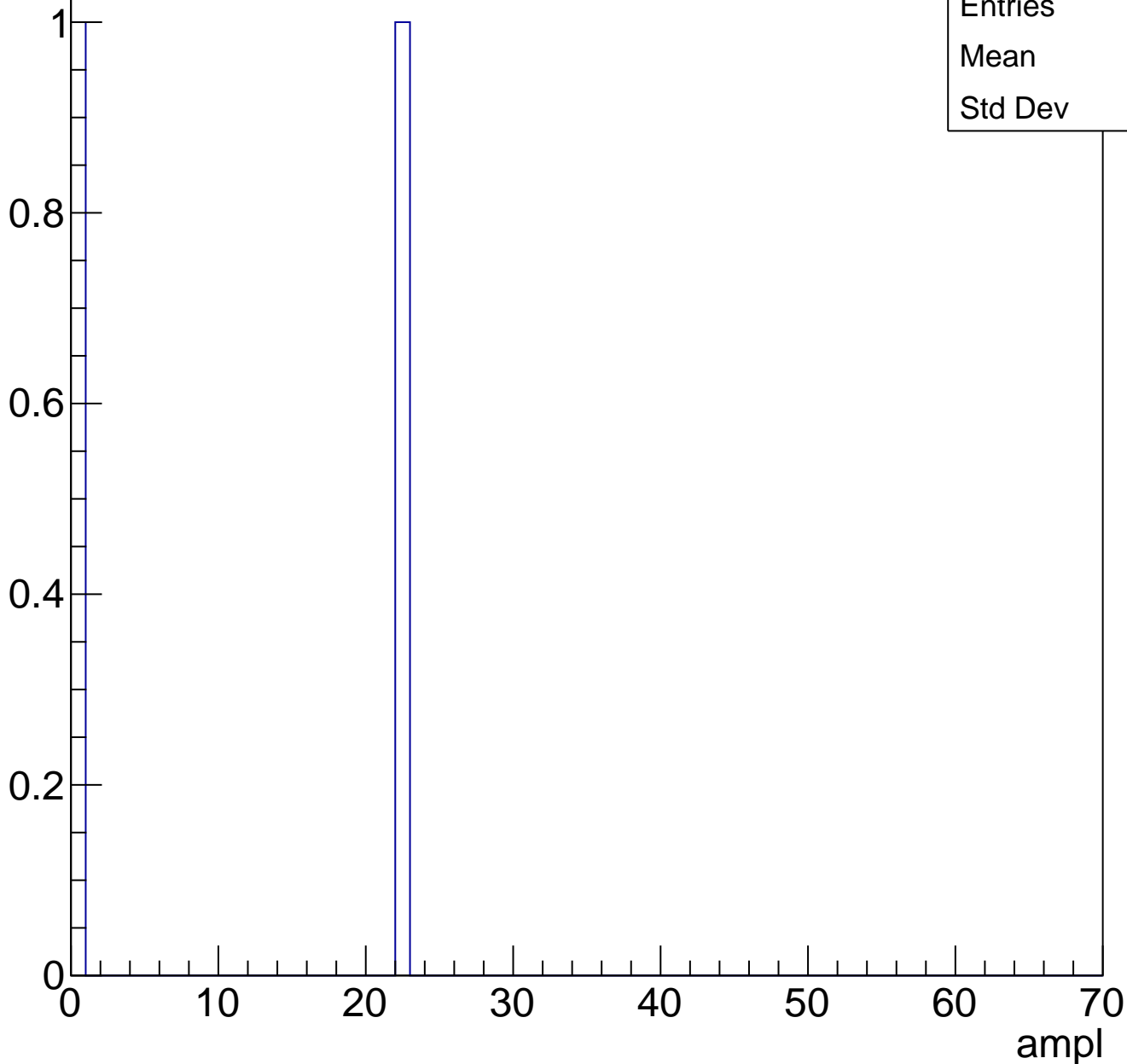




# B1L000S, U24-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U24-ch44, adc0

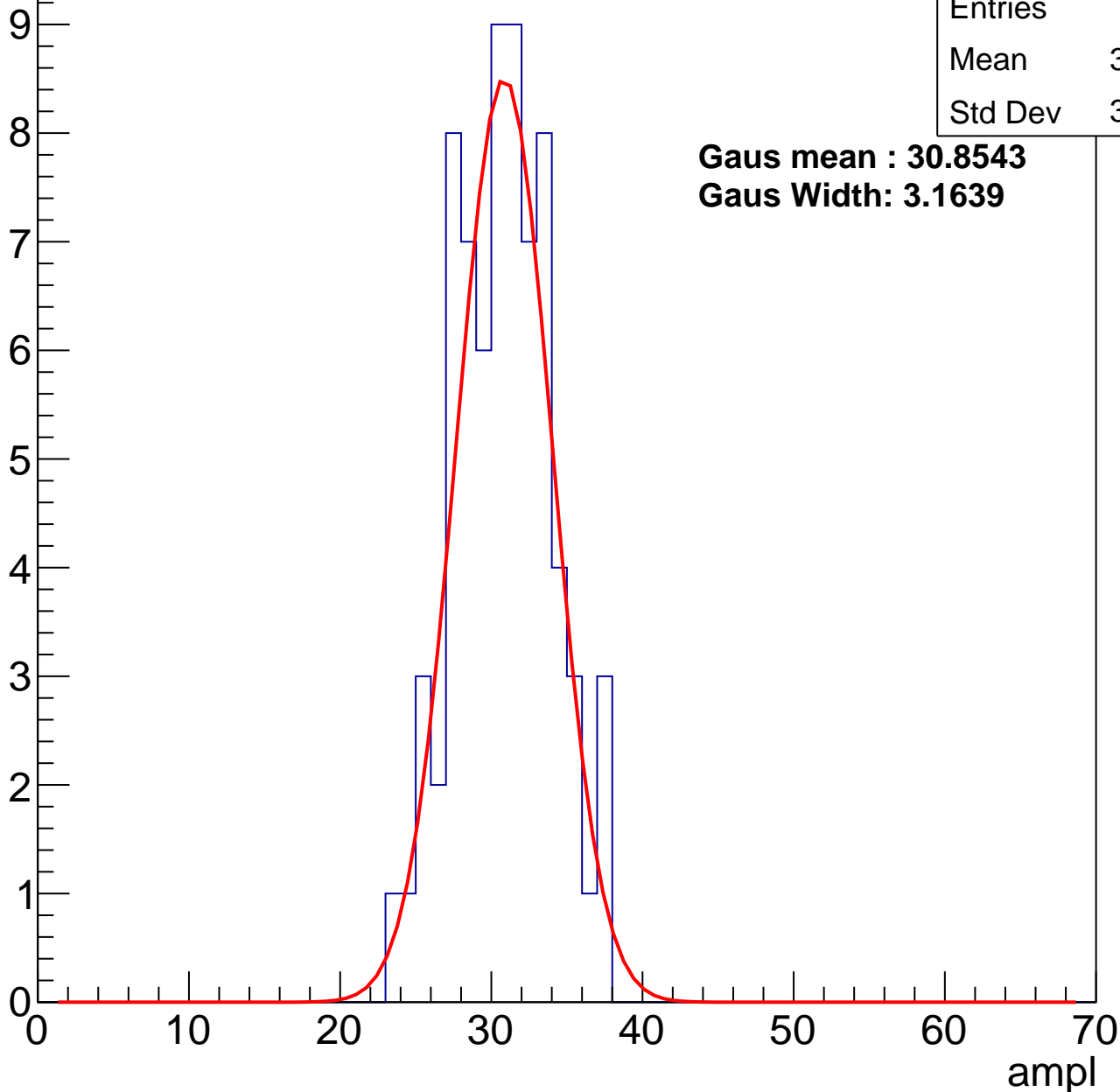
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	30.35
Std Dev	3.163

**Gaus mean : 30.8543**

**Gaus Width: 3.1639**



# B1L000S, U24-ch44, adc1

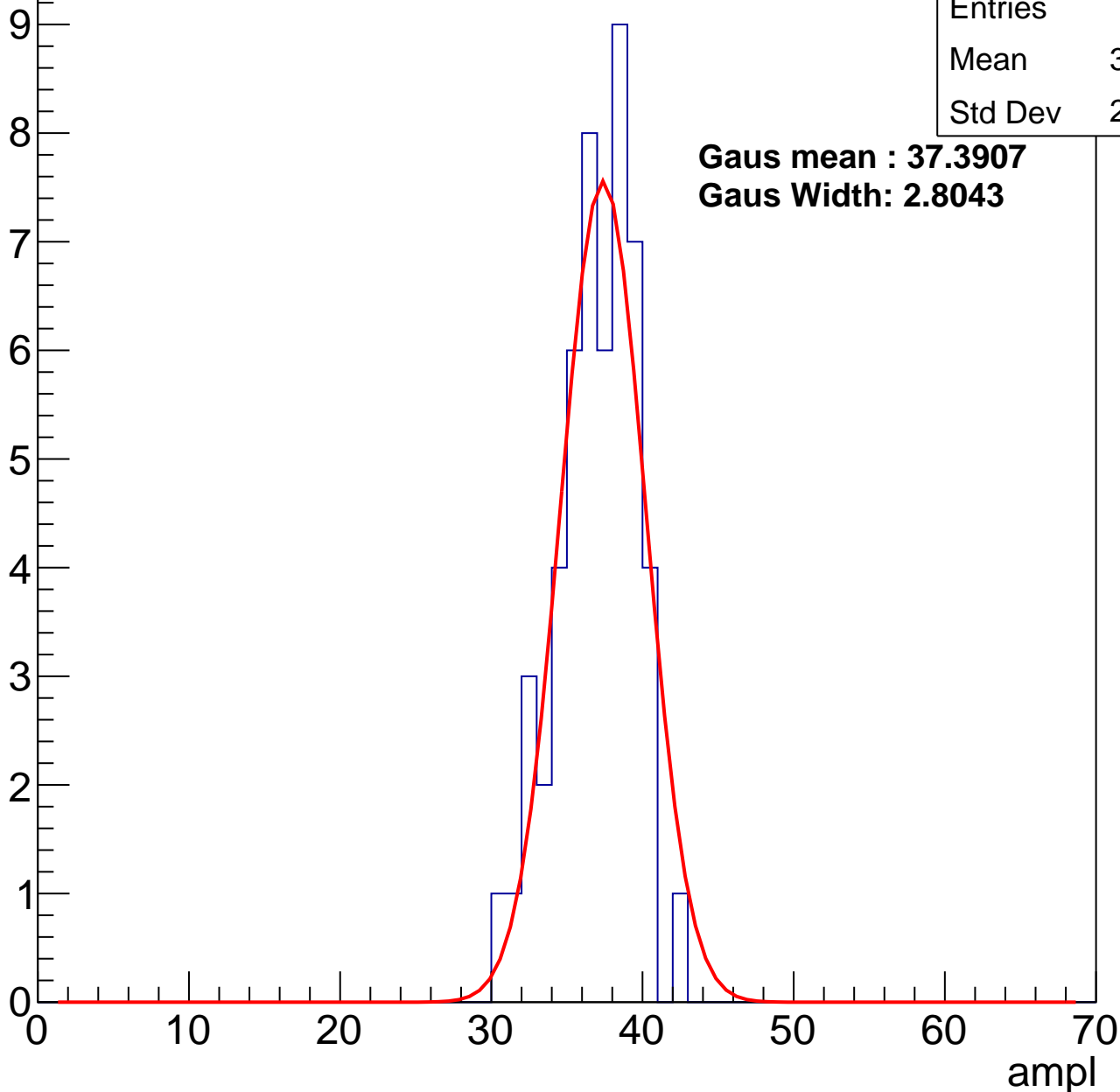
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	36.46
Std Dev	2.568

**Gaus mean : 37.3907**

**Gaus Width: 2.8043**



# B1L000S, U24-ch44, adc2

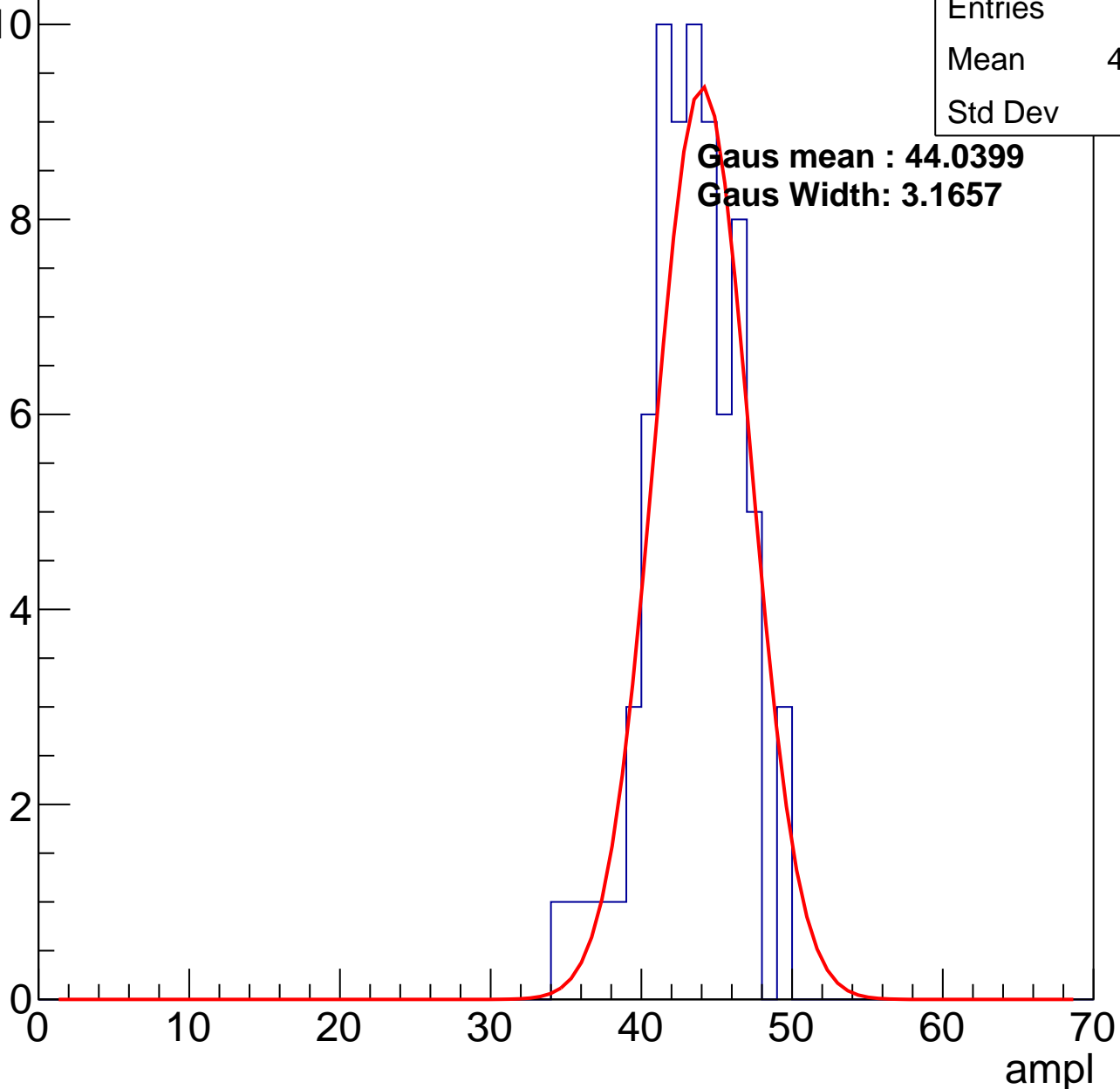
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	42.85
Std Dev	3.07

**Gaus mean : 44.0399**

**Gaus Width: 3.1657**

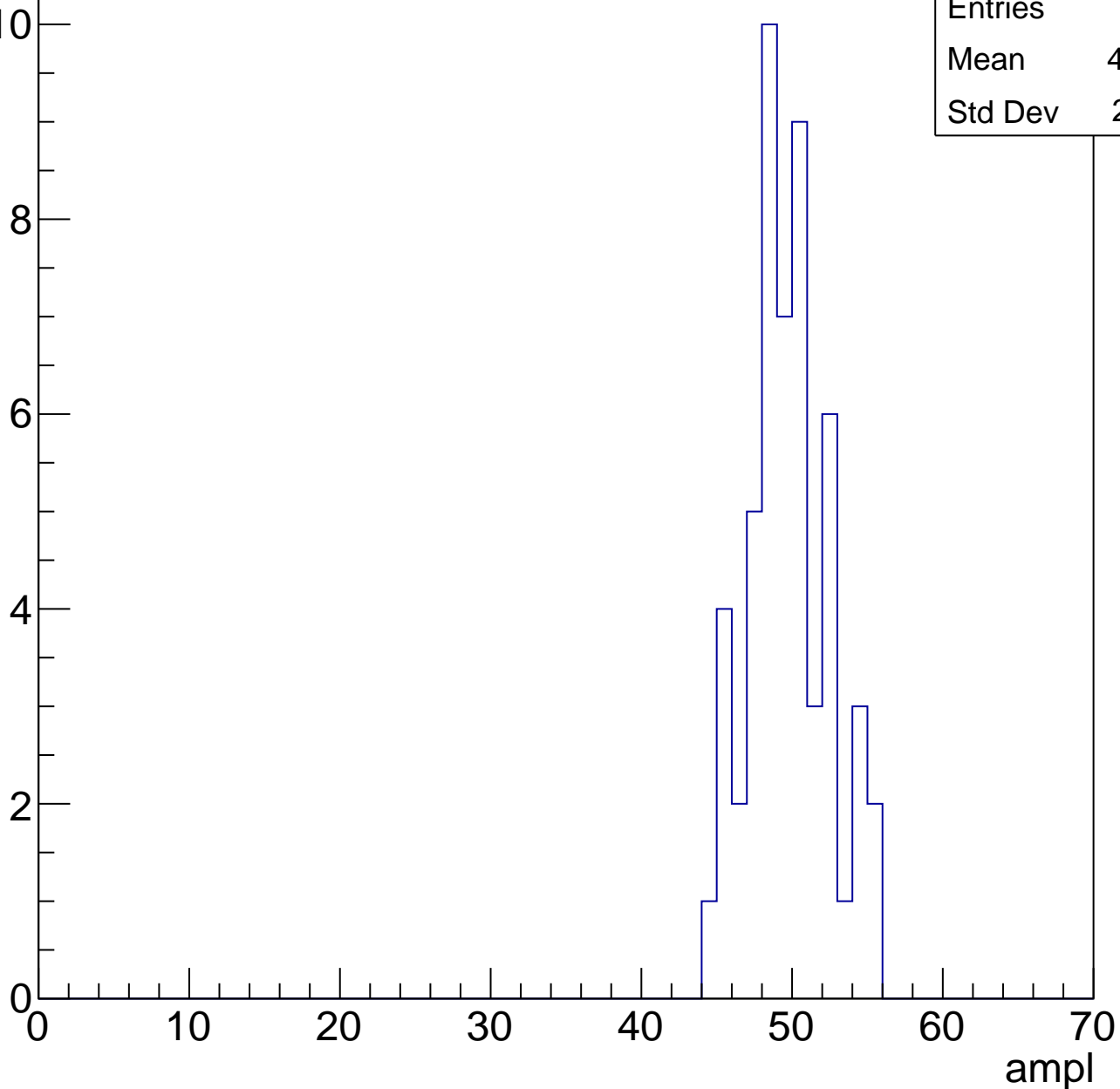


# B1L000S, U24-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	49.32
Std Dev	2.641

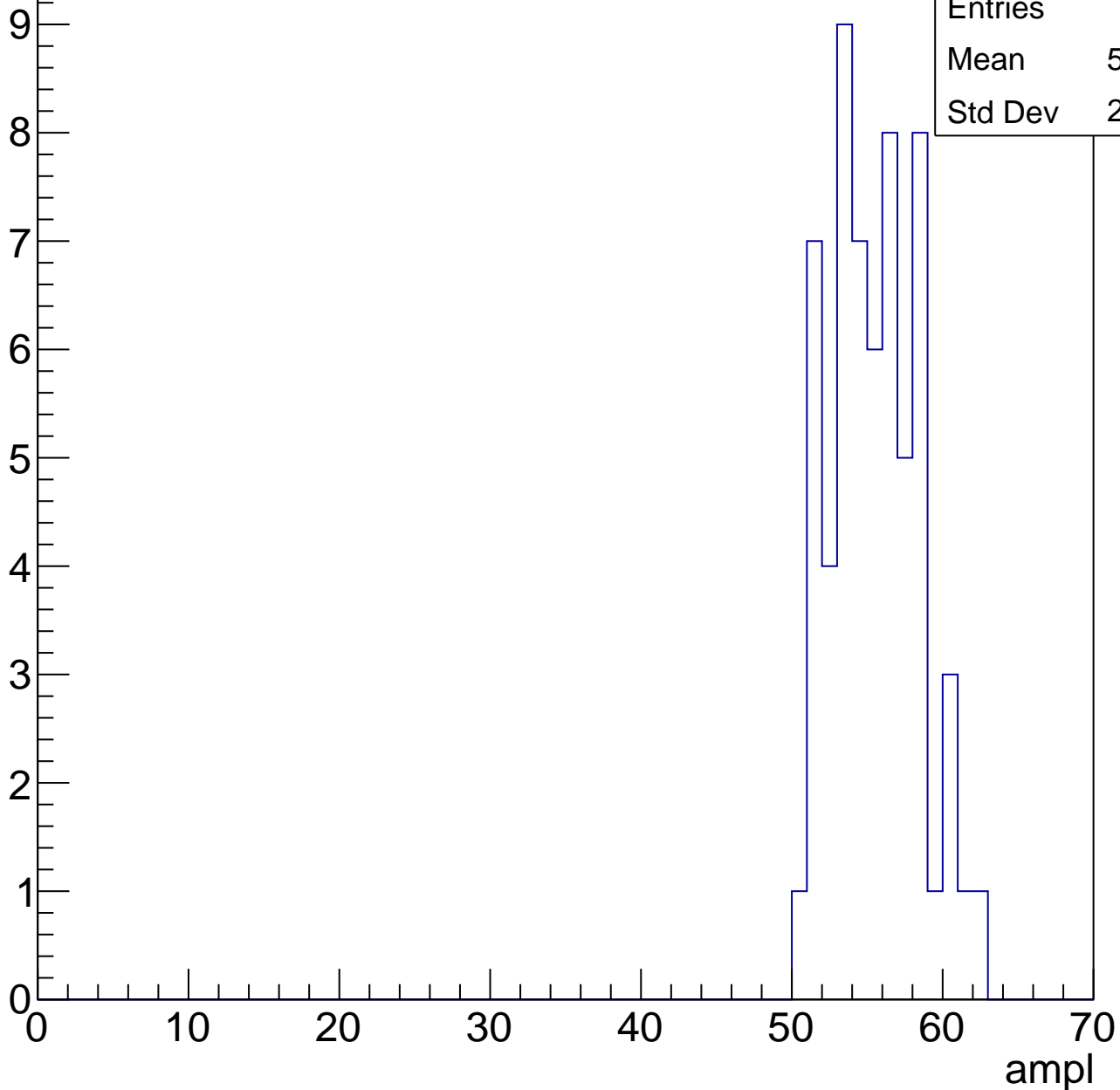


# B1L000S, U24-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

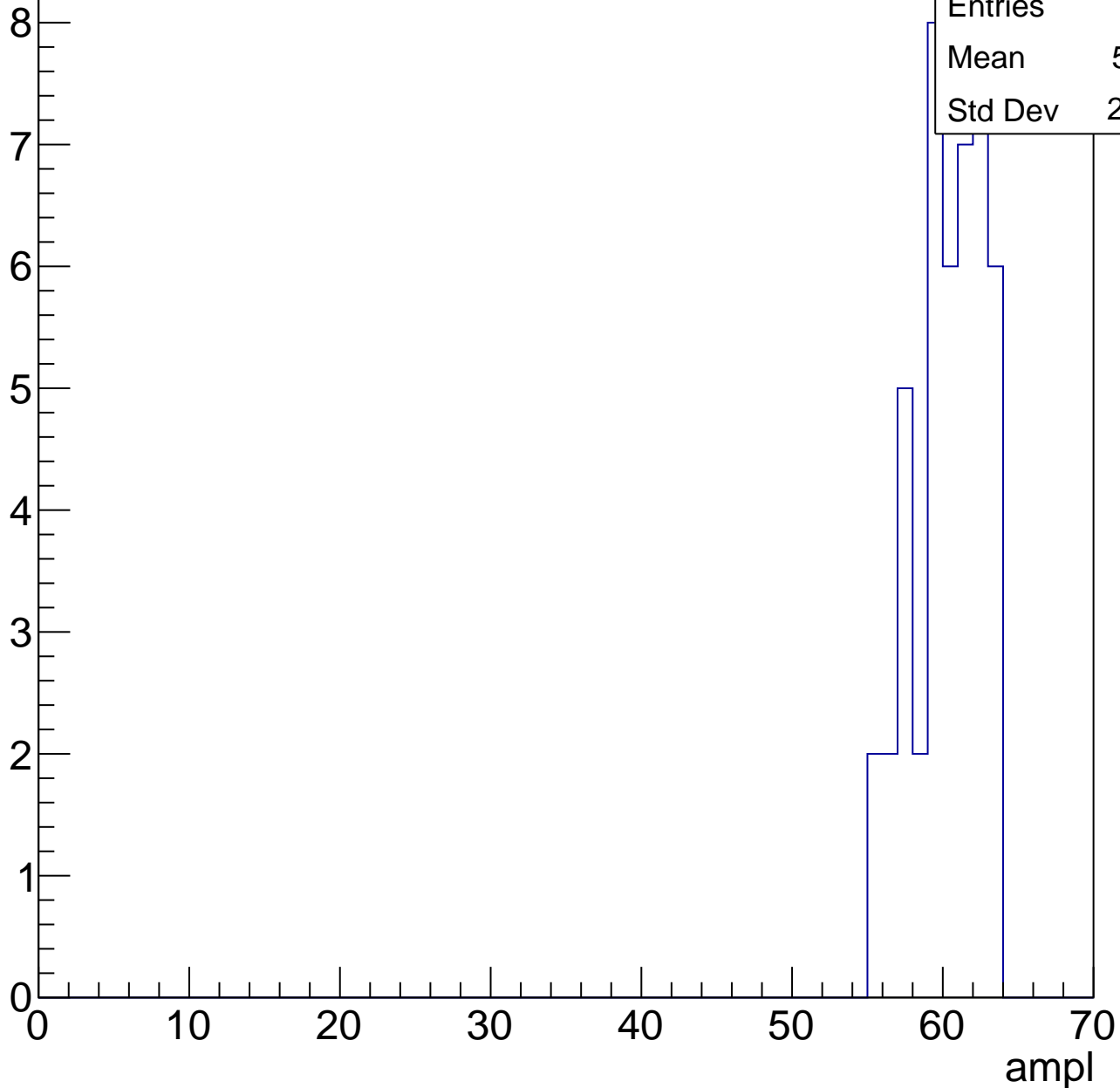
Entries	61
Mean	55.07
Std Dev	2.839



# B1L000S, U24-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

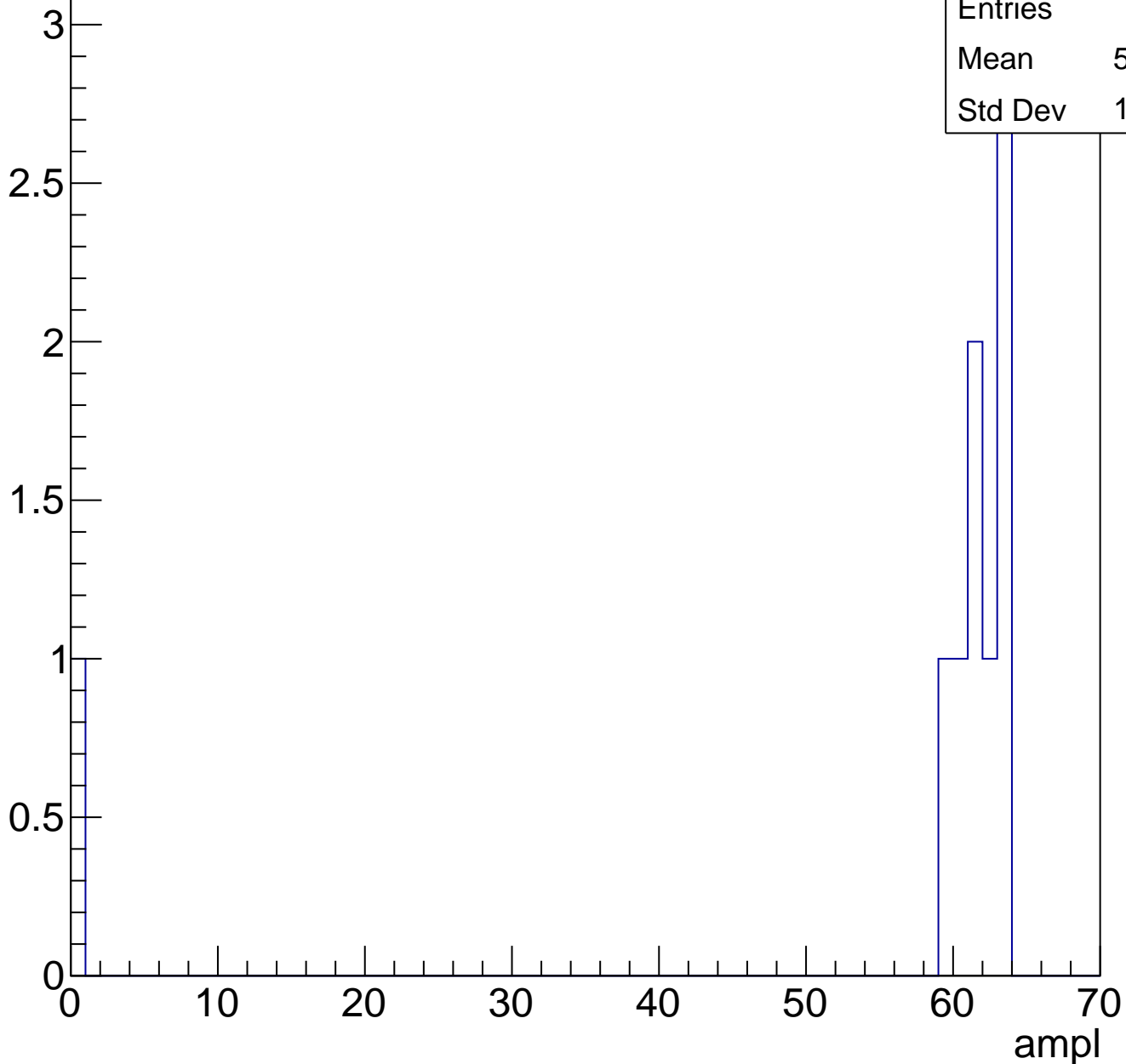


Entries	46
Mean	59.91
Std Dev	2.263

# B1L000S, U24-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L000S, U24-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	29.1
Std Dev	4.67

**Gaus mean : 29.5767**

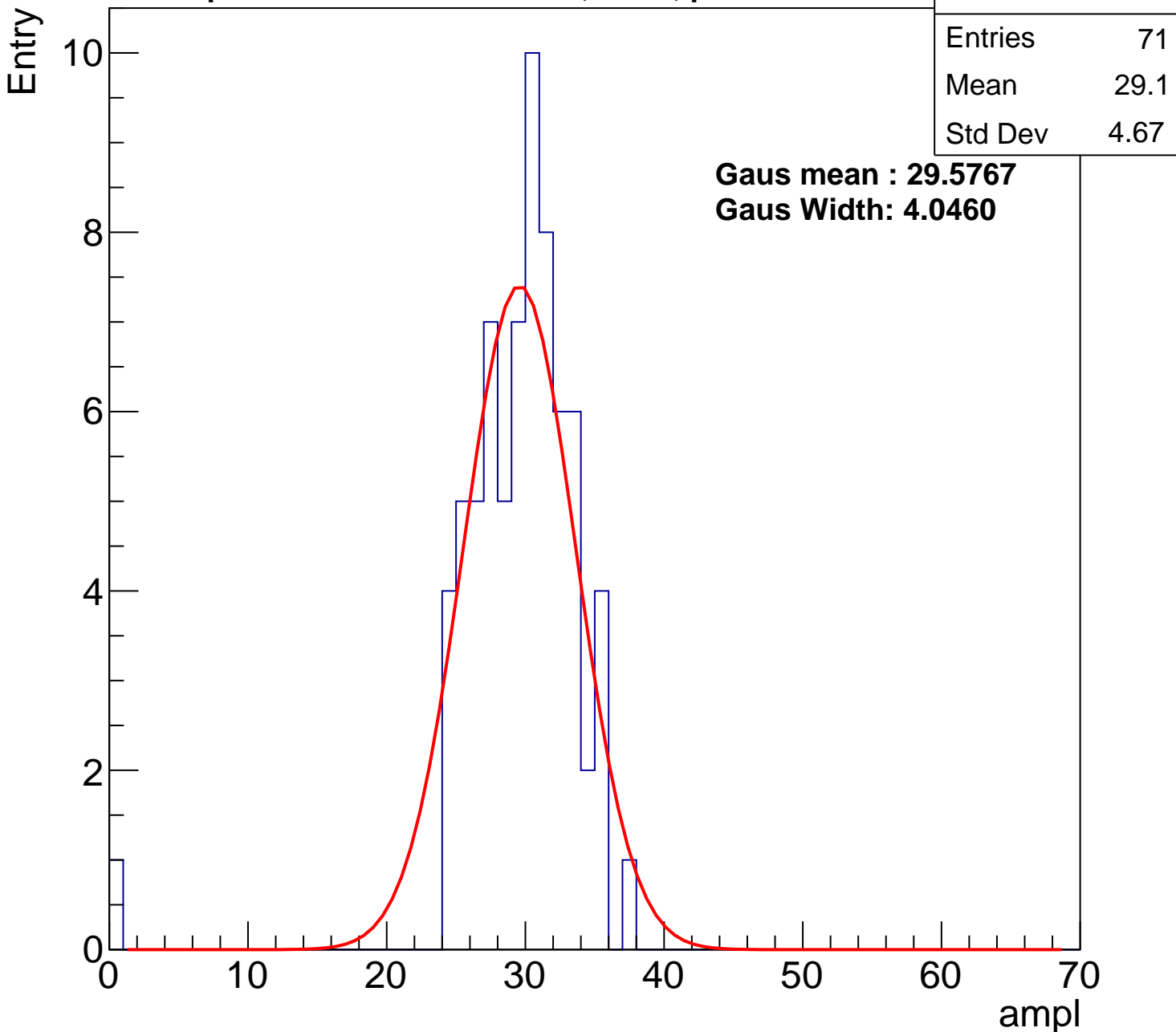
**Gaus Width: 4.0460**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch45, adc1

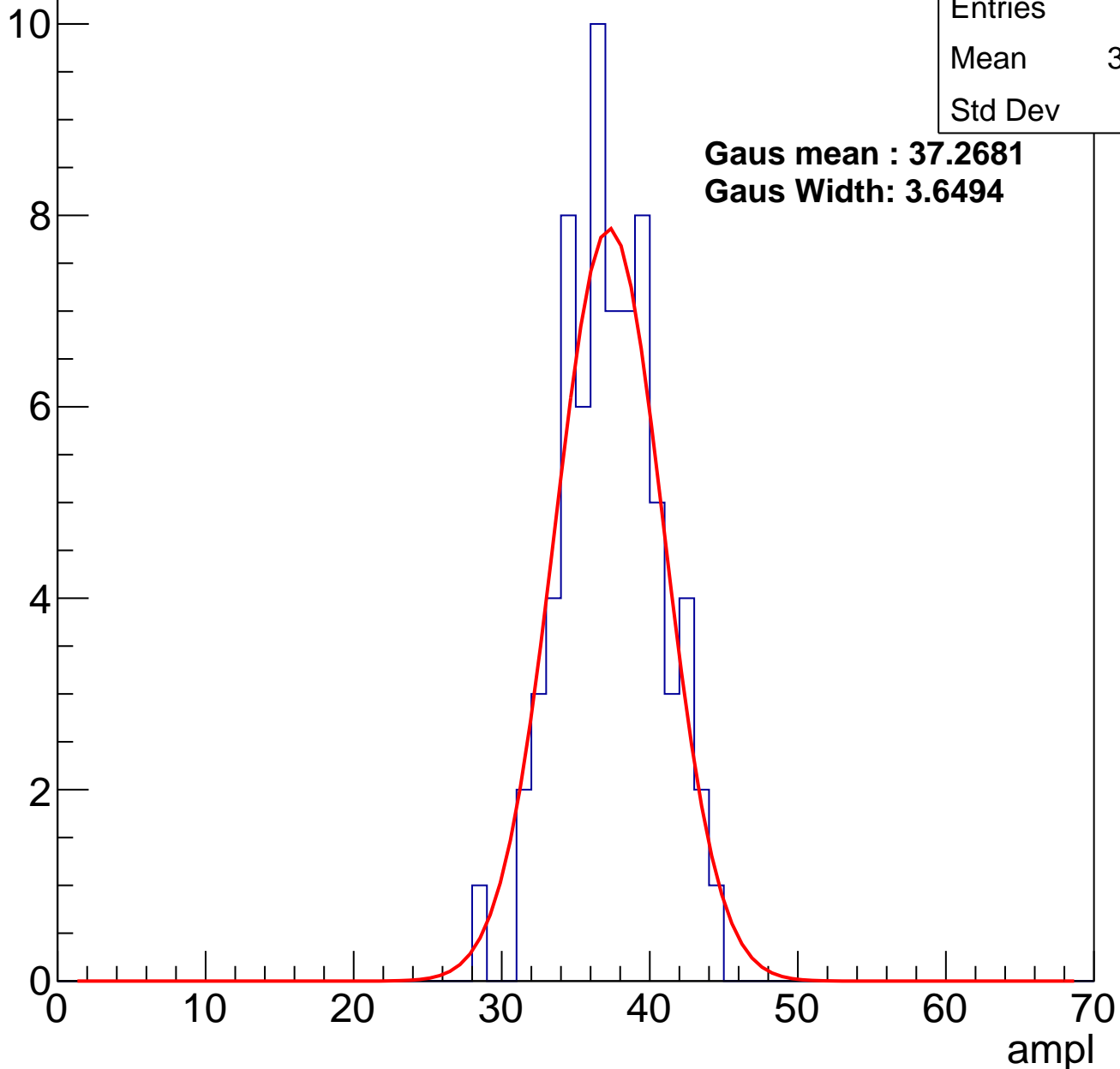
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	36.87
Std Dev	3.25

**Gaus mean : 37.2681**

**Gaus Width: 3.6494**

Entry



# B1L000S, U24-ch45, adc2

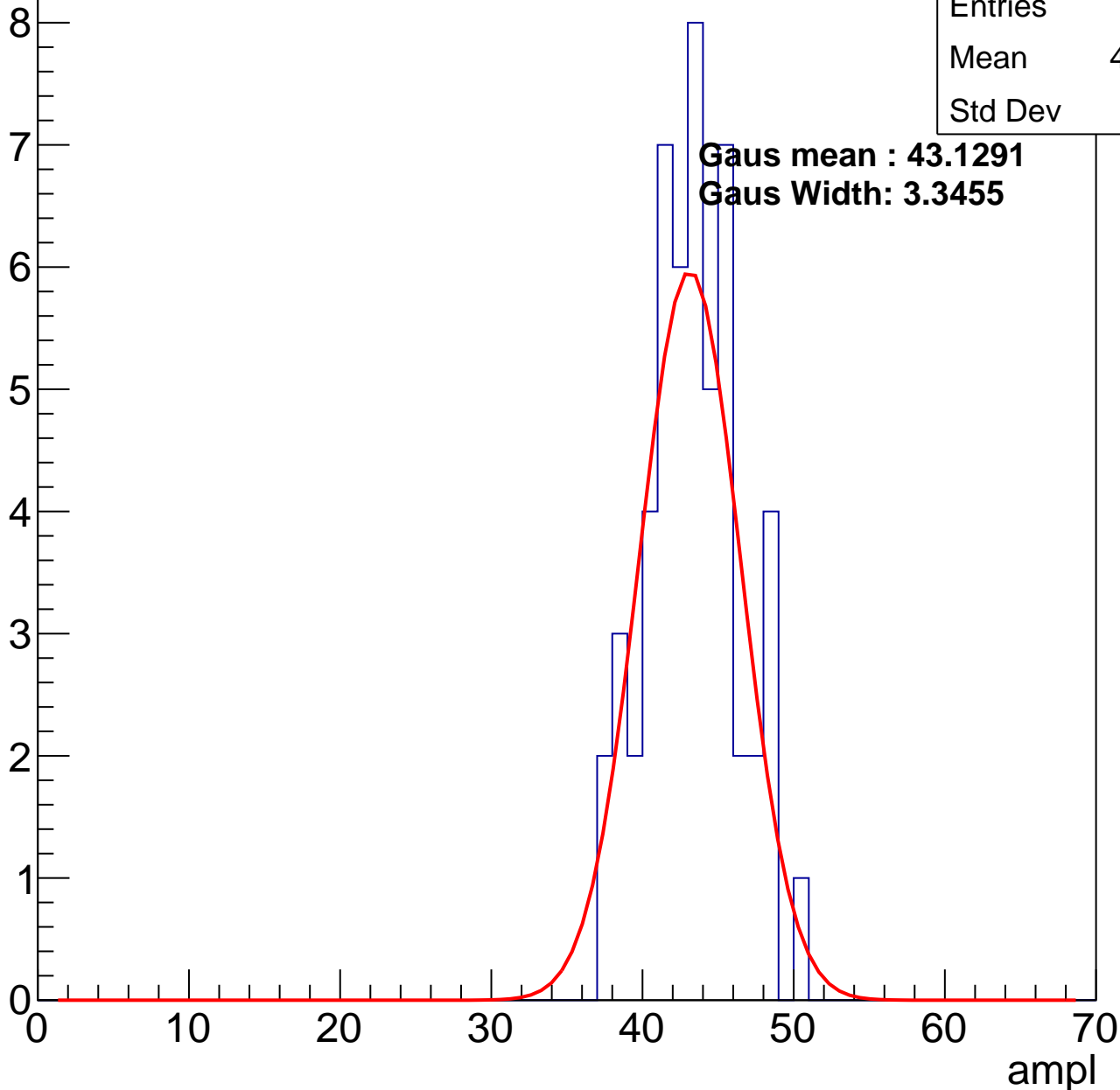
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	42.87
Std Dev	3.01

**Gaus mean : 43.1291**

**Gaus Width: 3.3455**

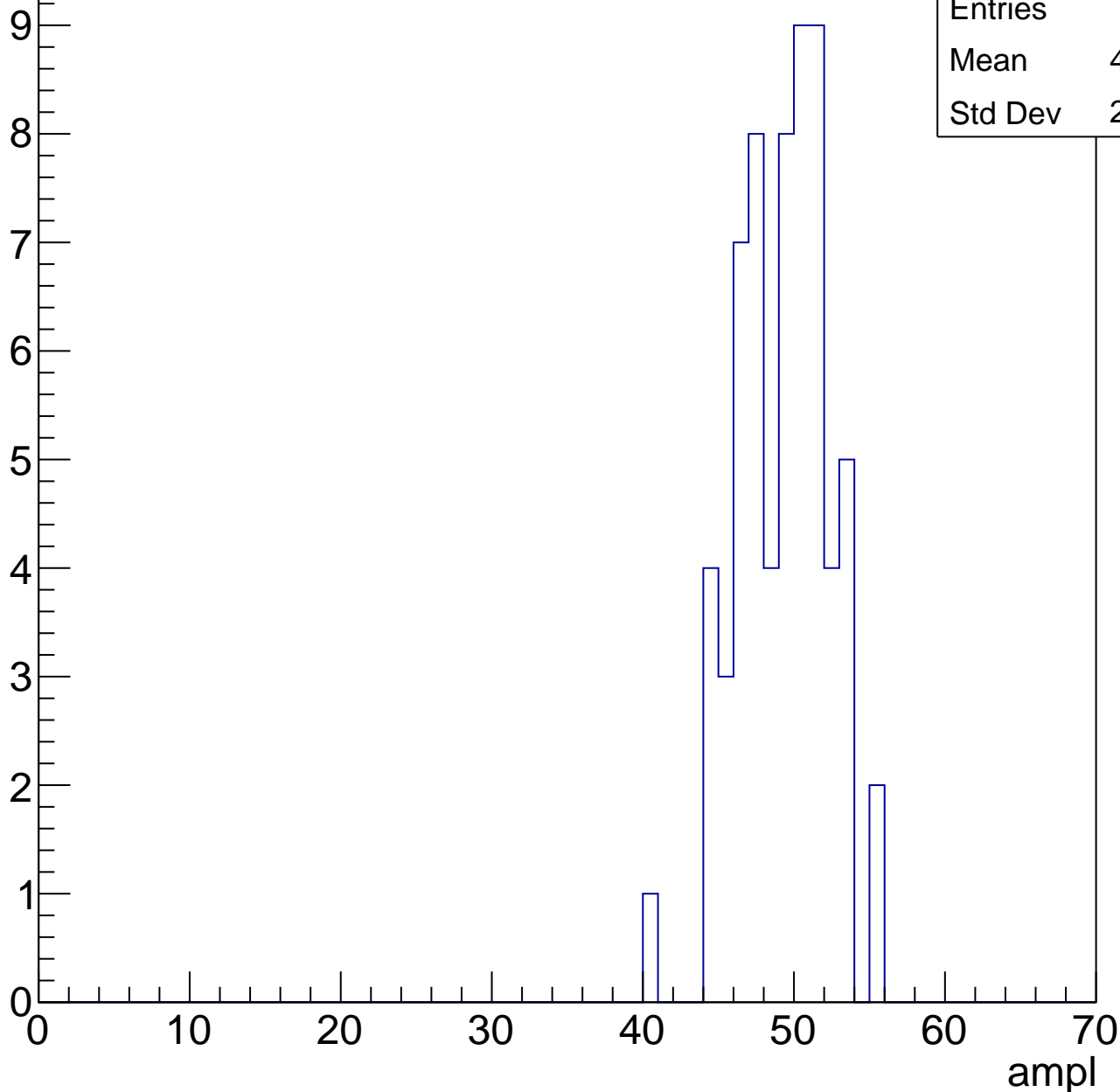


# B1L000S, U24-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.83
Std Dev	2.956

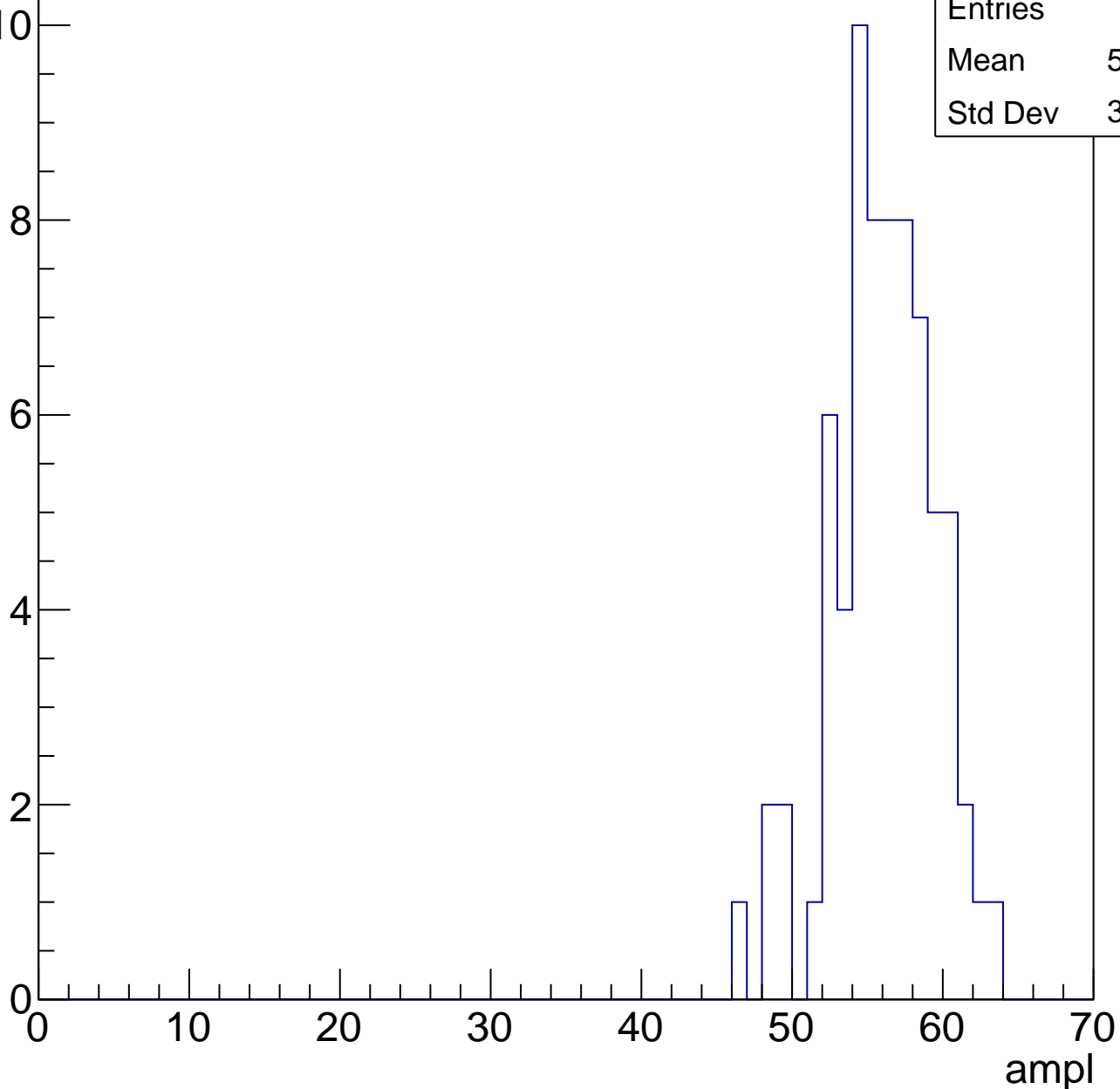


# B1L000S, U24-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	55.59
Std Dev	3.388

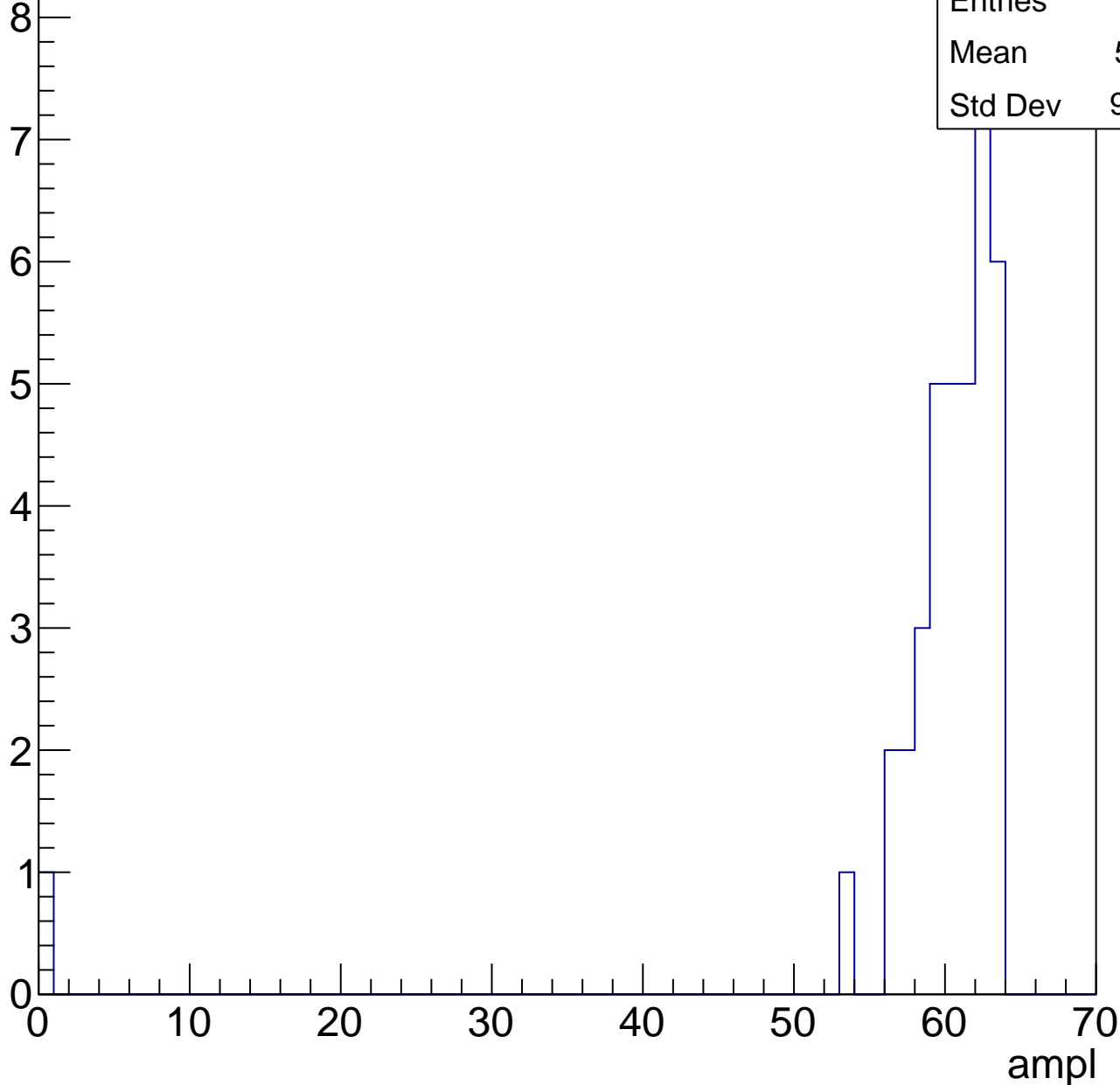


# B1L000S, U24-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

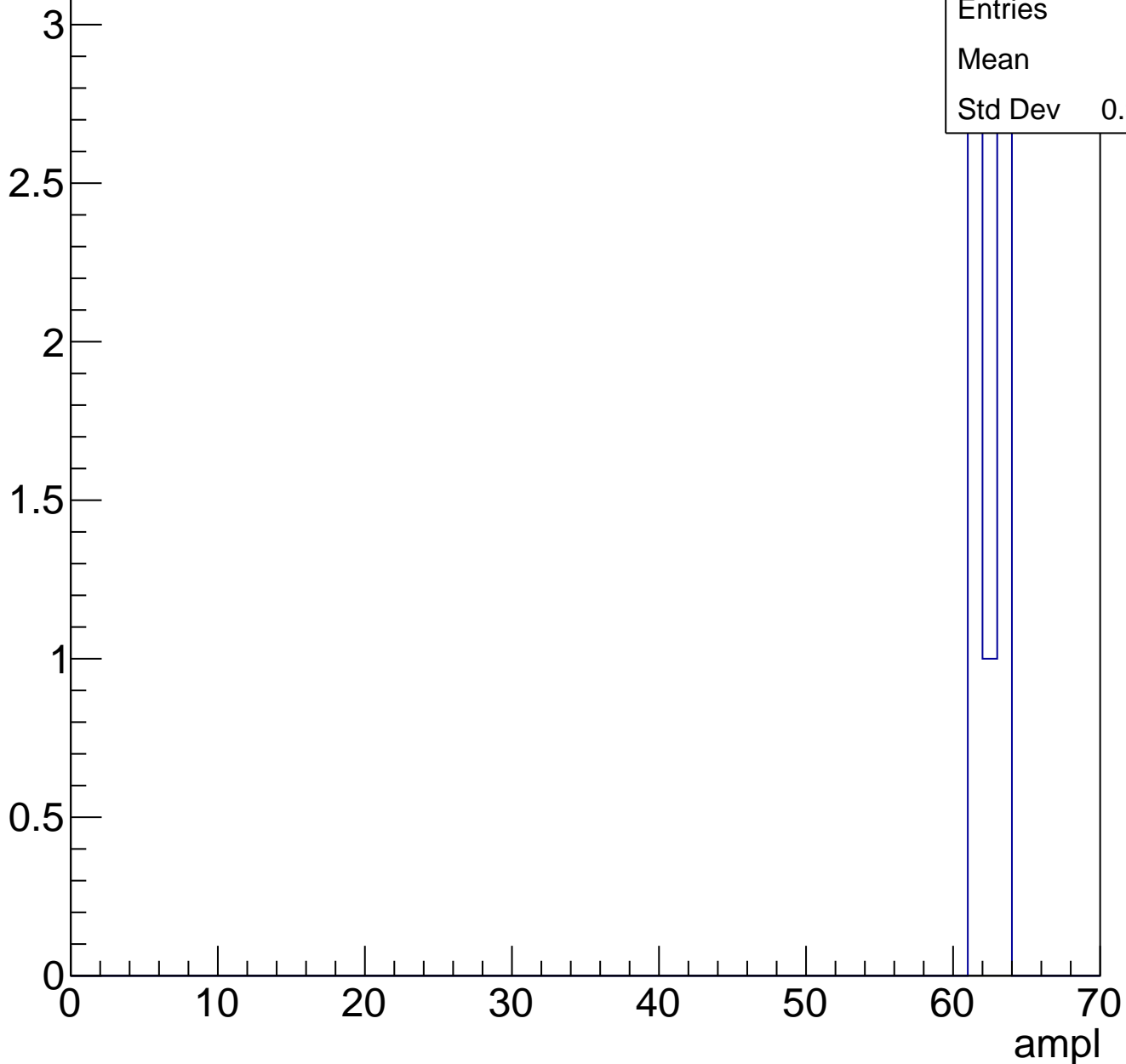
Entries	38
Mean	58.61
Std Dev	9.912



# B1L000S, U24-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch46, adc0

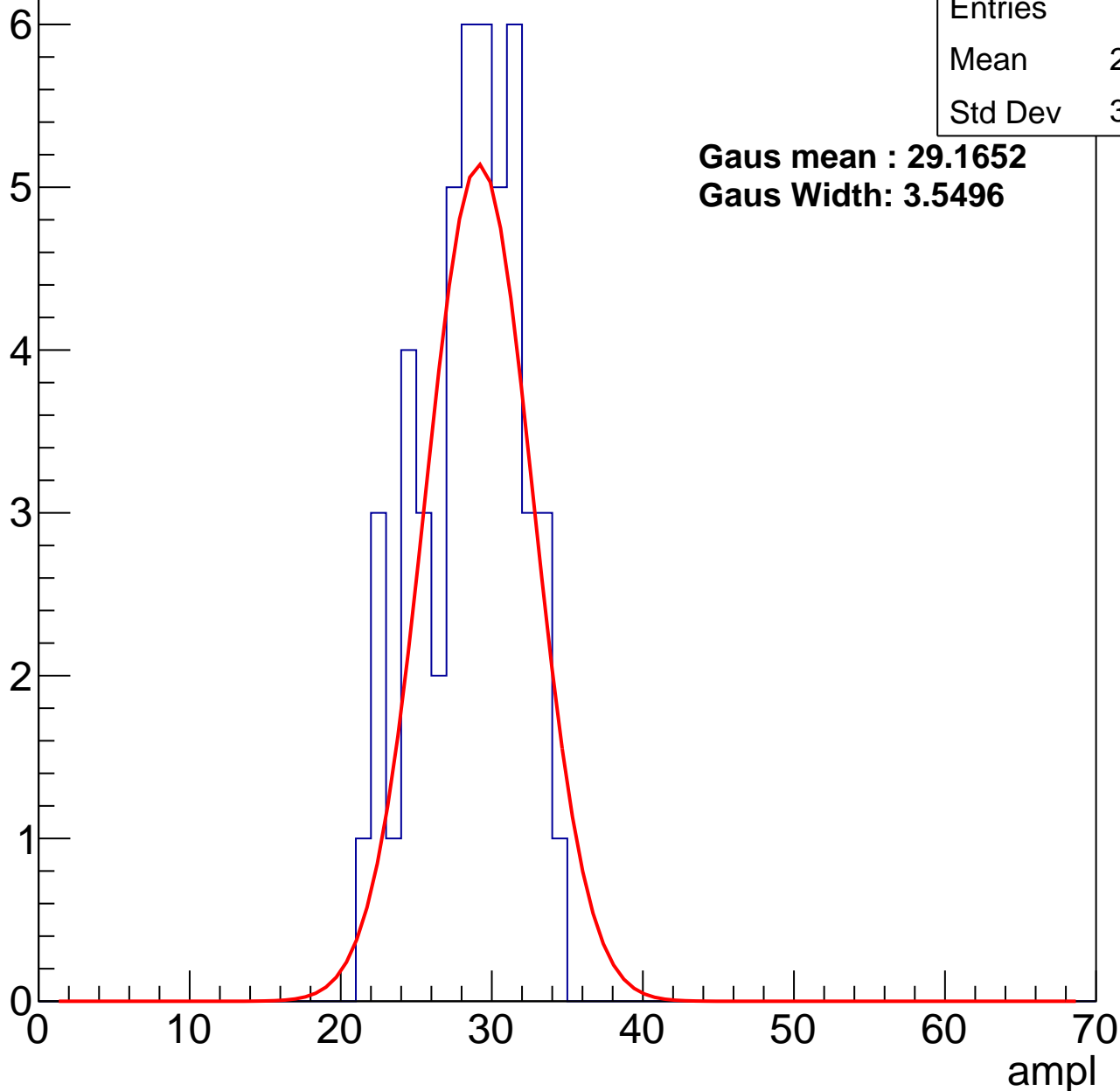
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	28.06
Std Dev	3.273

**Gaus mean : 29.1652**

**Gaus Width: 3.5496**



# B1L000S, U24-ch46, adc1

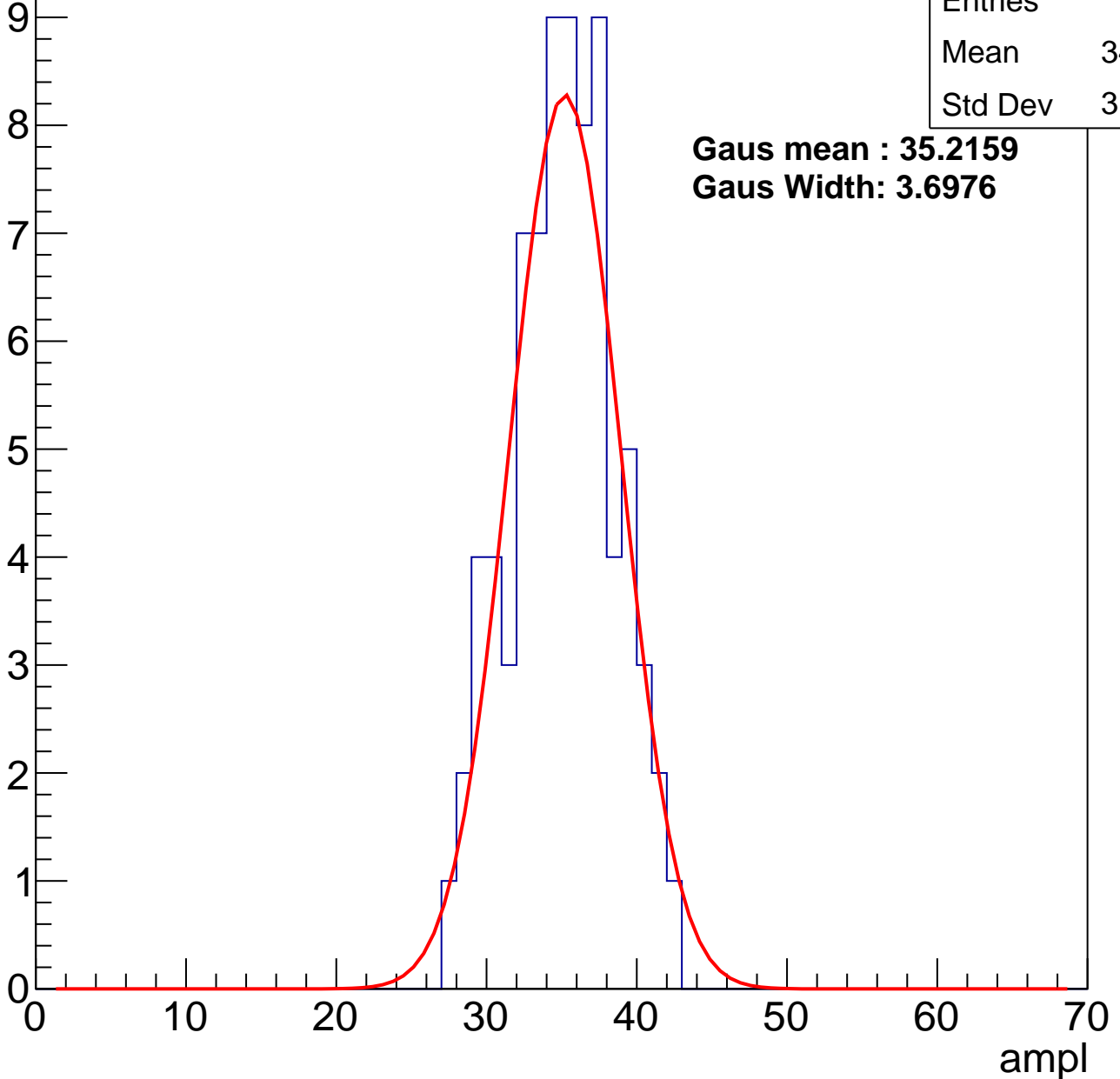
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	34.62
Std Dev	3.394

**Gaus mean : 35.2159**

**Gaus Width: 3.6976**



# B1L000S, U24-ch46, adc2

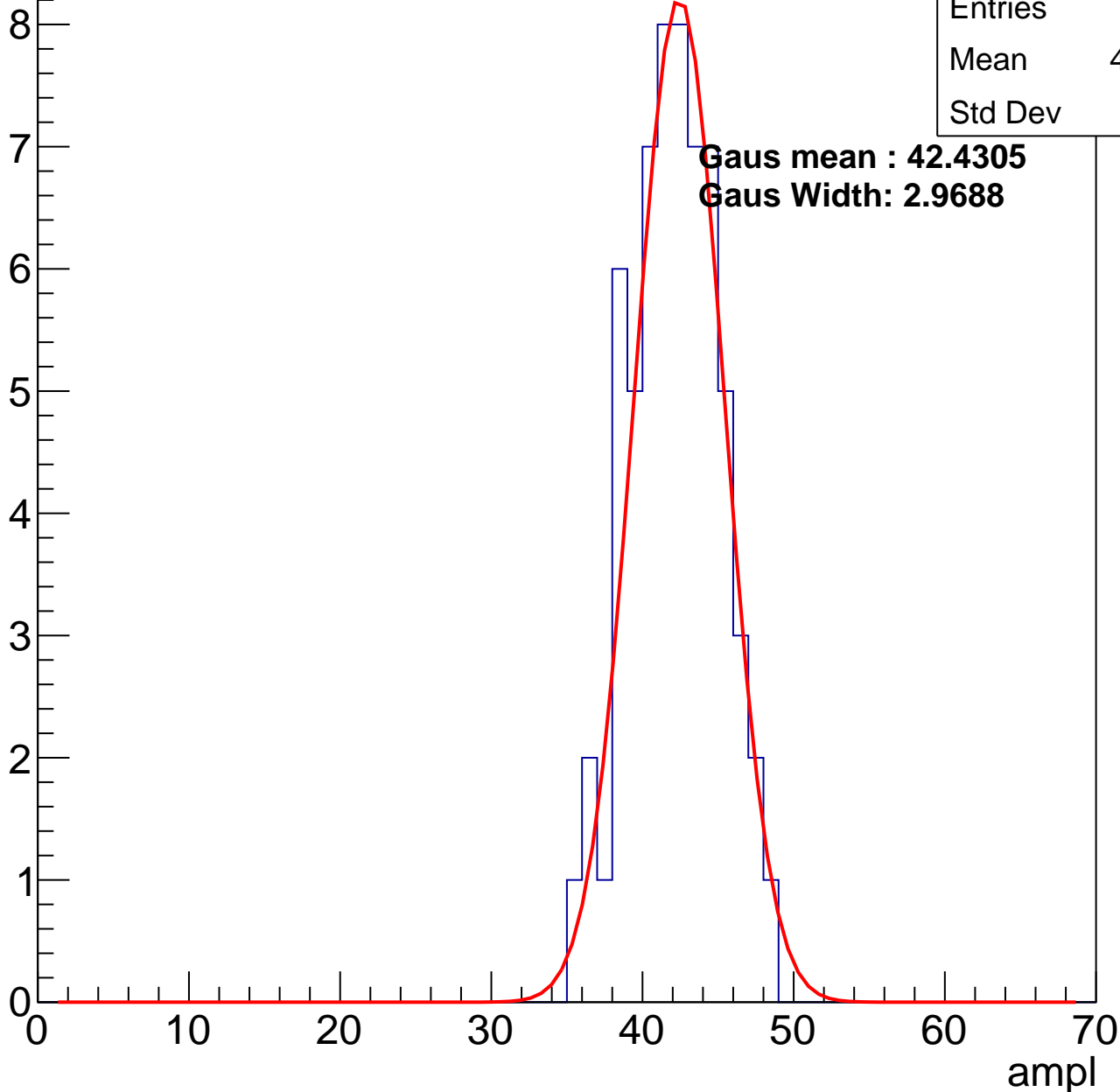
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.67
Std Dev	2.9

**Gaus mean : 42.4305**

**Gaus Width: 2.9688**

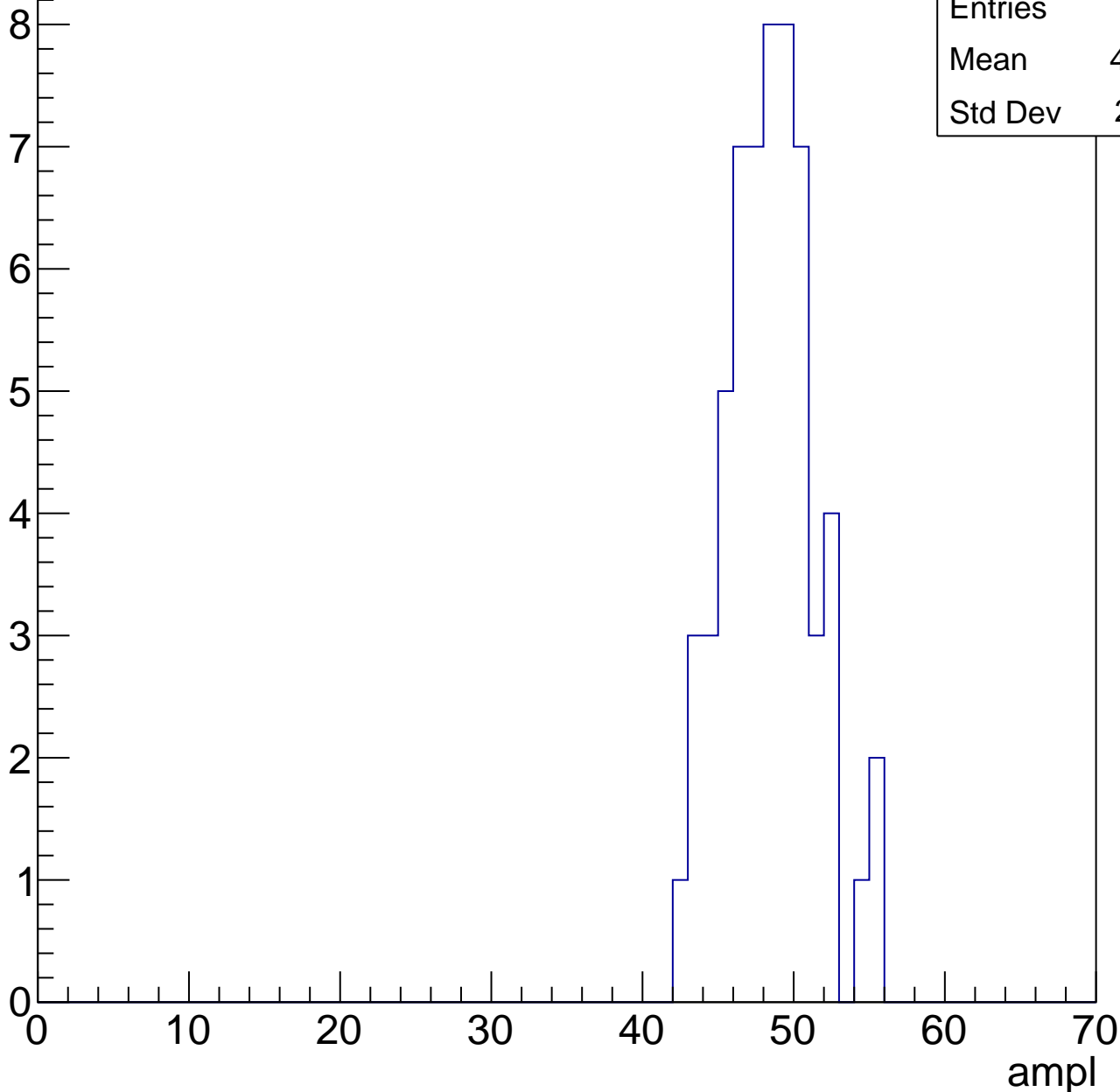


# B1L000S, U24-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.97
Std Dev	2.911

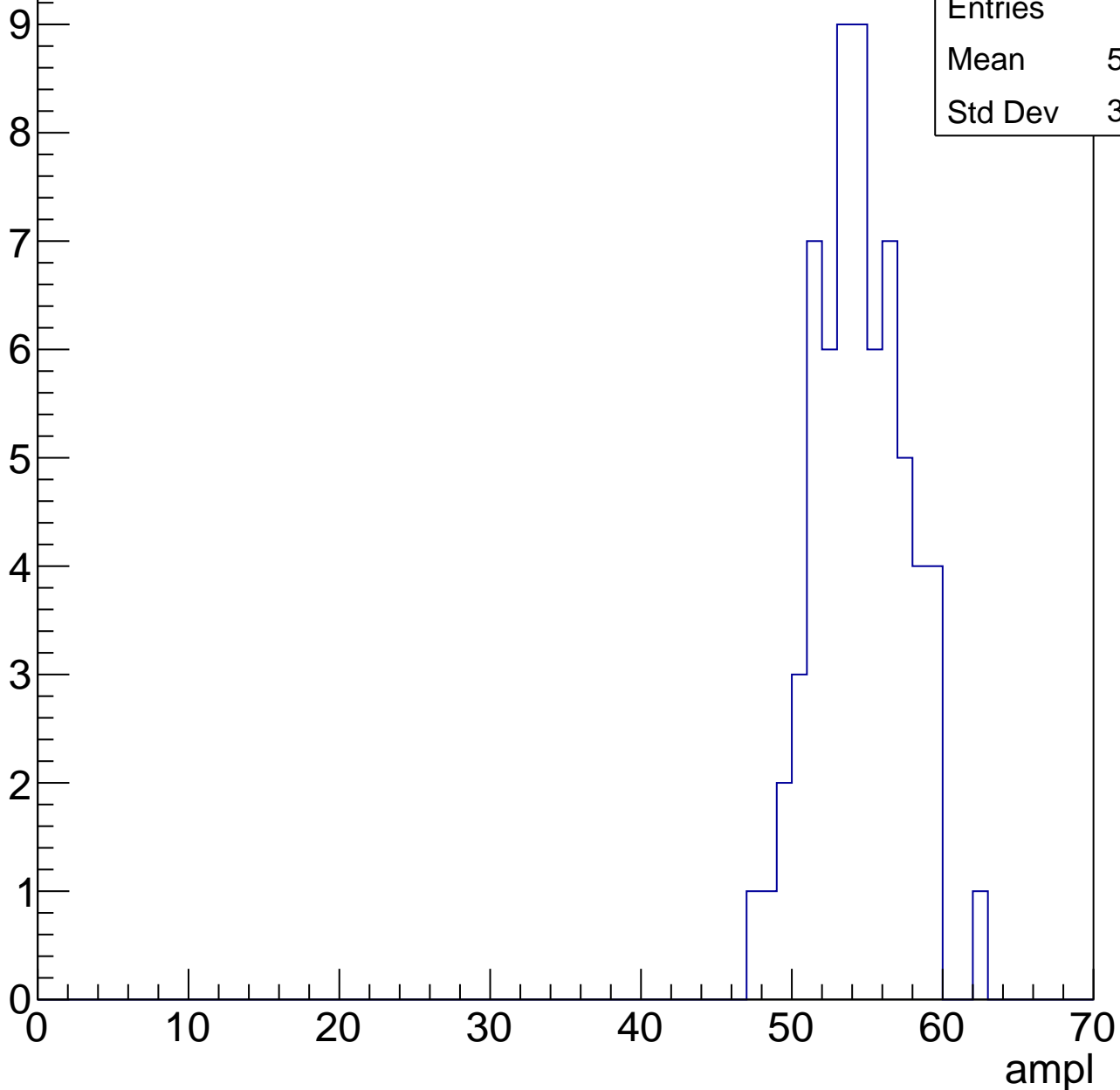


# B1L000S, U24-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.03
Std Dev	3.002

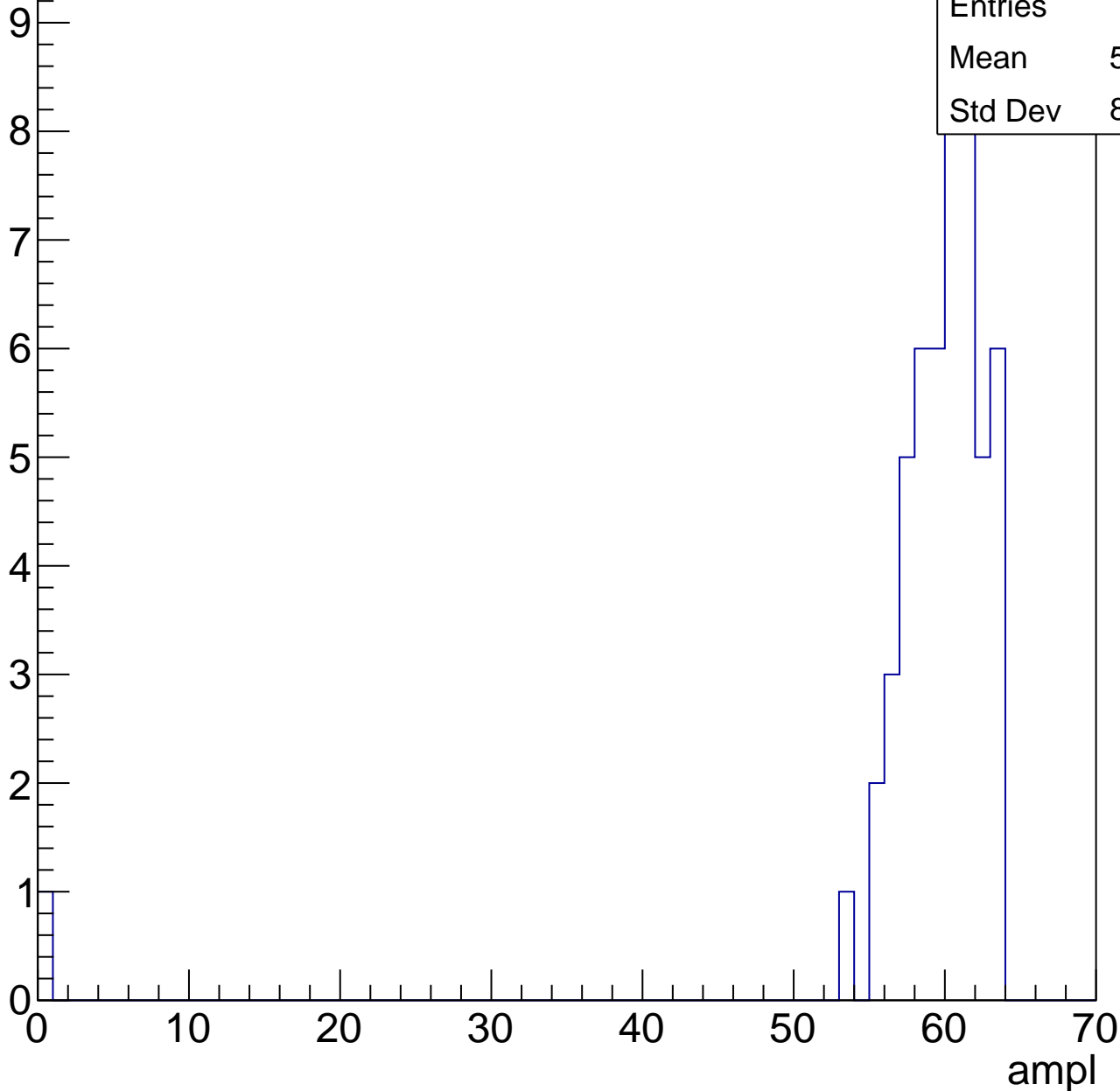


# B1L000S, U24-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

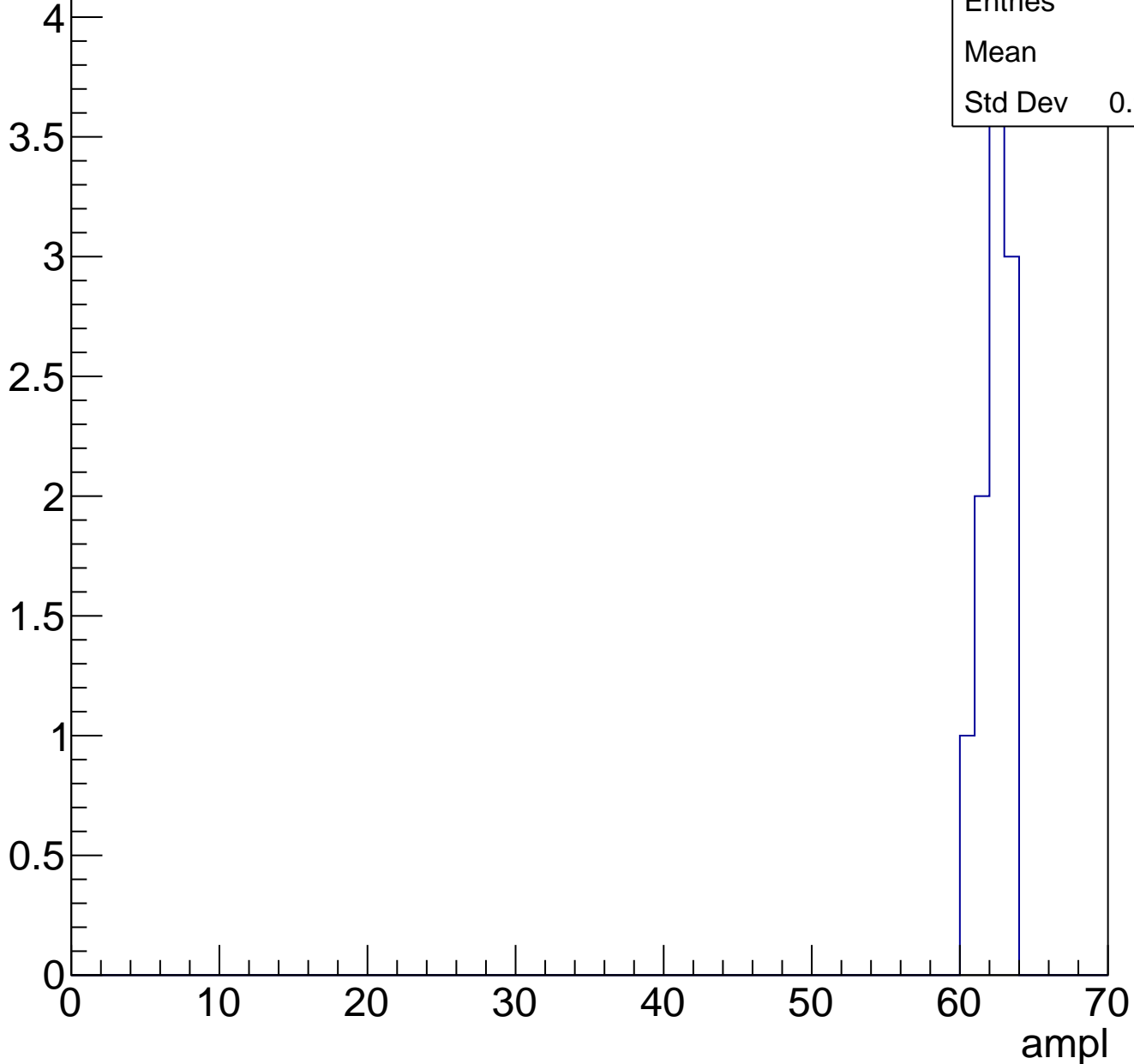
Entries	52
Mean	58.35
Std Dev	8.505



# B1L000S, U24-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U24-ch47, adc0

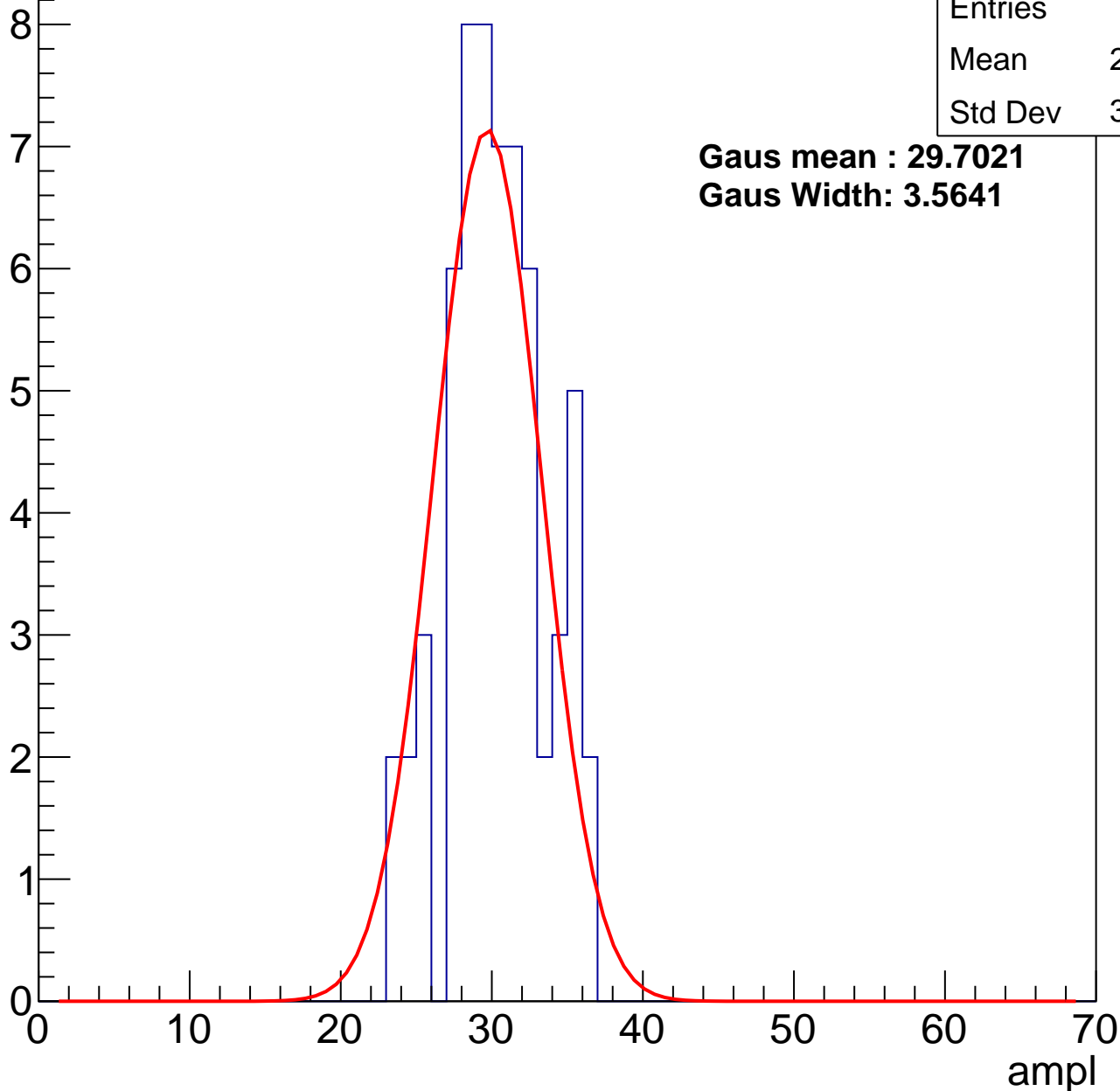
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	29.85
Std Dev	3.218

**Gaus mean : 29.7021**

**Gaus Width: 3.5641**



# B1L000S, U24-ch47, adc1

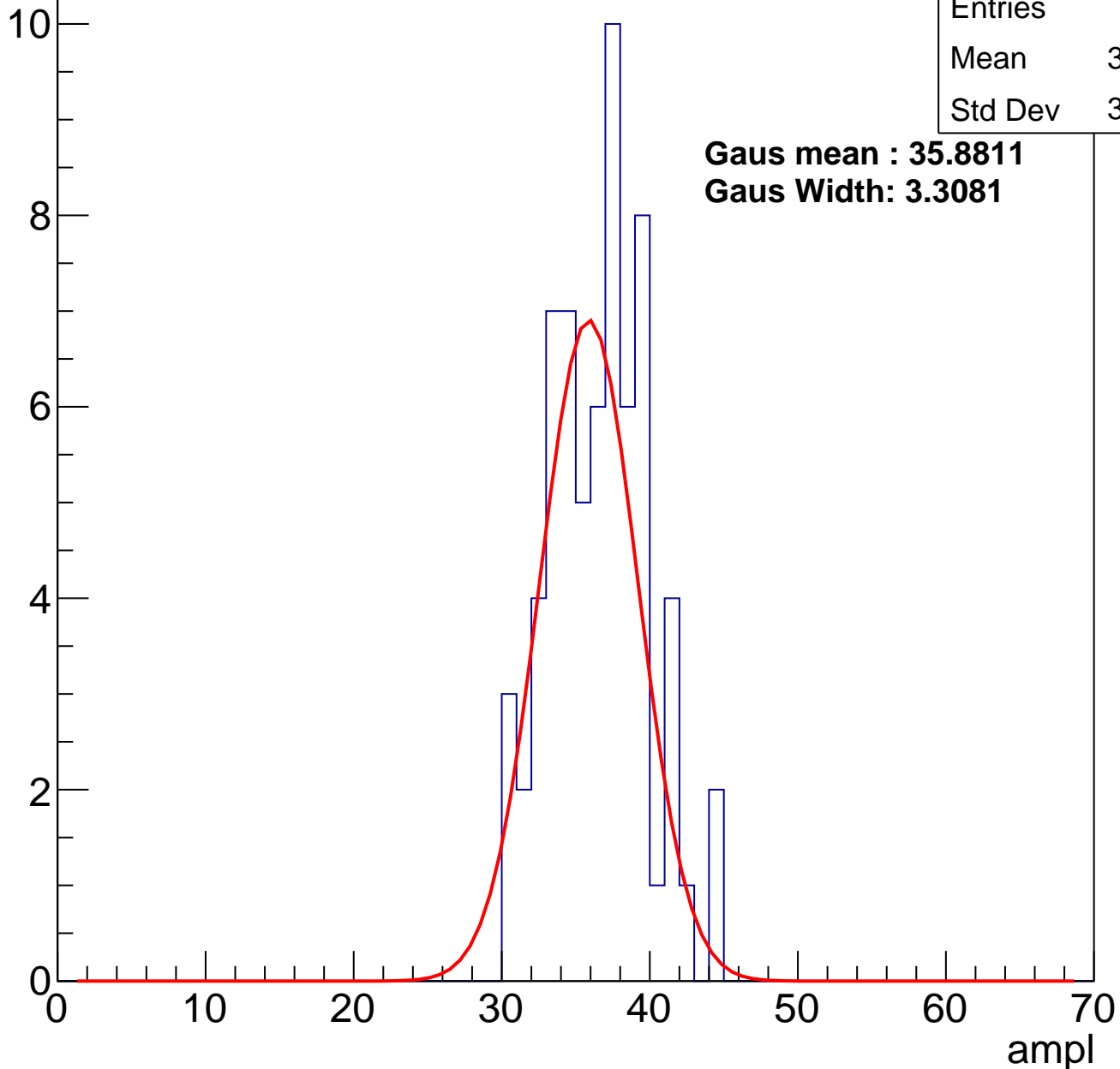
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	36.12
Std Dev	3.264

**Gaus mean : 35.8811**

**Gaus Width: 3.3081**

Entry



# B1L000S, U24-ch47, adc2

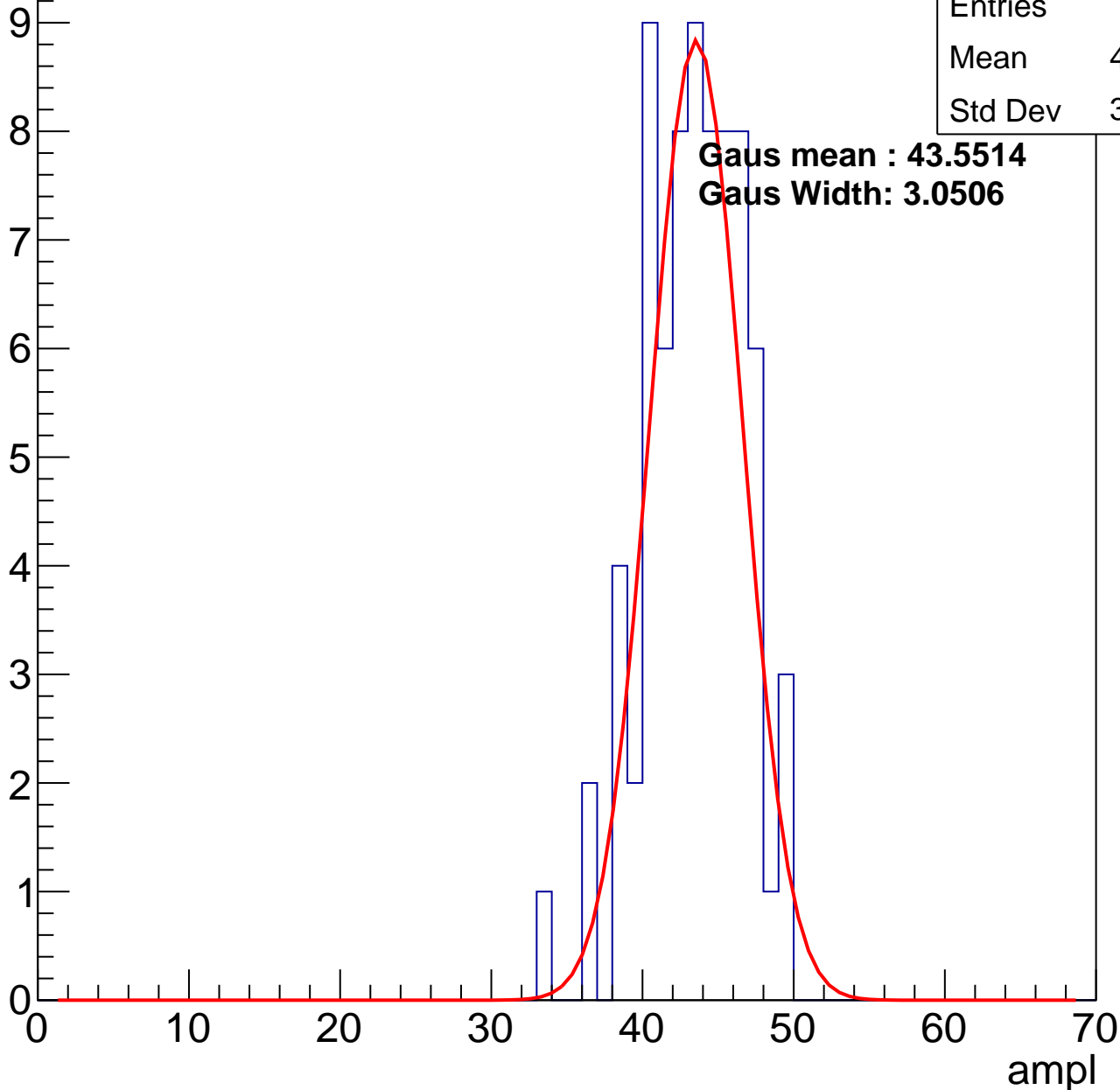
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.95
Std Dev	3.233

**Gaus mean : 43.5514**

**Gaus Width: 3.0506**

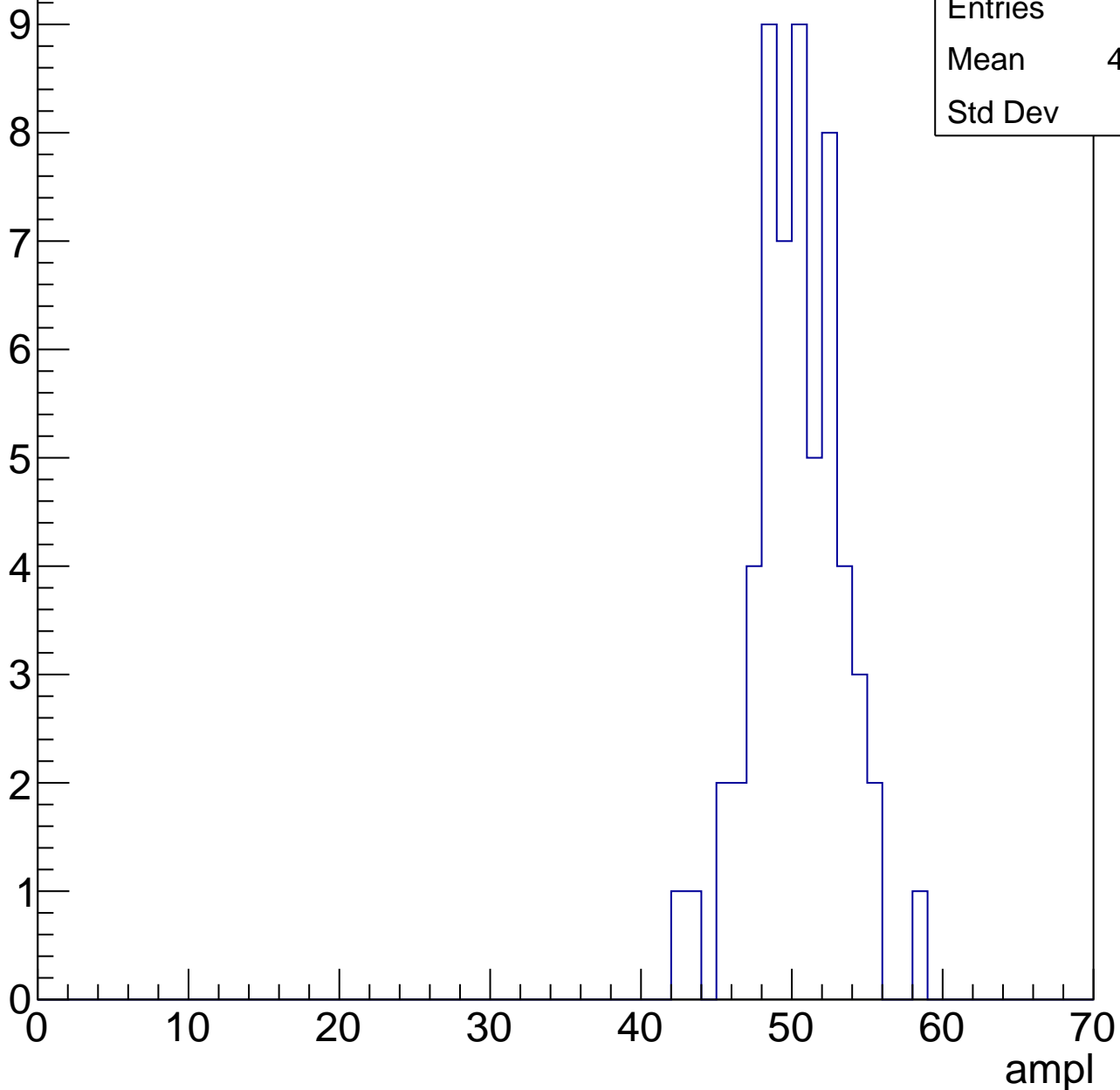


# B1L000S, U24-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	49.88
Std Dev	2.96



# B1L000S, U24-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

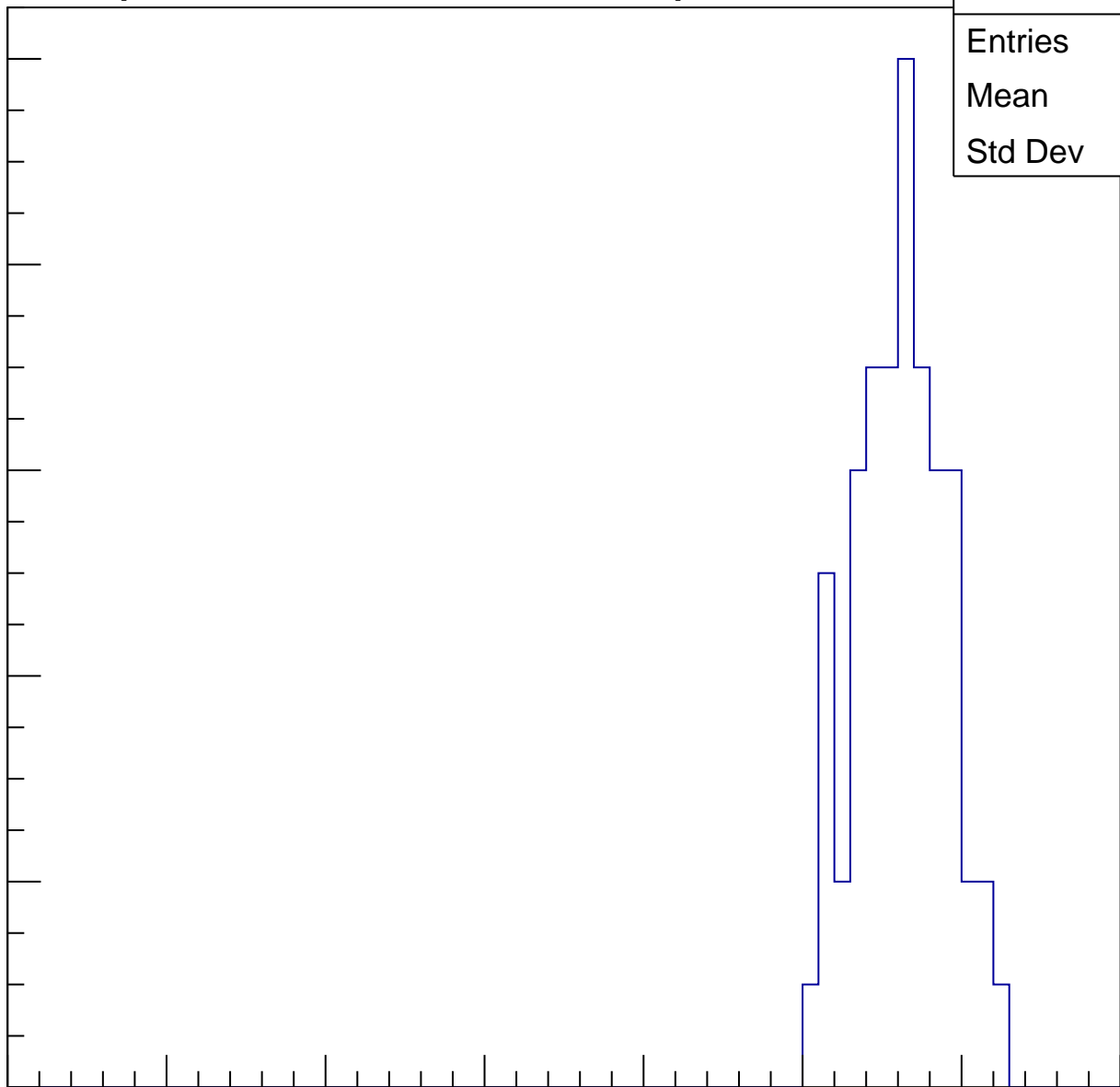
Entries	62
Mean	55.73
Std Dev	2.783

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

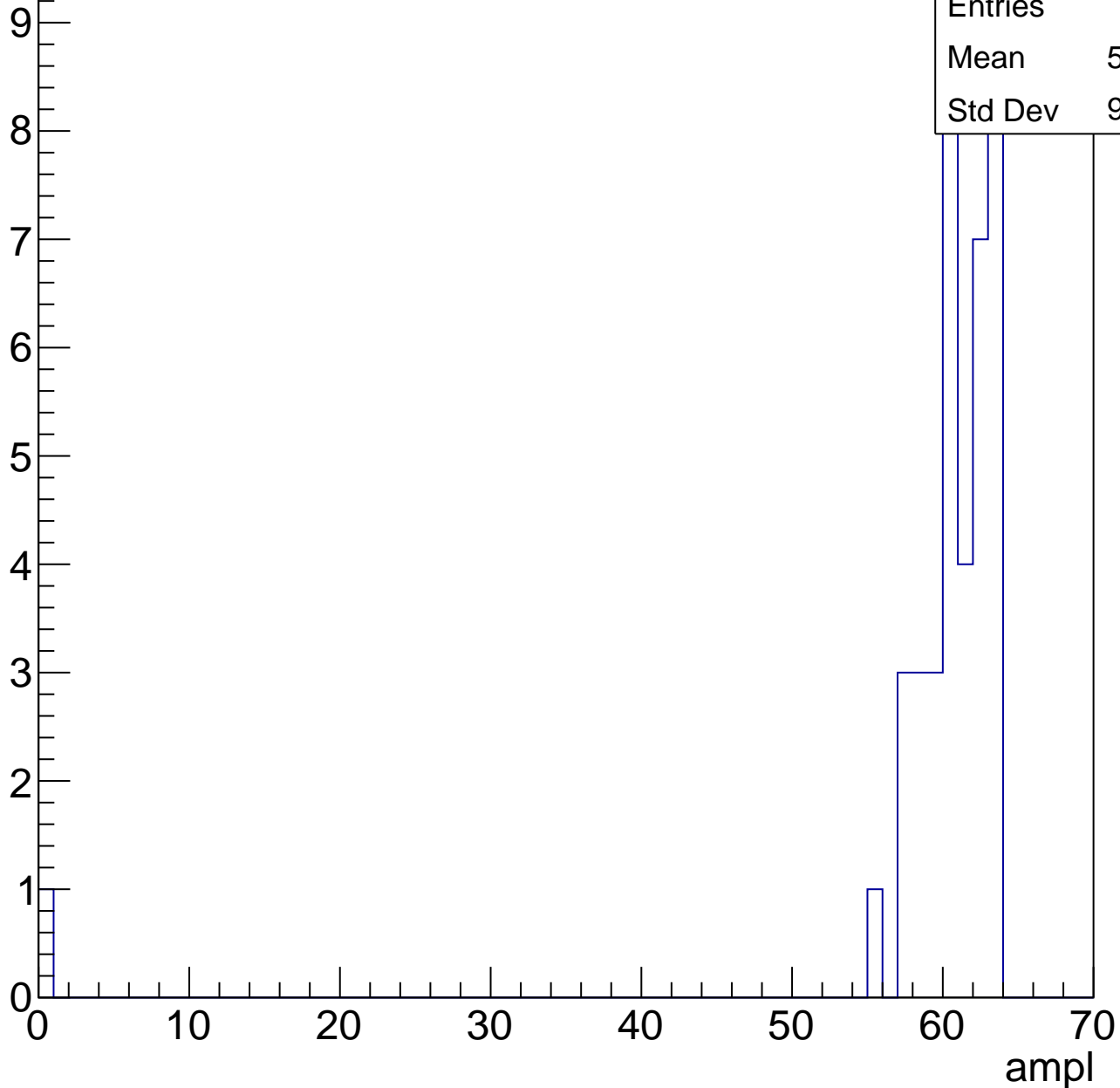
ampl



# B1L000S, U24-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

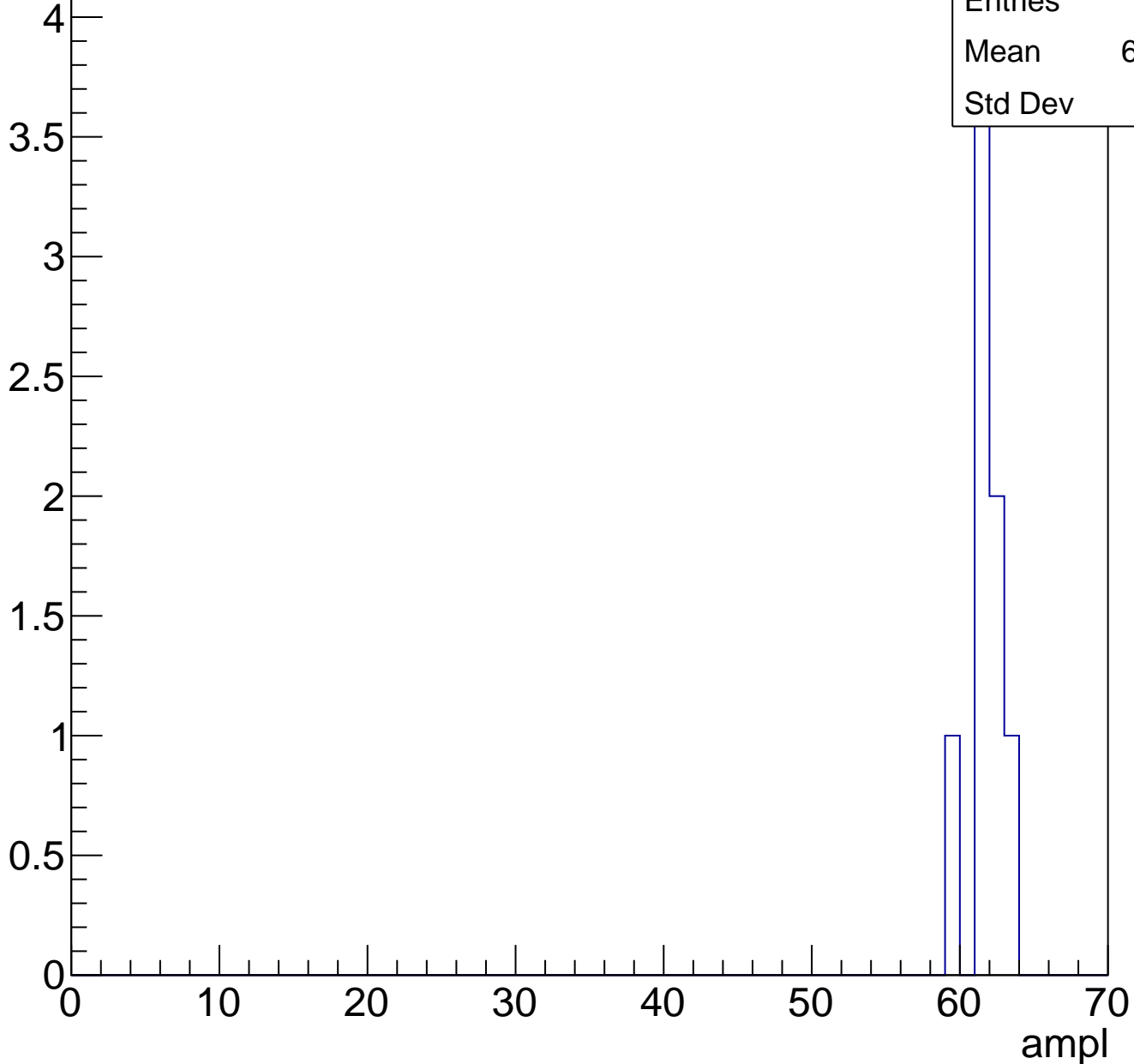
Entry



# B1L000S, U24-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U24-ch48, adc0

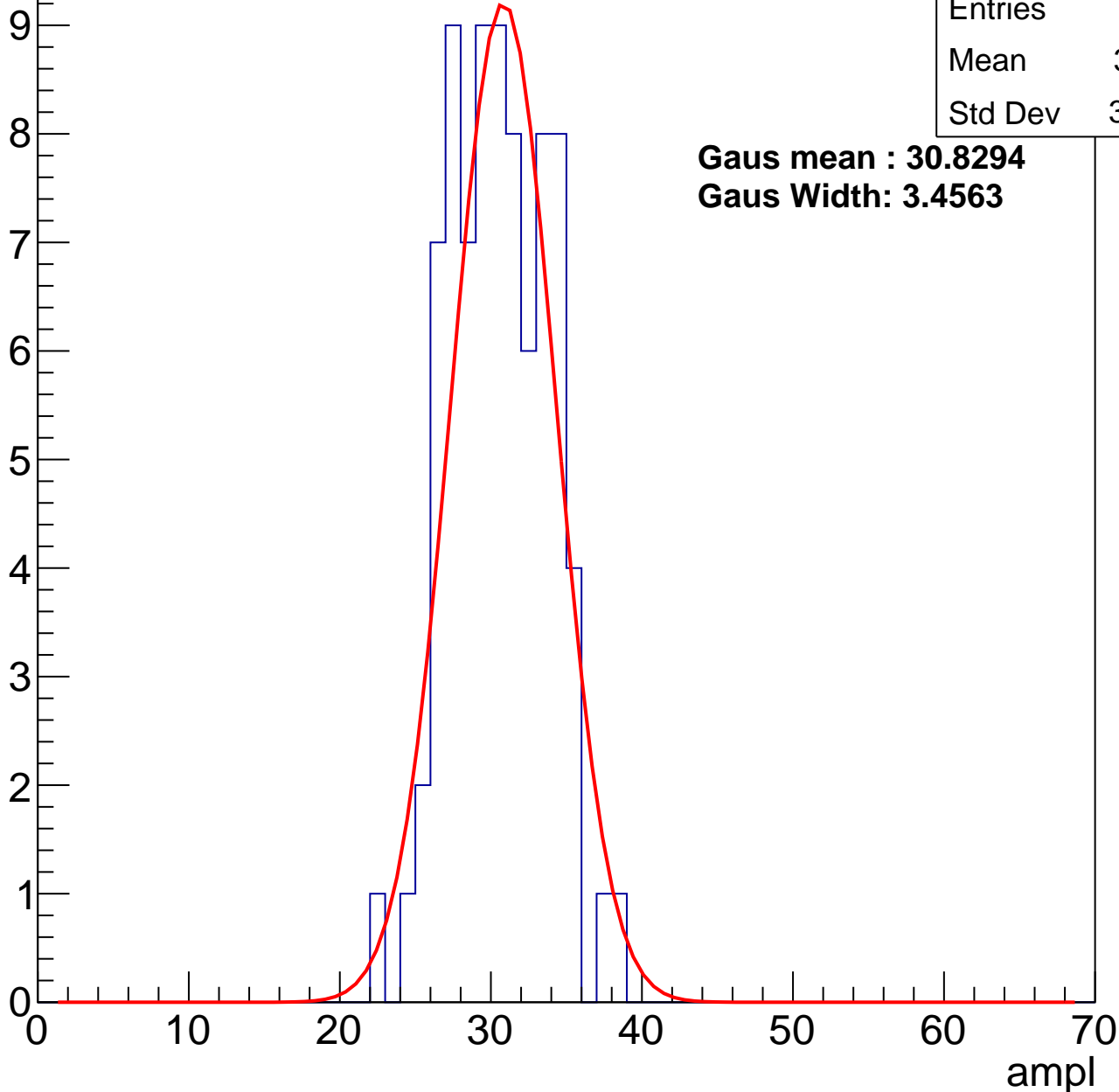
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	30.11
Std Dev	3.189

**Gaus mean : 30.8294**

**Gaus Width: 3.4563**



# B1L000S, U24-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	37.11
Std Dev	2.909

**Gaus mean : 37.9937**

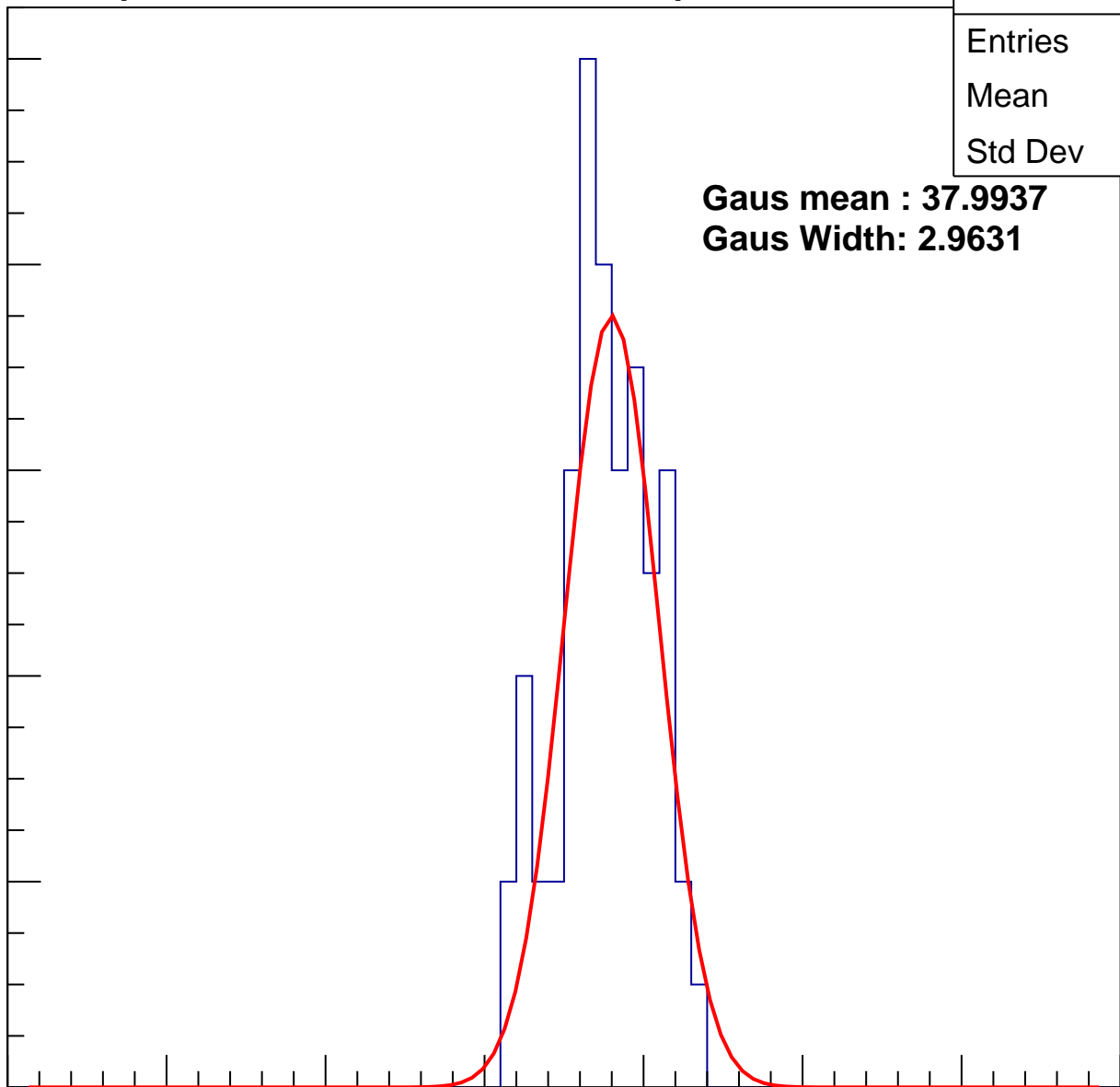
**Gaus Width: 2.9631**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch48, adc2

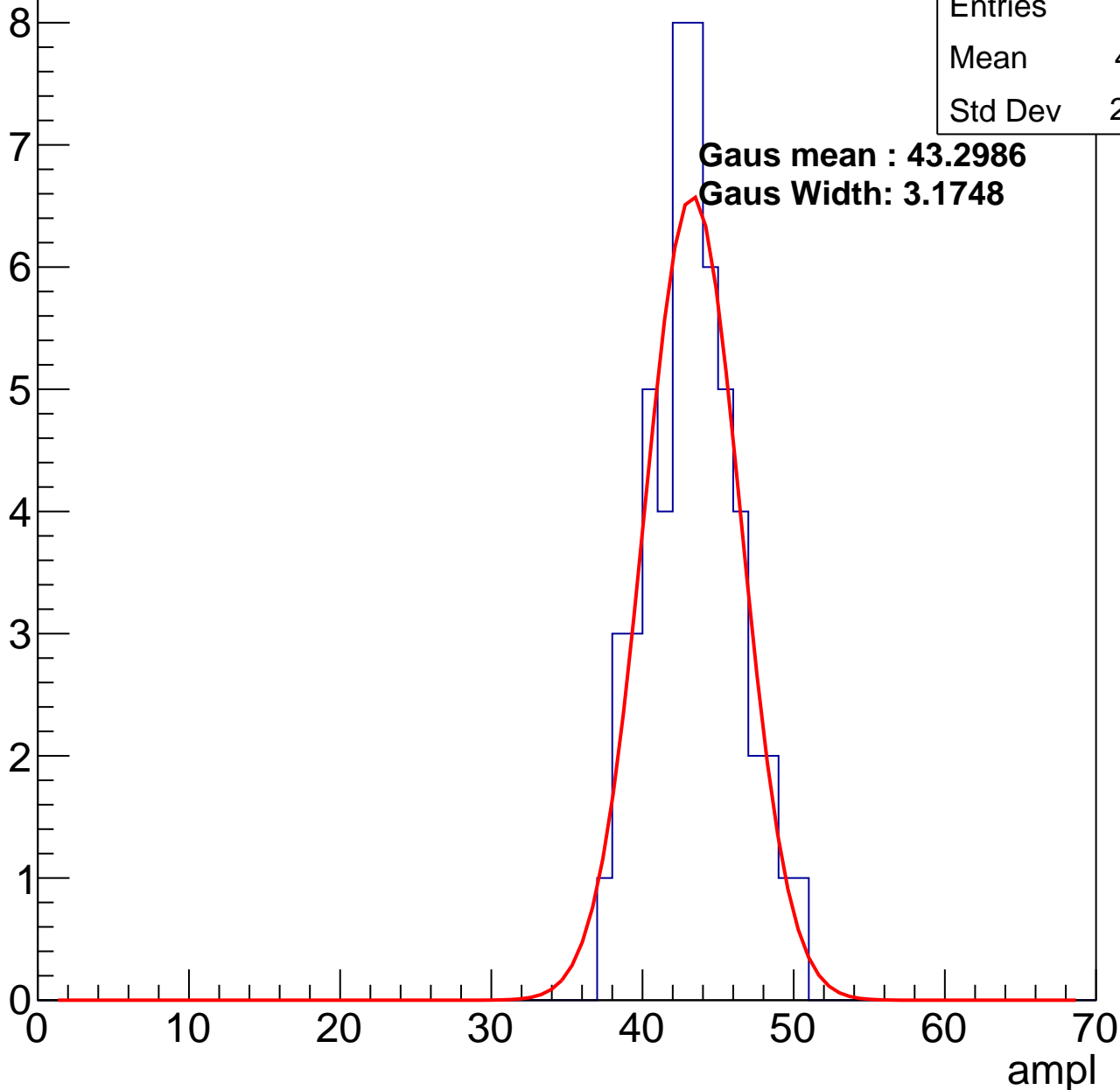
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	42.91
Std Dev	2.935

**Gaus mean : 43.2986**

**Gaus Width: 3.1748**

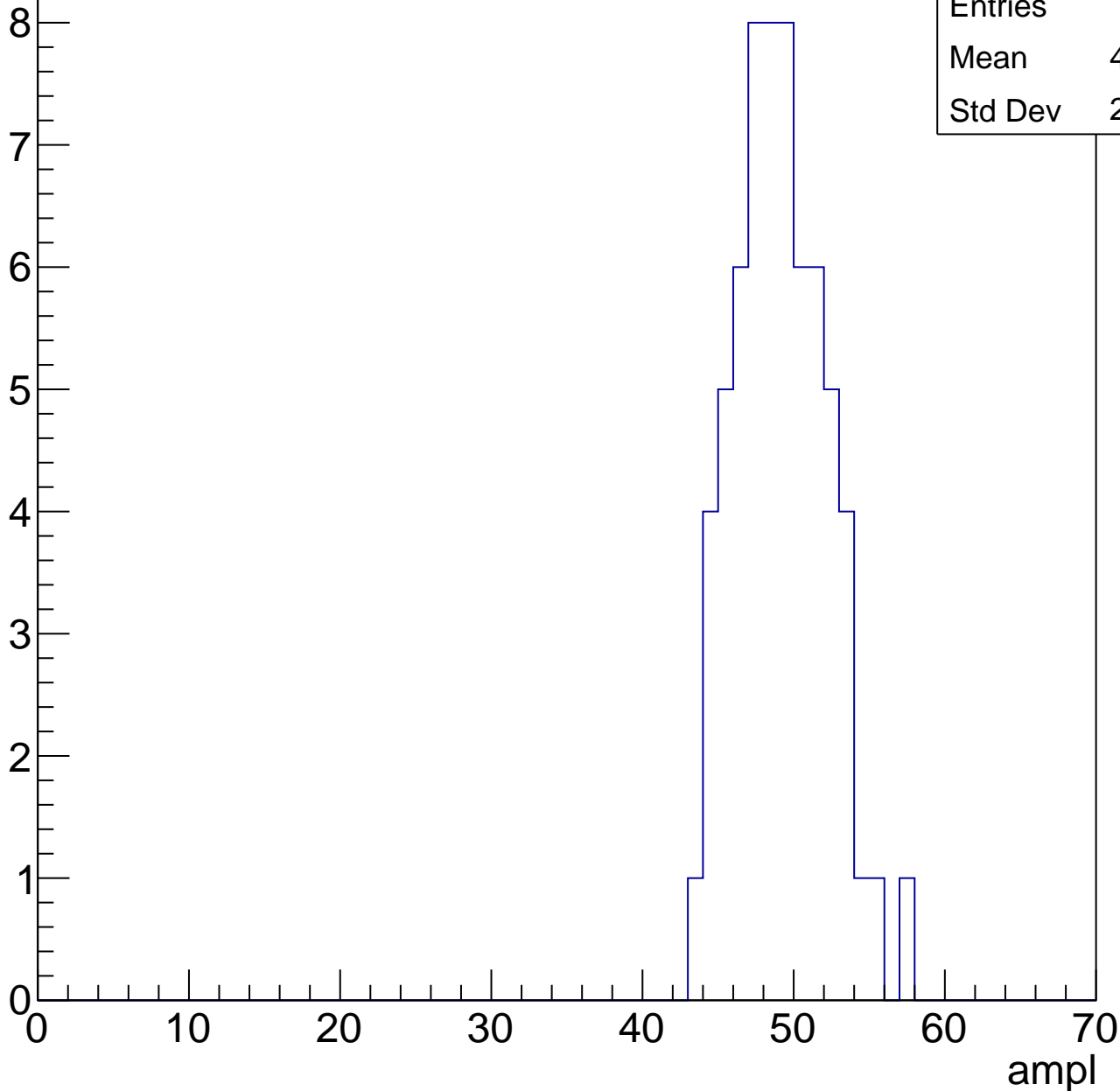


# B1L000S, U24-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.69
Std Dev	2.978

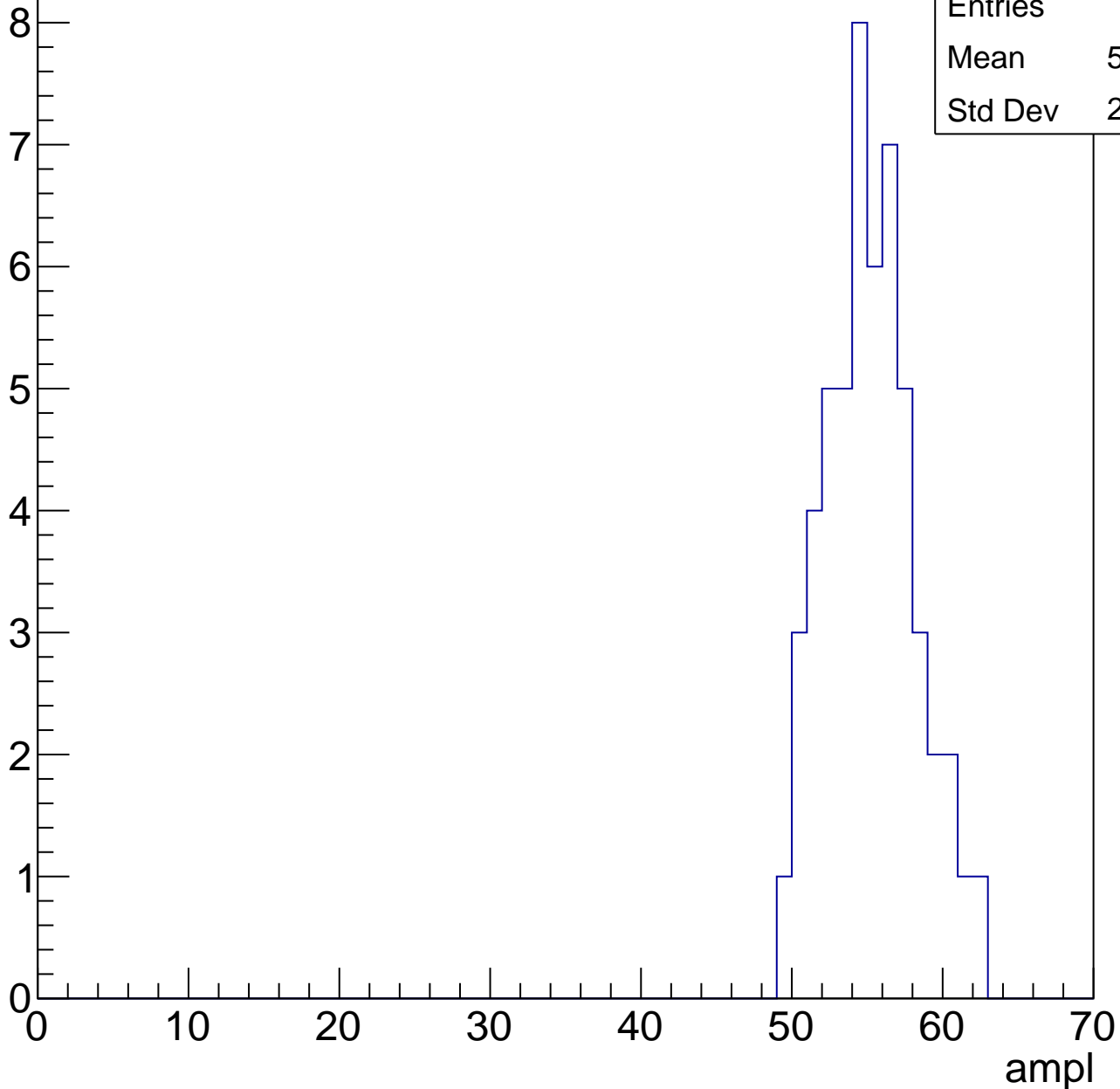


# B1L000S, U24-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	54.75
Std Dev	2.965

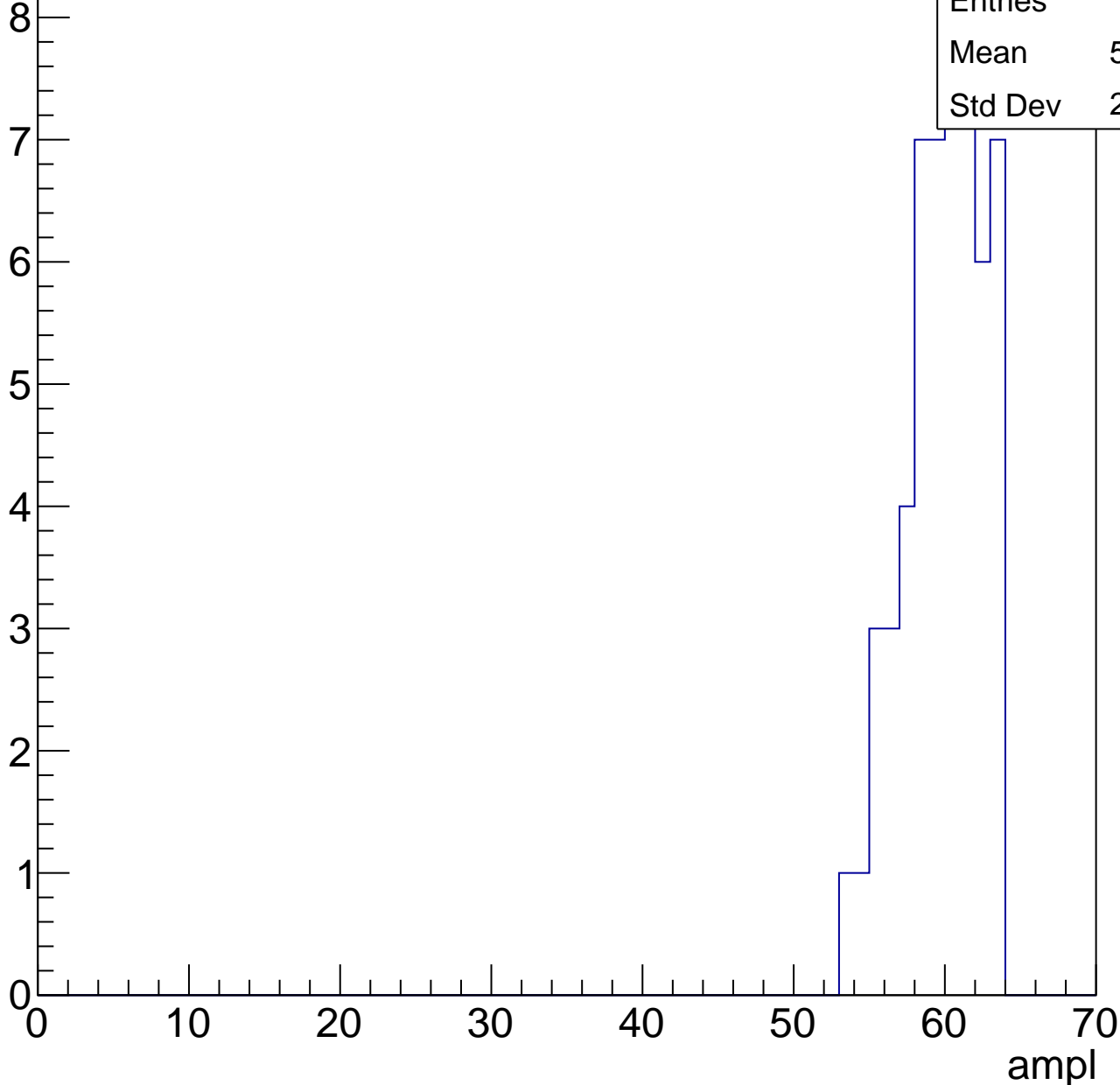


# B1L000S, U24-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

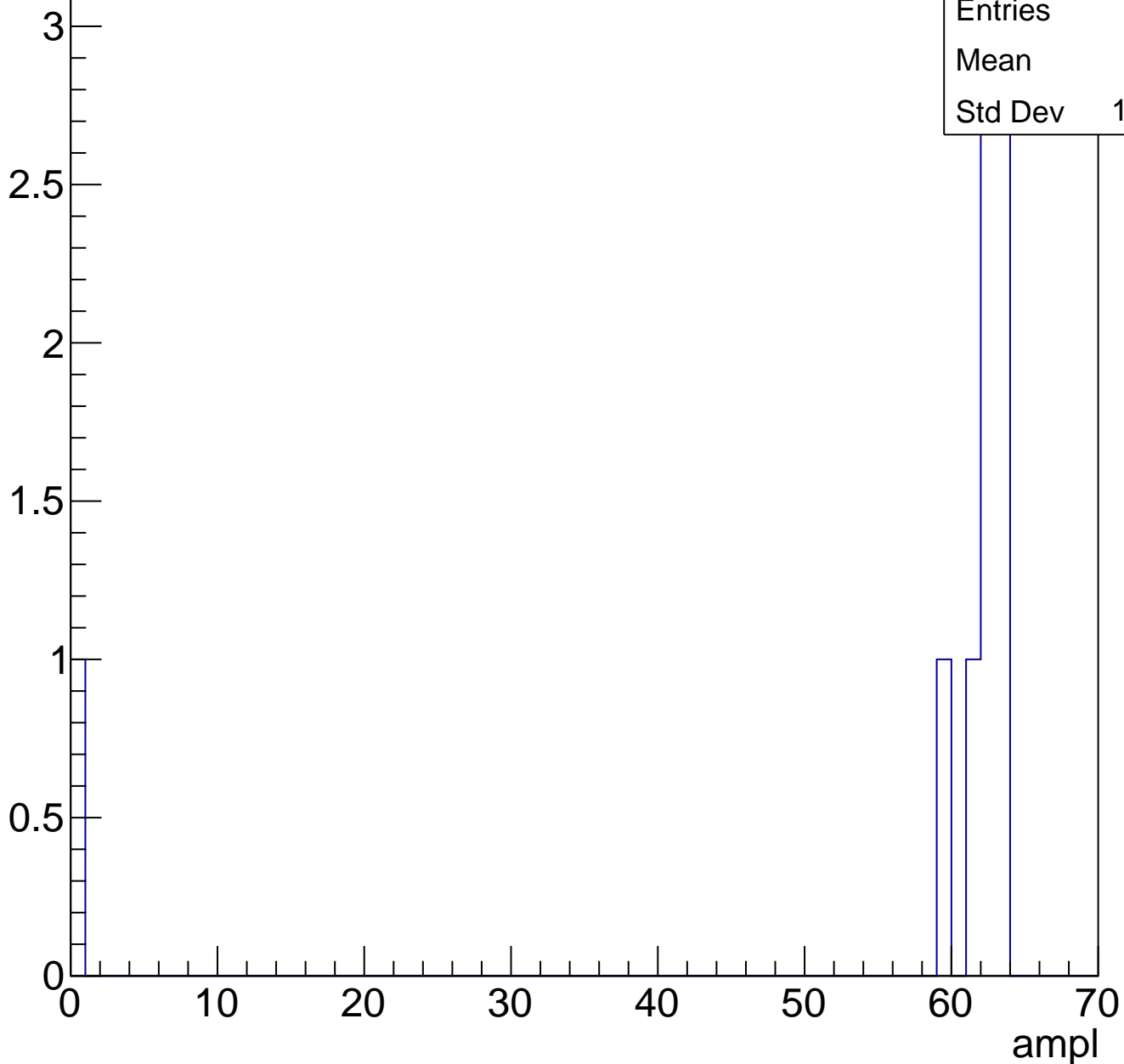
Entries	55
Mean	59.42
Std Dev	2.542



# B1L000S, U24-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch49, adc0

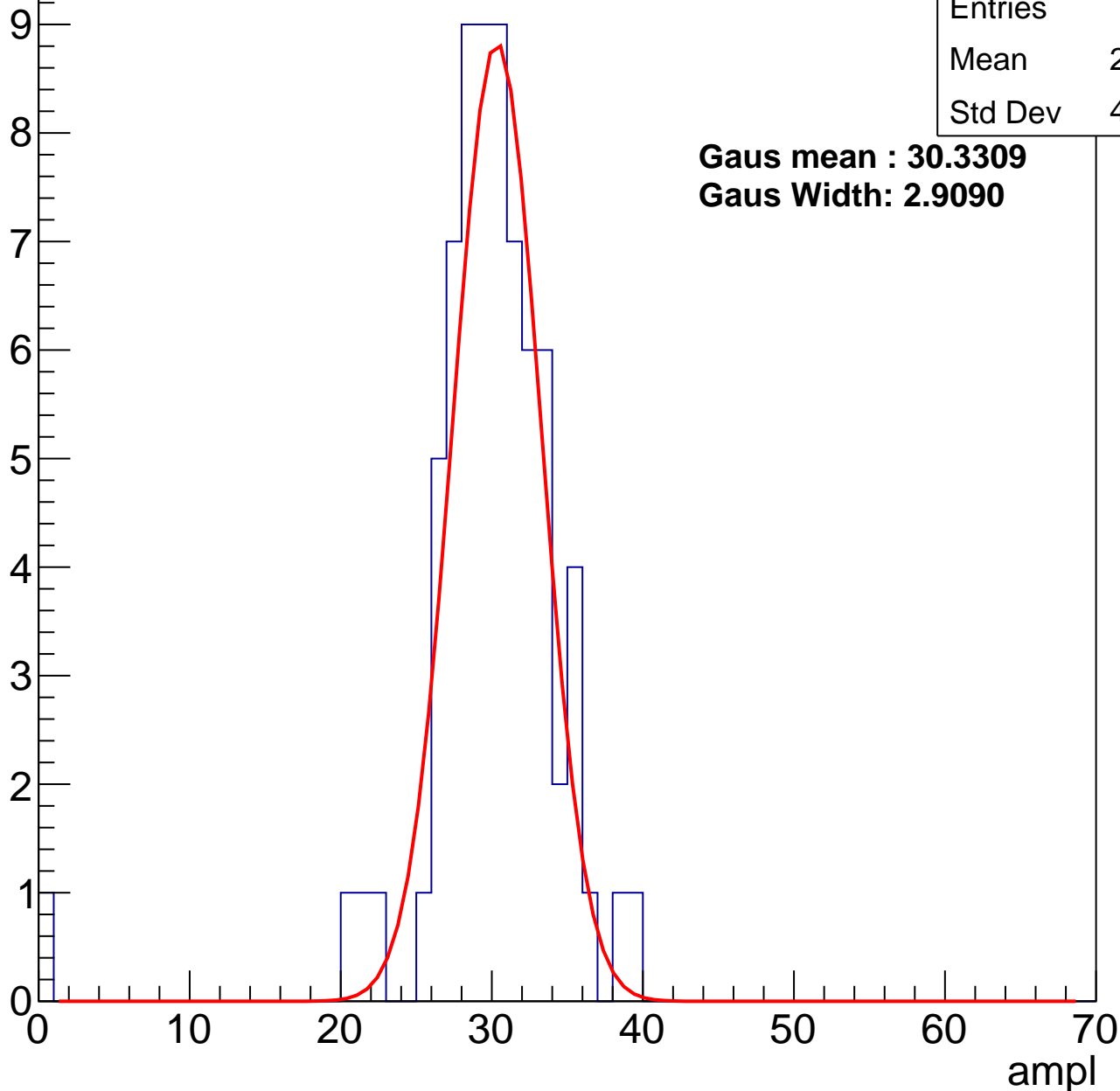
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	29.42
Std Dev	4.904

**Gaus mean : 30.3309**

**Gaus Width: 2.9090**



# B1L000S, U24-ch49, adc1

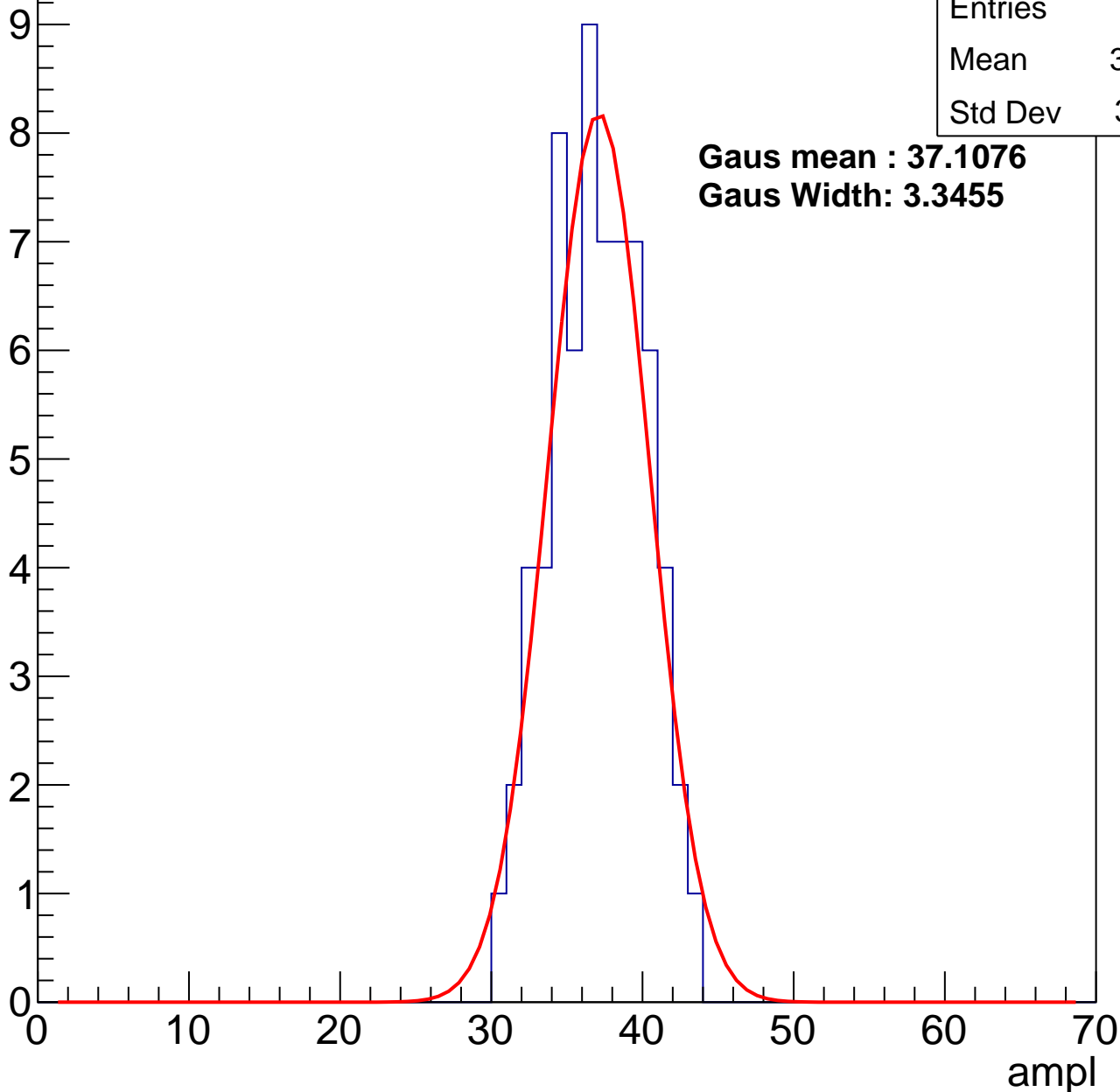
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.57
Std Dev	3.011

**Gaus mean : 37.1076**

**Gaus Width: 3.3455**

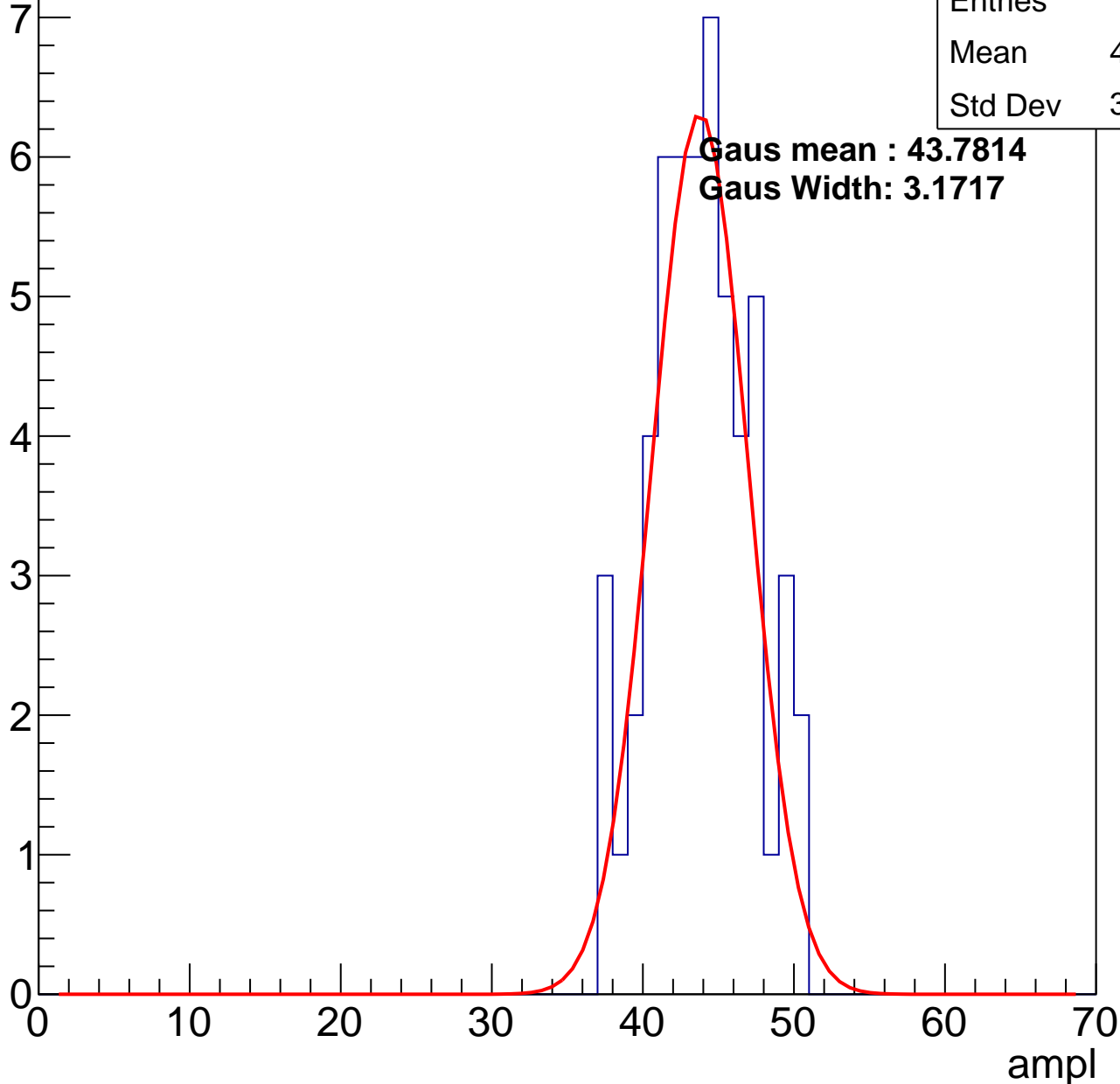


# B1L000S, U24-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	43.45
Std Dev	3.285

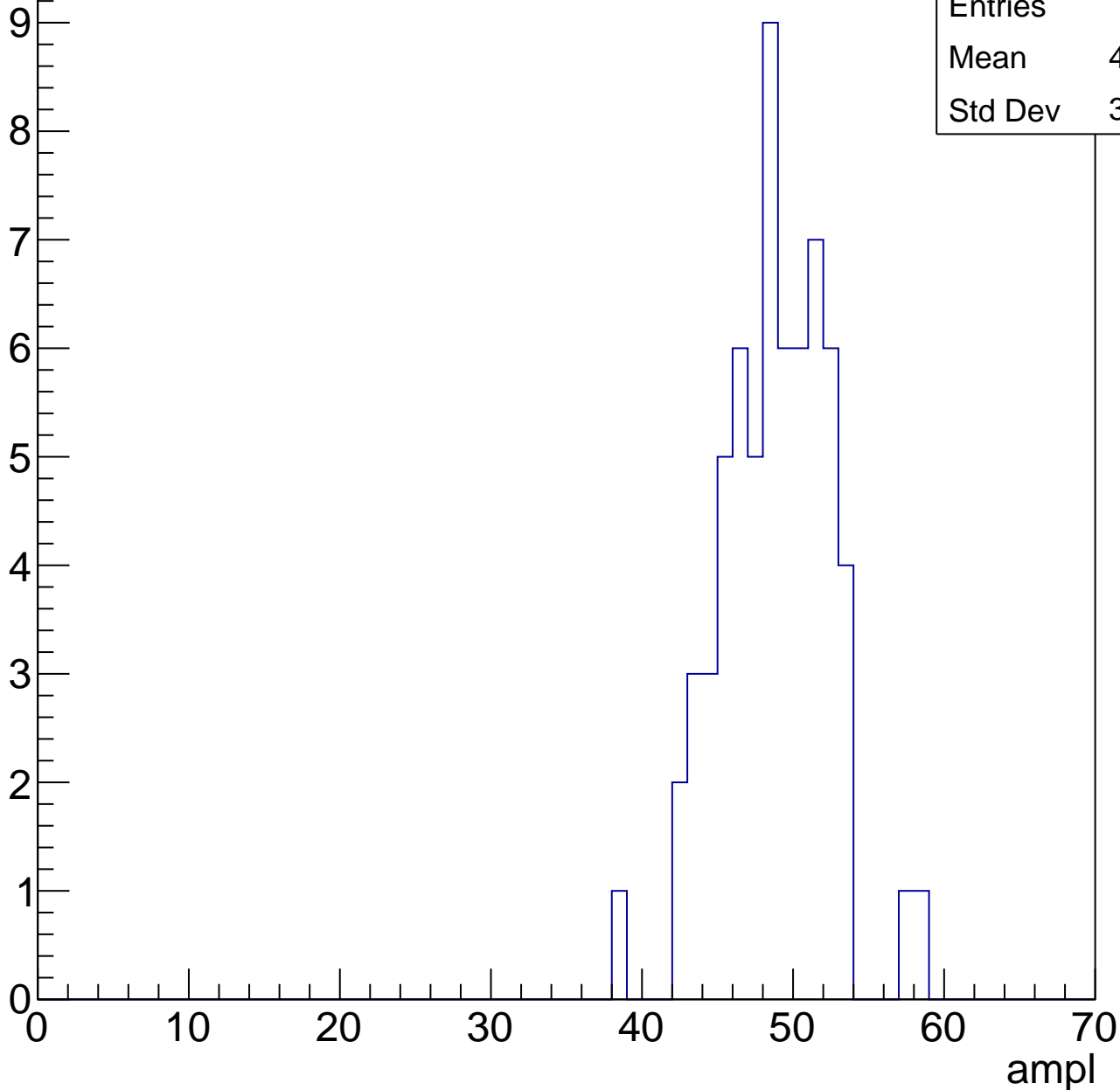


# B1L000S, U24-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	48.32
Std Dev	3.578

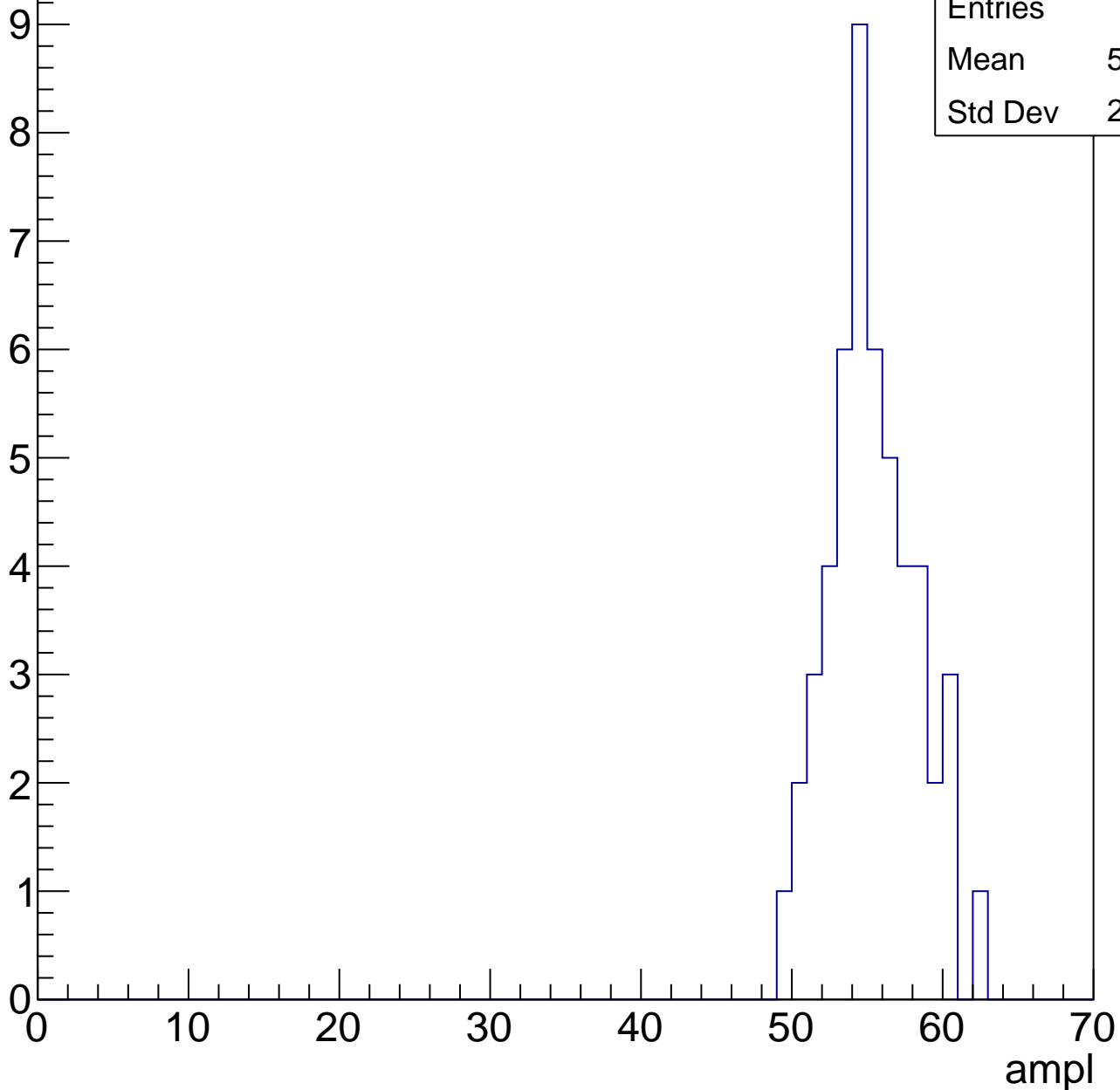


# B1L000S, U24-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

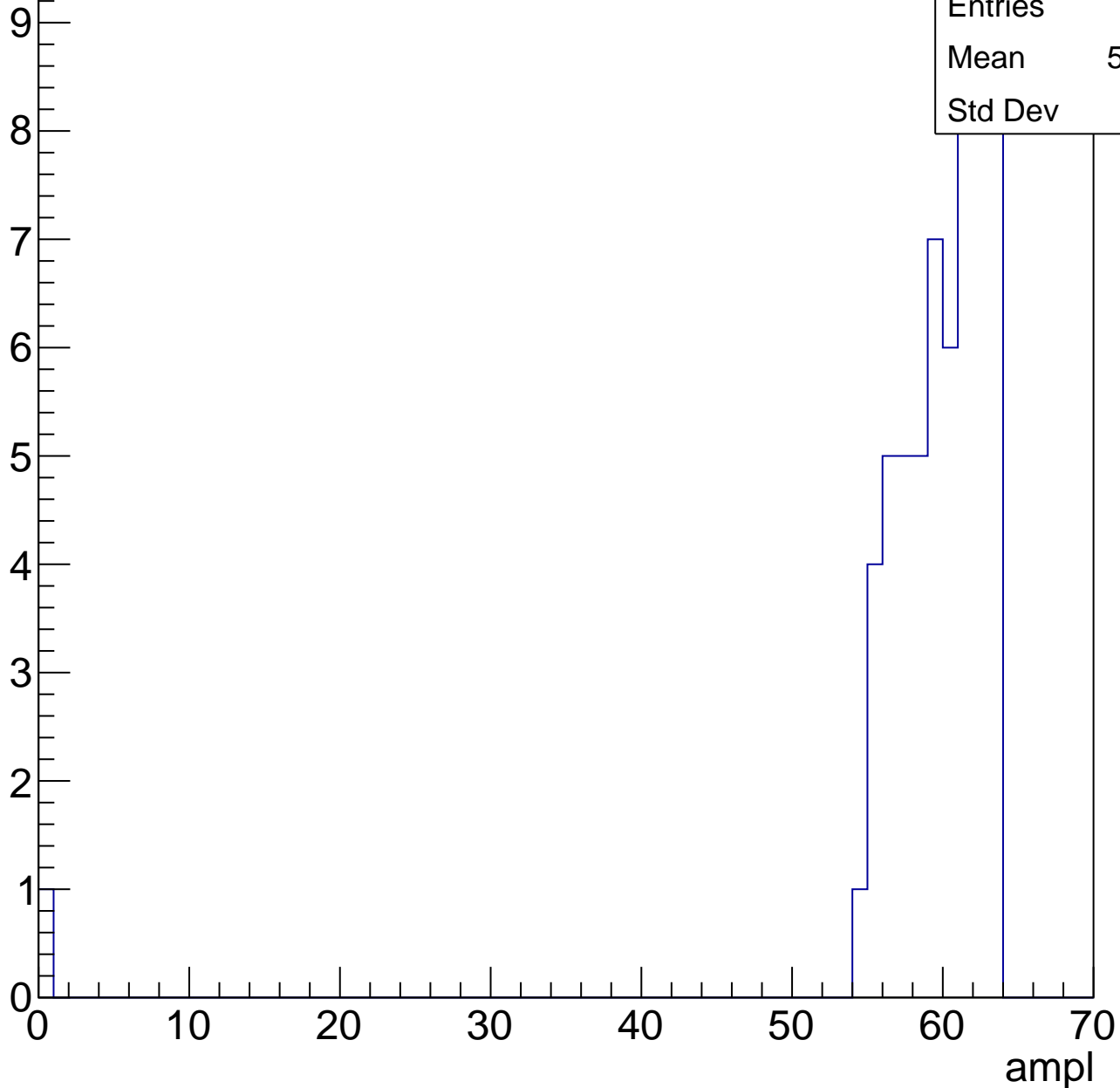
Entries	50
Mean	54.88
Std Dev	2.882



# B1L000S, U24-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

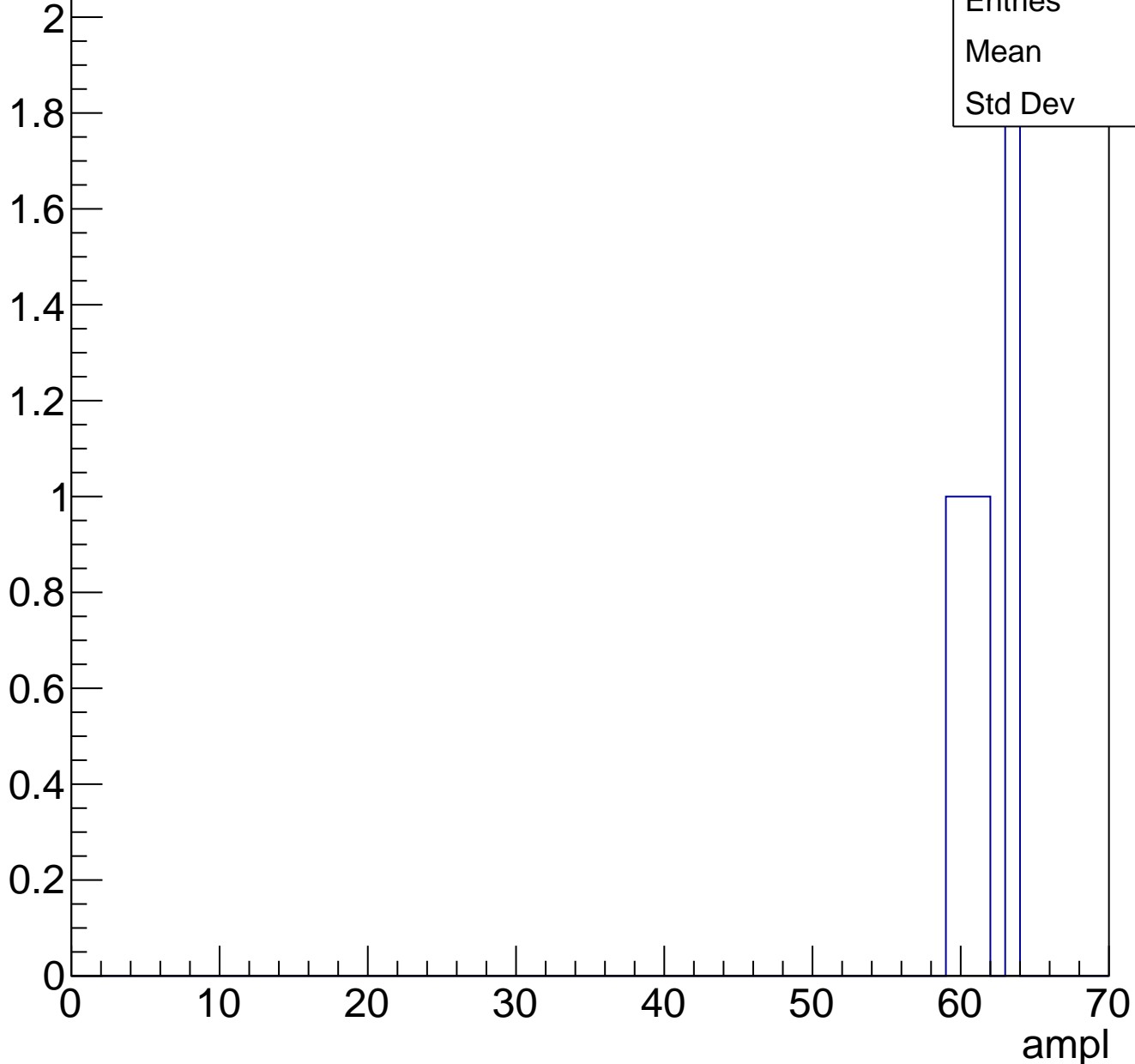
Entry



# B1L000S, U24-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	29.02
Std Dev	2.826

**Gaus mean : 29.6871**

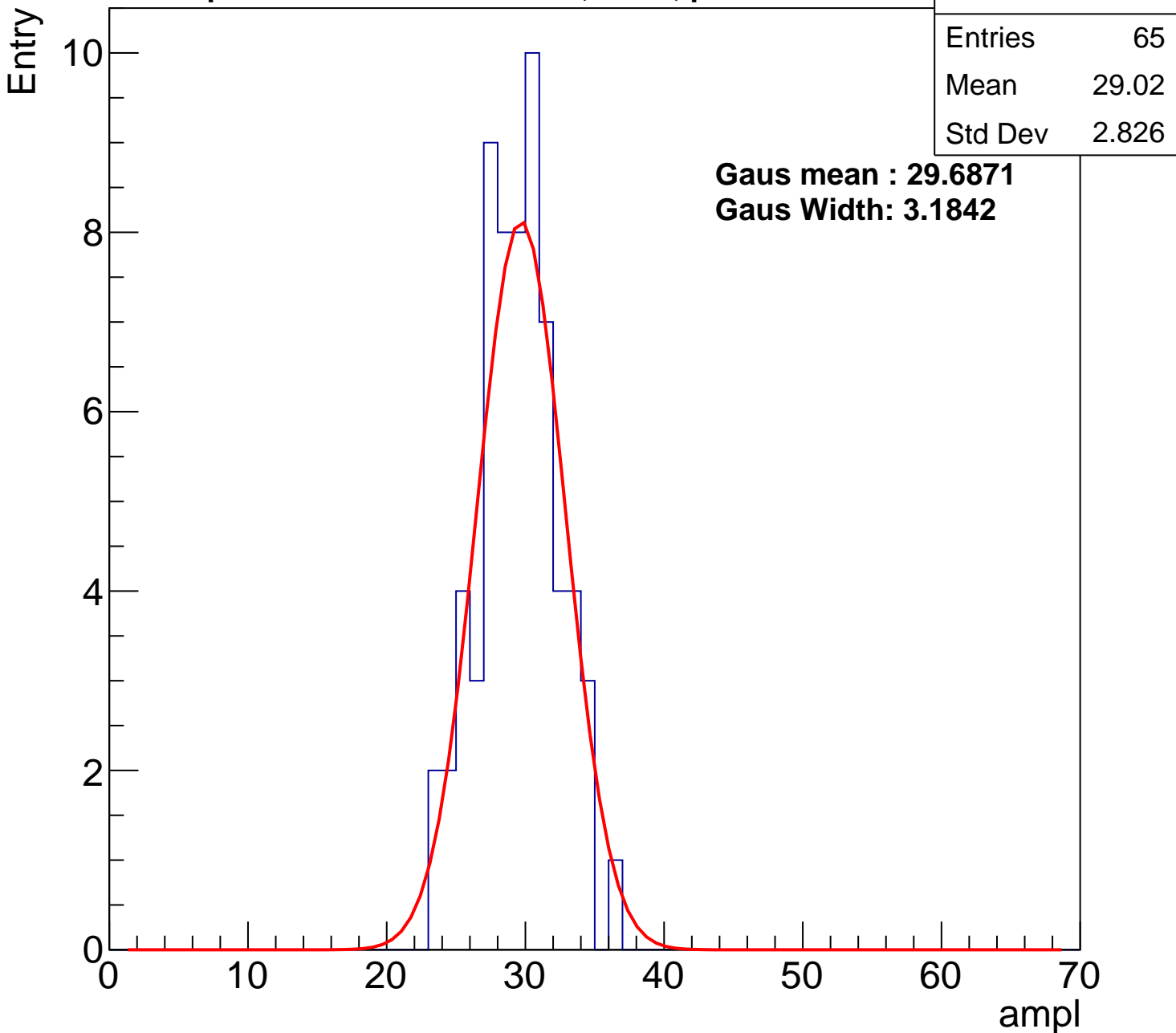
**Gaus Width: 3.1842**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



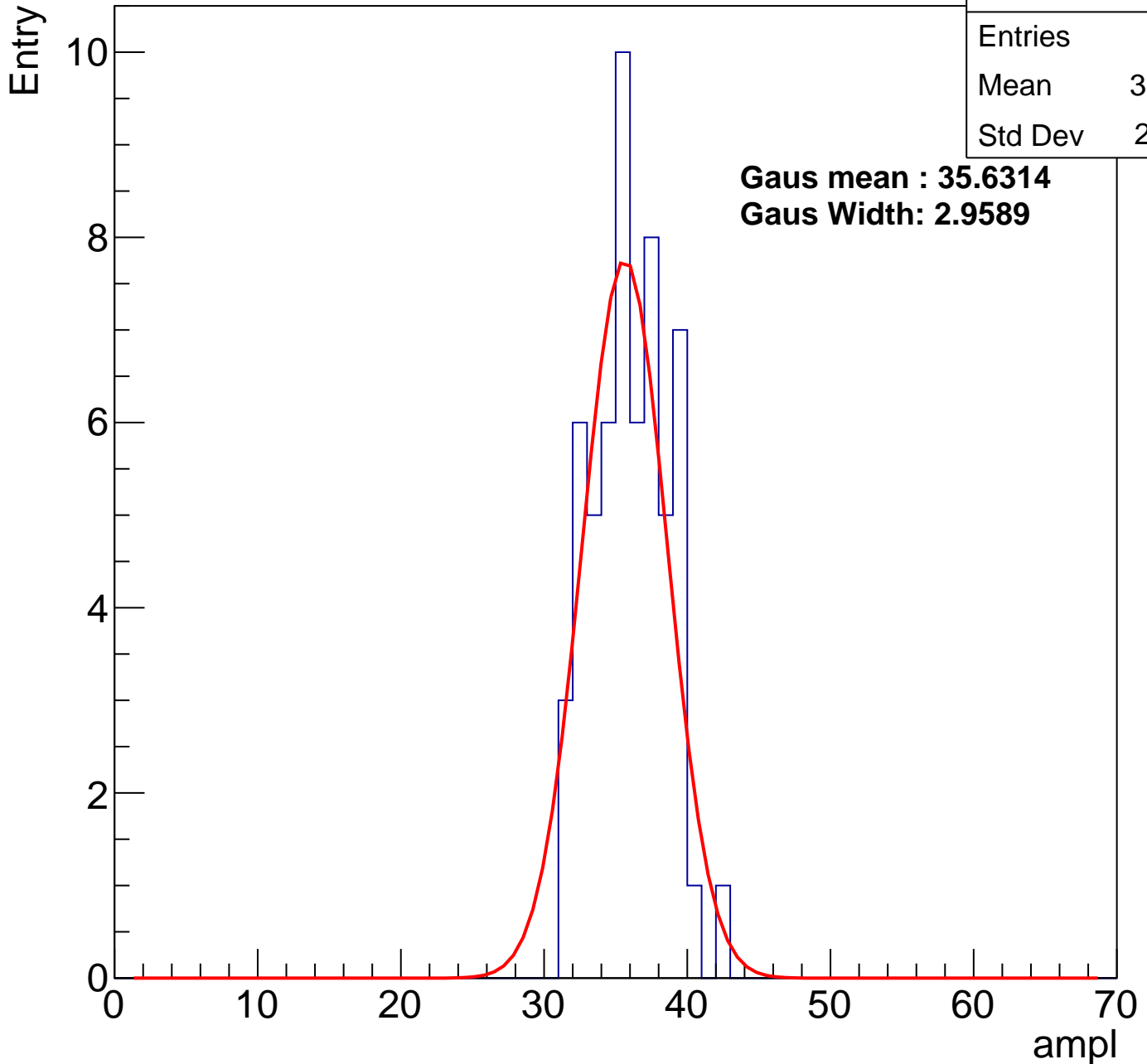
# B1L000S, U24-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	35.53
Std Dev	2.561

**Gaus mean : 35.6314**

**Gaus Width: 2.9589**



# B1L000S, U24-ch50, adc2

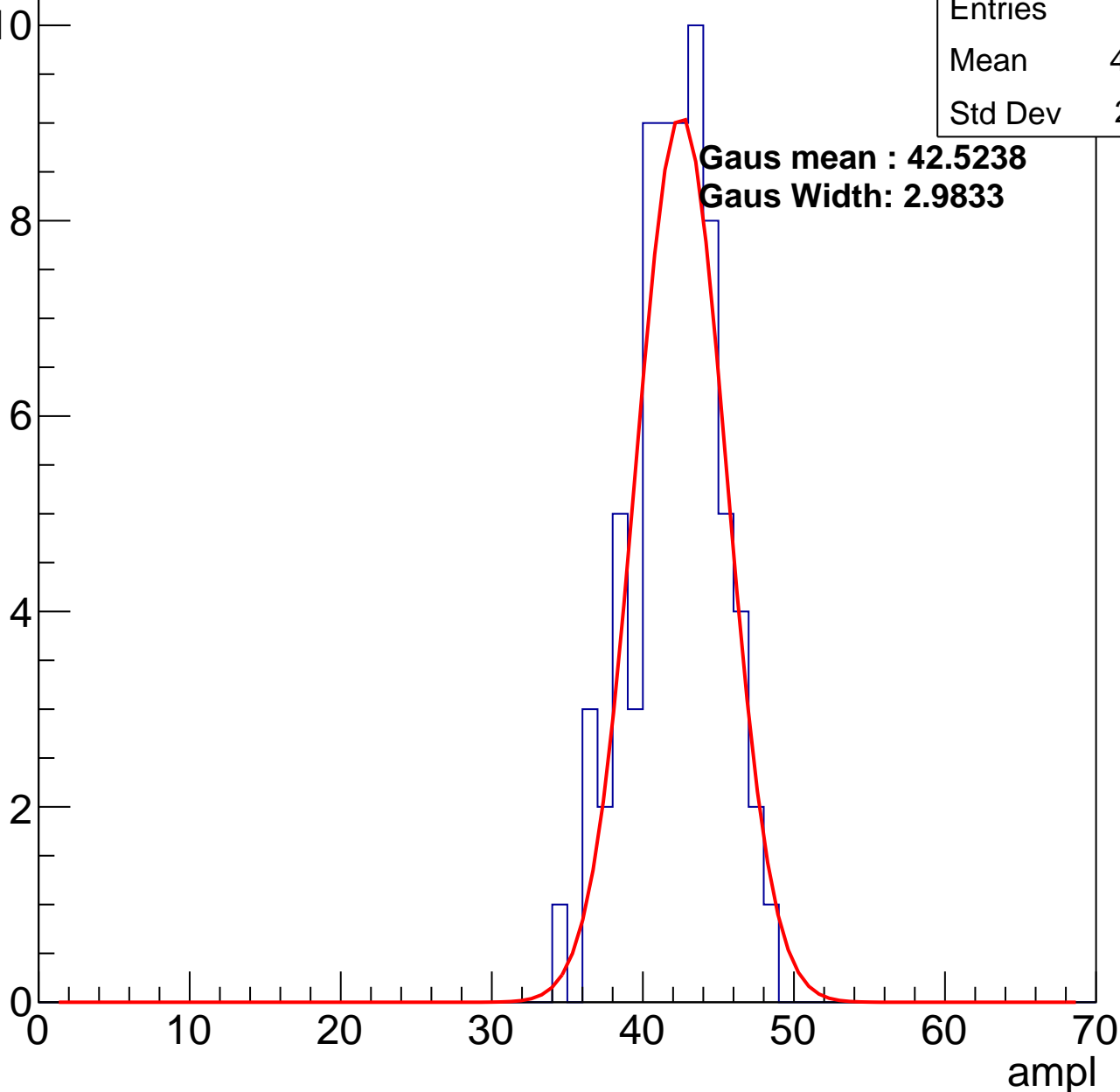
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	41.73
Std Dev	2.921

**Gaus mean : 42.5238**

**Gaus Width: 2.9833**

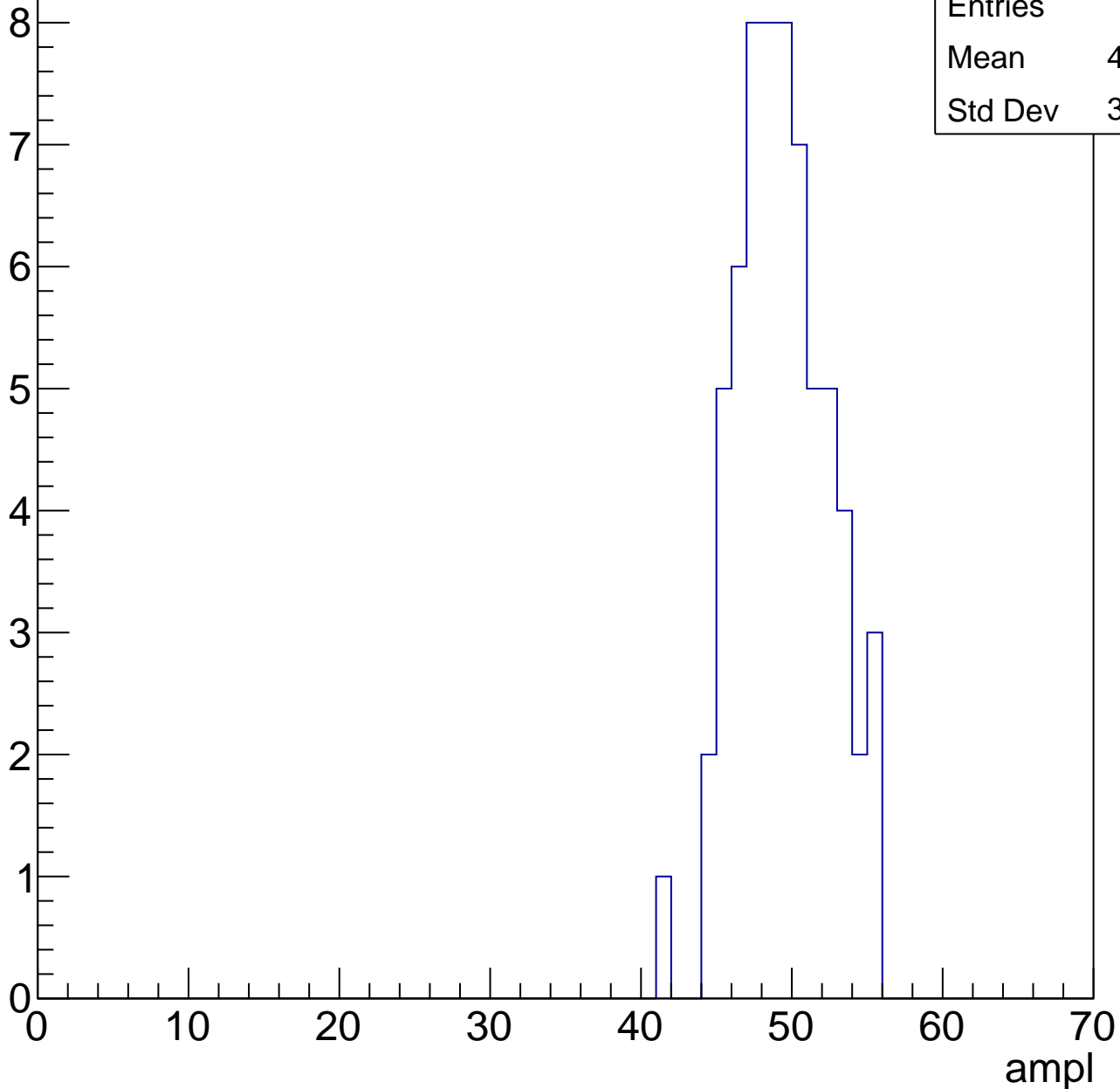


# B1L000S, U24-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.94
Std Dev	3.015

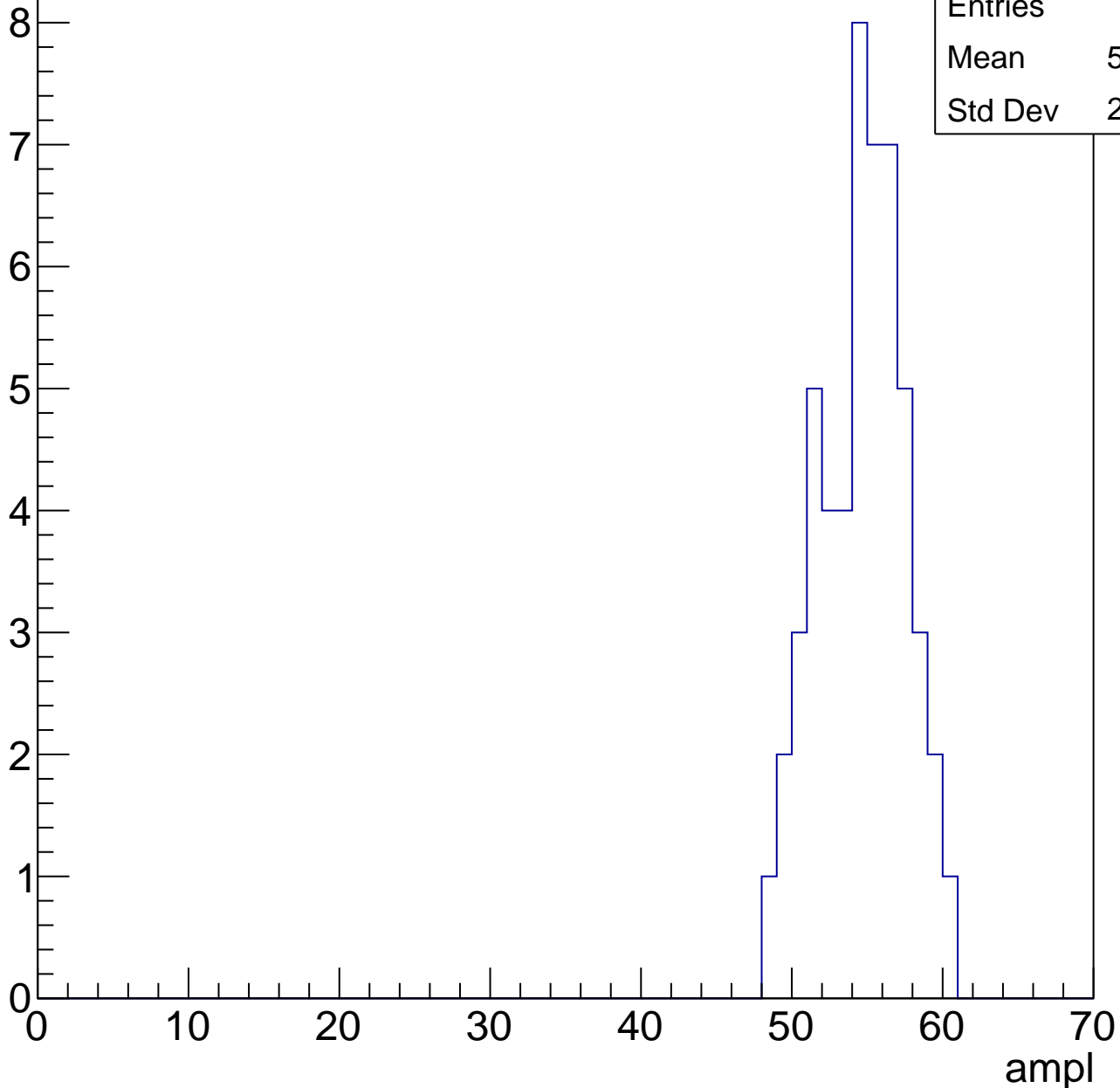


# B1L000S, U24-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	54.17
Std Dev	2.813

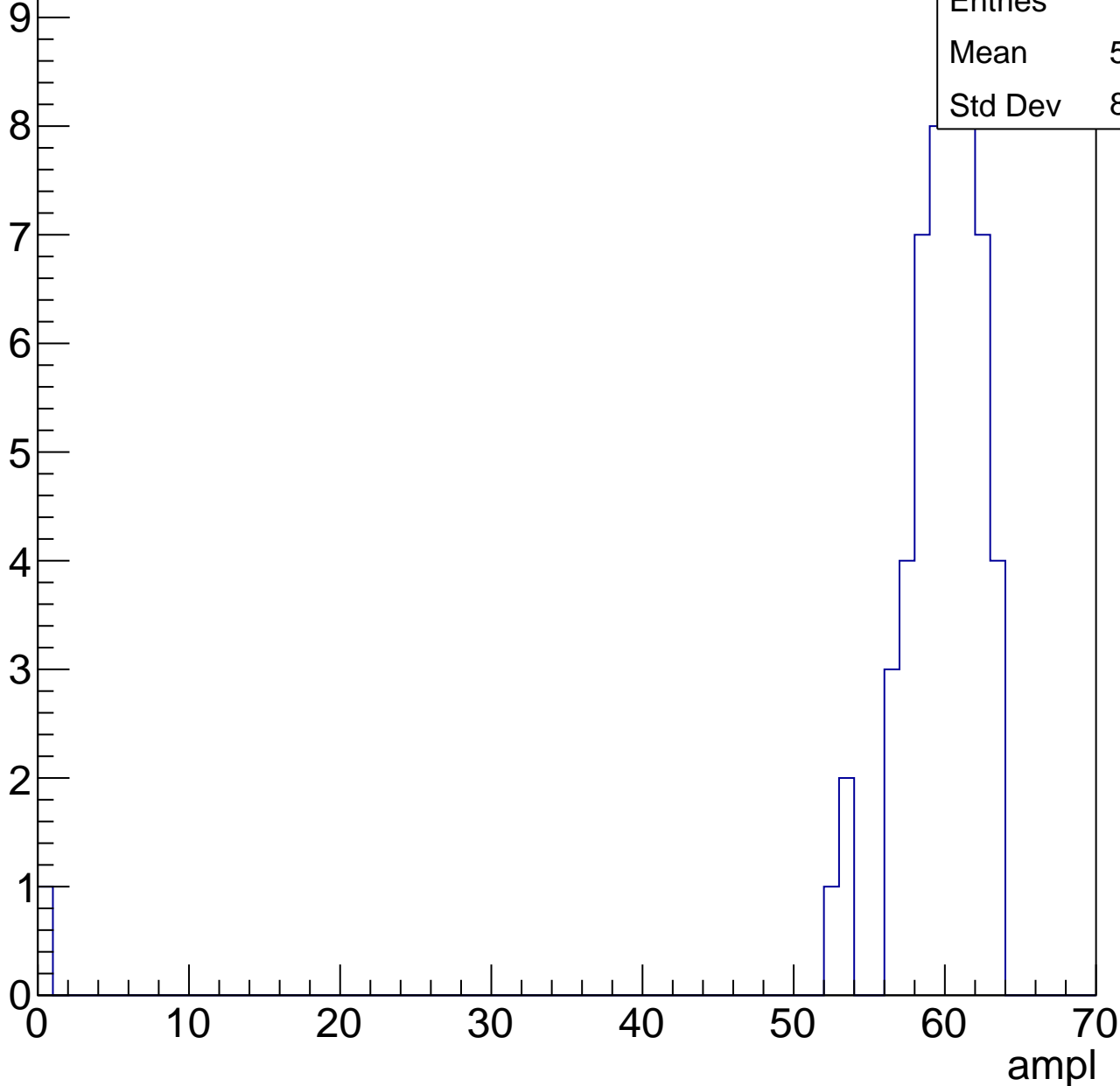


# B1L000S, U24-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	58.28
Std Dev	8.383

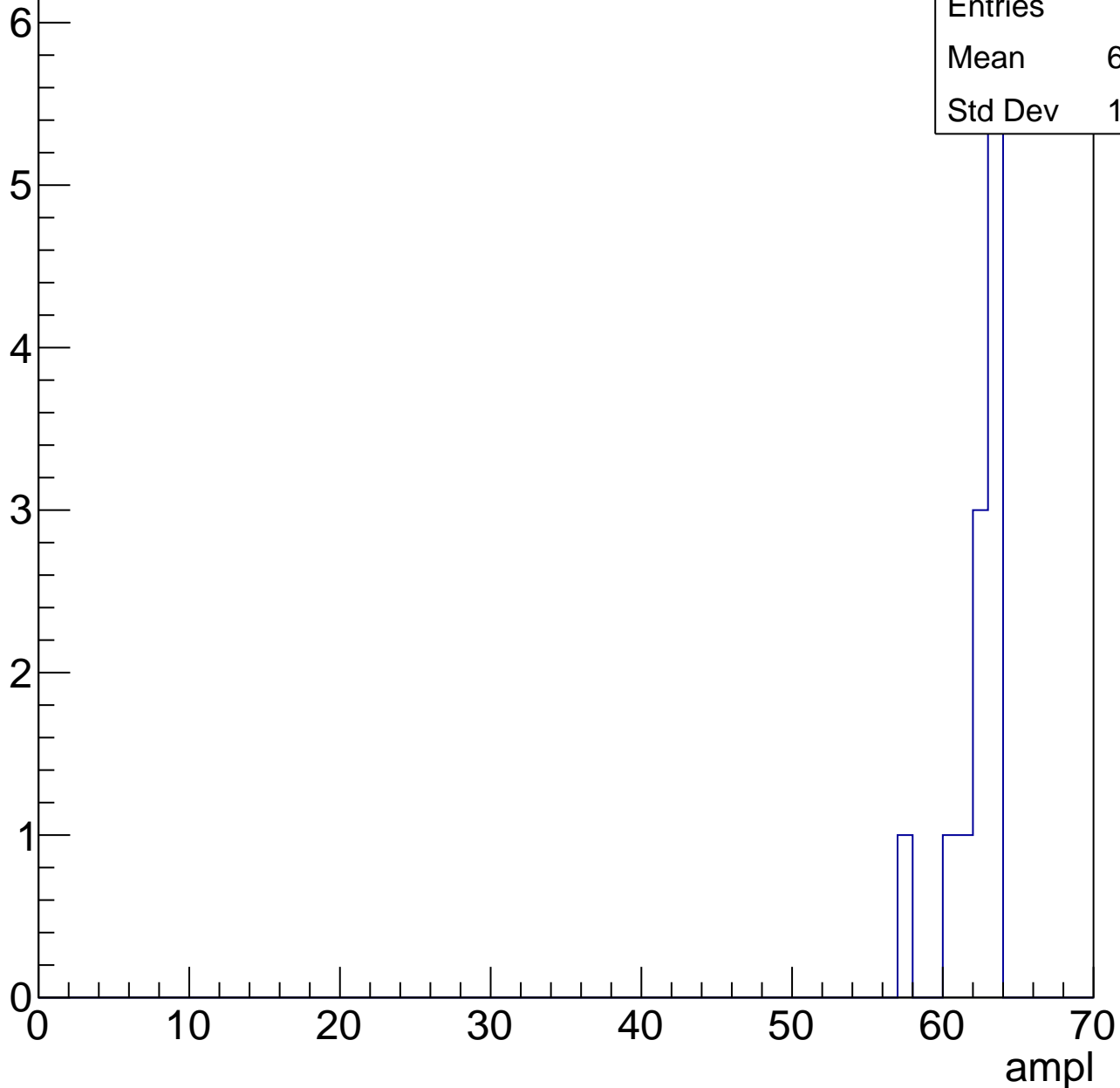


# B1L000S, U24-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.83
Std Dev	1.724





# B1L000S, U24-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U24-ch51, adc0

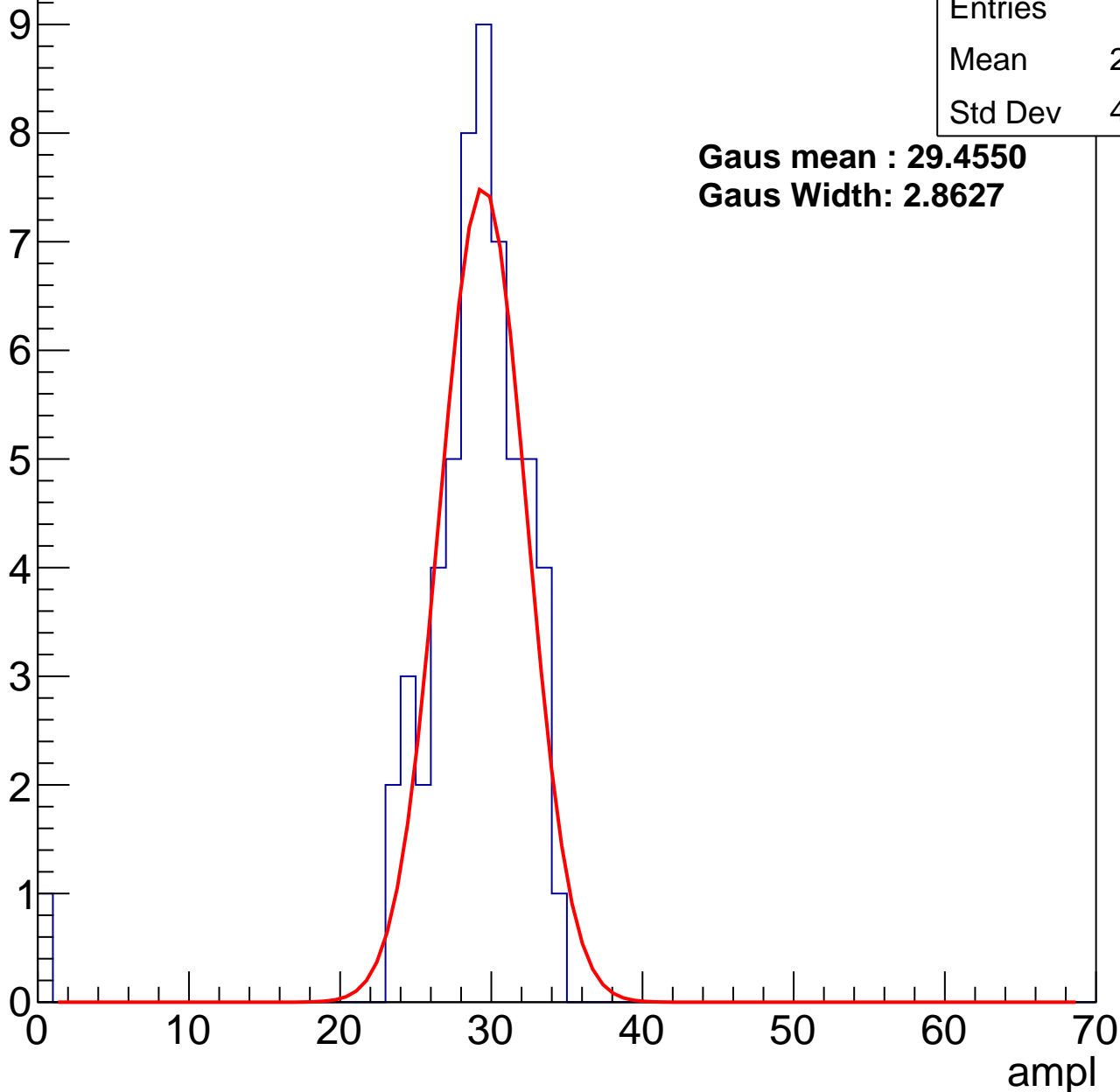
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	28.27
Std Dev	4.658

**Gaus mean : 29.4550**

**Gaus Width: 2.8627**



# B1L000S, U24-ch51, adc1

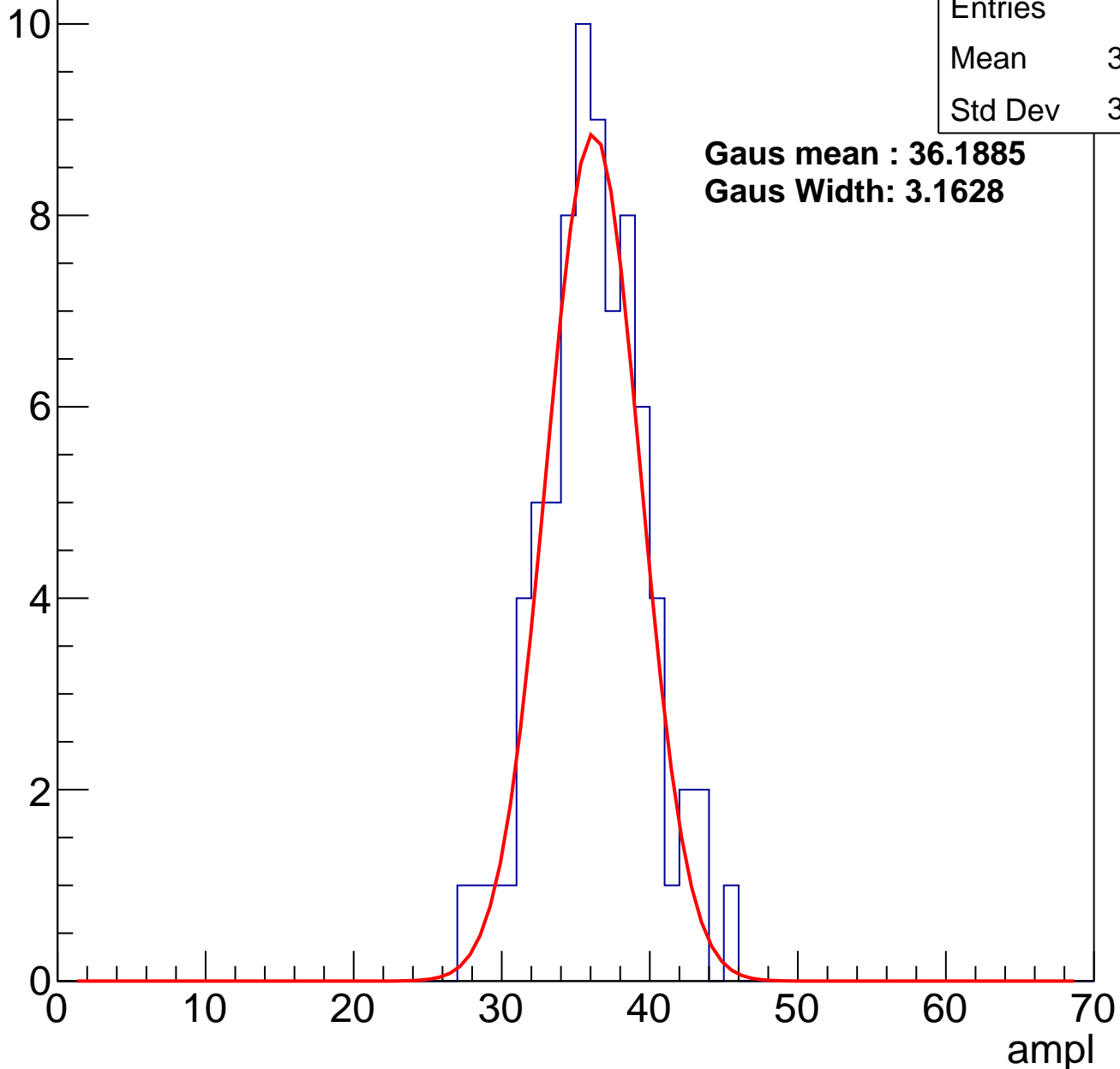
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	35.82
Std Dev	3.493

**Gaus mean : 36.1885**

**Gaus Width: 3.1628**

Entry



# B1L000S, U24-ch51, adc2

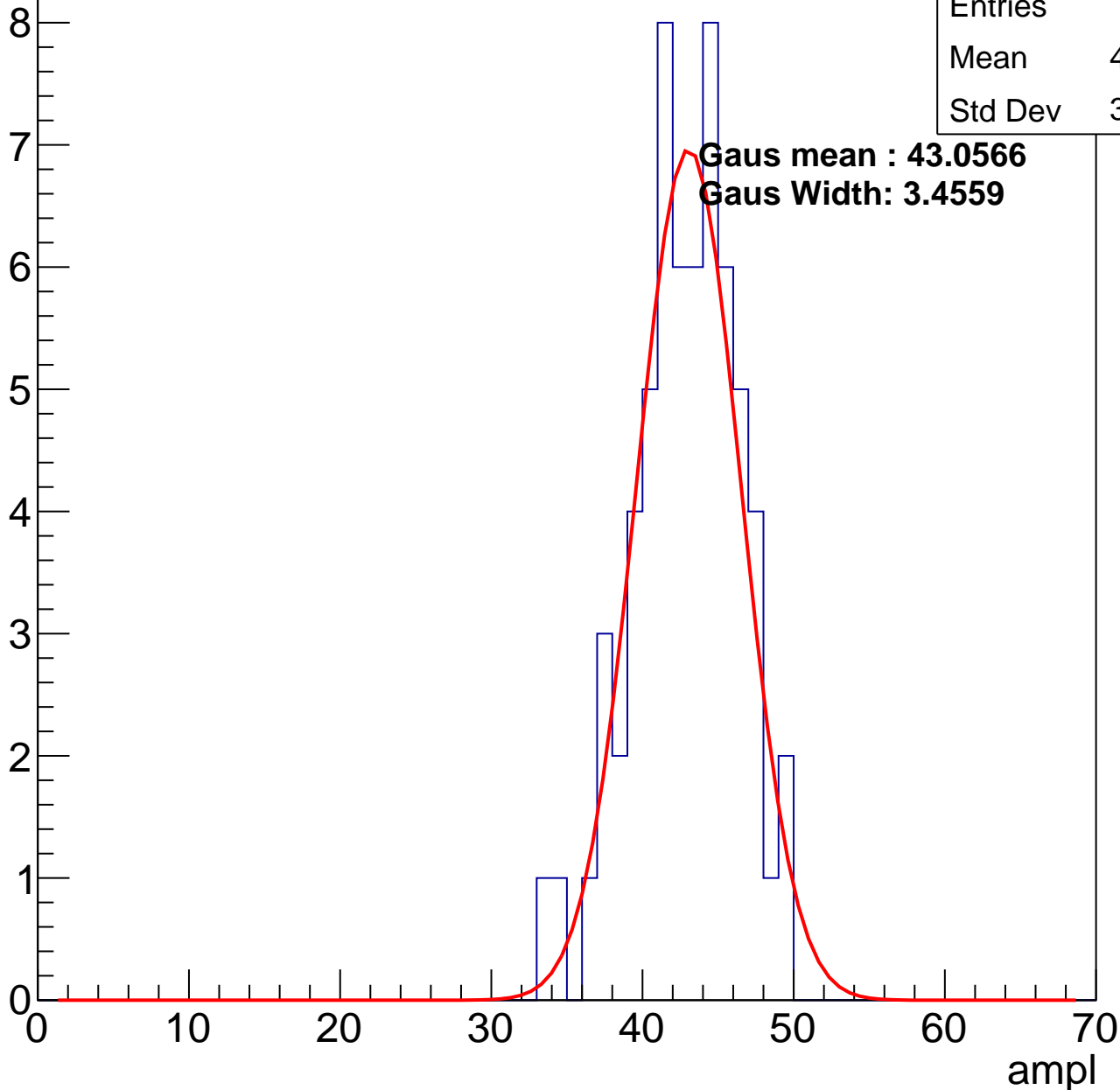
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	42.38
Std Dev	3.448

**Gaus mean : 43.0566**

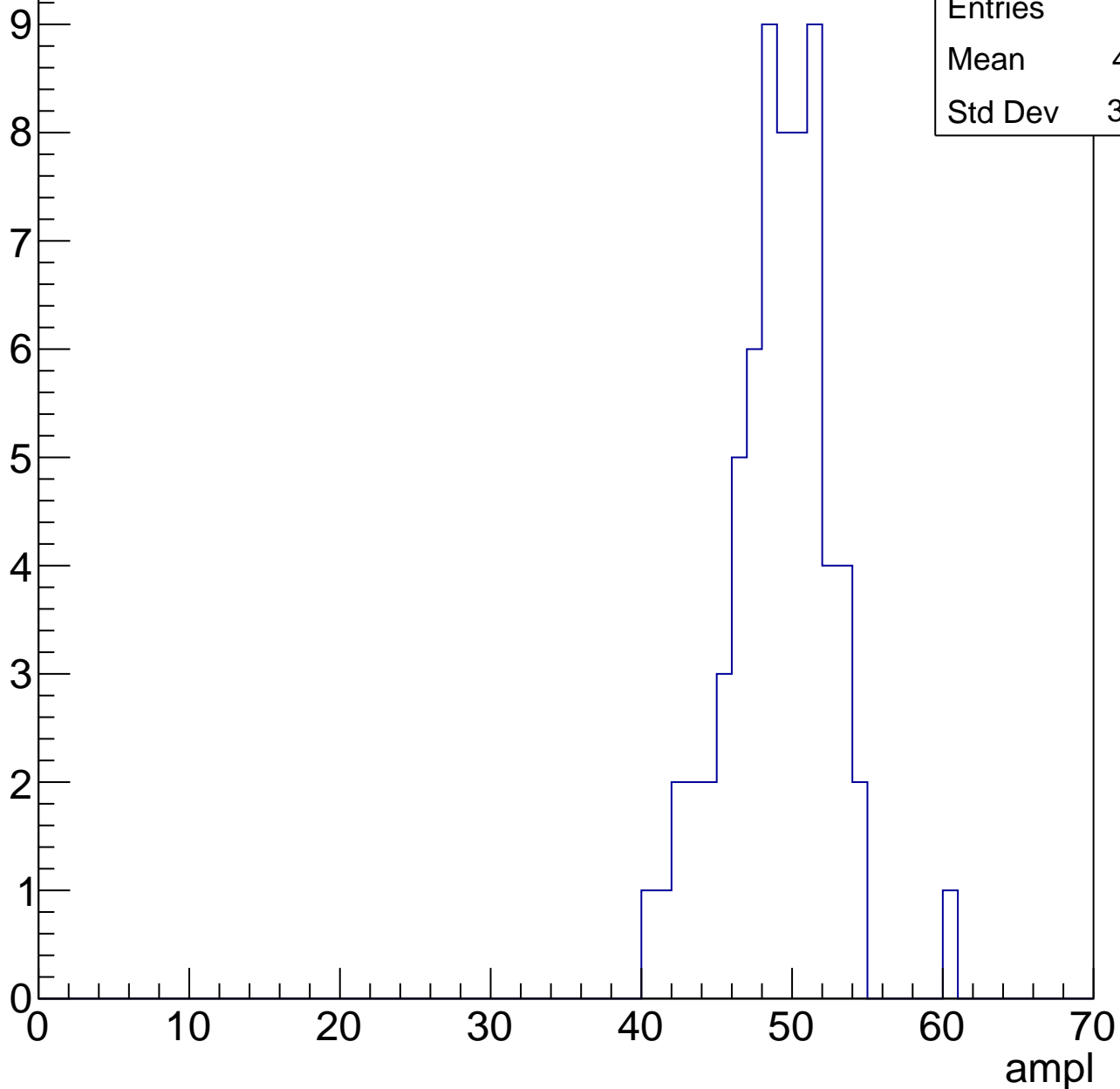
**Gaus Width: 3.4559**



# B1L000S, U24-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

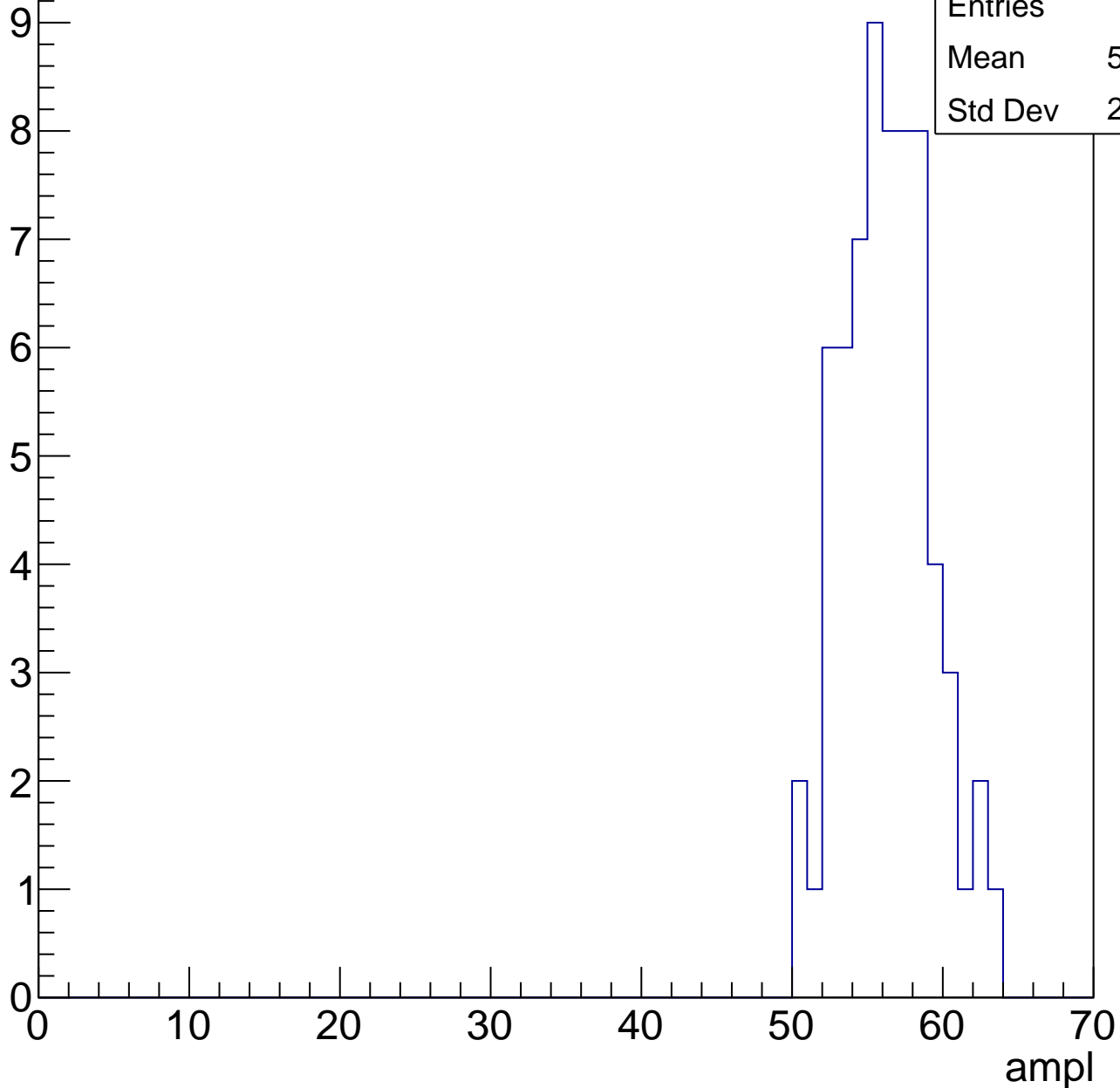
Entry



# B1L000S, U24-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

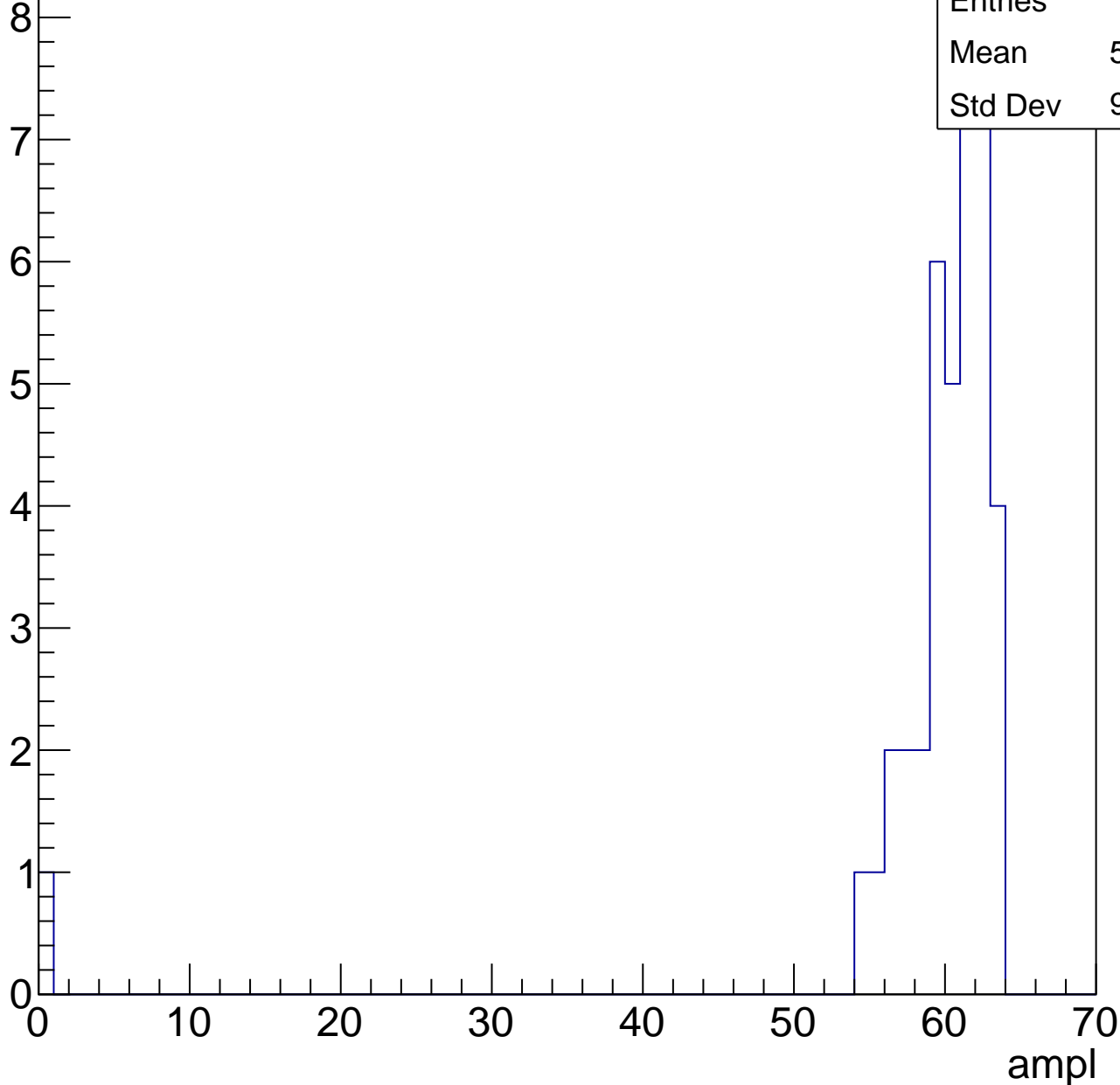


# B1L000S, U24-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	58.52
Std Dev	9.636

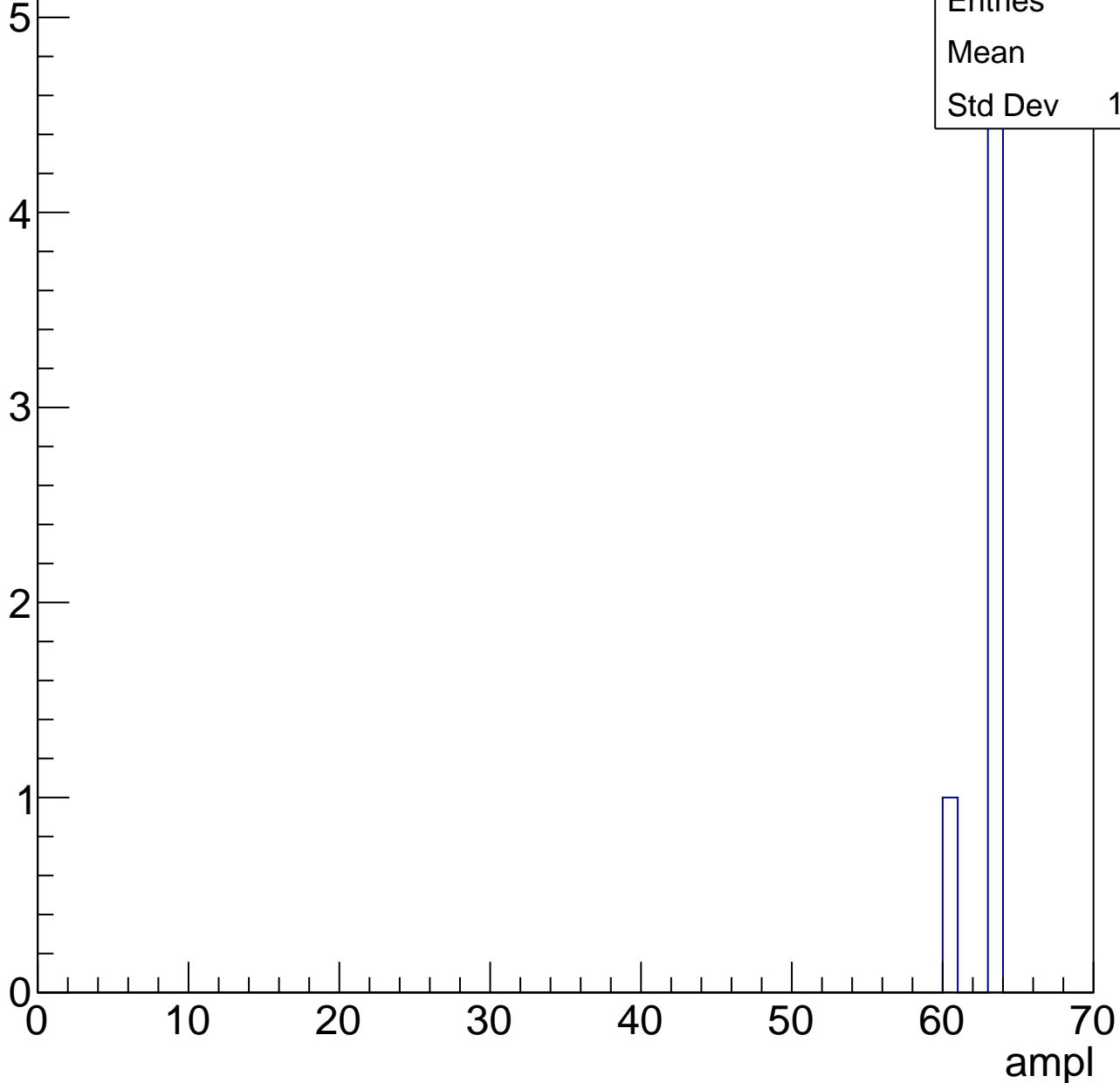


# B1L000S, U24-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	6
Mean	62.5
Std Dev	1.118

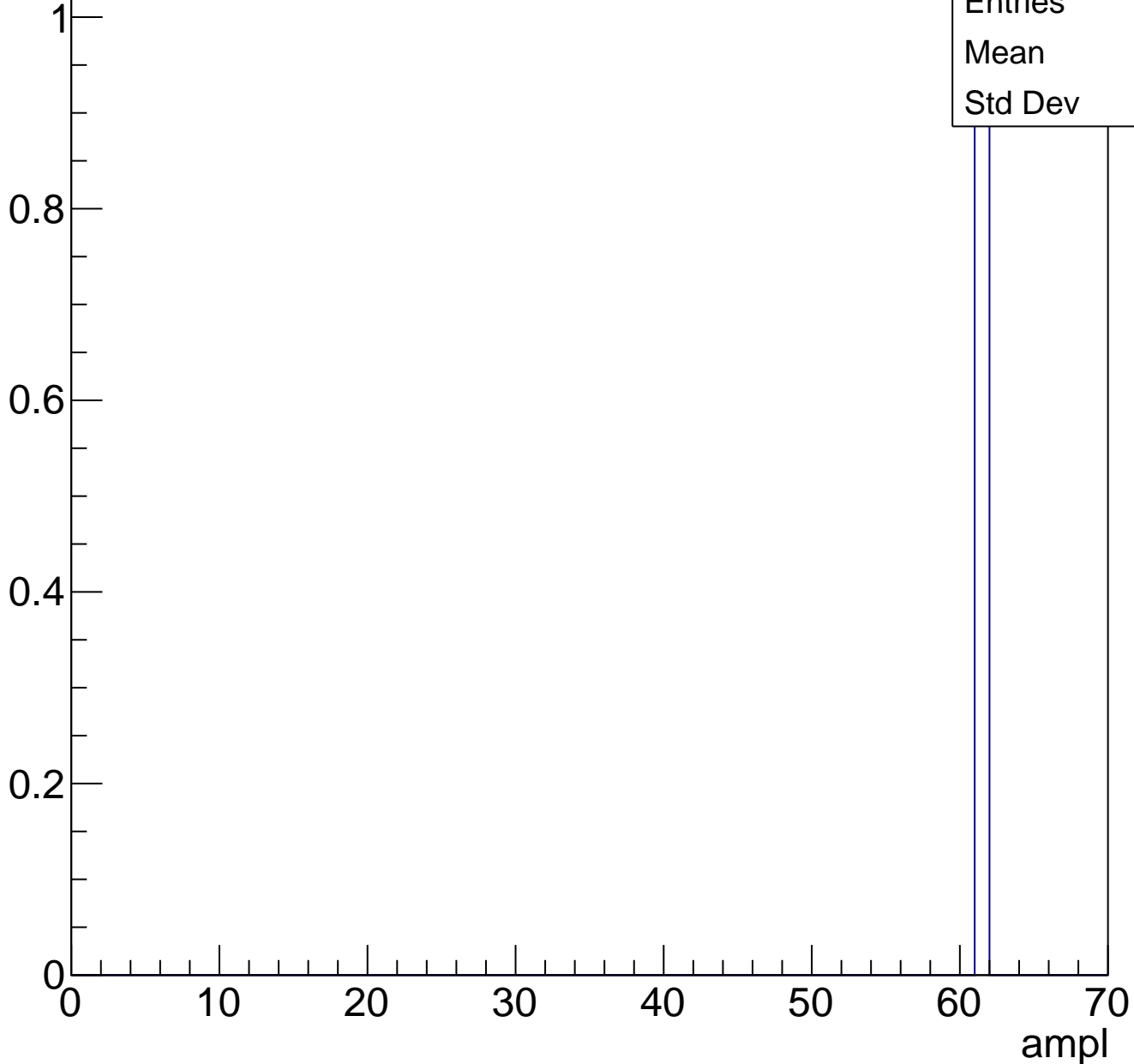




# B1L000S, U24-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch52, adc0

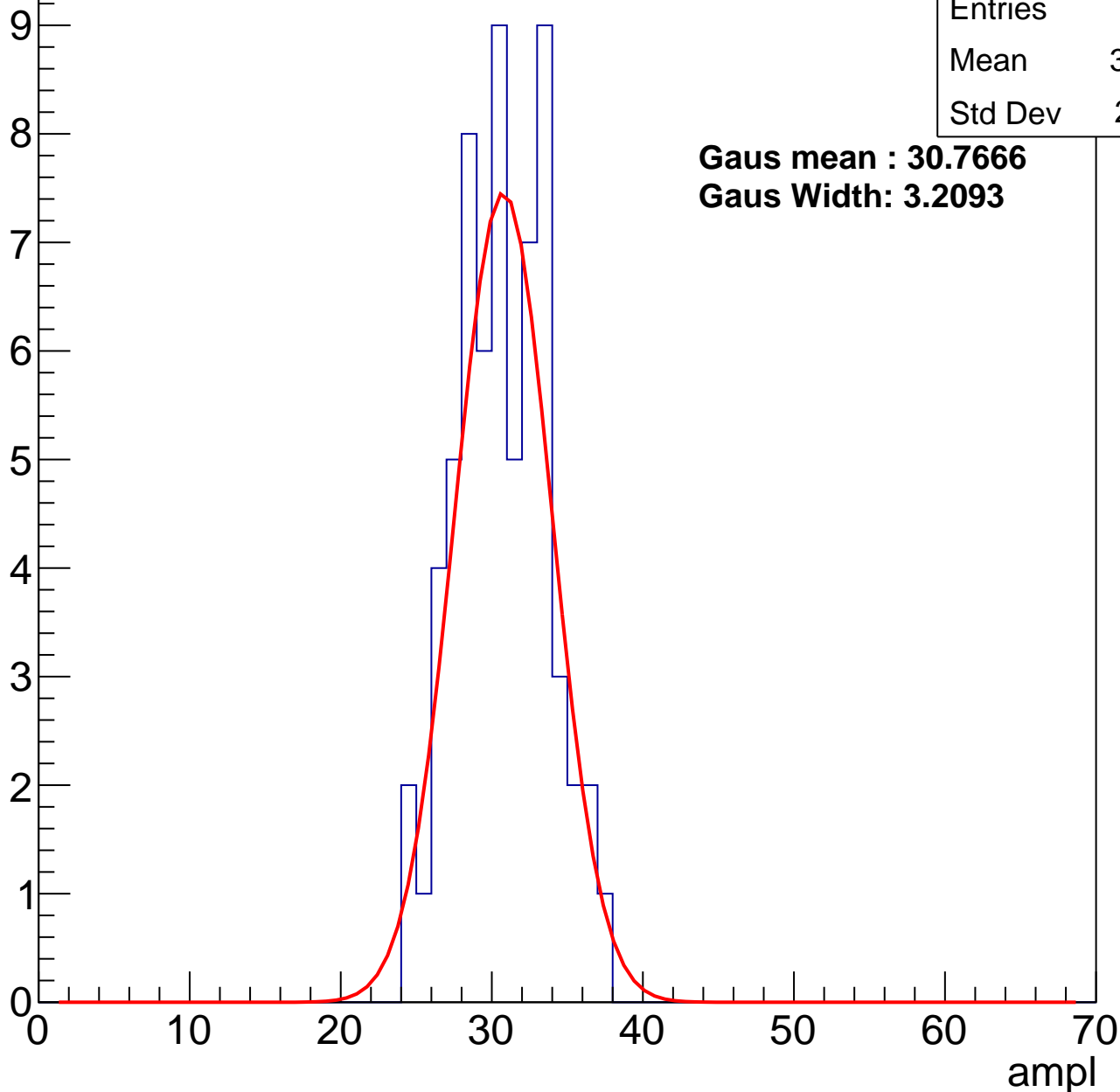
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	30.27
Std Dev	2.991

**Gaus mean : 30.7666**

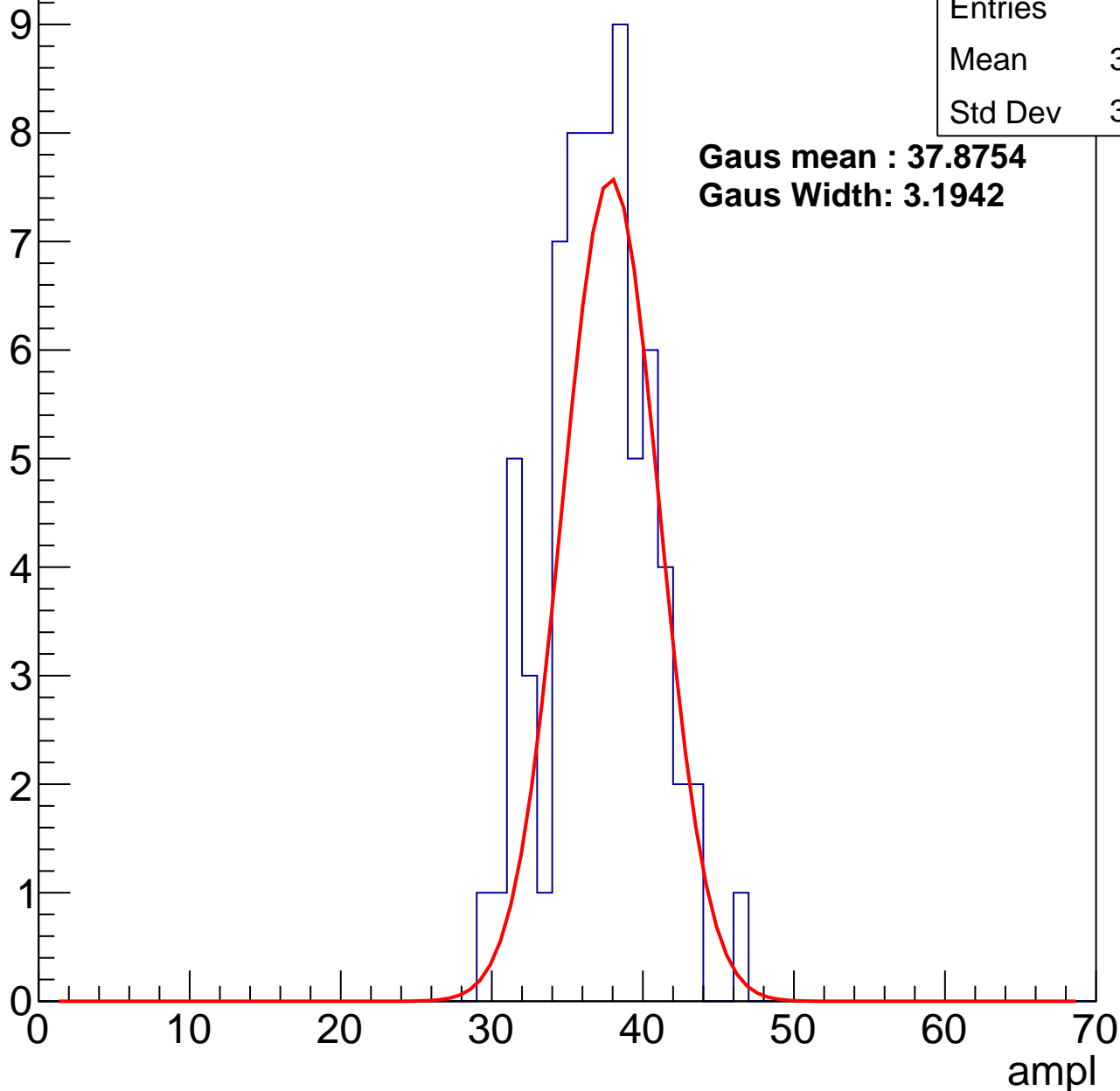
**Gaus Width: 3.2093**



# B1L000S, U24-ch52, adc1

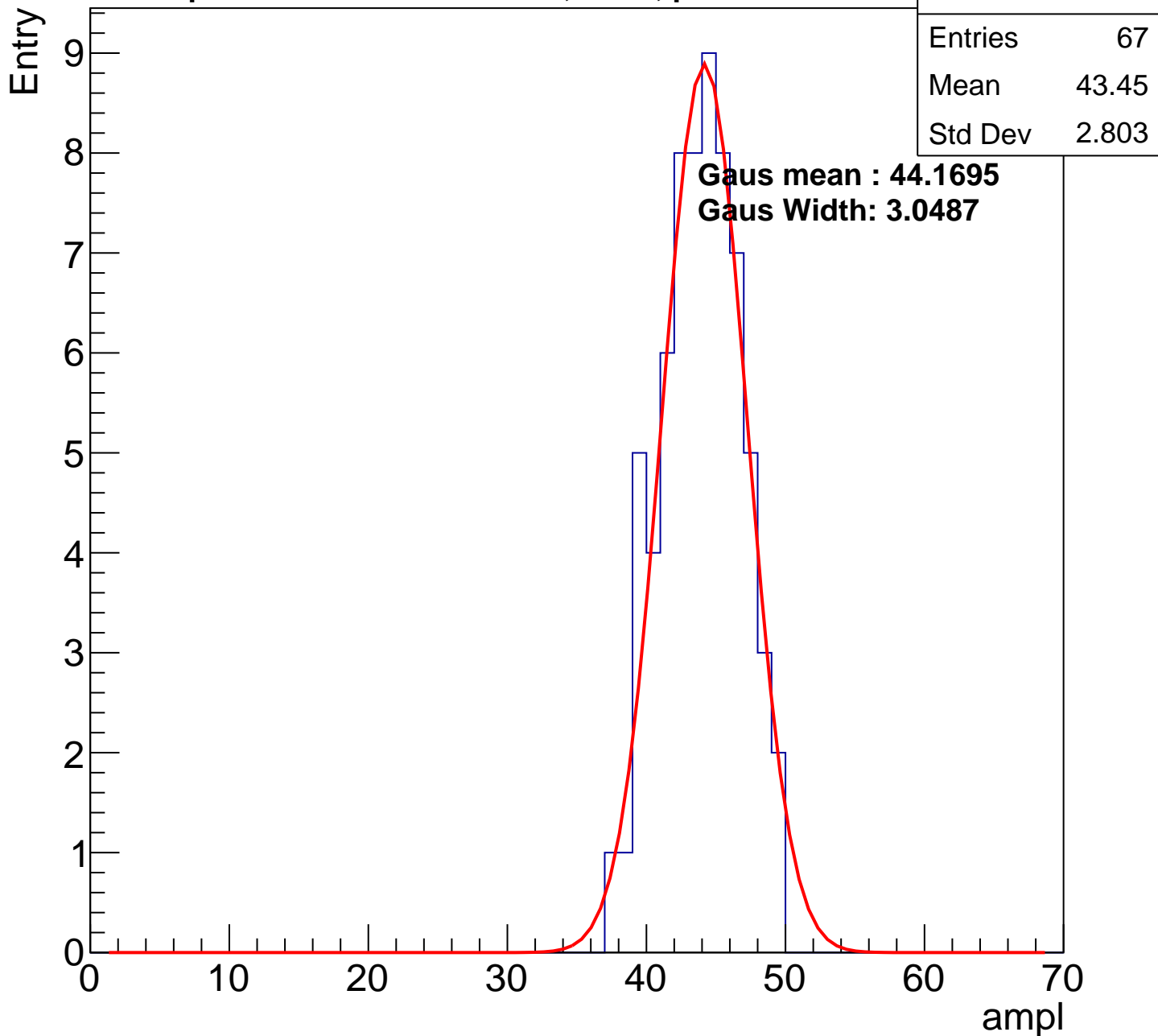
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch52, adc2

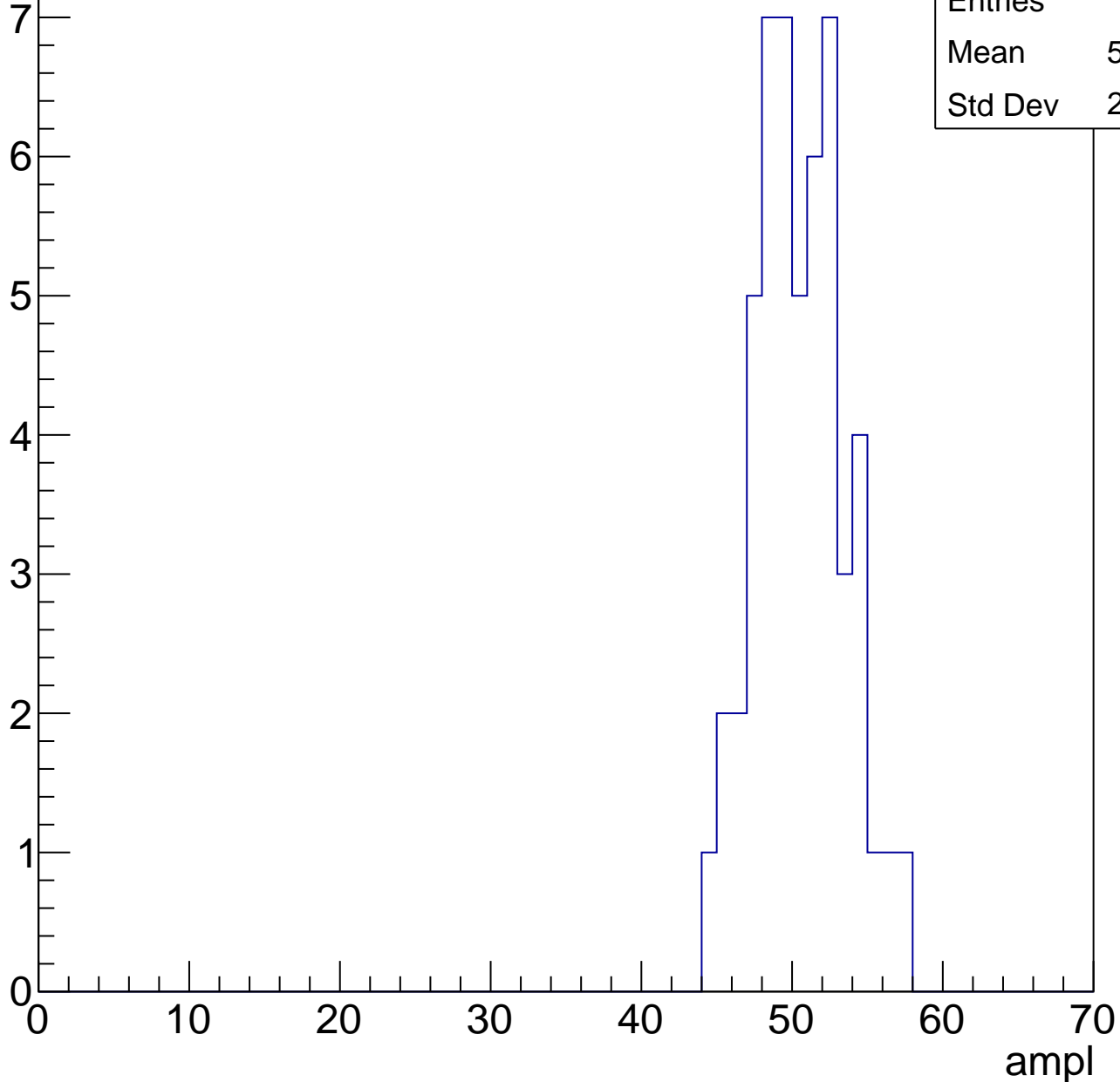
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



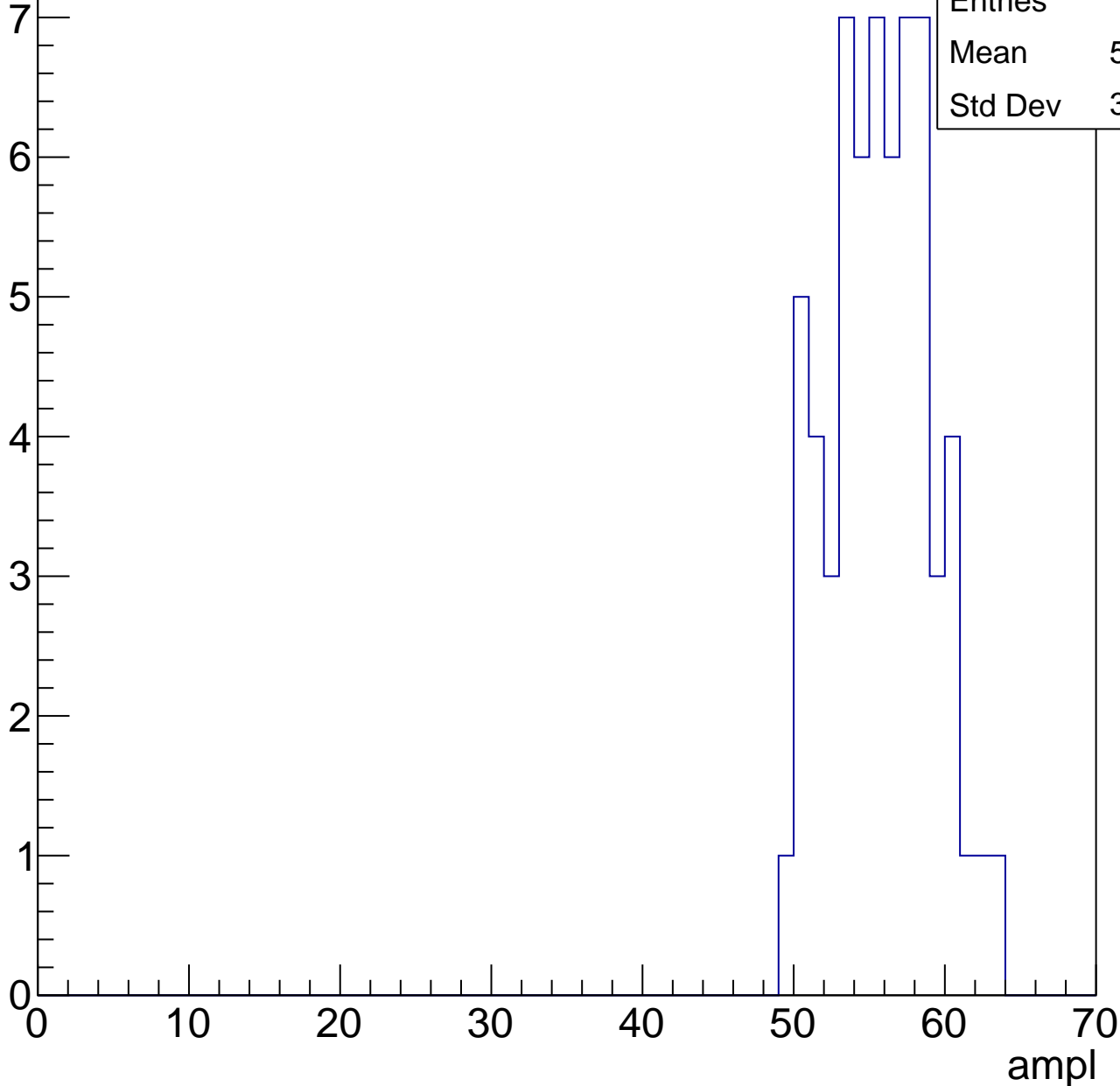
Entries	52
Mean	50.06
Std Dev	2.885

# B1L000S, U24-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

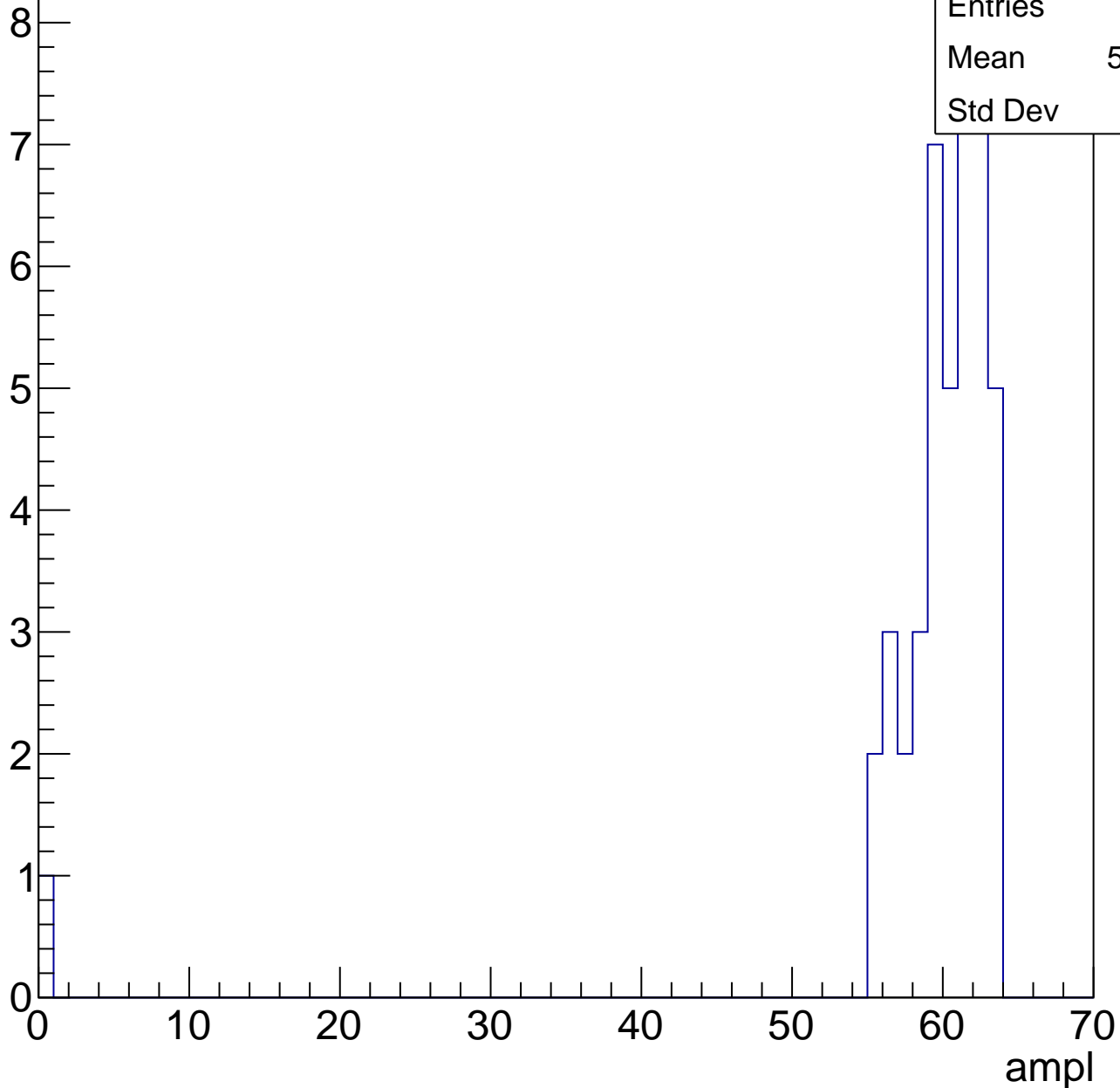
Entries	63
Mean	55.29
Std Dev	3.273



# B1L000S, U24-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

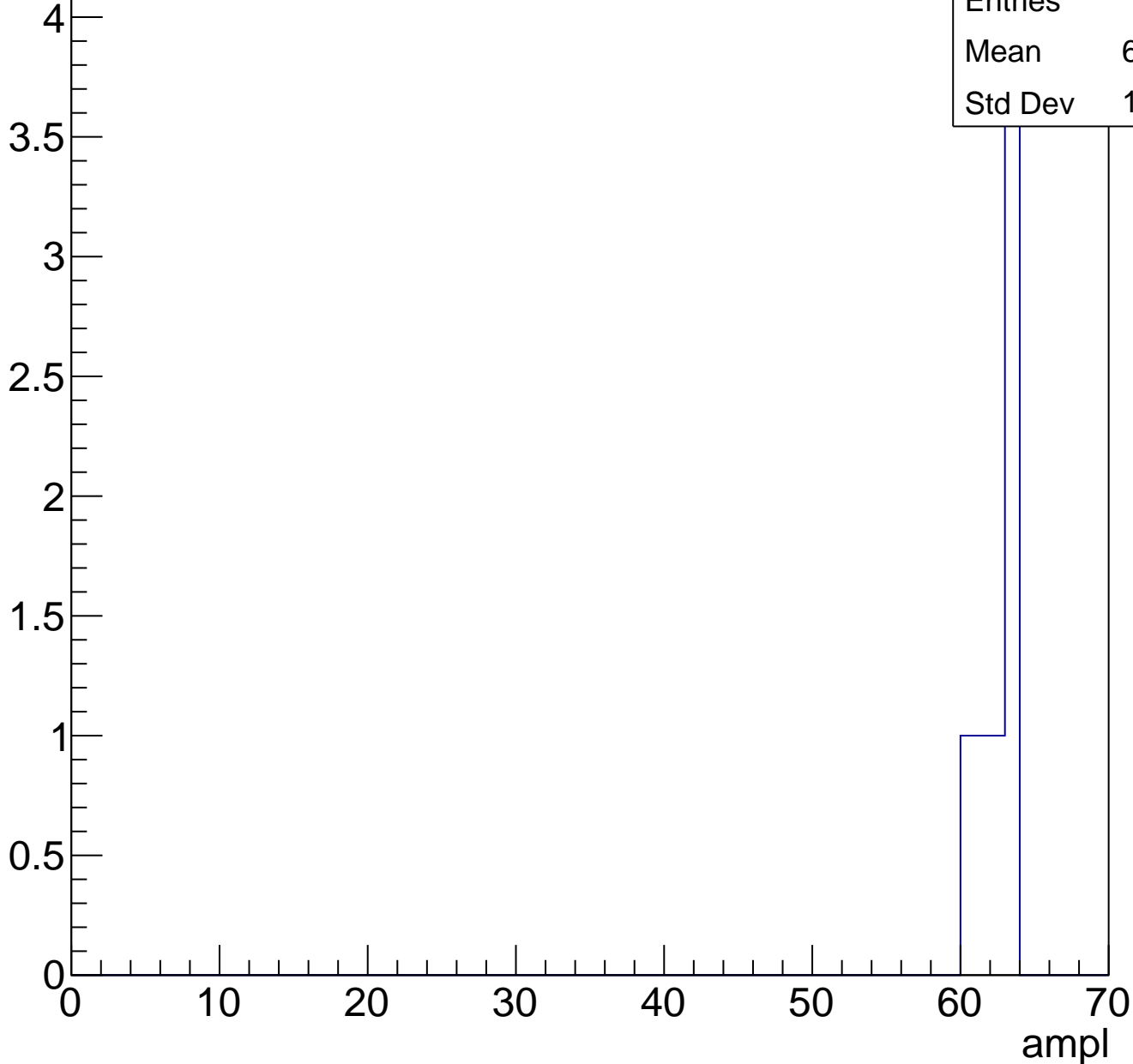
Entry



# B1L000S, U24-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch53, adc0

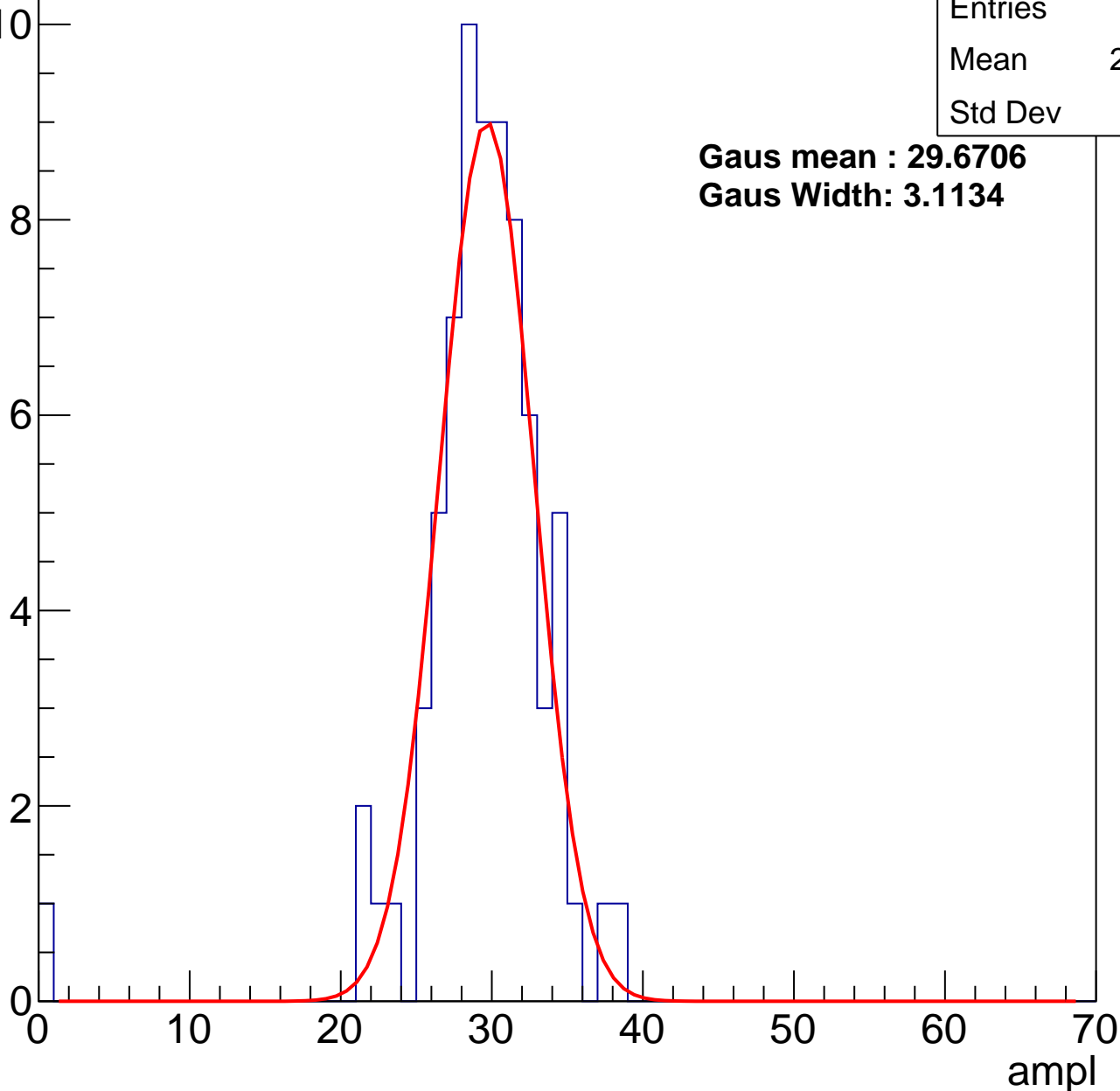
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.92
Std Dev	4.73

**Gaus mean : 29.6706**

**Gaus Width: 3.1134**

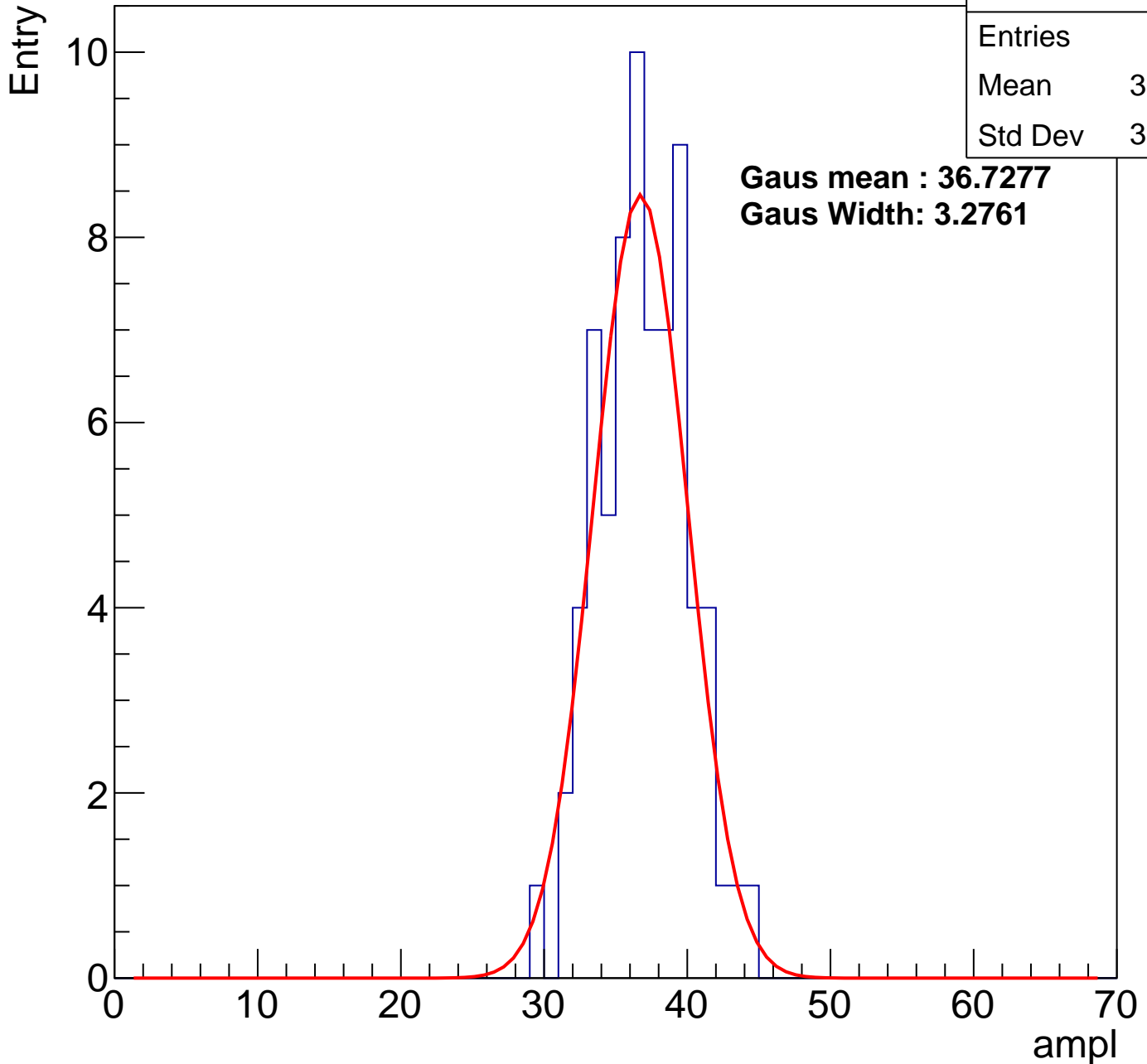


# B1L000S, U24-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	36.46
Std Dev	3.067

**Gaus mean : 36.7277**  
**Gaus Width: 3.2761**



# B1L000S, U24-ch53, adc2

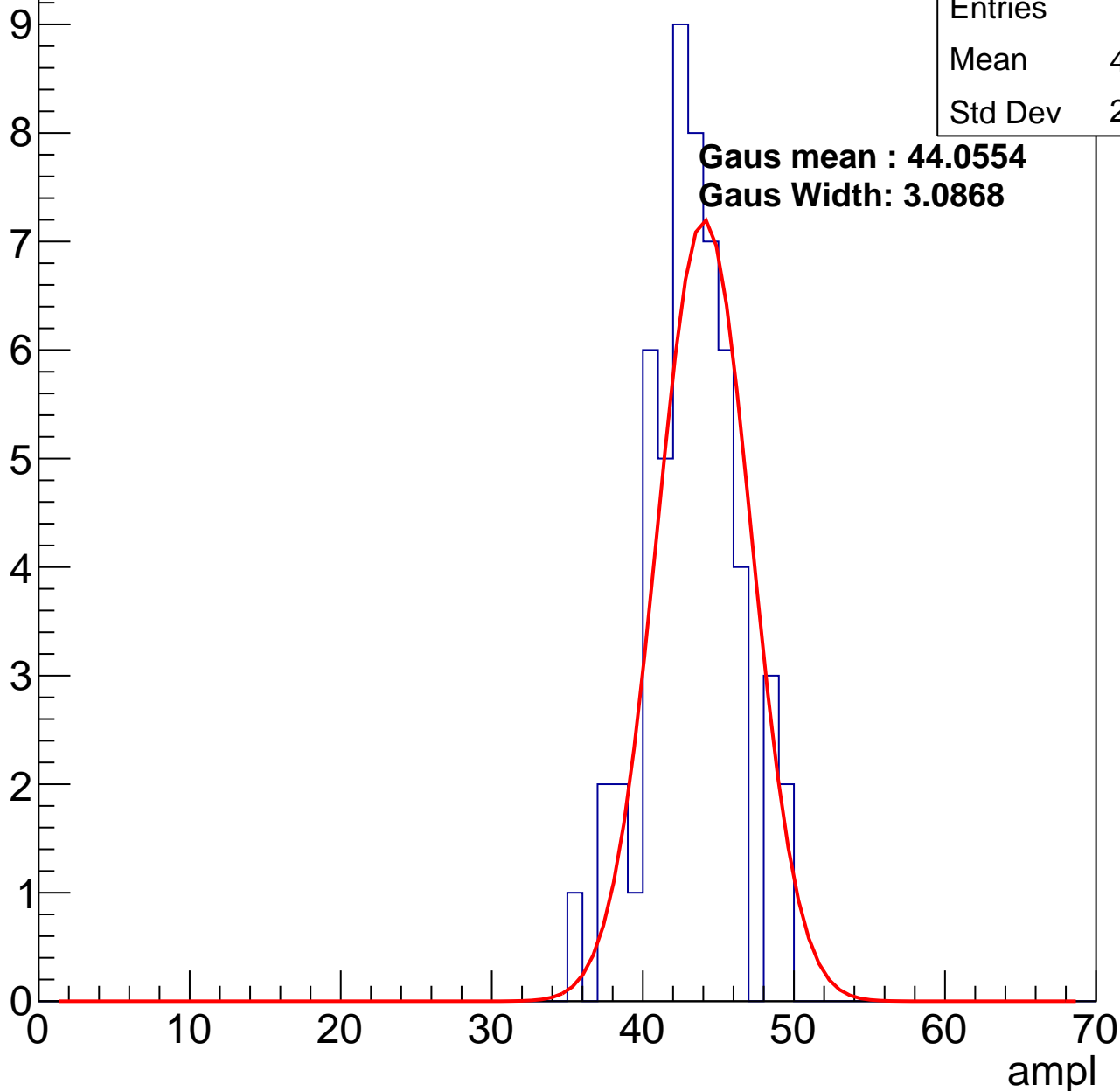
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.77
Std Dev	2.976

**Gaus mean : 44.0554**

**Gaus Width: 3.0868**



# B1L000S, U24-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

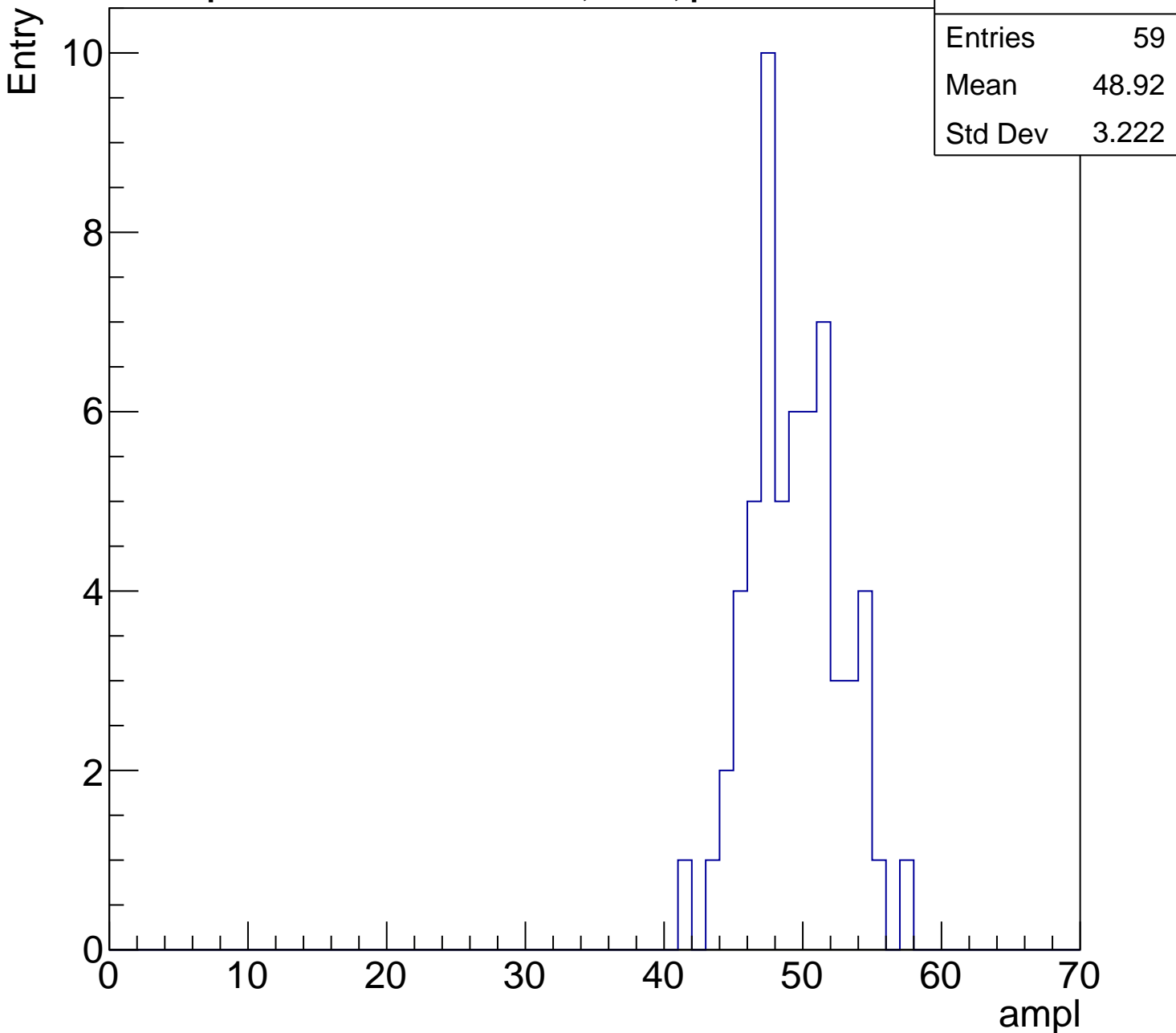
Entries	59
Mean	48.92
Std Dev	3.222

Entry

10  
8  
6  
4  
2  
0

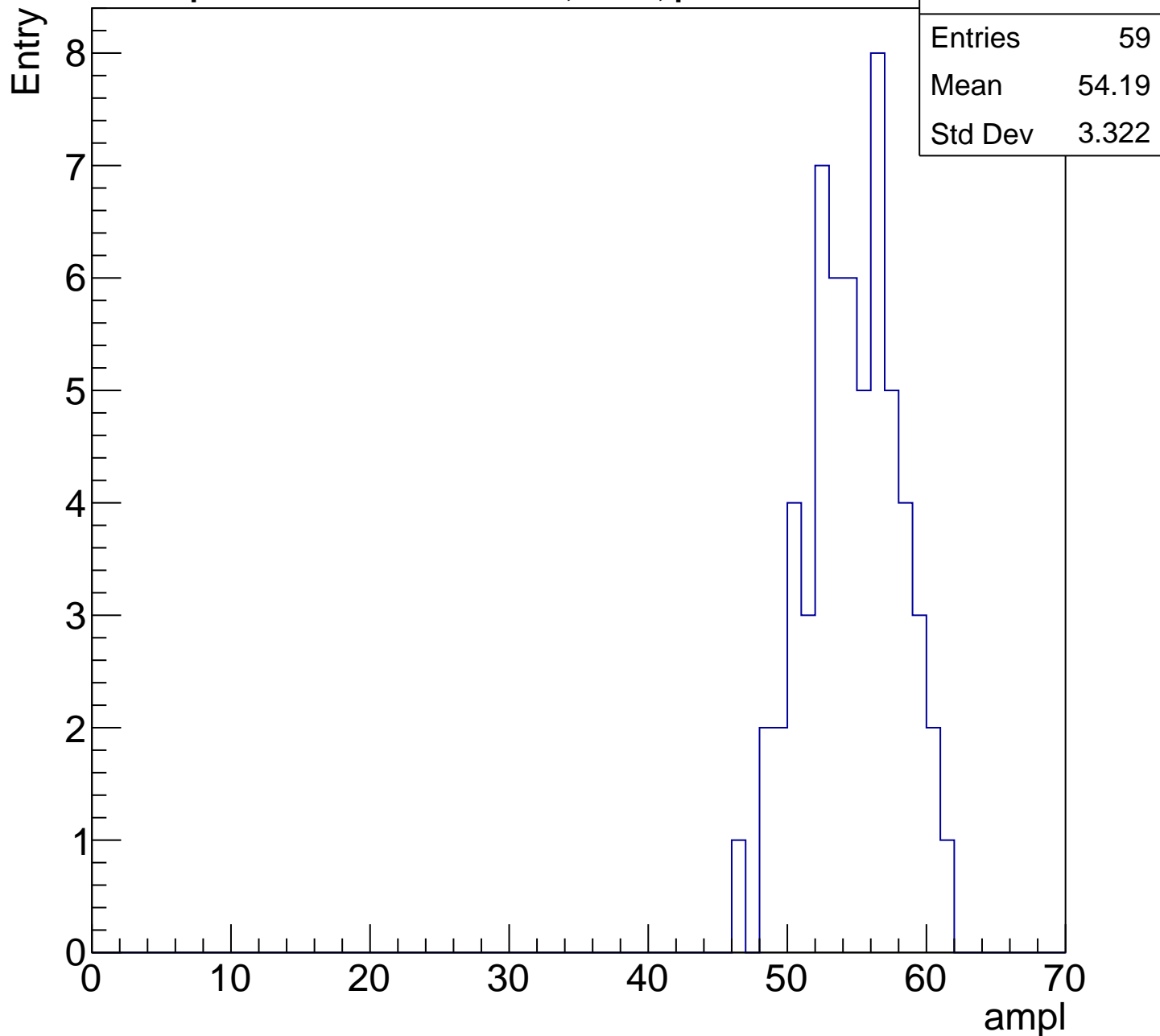
0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

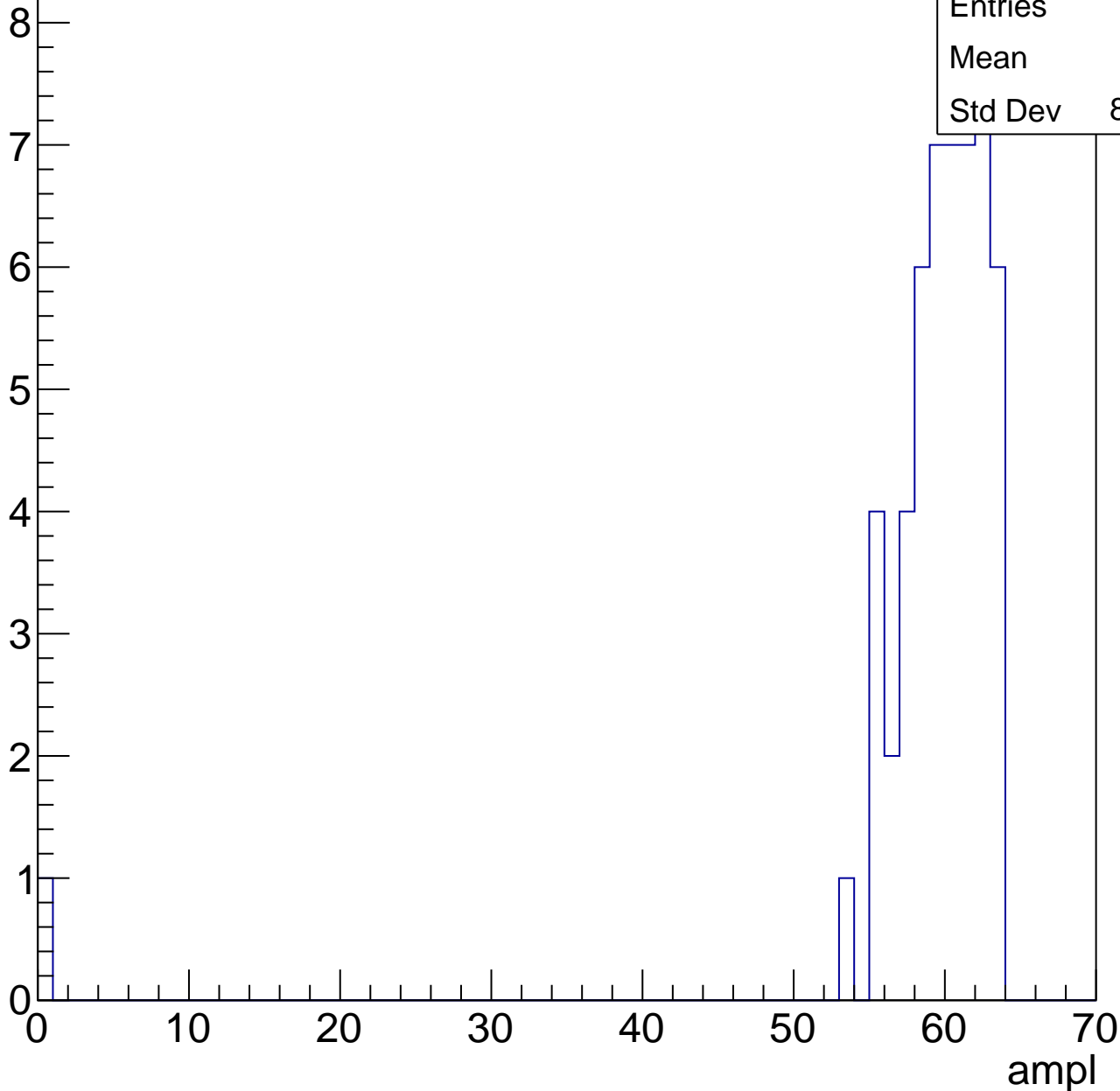


# B1L000S, U24-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

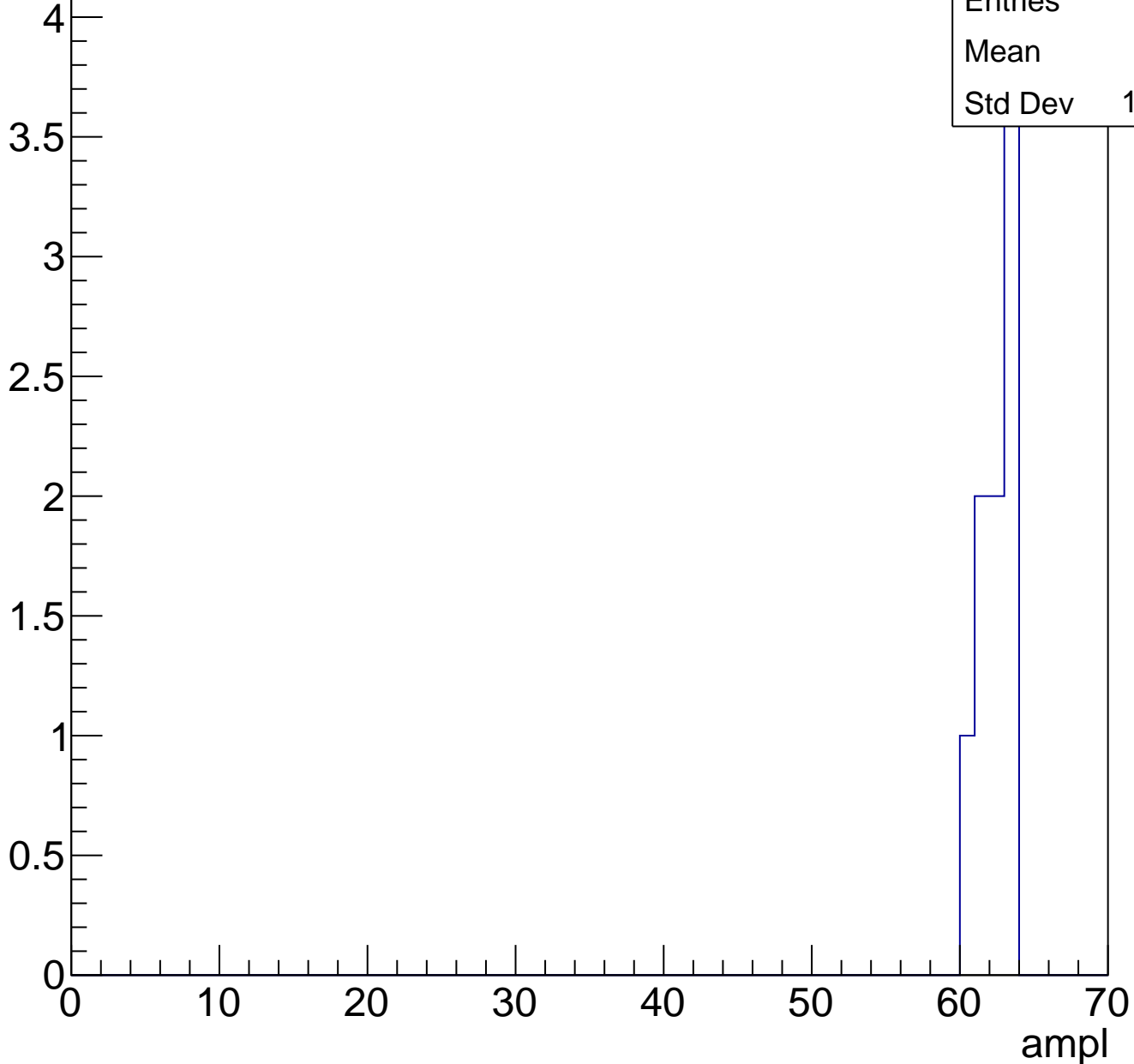
Entries	53
Mean	58.4
Std Dev	8.473



# B1L000S, U24-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch54, adc0

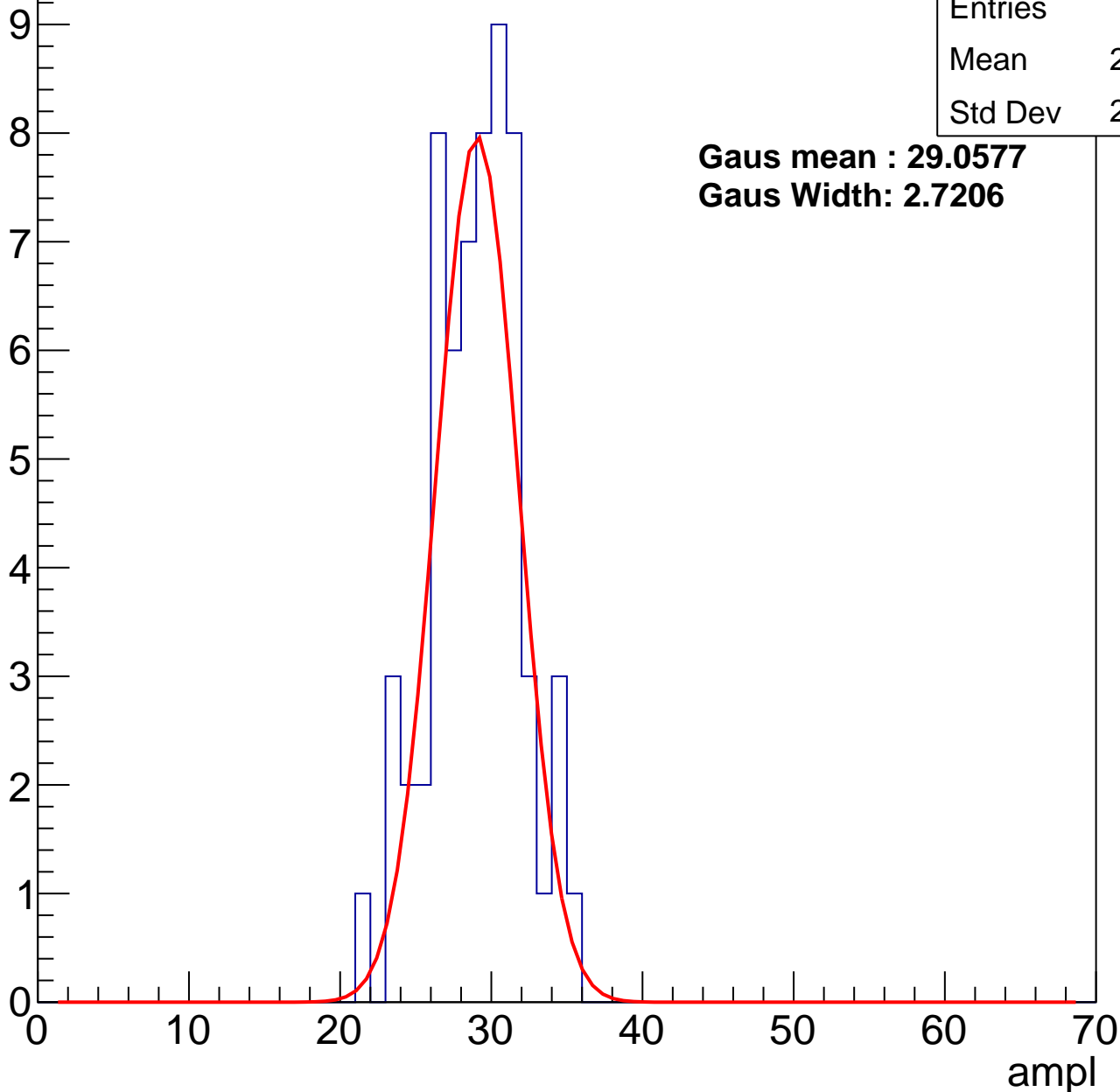
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.55
Std Dev	2.949

**Gaus mean : 29.0577**

**Gaus Width: 2.7206**



# B1L000S, U24-ch54, adc1

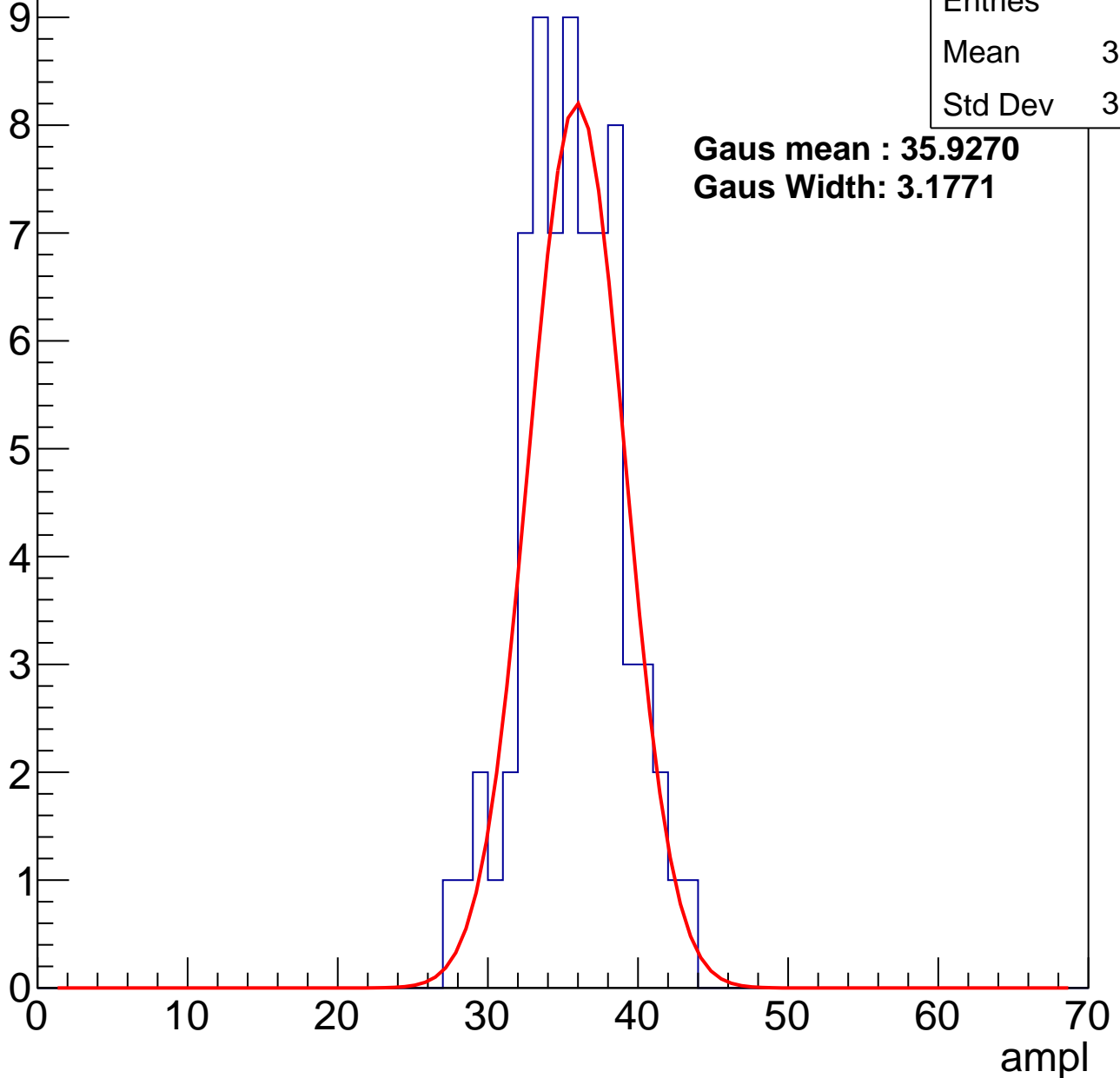
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.18
Std Dev	3.273

**Gaus mean : 35.9270**

**Gaus Width: 3.1771**



# B1L000S, U24-ch54, adc2

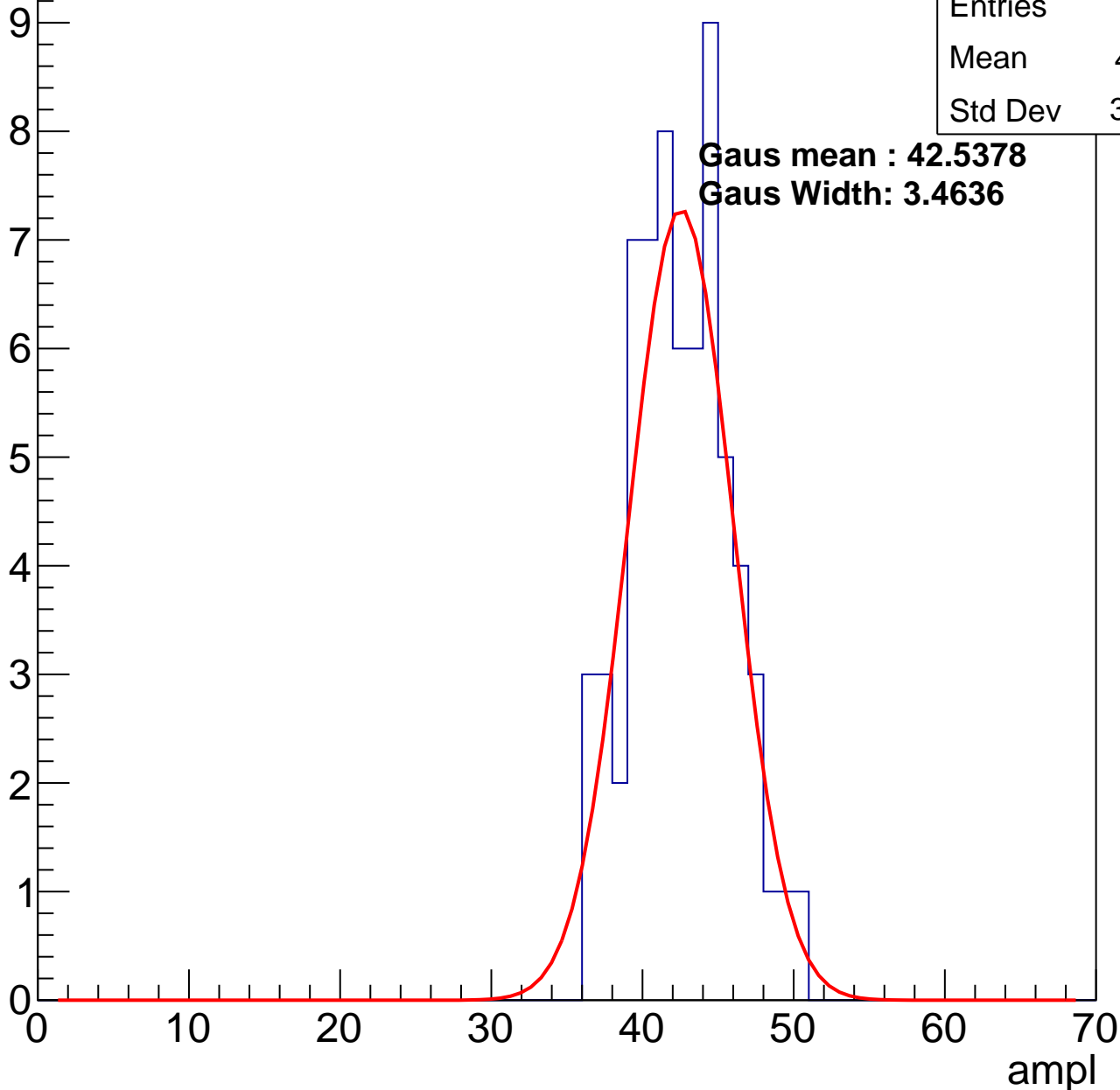
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	42.11
Std Dev	3.229

**Gaus mean : 42.5378**

**Gaus Width: 3.4636**

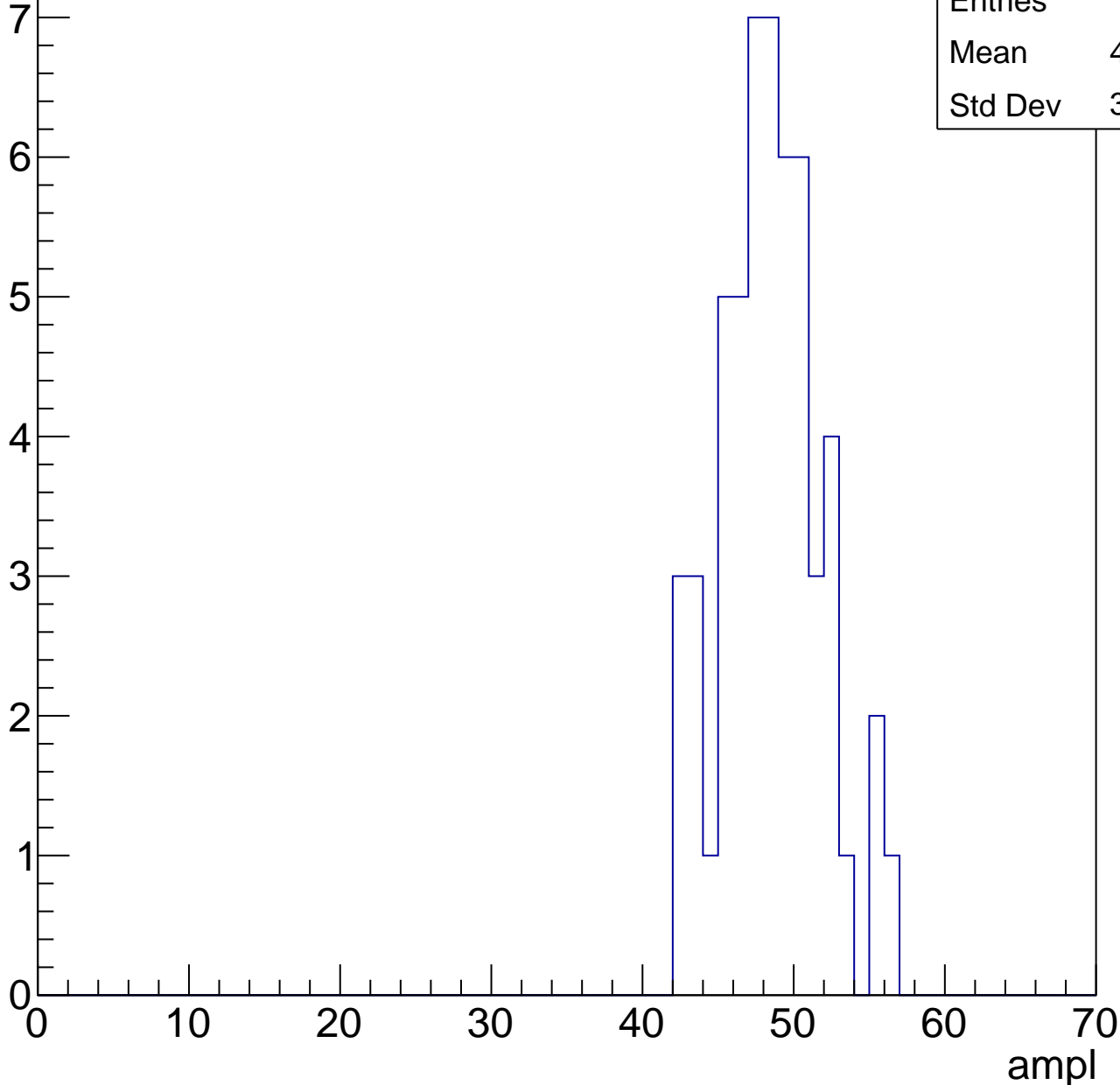


# B1L000S, U24-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	48.02
Std Dev	3.274

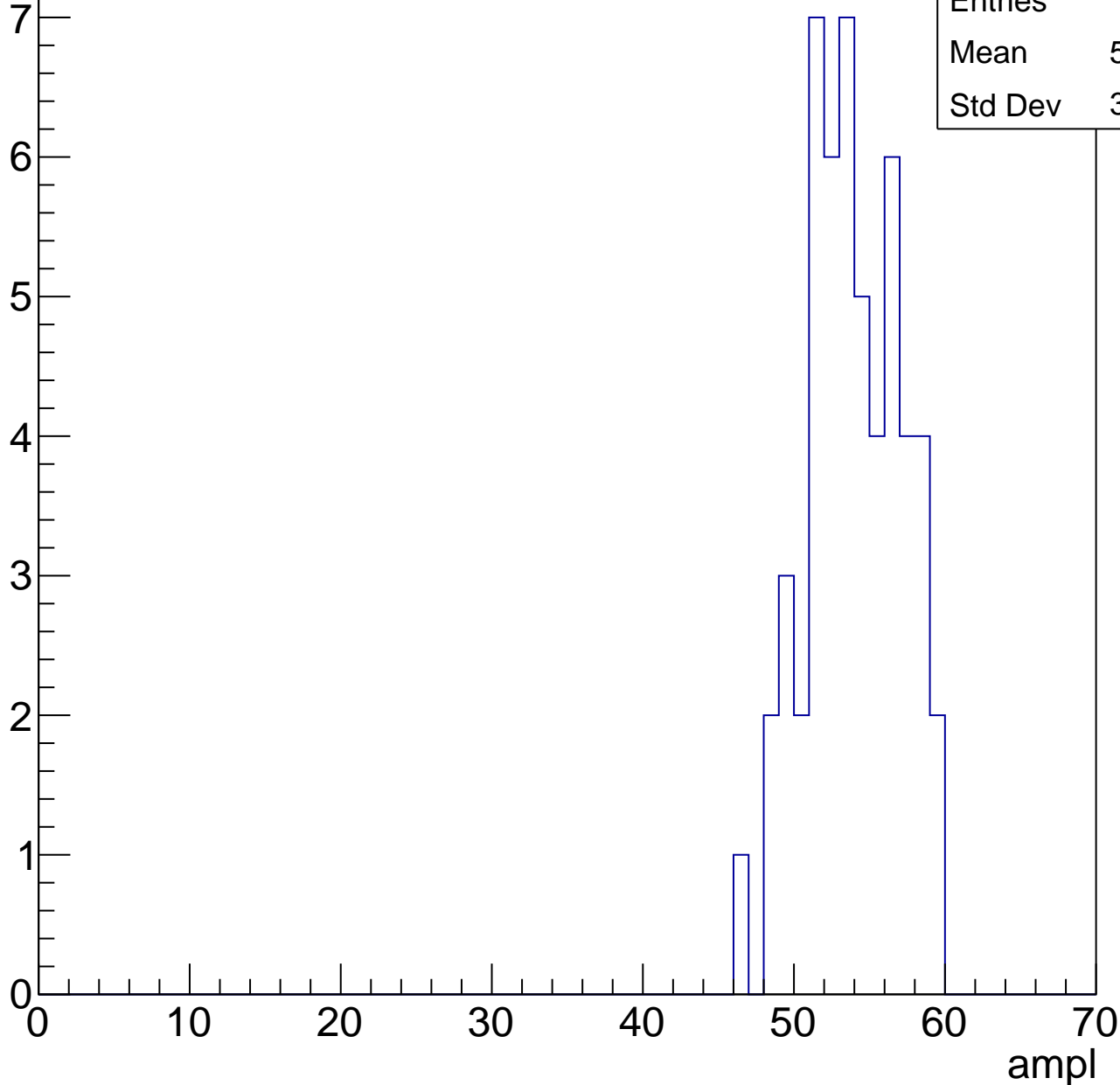


# B1L000S, U24-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	53.45
Std Dev	3.069

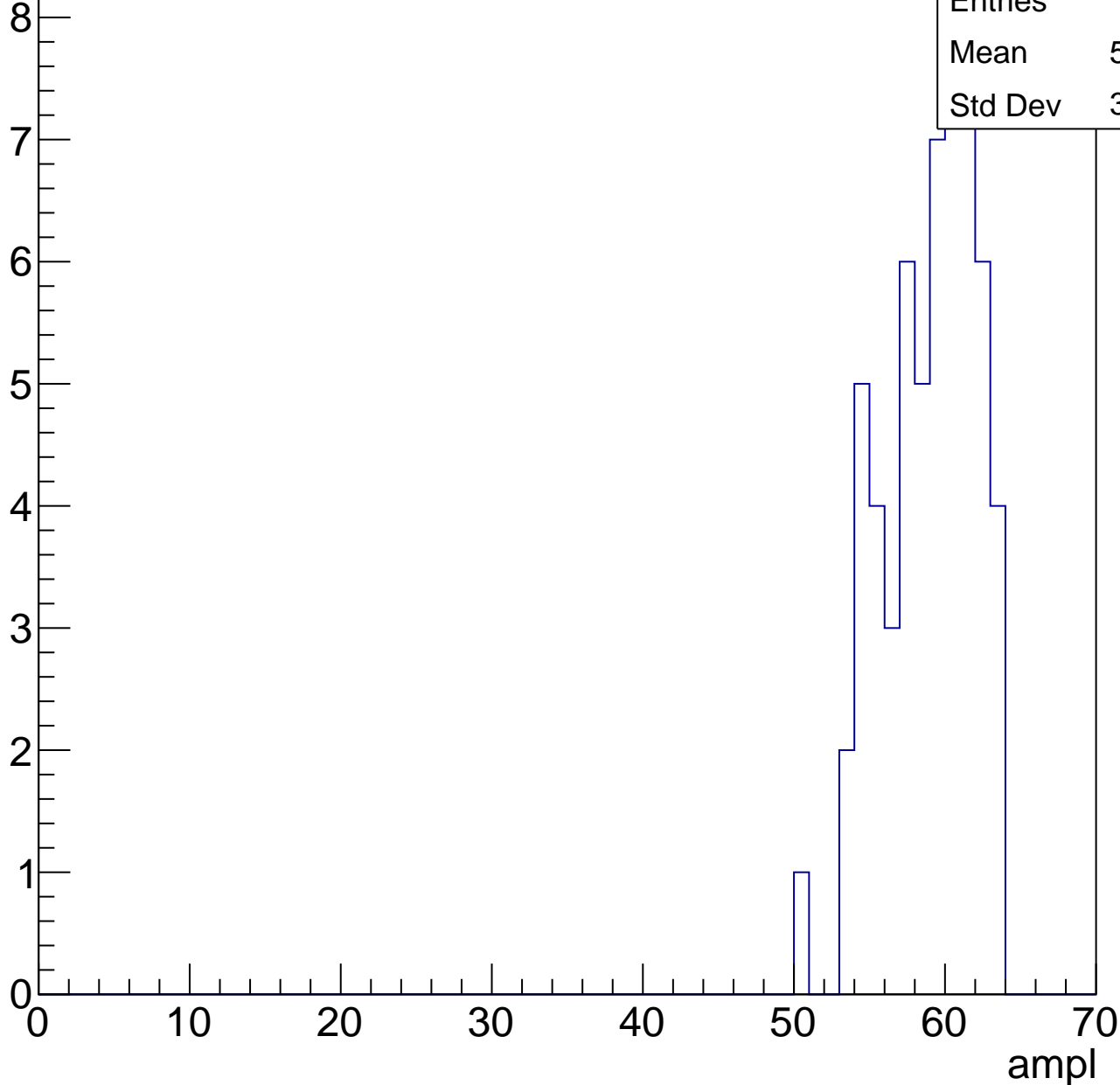


# B1L000S, U24-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	58.49
Std Dev	3.027

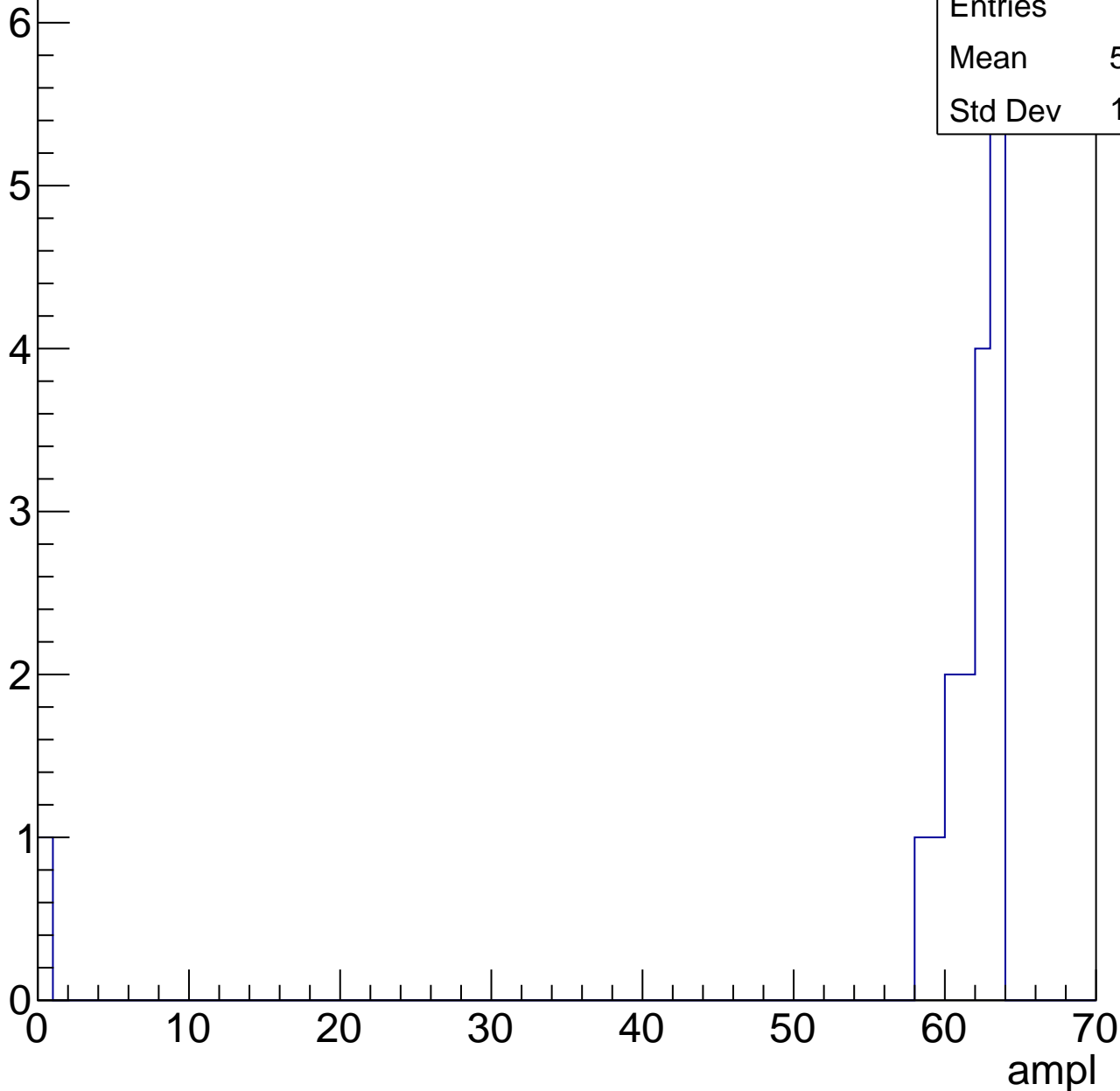


# B1L000S, U24-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.94
Std Dev	14.56





# B1L000S, U24-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch55, adc0

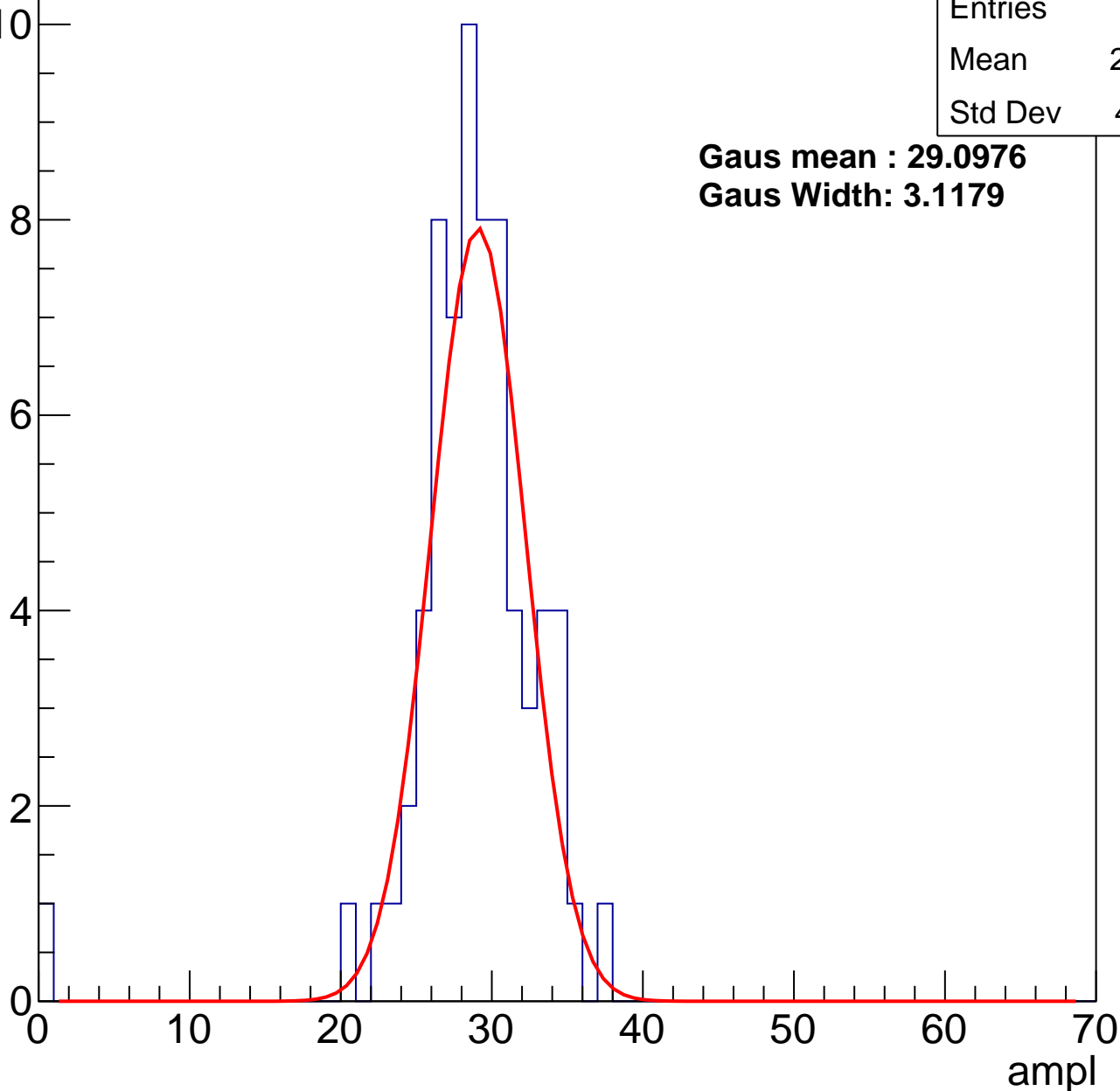
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.26
Std Dev	4.711

**Gaus mean : 29.0976**

**Gaus Width: 3.1179**



# B1L000S, U24-ch55, adc1

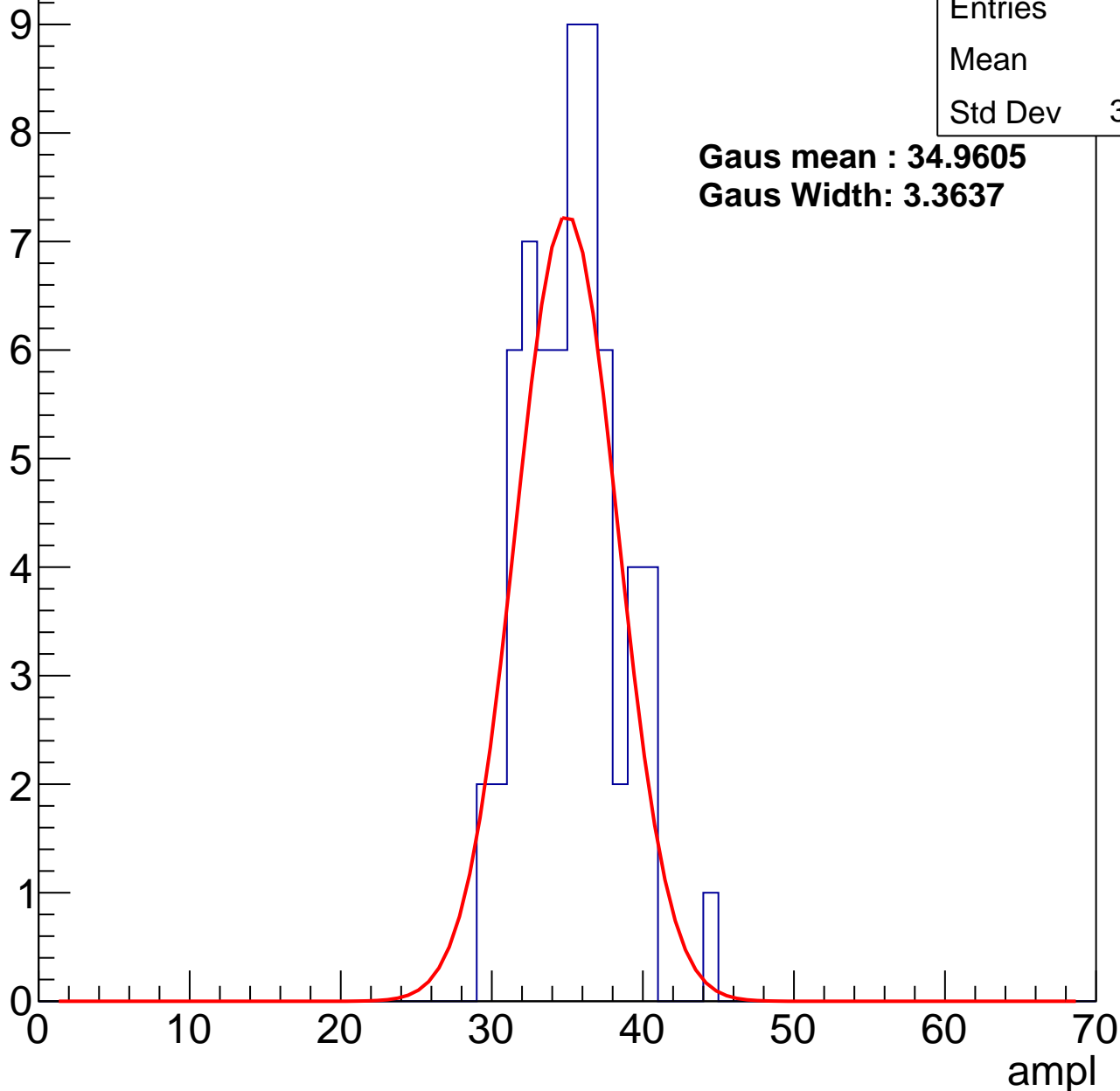
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.8
Std Dev	3.073

**Gaus mean : 34.9605**

**Gaus Width: 3.3637**



# B1L000S, U24-ch55, adc2

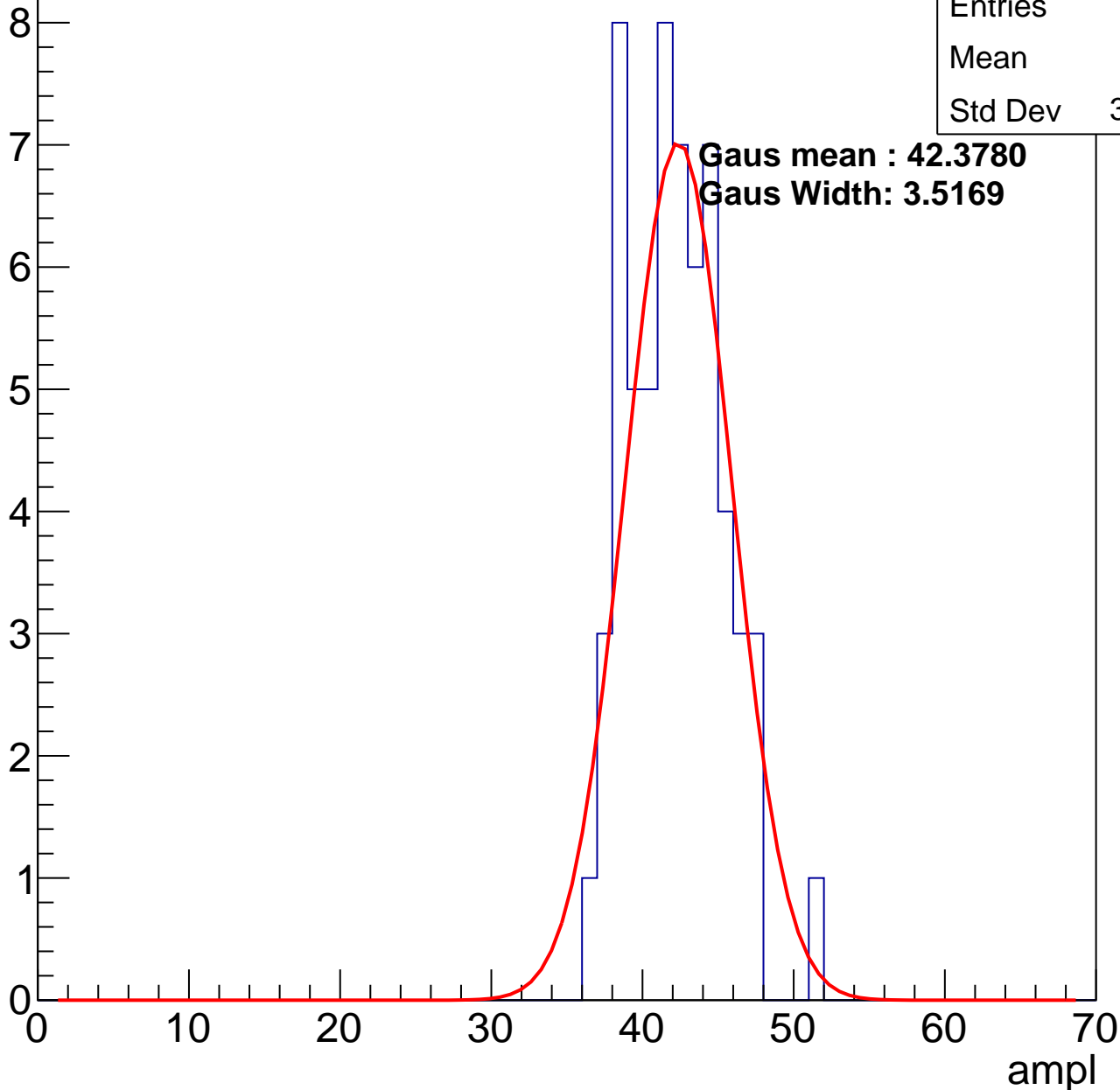
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.7
Std Dev	3.085

**Gaus mean : 42.3780**

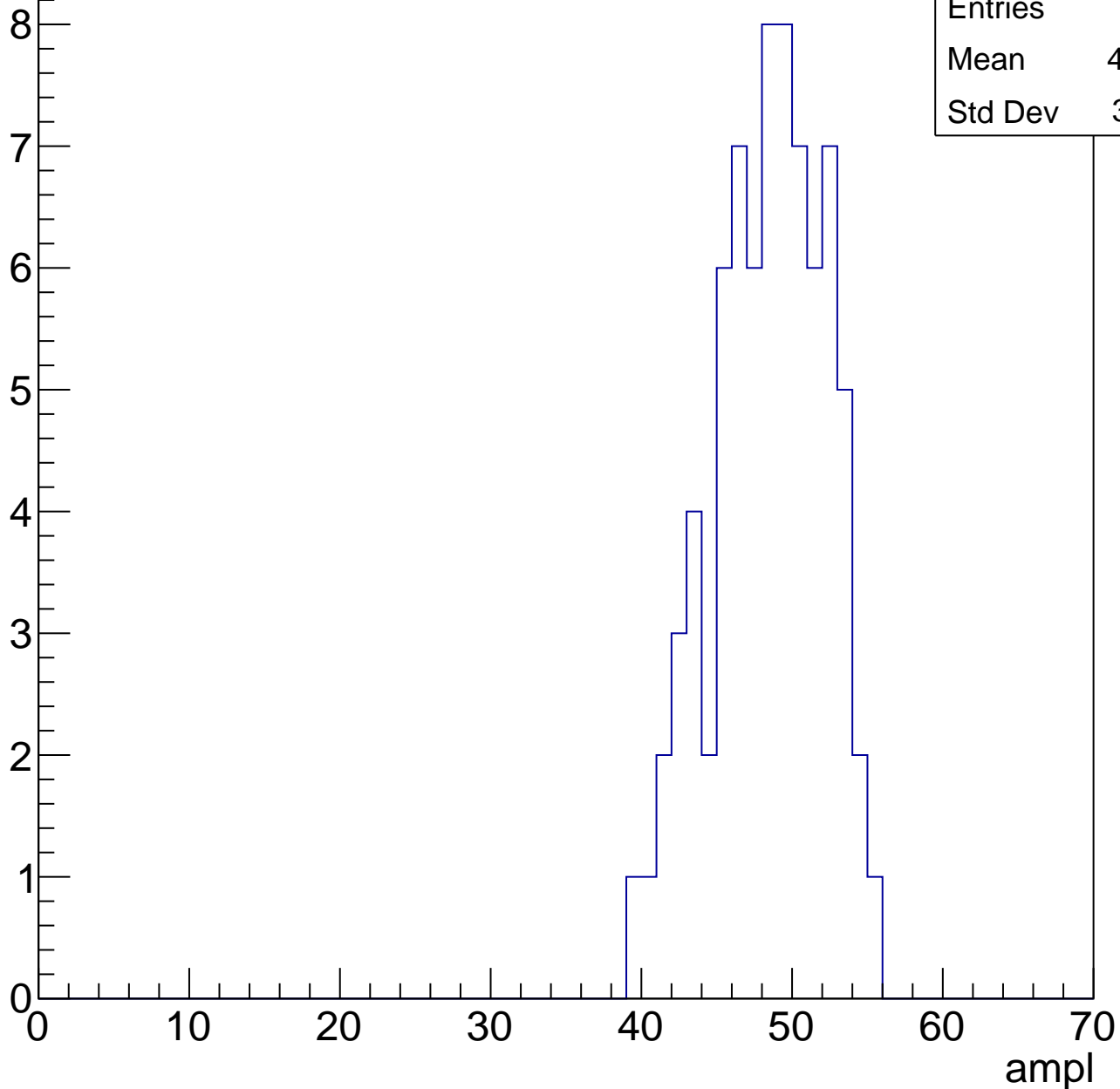
**Gaus Width: 3.5169**



# B1L000S, U24-ch55, adc3

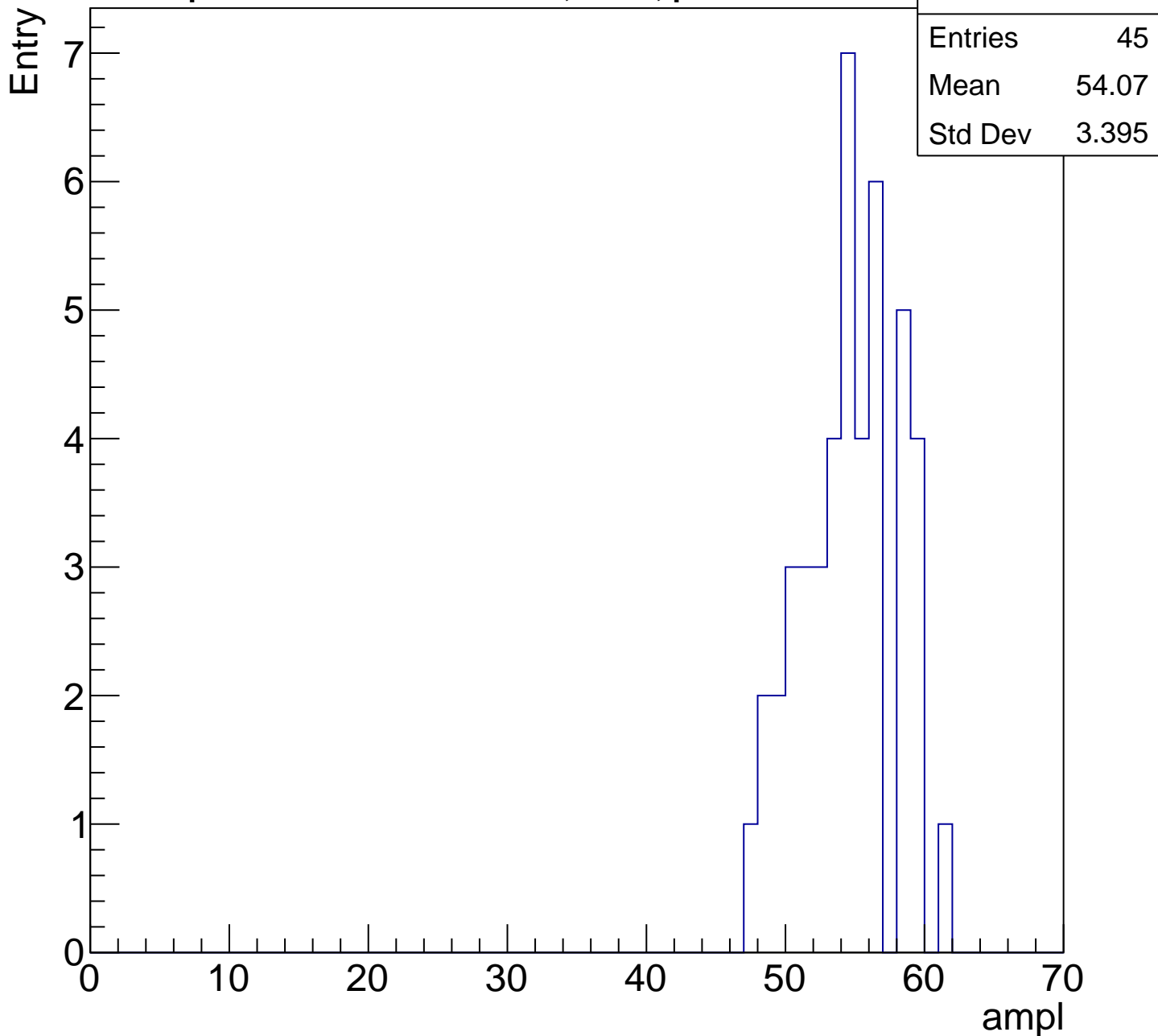
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	59
Std Dev	2.685

Entry

10

8

6

4

2

0

0

10

20

30

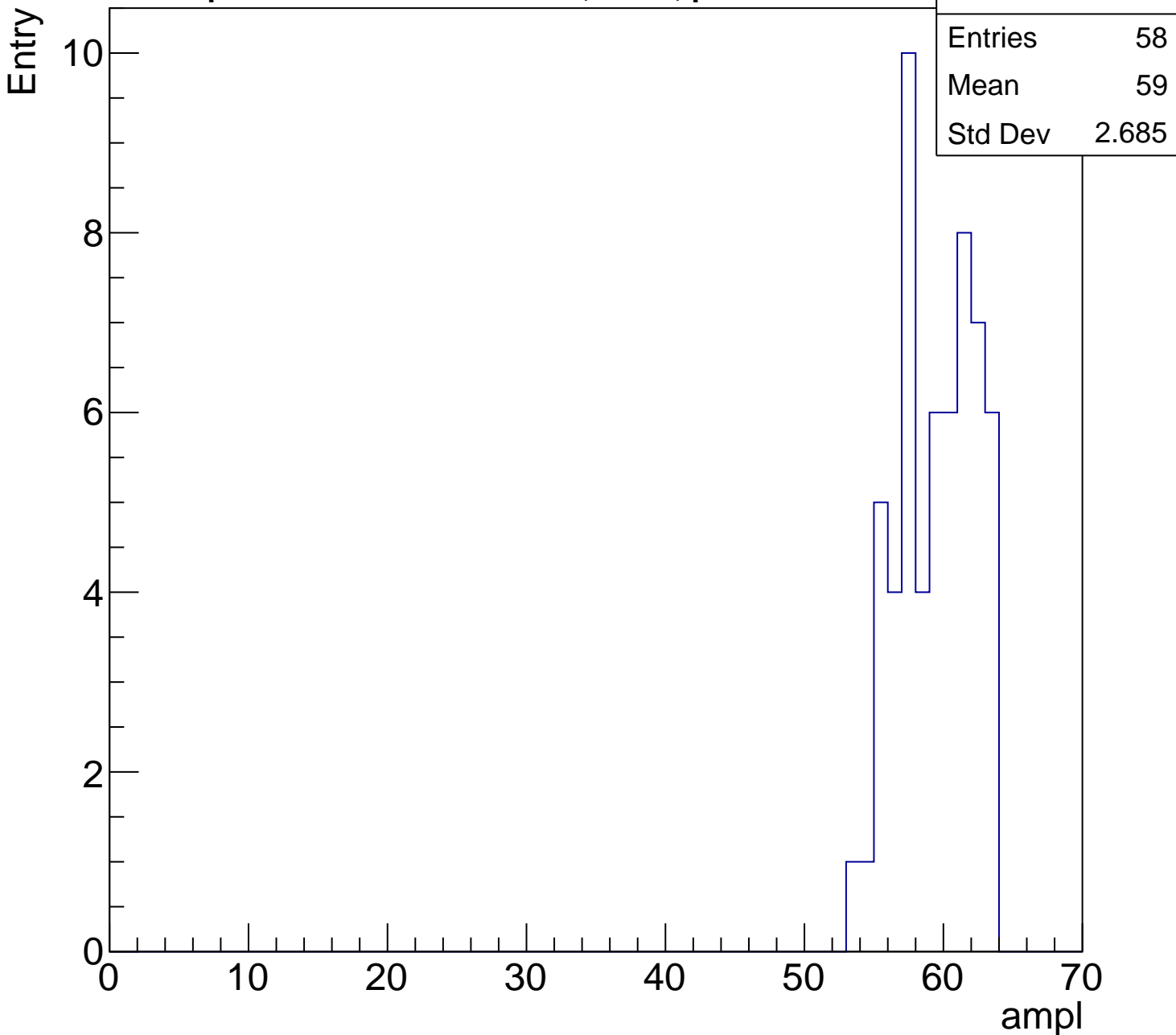
40

50

60

ampl

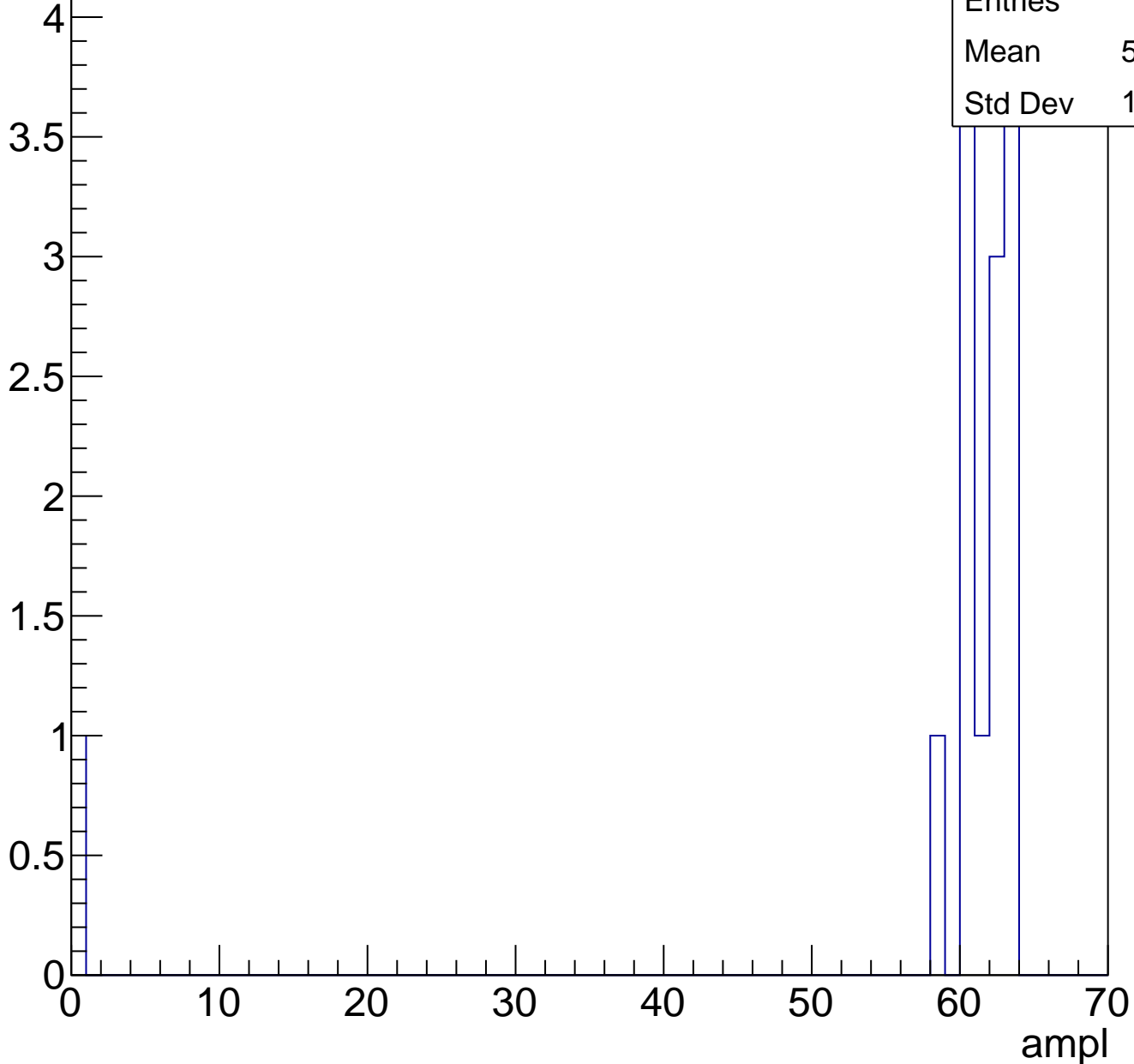
70



# B1L000S, U24-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch56, adc0

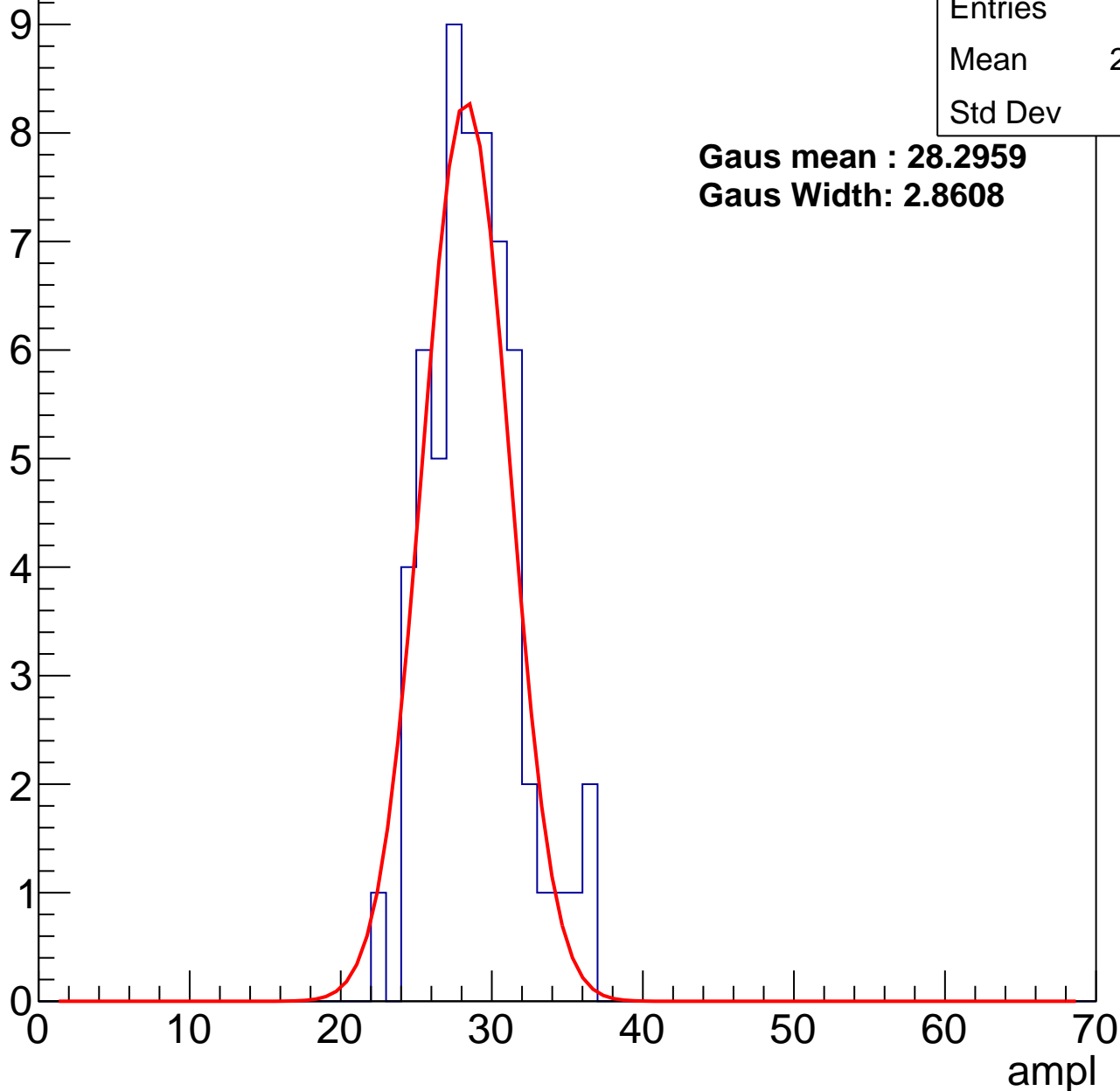
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.38
Std Dev	2.96

**Gaus mean : 28.2959**

**Gaus Width: 2.8608**



# B1L000S, U24-ch56, adc1

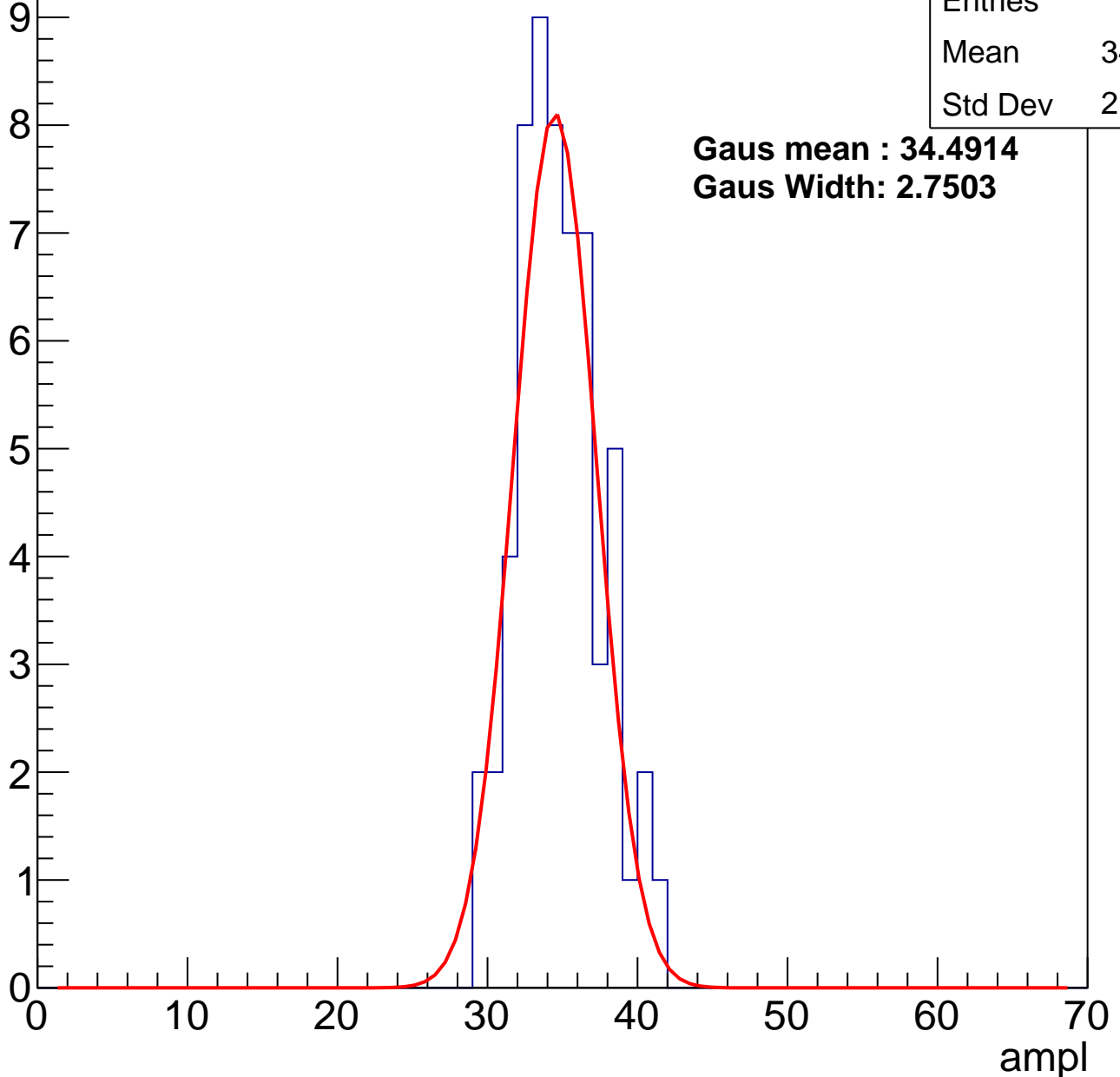
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	34.32
Std Dev	2.734

**Gaus mean : 34.4914**

**Gaus Width: 2.7503**



# B1L000S, U24-ch56, adc2

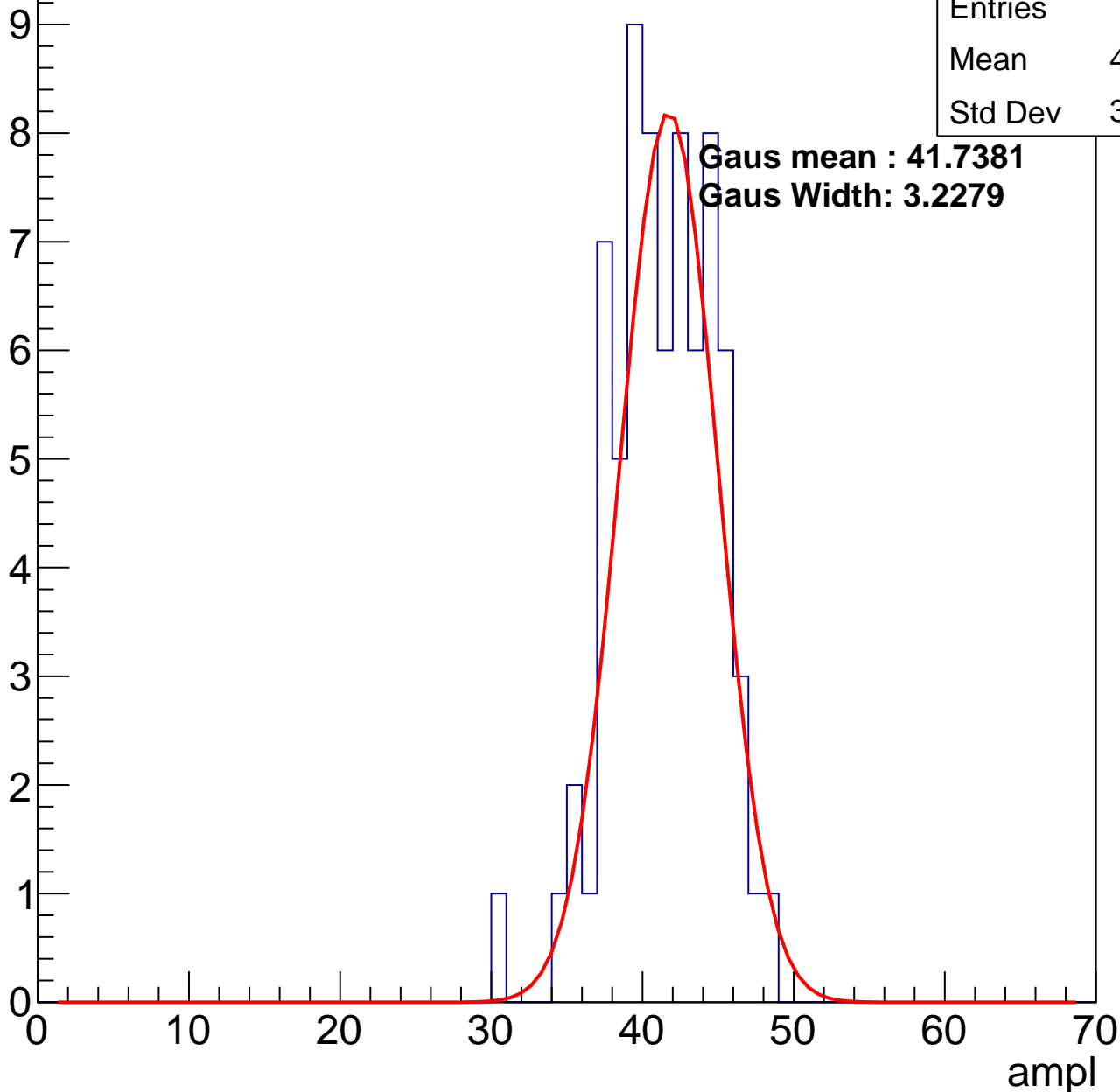
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	40.89
Std Dev	3.366

**Gaus mean : 41.7381**

**Gaus Width: 3.2279**



# B1L000S, U24-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

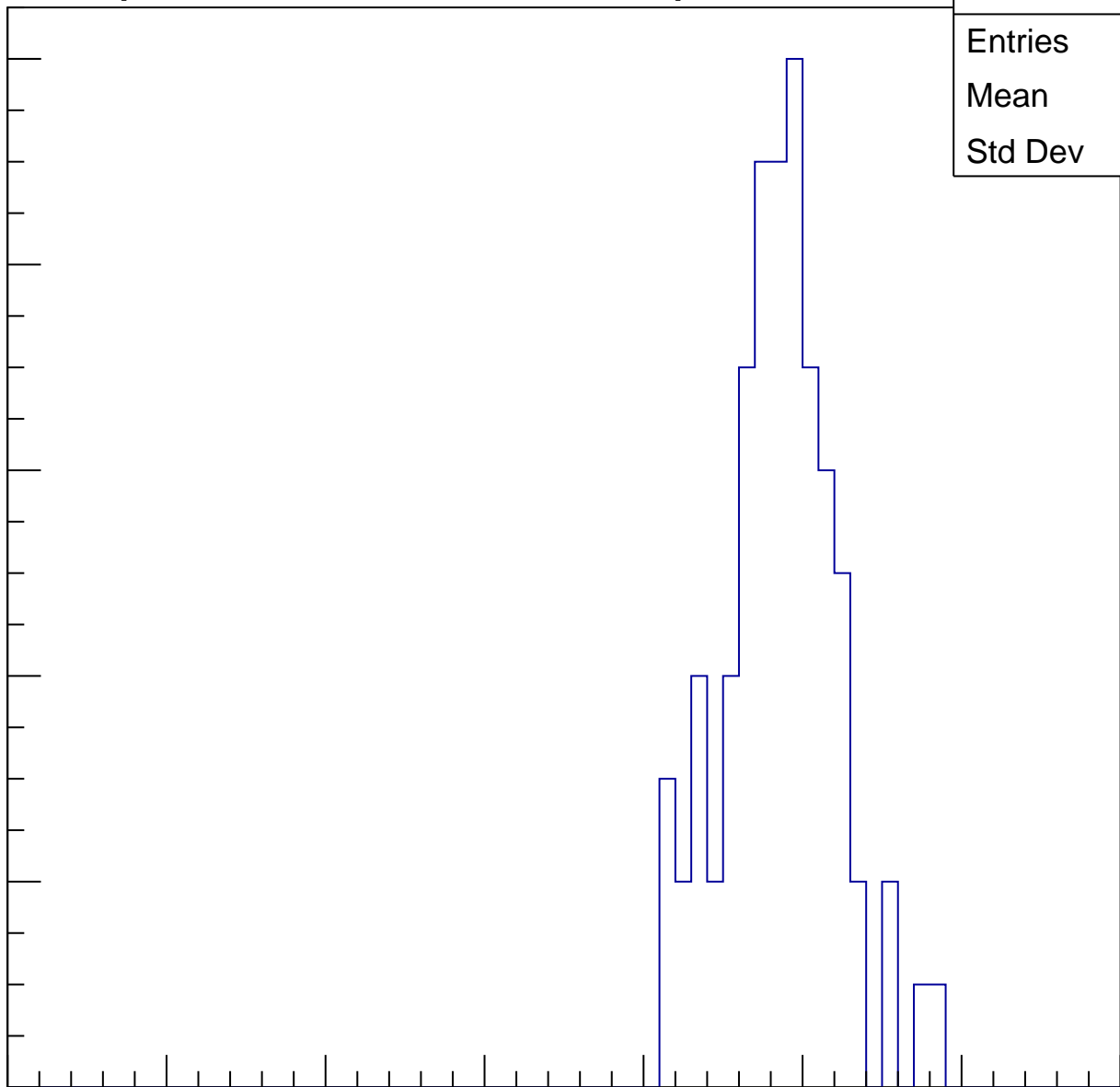
Entries	74
Mean	48.12
Std Dev	3.533

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

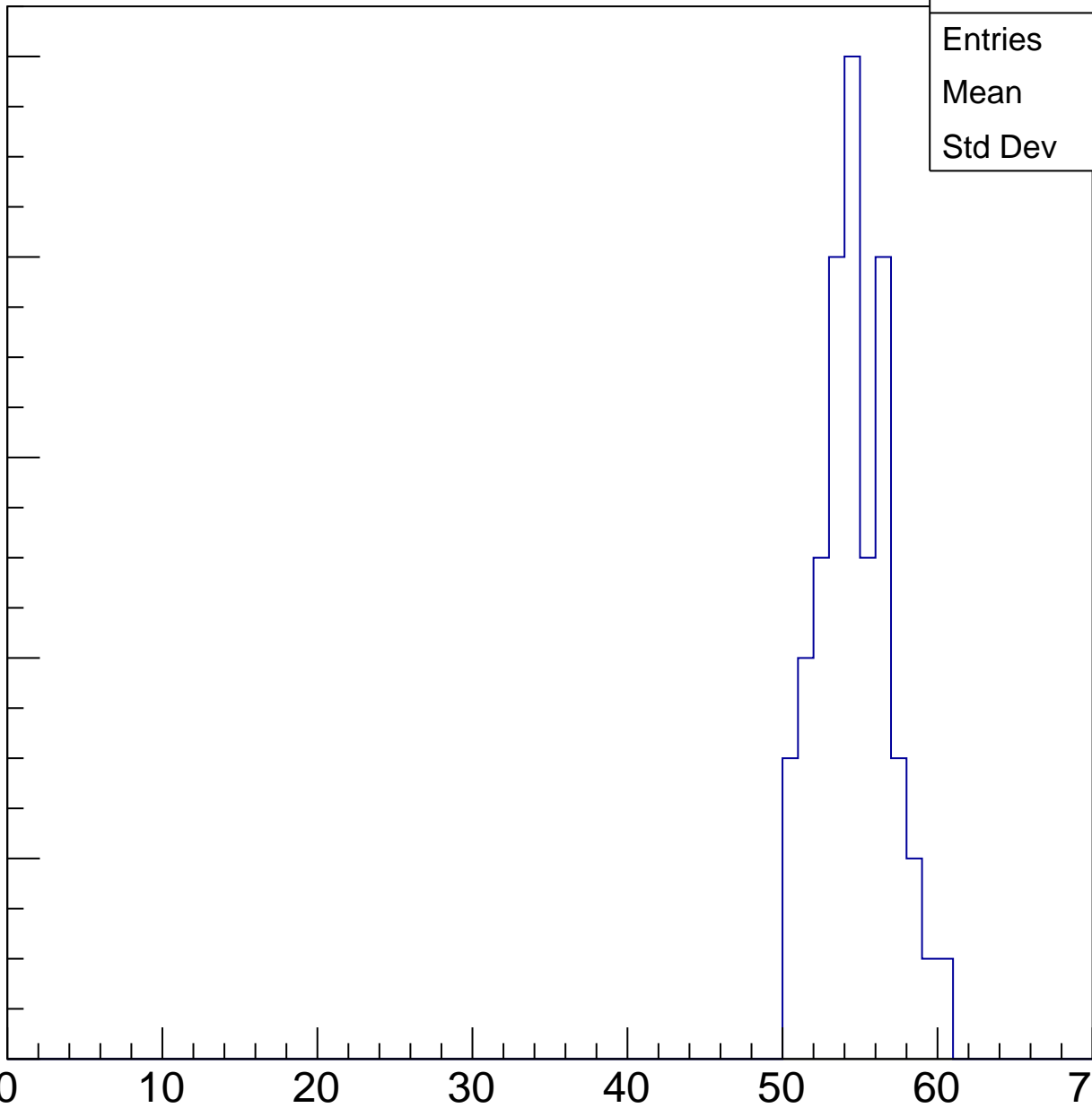
Entries	50
Mean	54.14
Std Dev	2.315

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch56, adc5

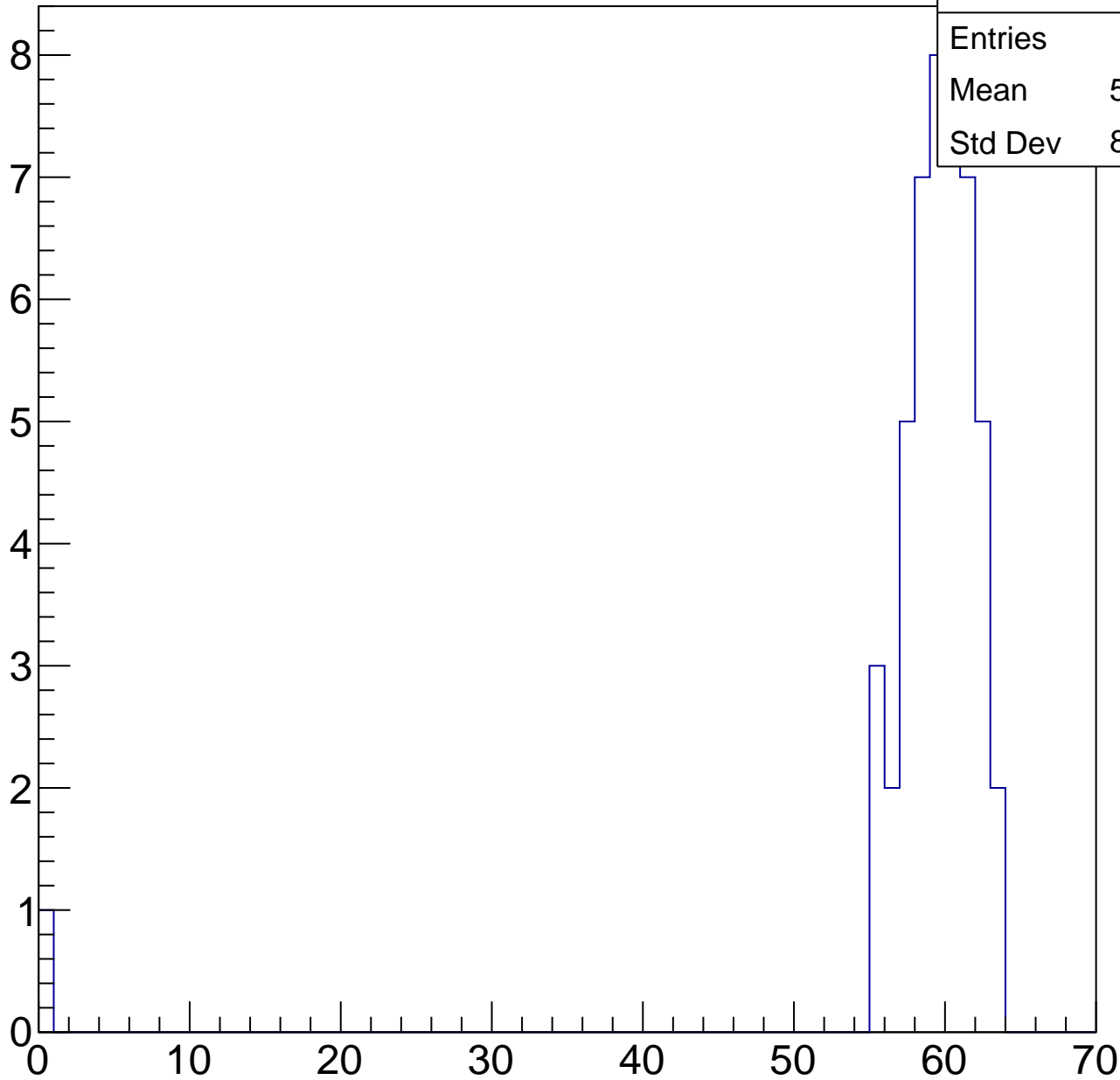
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	57.98
Std Dev	8.705

ampl

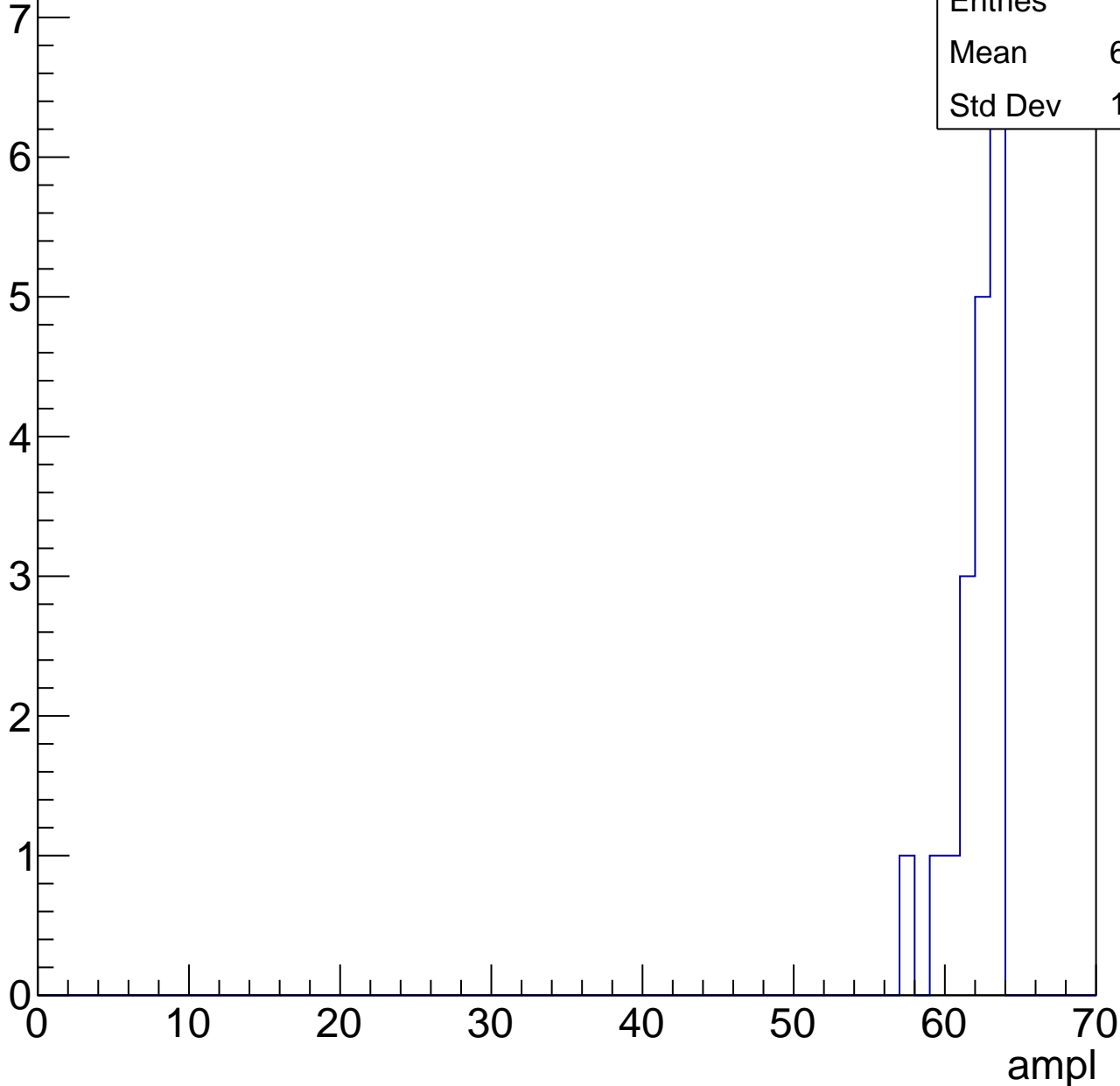


# B1L000S, U24-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.67
Std Dev	1.599

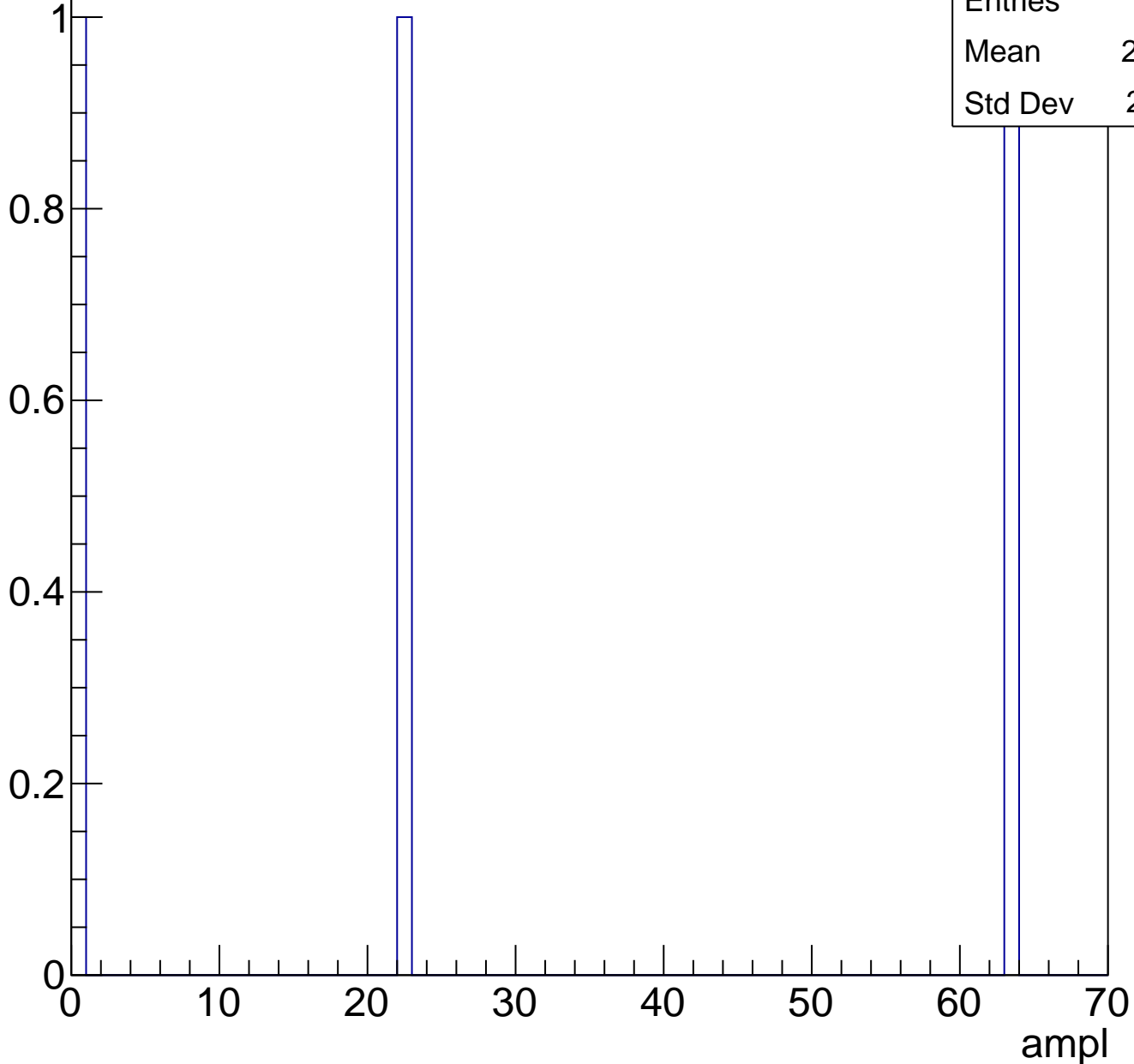




# B1L000S, U24-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L000S, U24-ch57, adc0

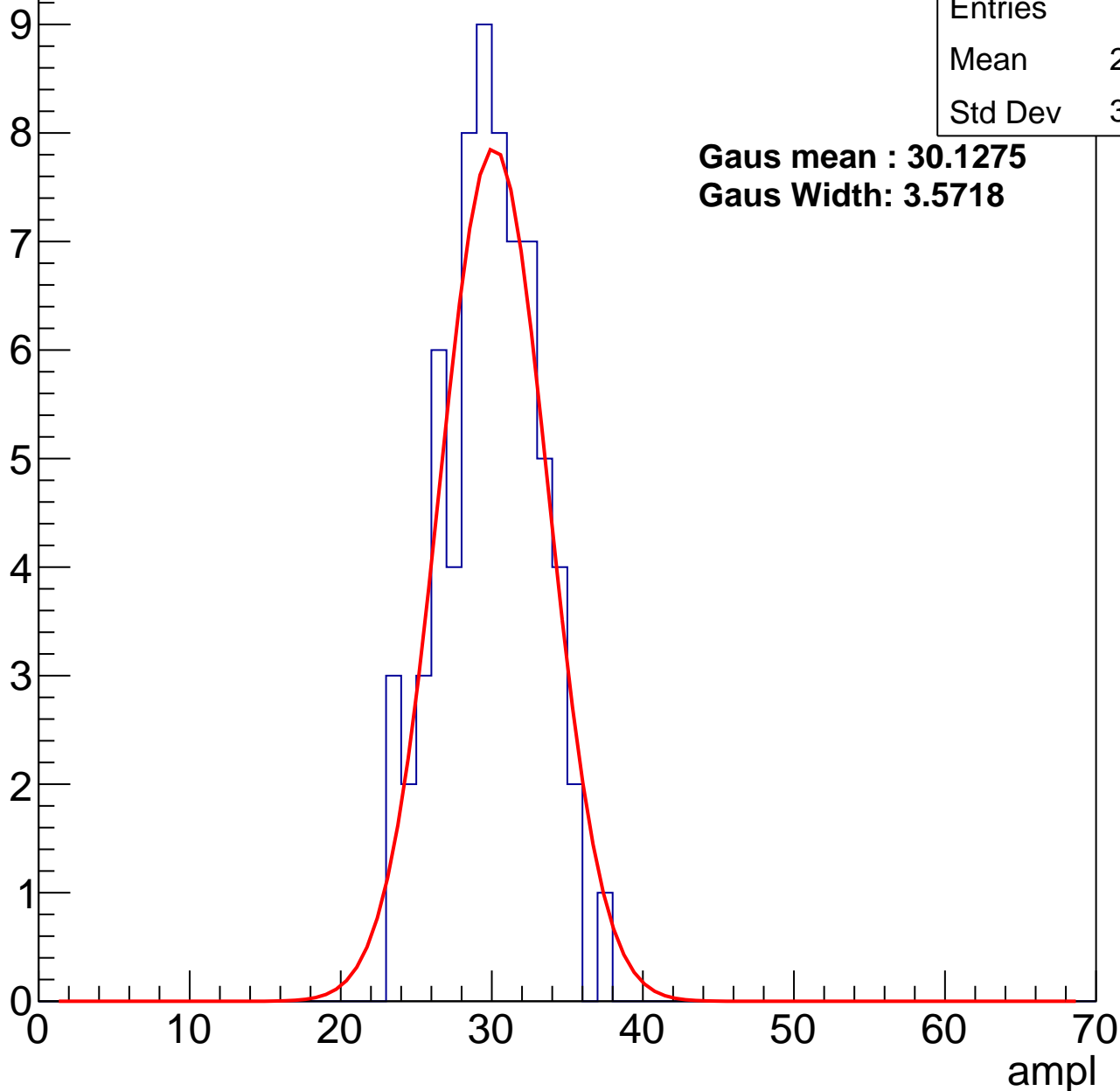
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.42
Std Dev	3.155

**Gaus mean : 30.1275**

**Gaus Width: 3.5718**



# B1L000S, U24-ch57, adc1

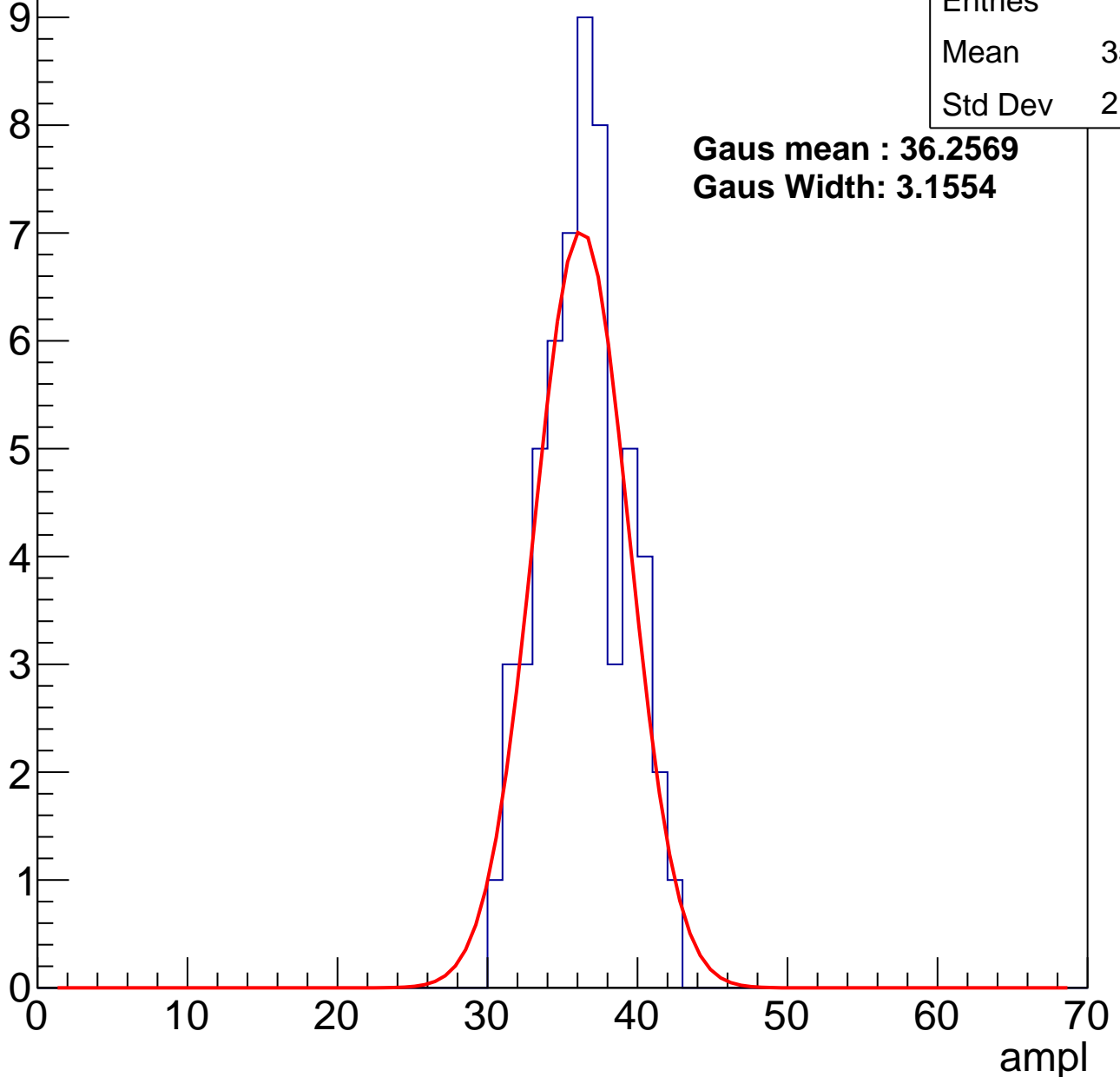
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.89
Std Dev	2.808

**Gaus mean : 36.2569**

**Gaus Width: 3.1554**



# B1L000S, U24-ch57, adc2

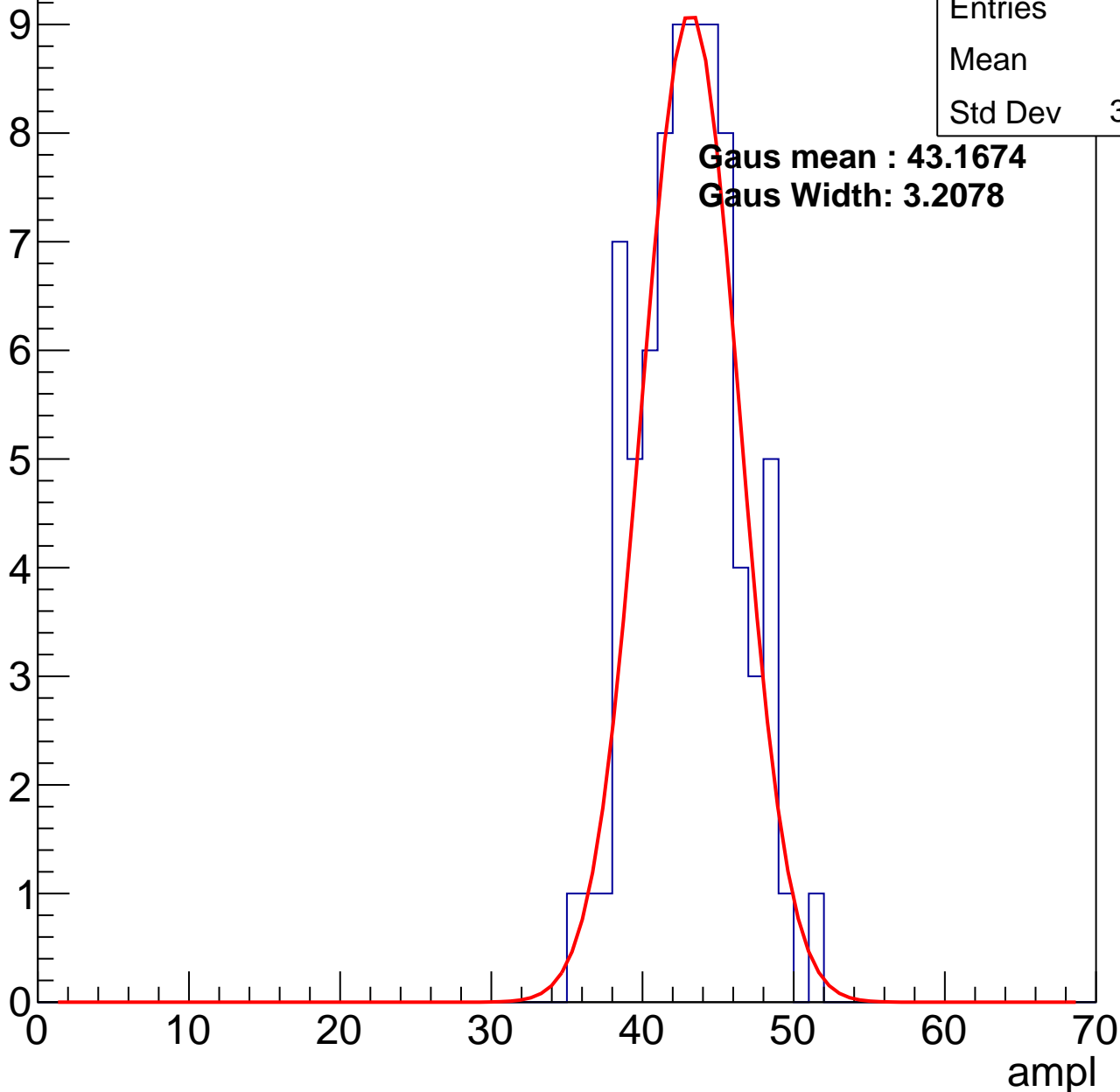
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	42.6
Std Dev	3.275

**Gaus mean : 43.1674**

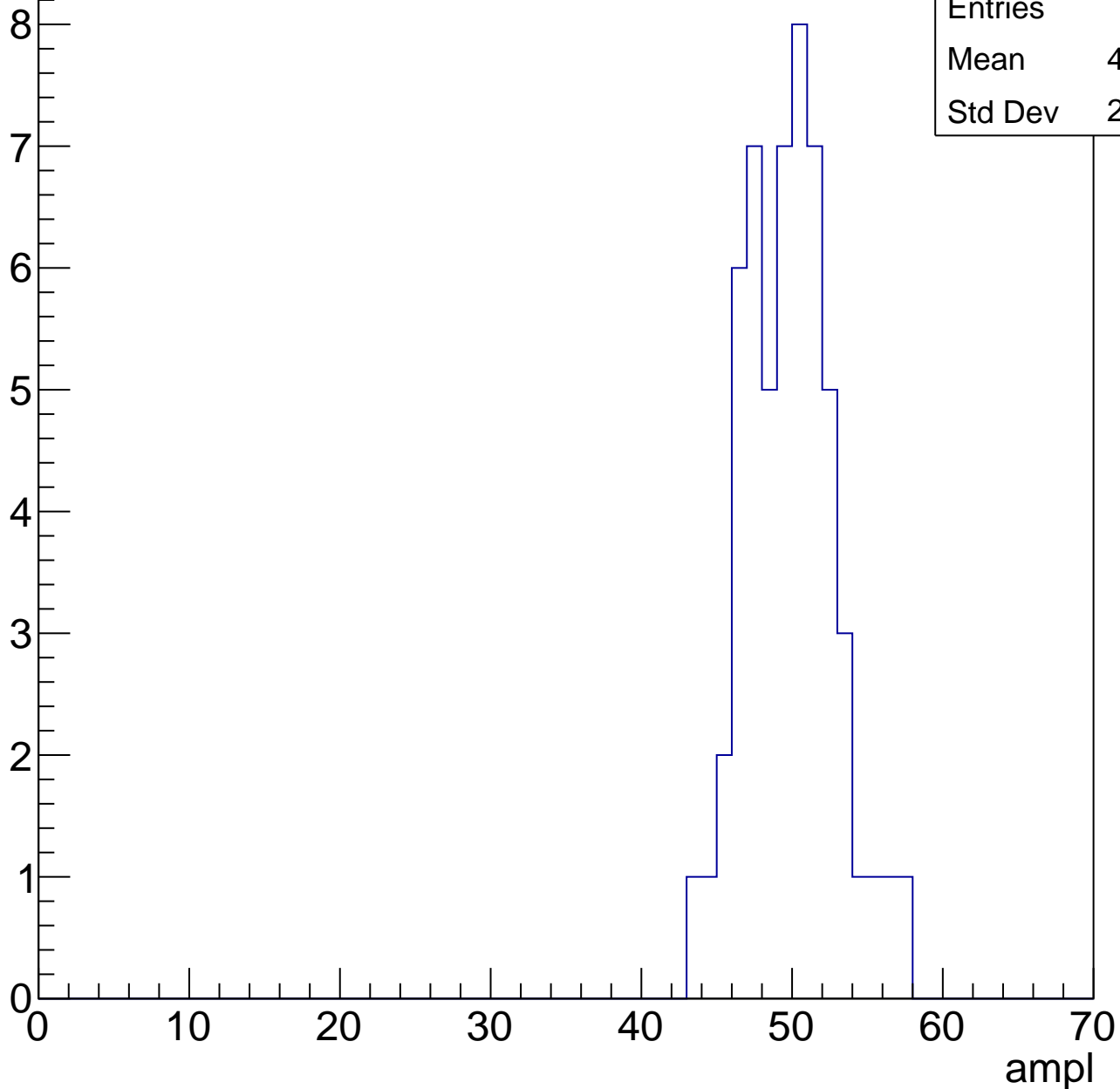
**Gaus Width: 3.2078**



# B1L000S, U24-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



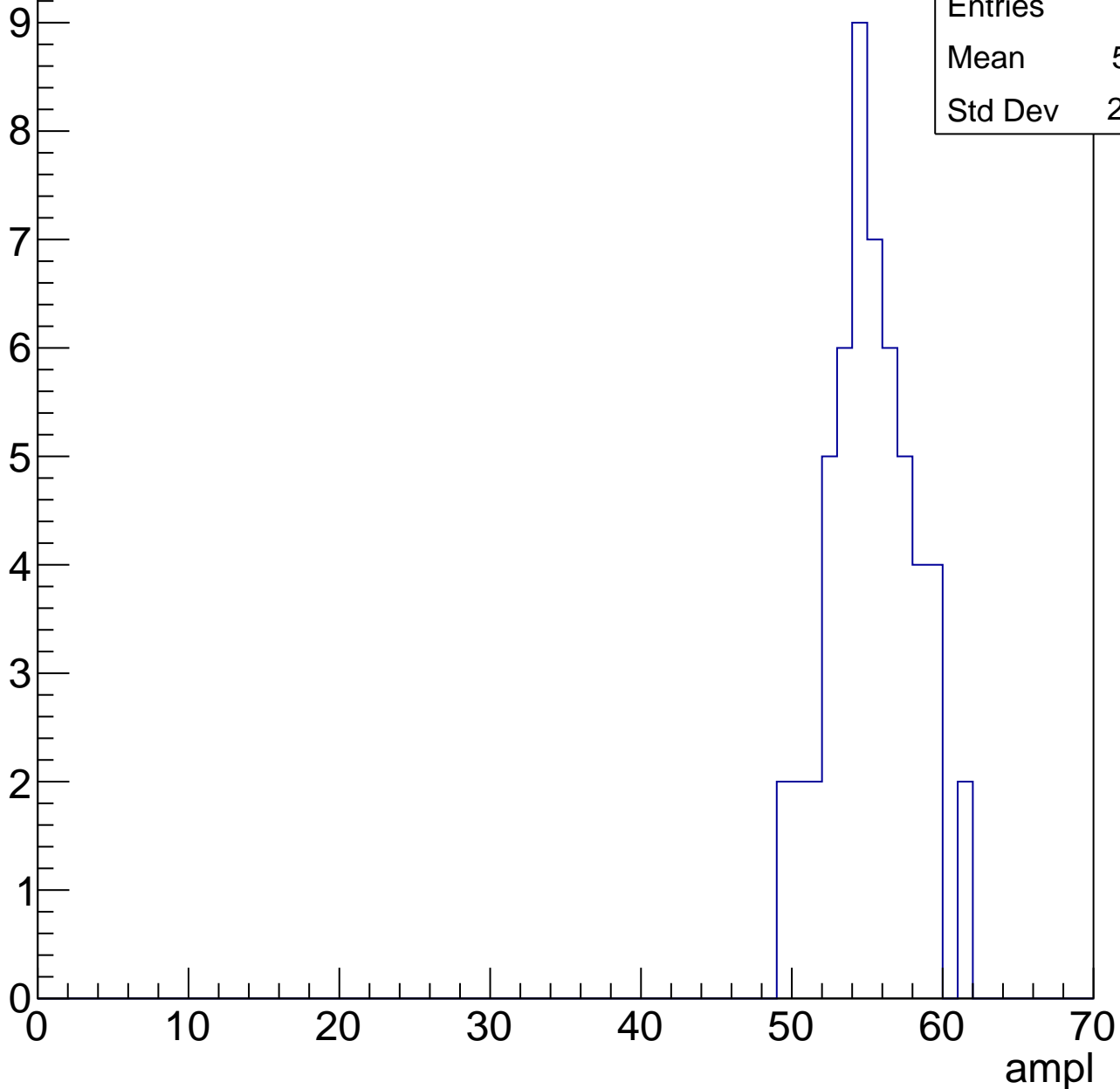
Entries	56
Mean	49.34
Std Dev	2.917

# B1L000S, U24-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.81
Std Dev	2.816

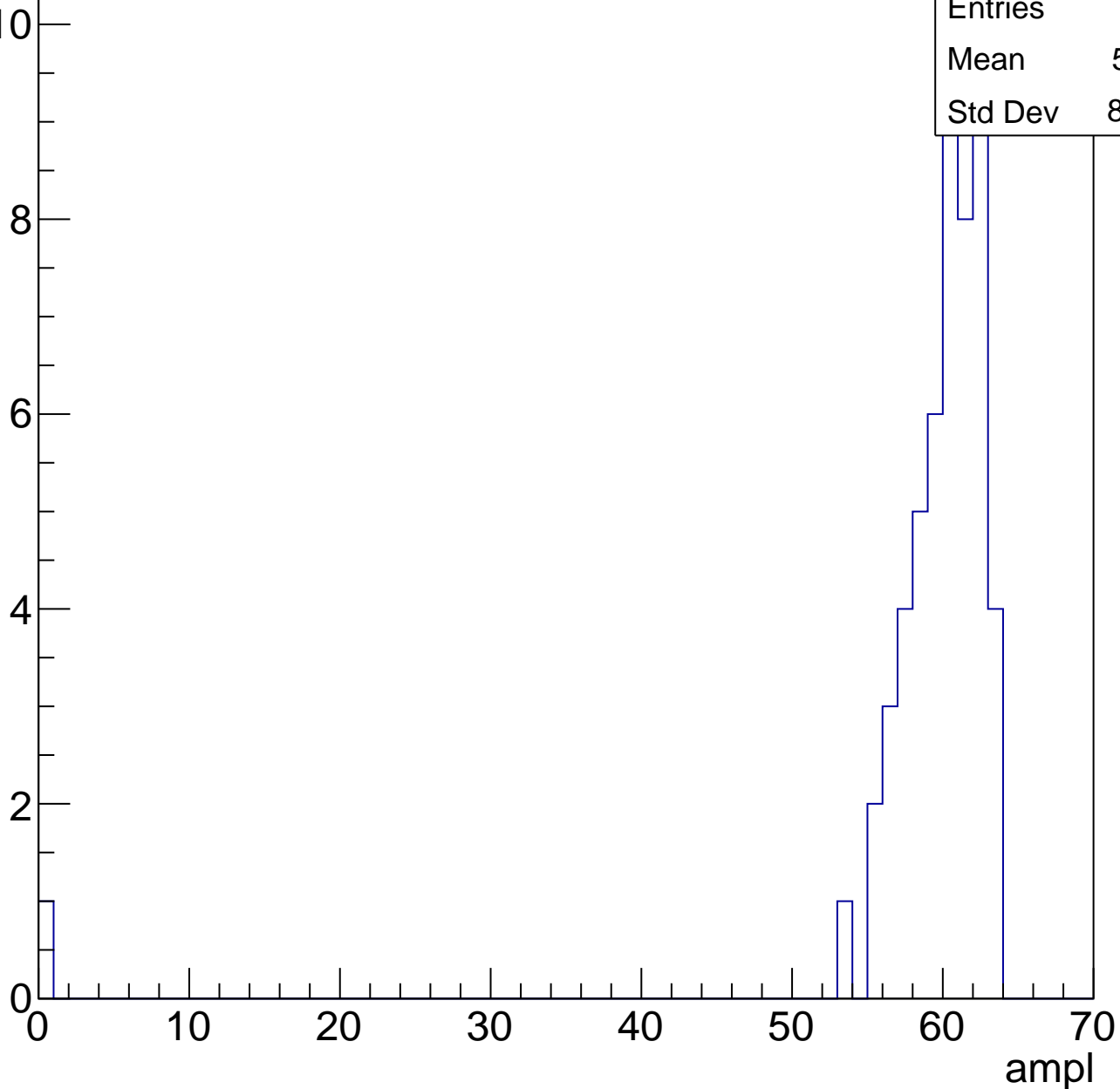


# B1L000S, U24-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.51
Std Dev	8.435

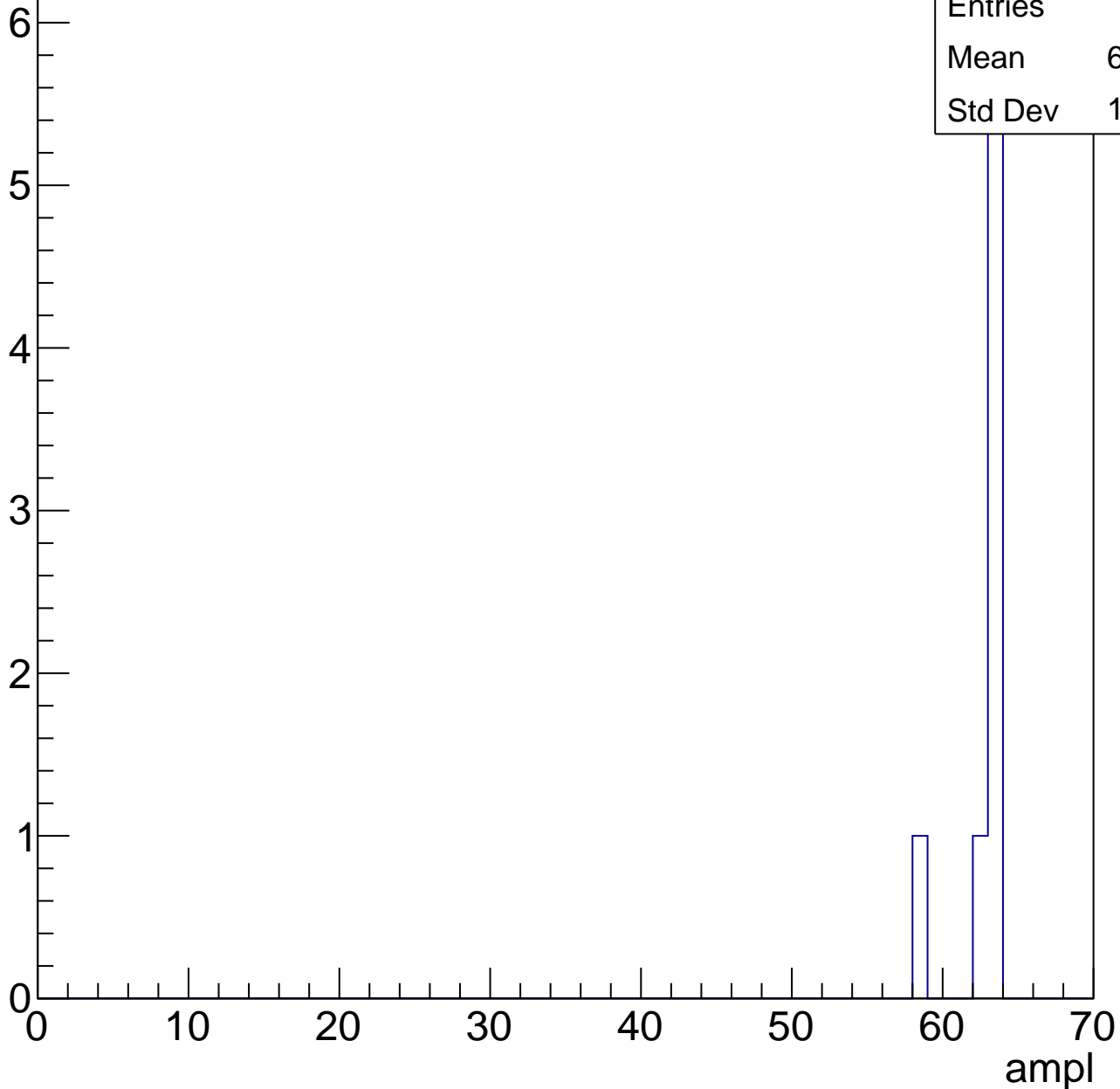


# B1L000S, U24-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.25
Std Dev	1.639





# B1L000S, U24-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch58, adc0

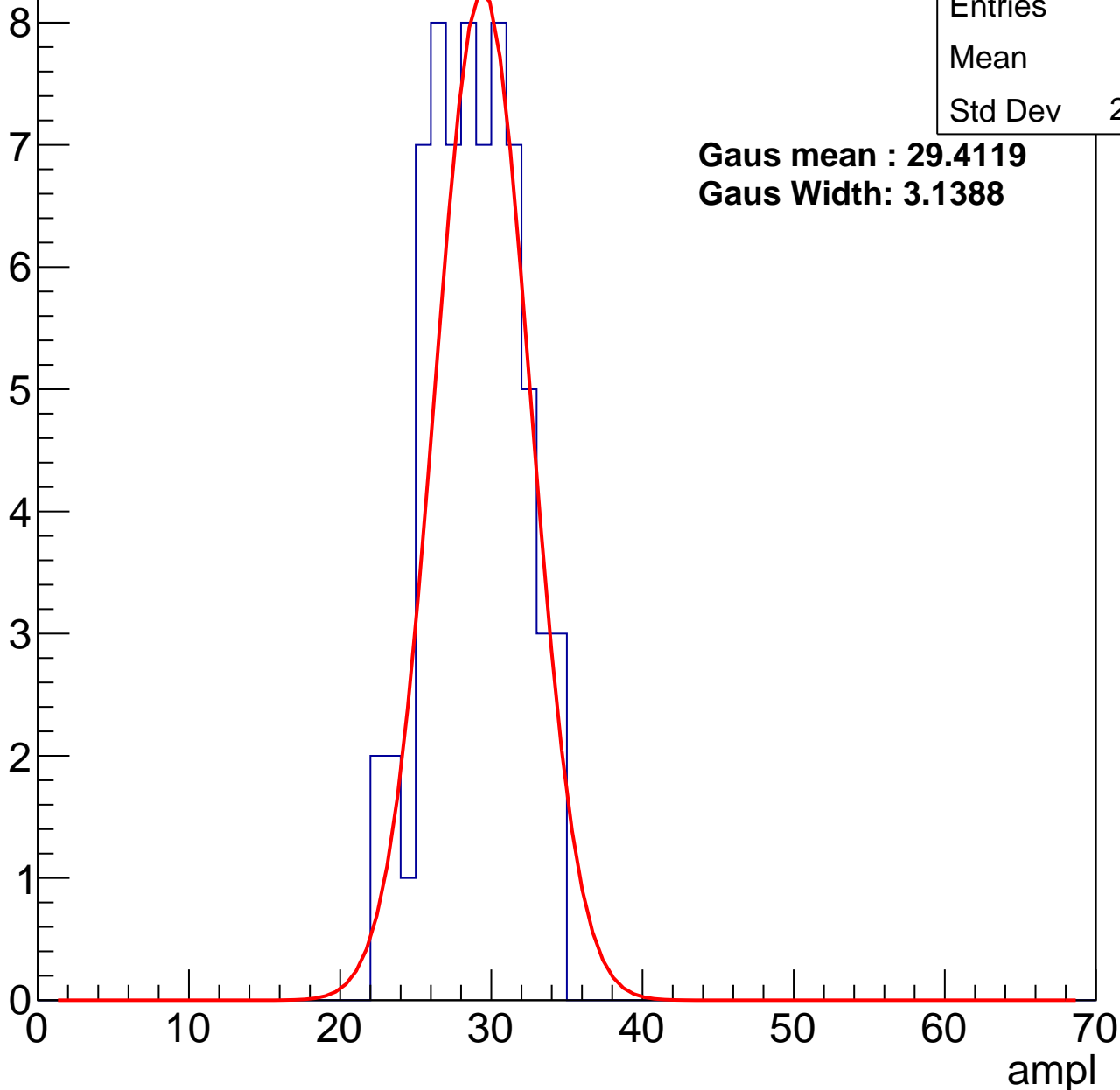
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.4
Std Dev	2.956

**Gaus mean : 29.4119**

**Gaus Width: 3.1388**



# B1L000S, U24-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	34.8
Std Dev	3.198

**Gaus mean : 35.2131**

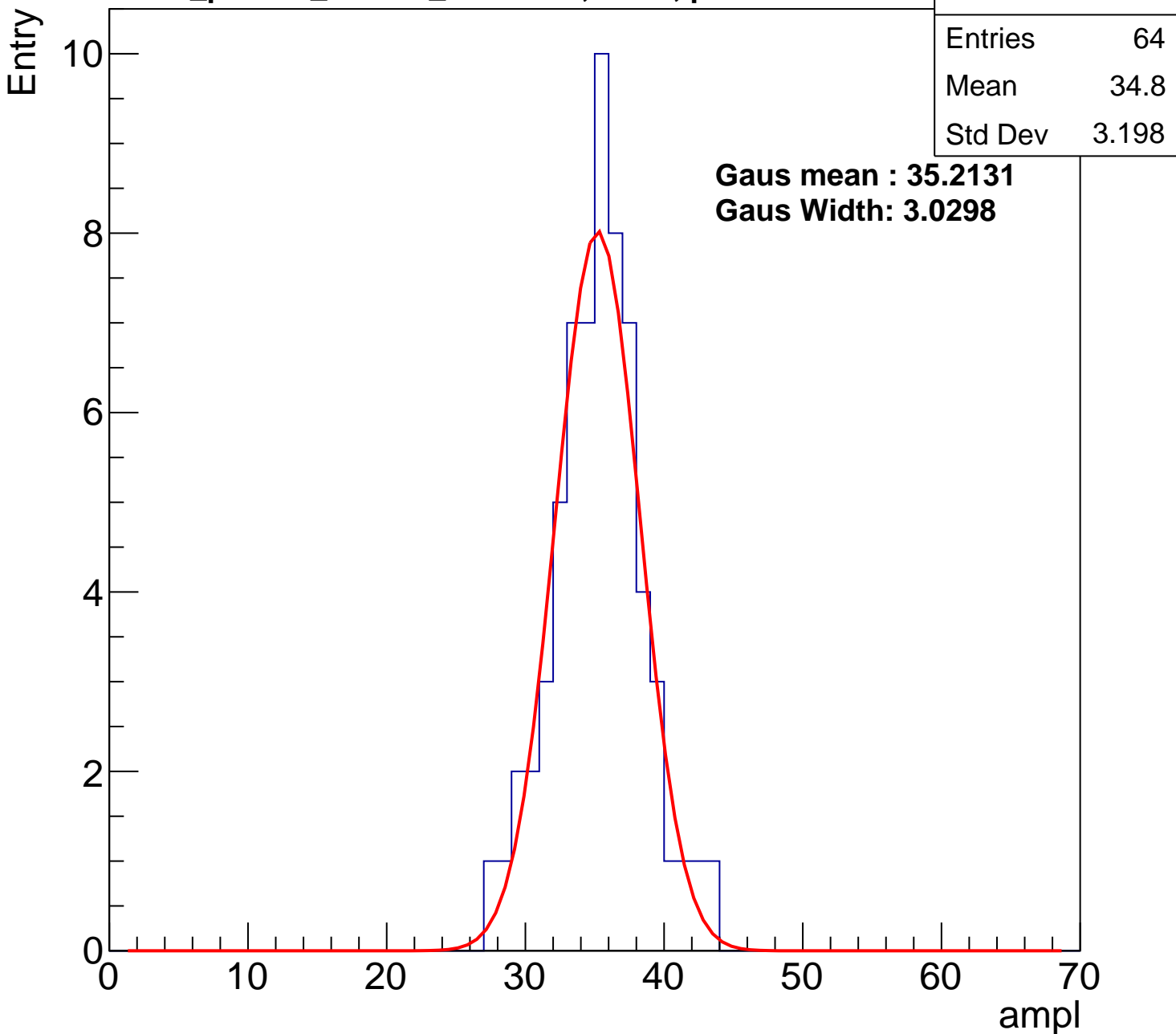
**Gaus Width: 3.0298**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch58, adc2

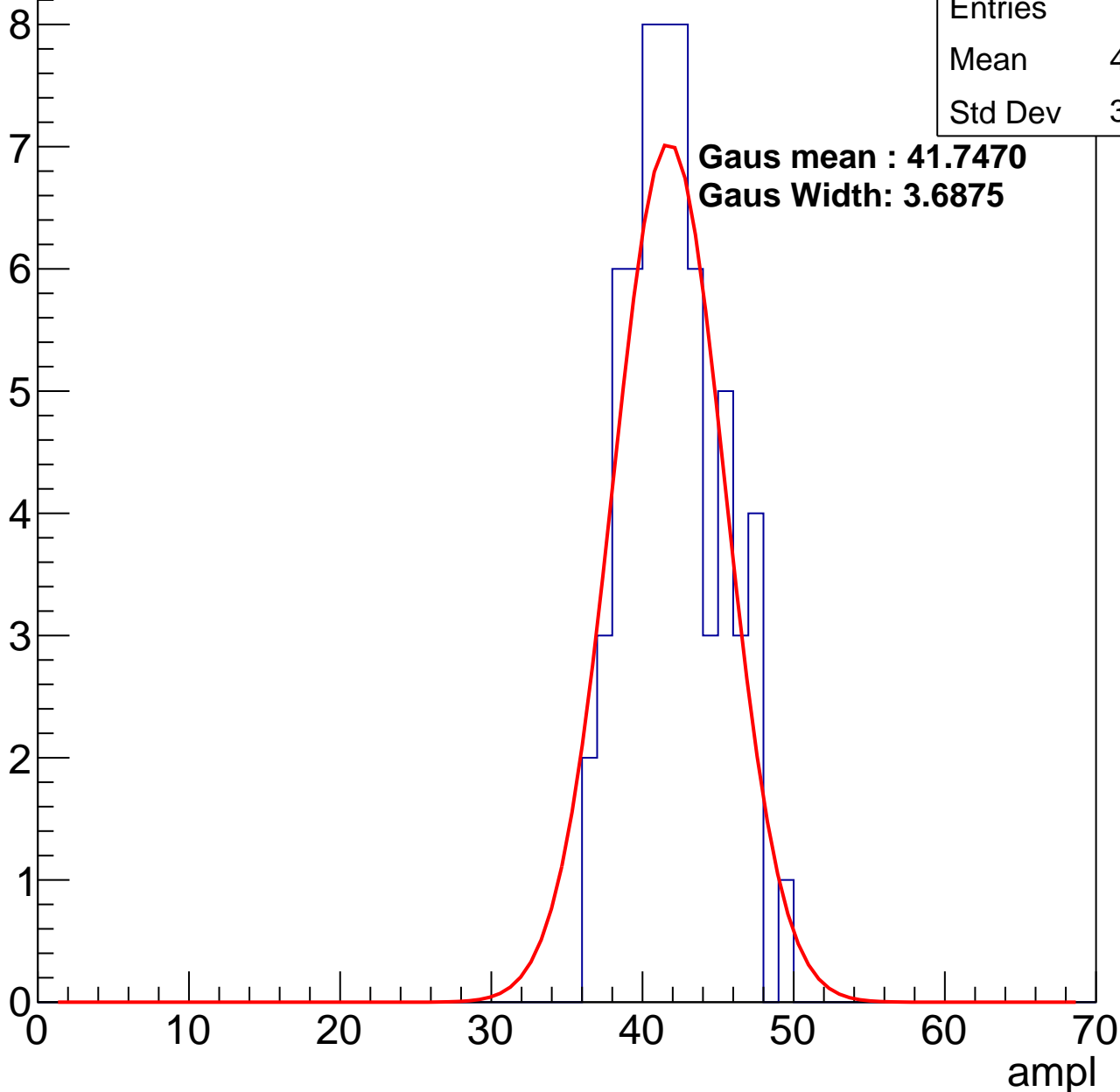
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.57
Std Dev	3.048

**Gaus mean : 41.7470**

**Gaus Width: 3.6875**

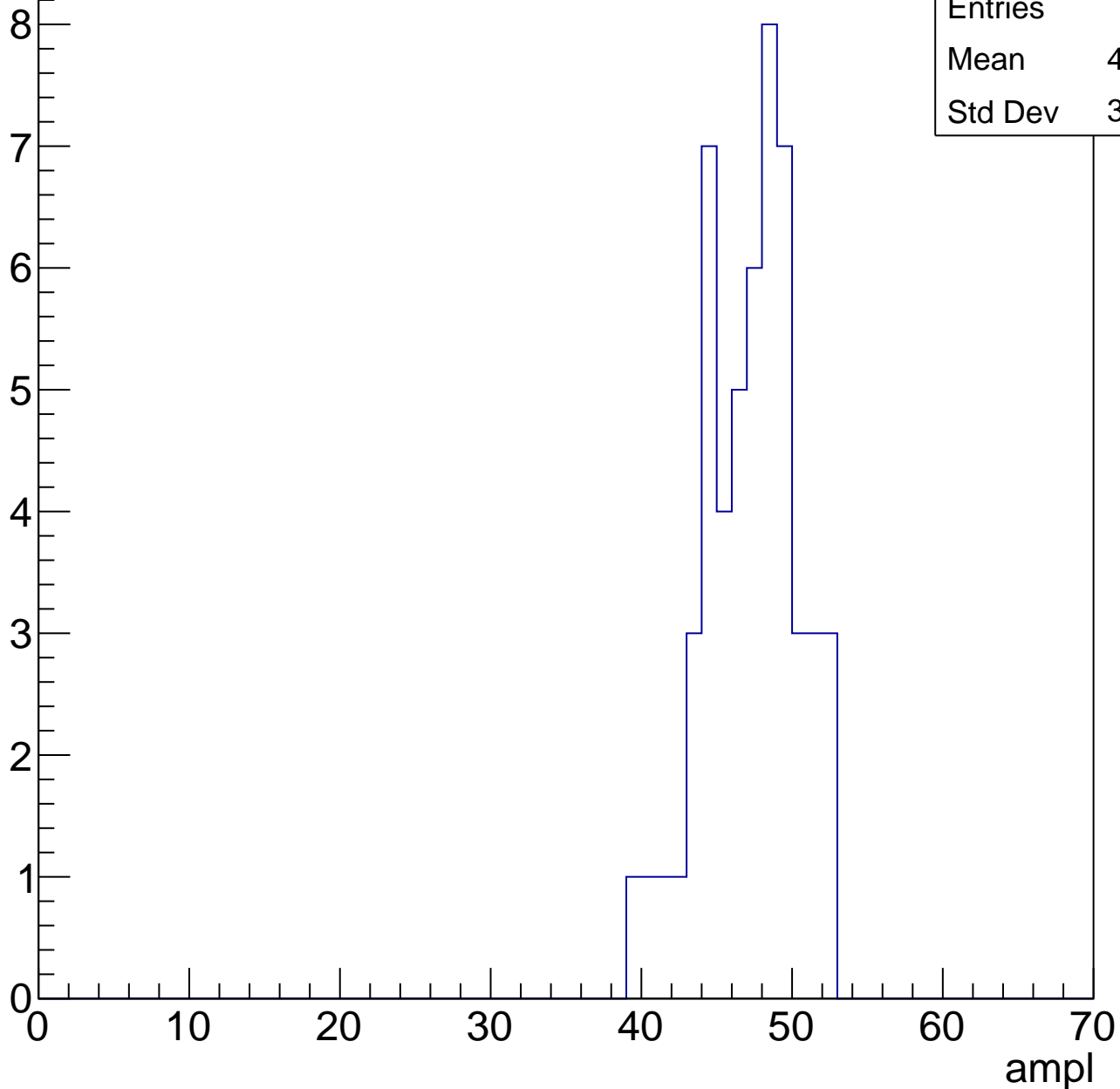


# B1L000S, U24-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

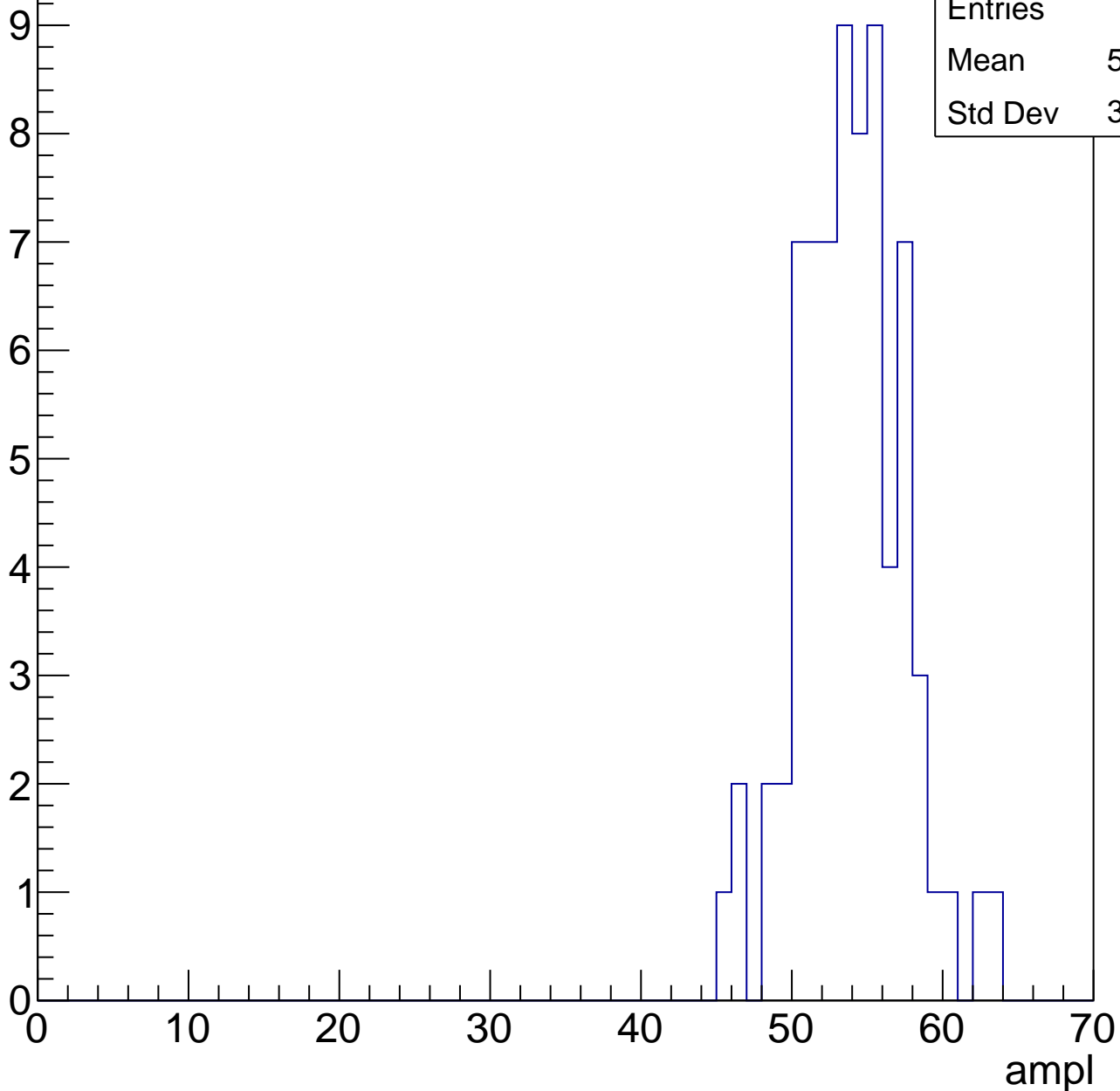
Entries	53
Mean	46.74
Std Dev	3.035



# B1L000S, U24-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

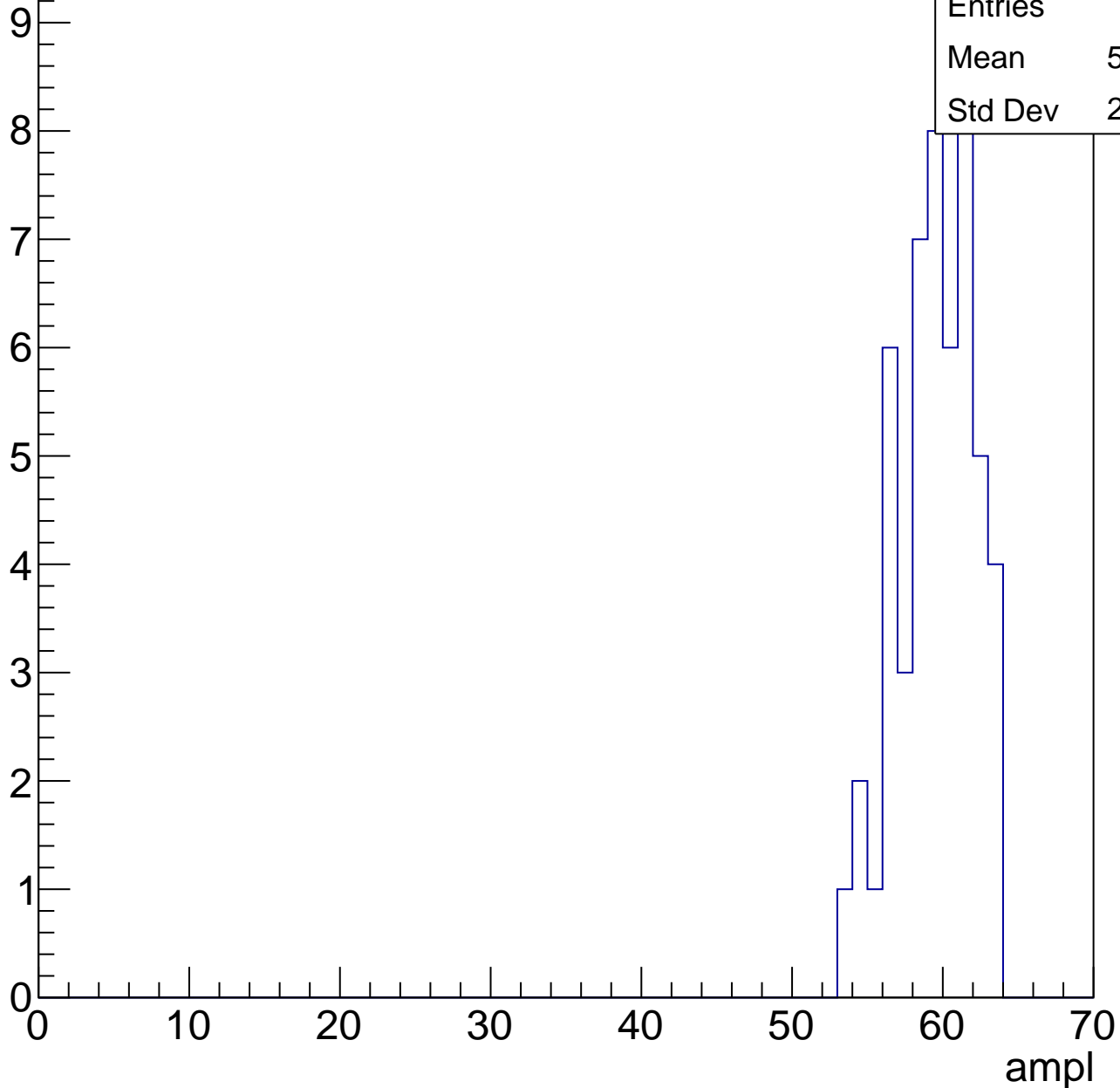
Entry



# B1L000S, U24-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

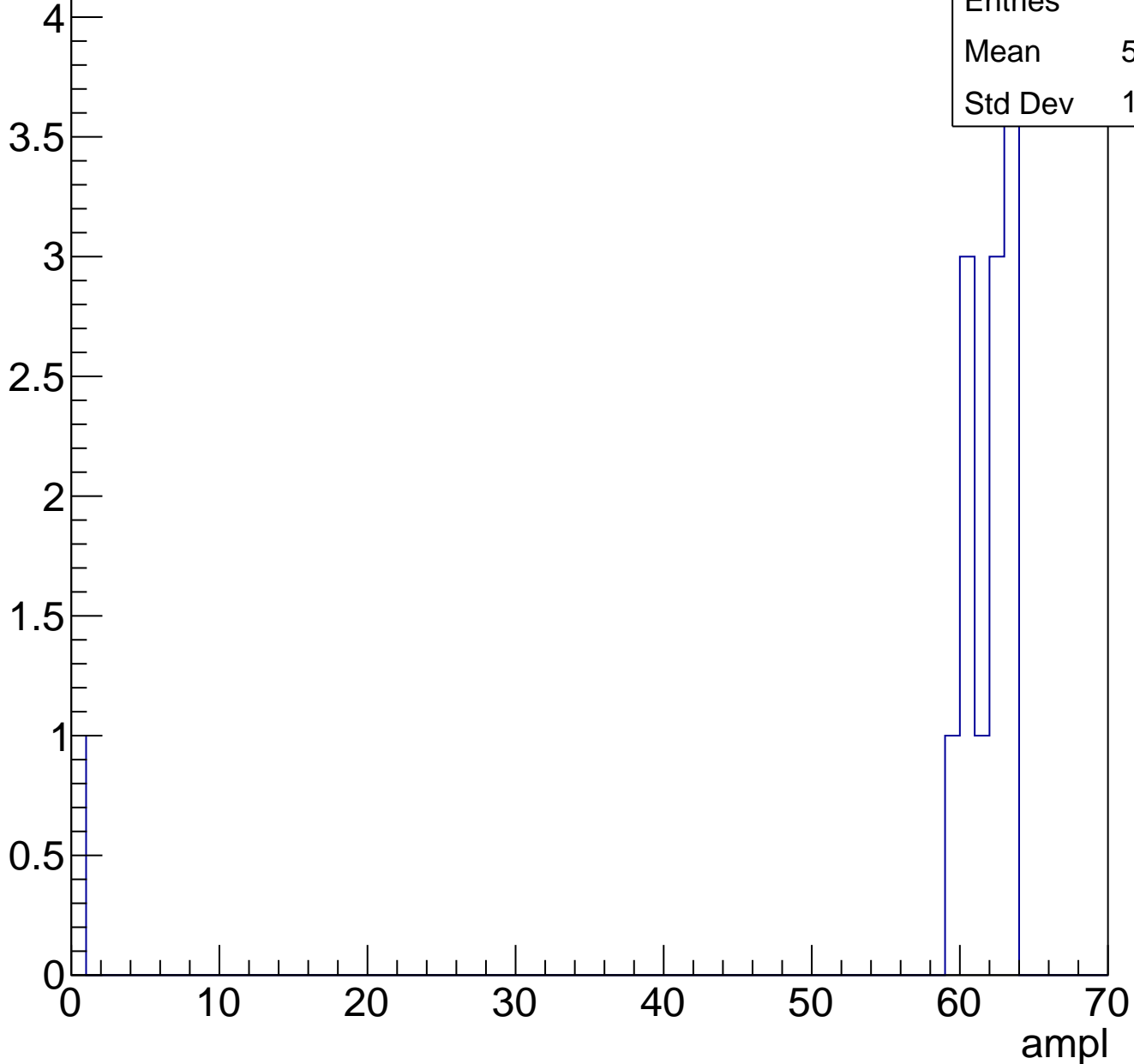
Entry



# B1L000S, U24-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	55
Mean	29.4
Std Dev	5.047

**Gaus mean : 30.4364**

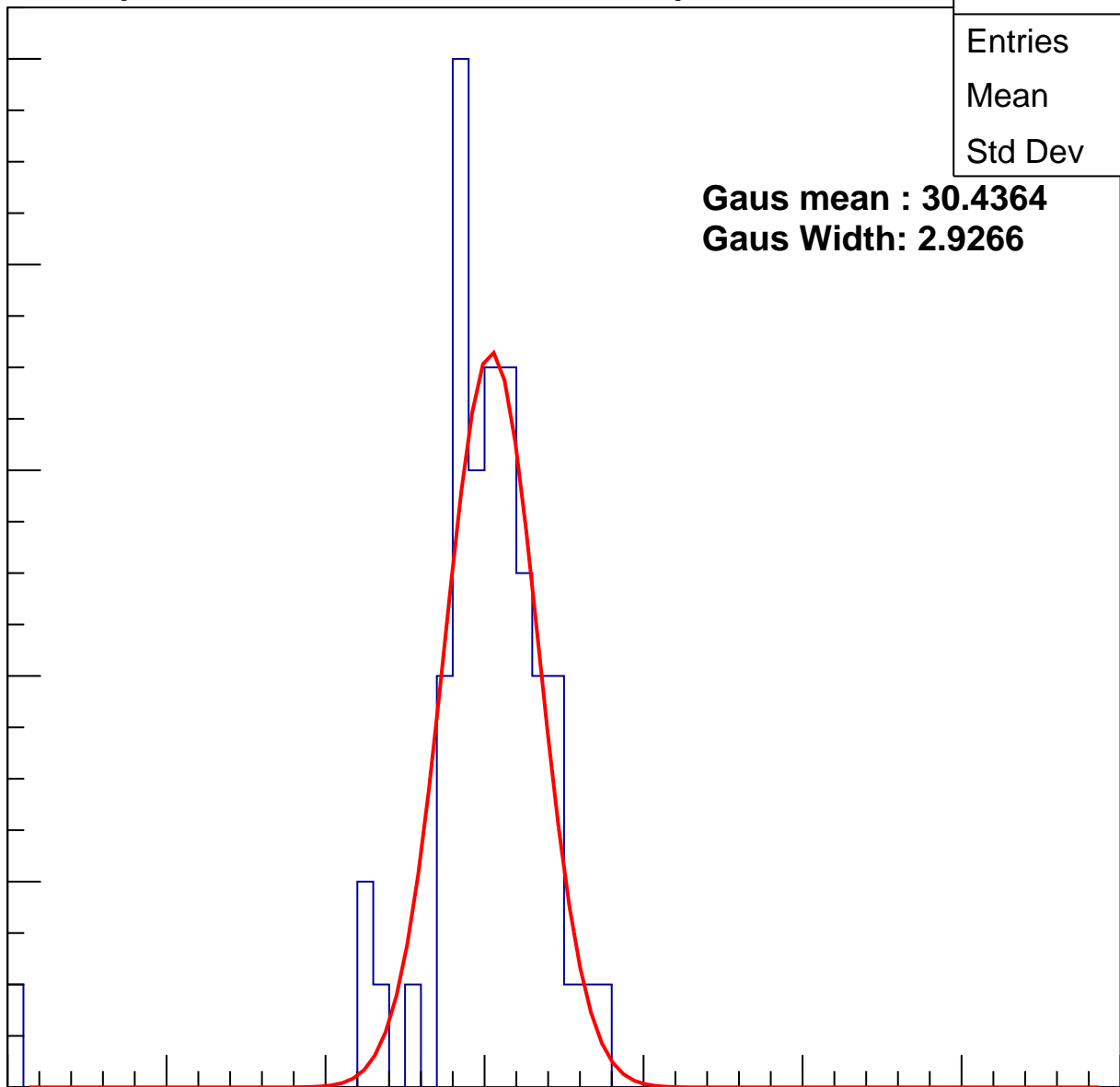
**Gaus Width: 2.9266**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch59, adc1

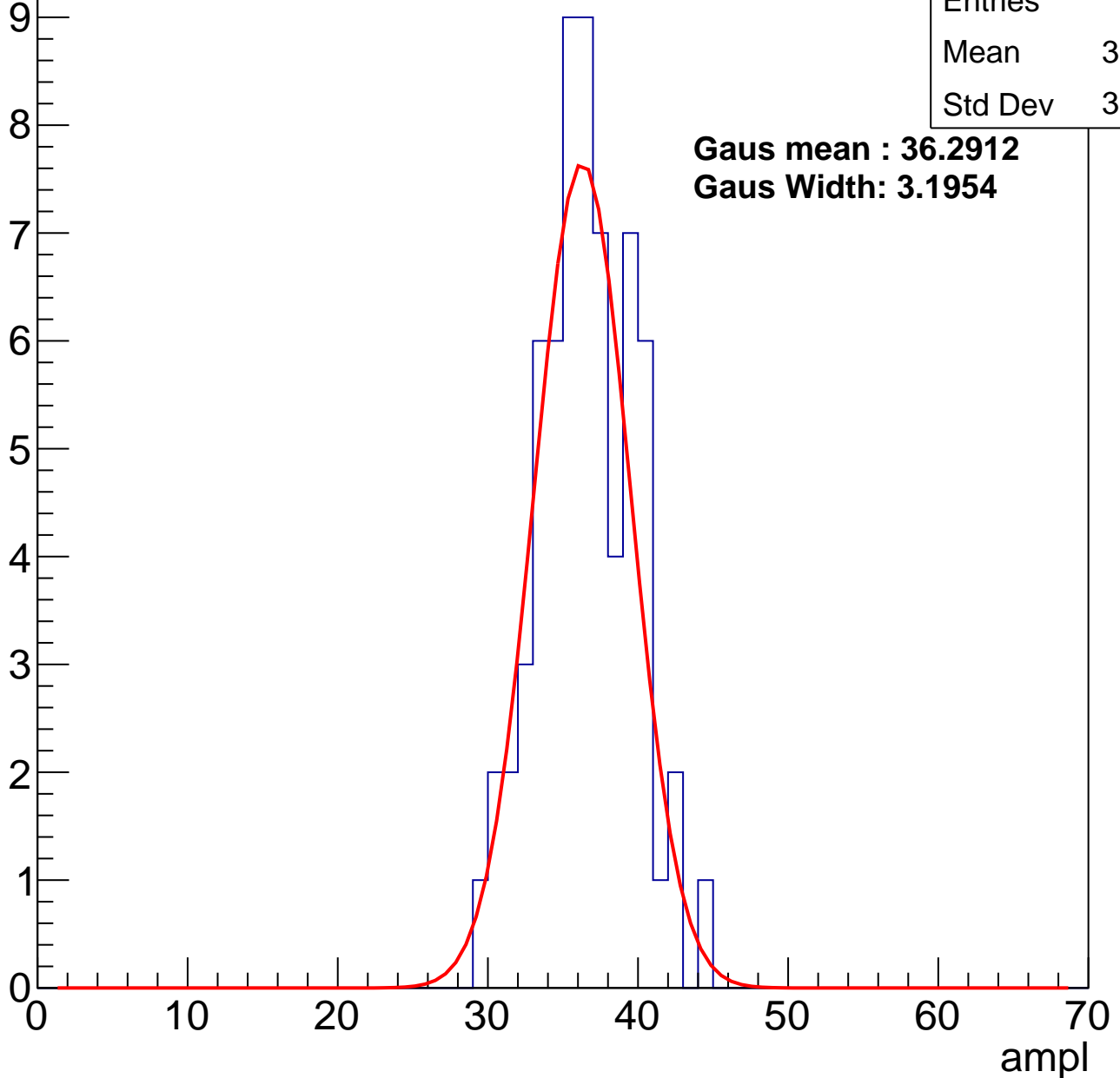
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	36.08
Std Dev	3.135

**Gaus mean : 36.2912**

**Gaus Width: 3.1954**



# B1L000S, U24-ch59, adc2

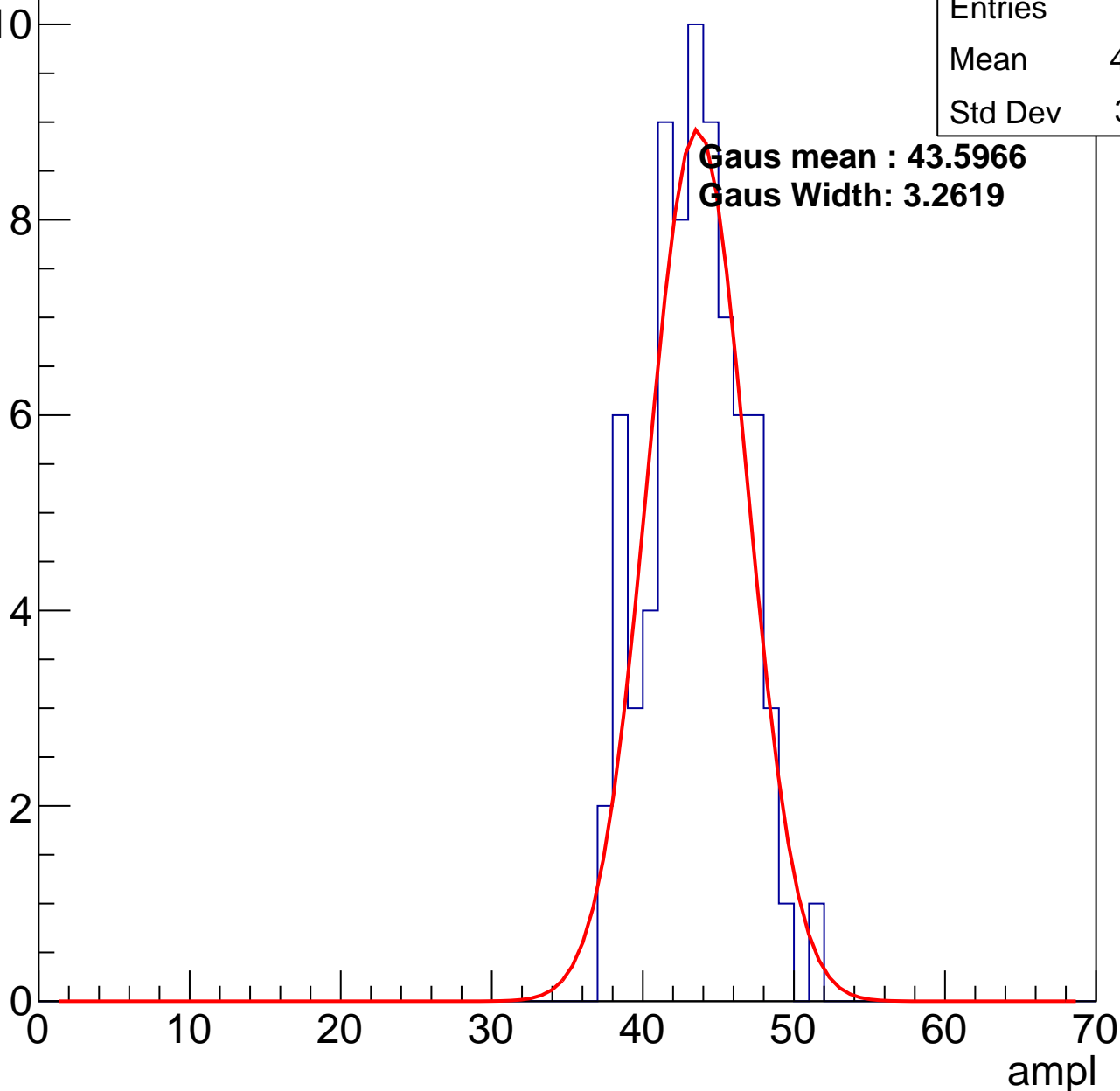
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	43.03
Std Dev	3.081

**Gaus mean : 43.5966**

**Gaus Width: 3.2619**

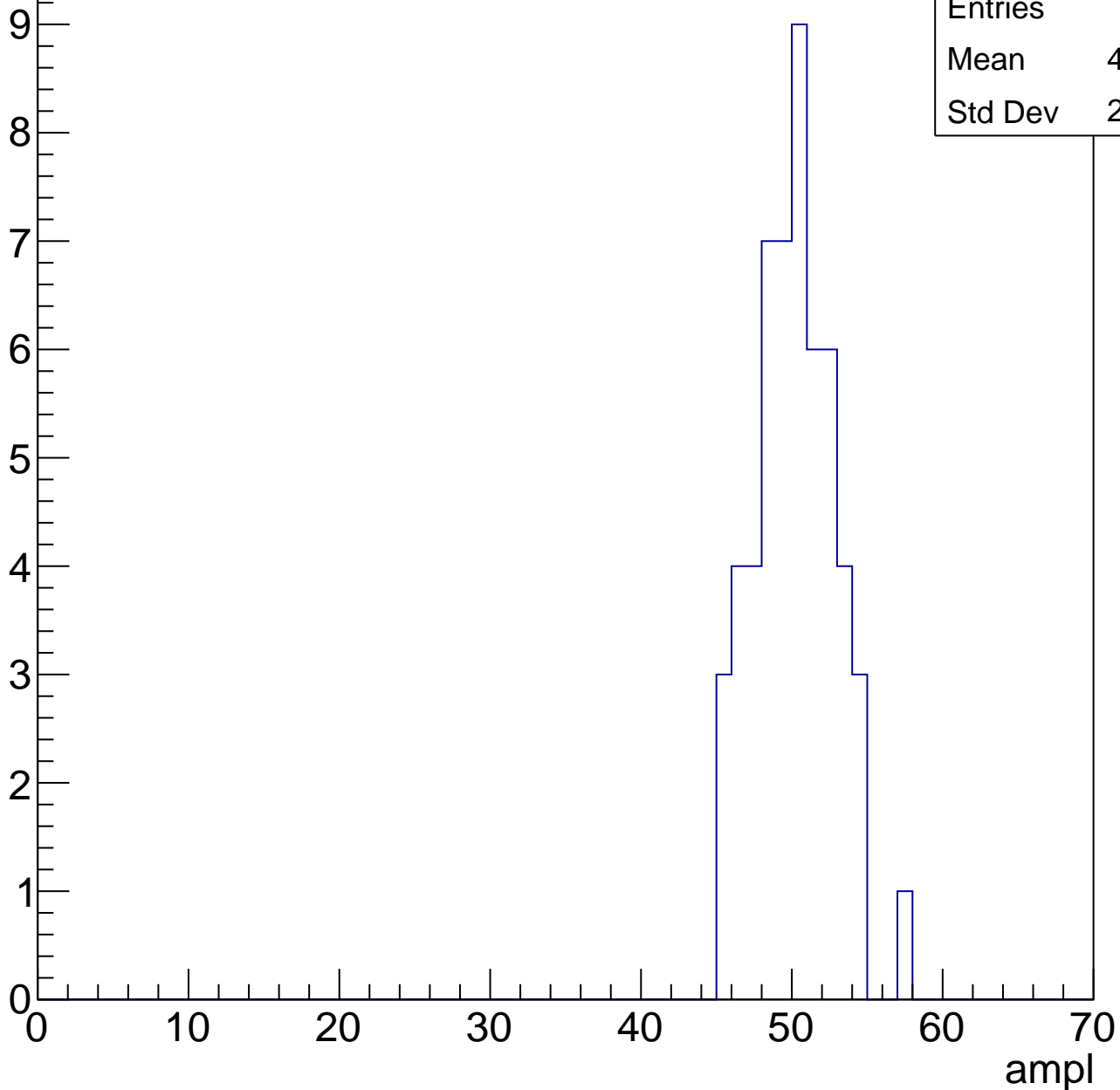


# B1L000S, U24-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

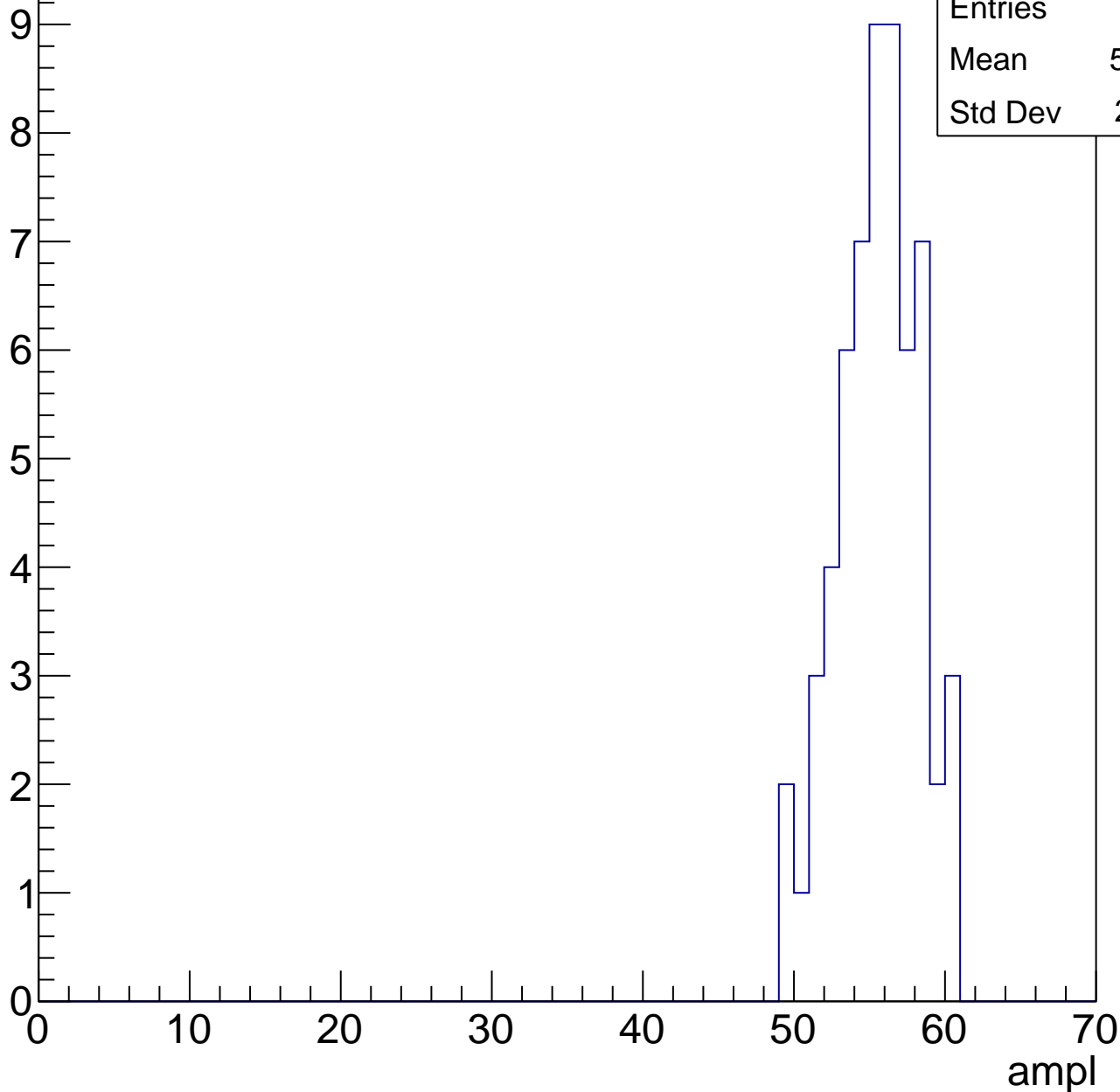
Entries	54
Mean	49.72
Std Dev	2.613



# B1L000S, U24-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

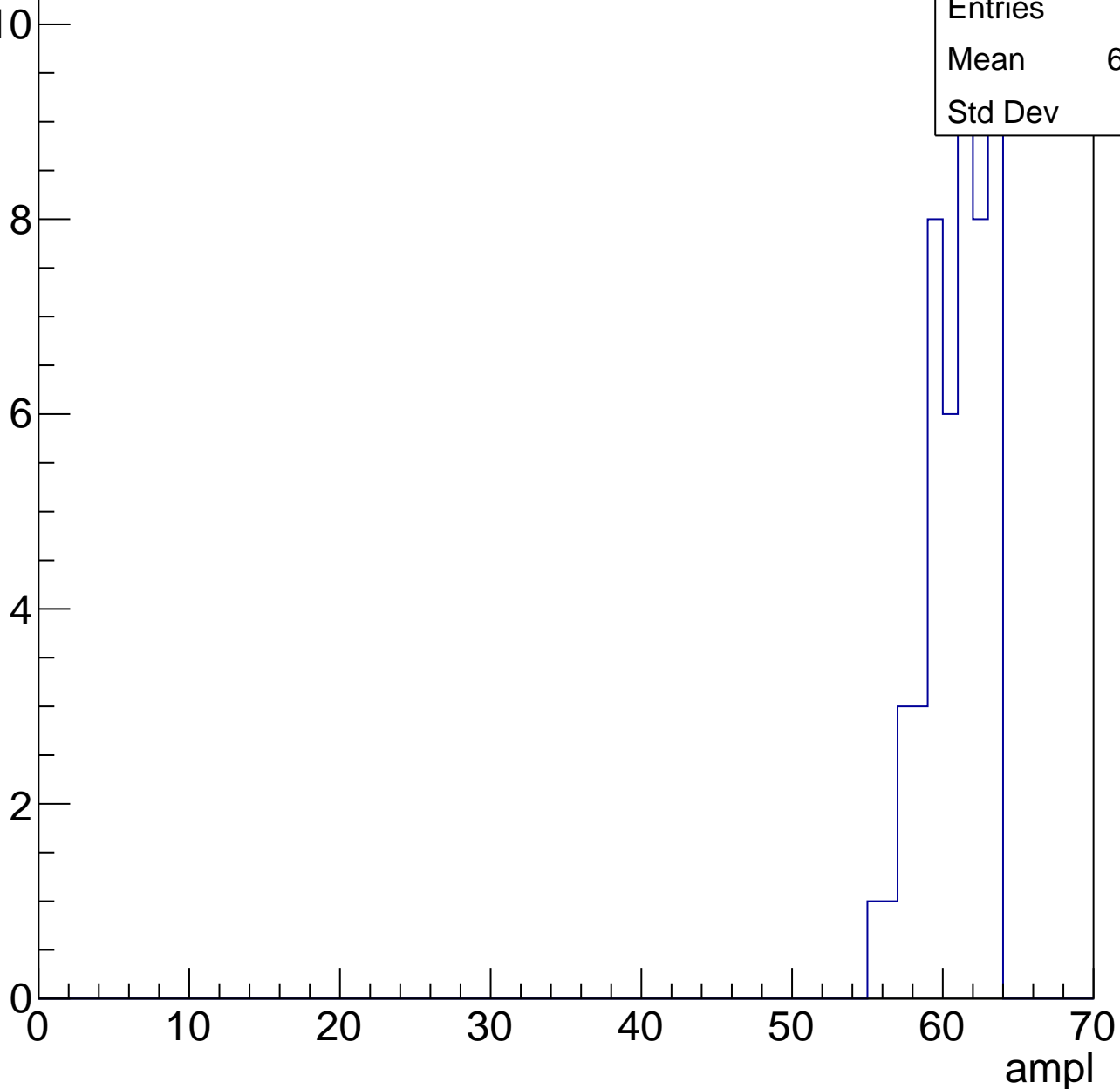


# B1L000S, U24-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	60.43
Std Dev	2.03



# B1L000S, U24-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

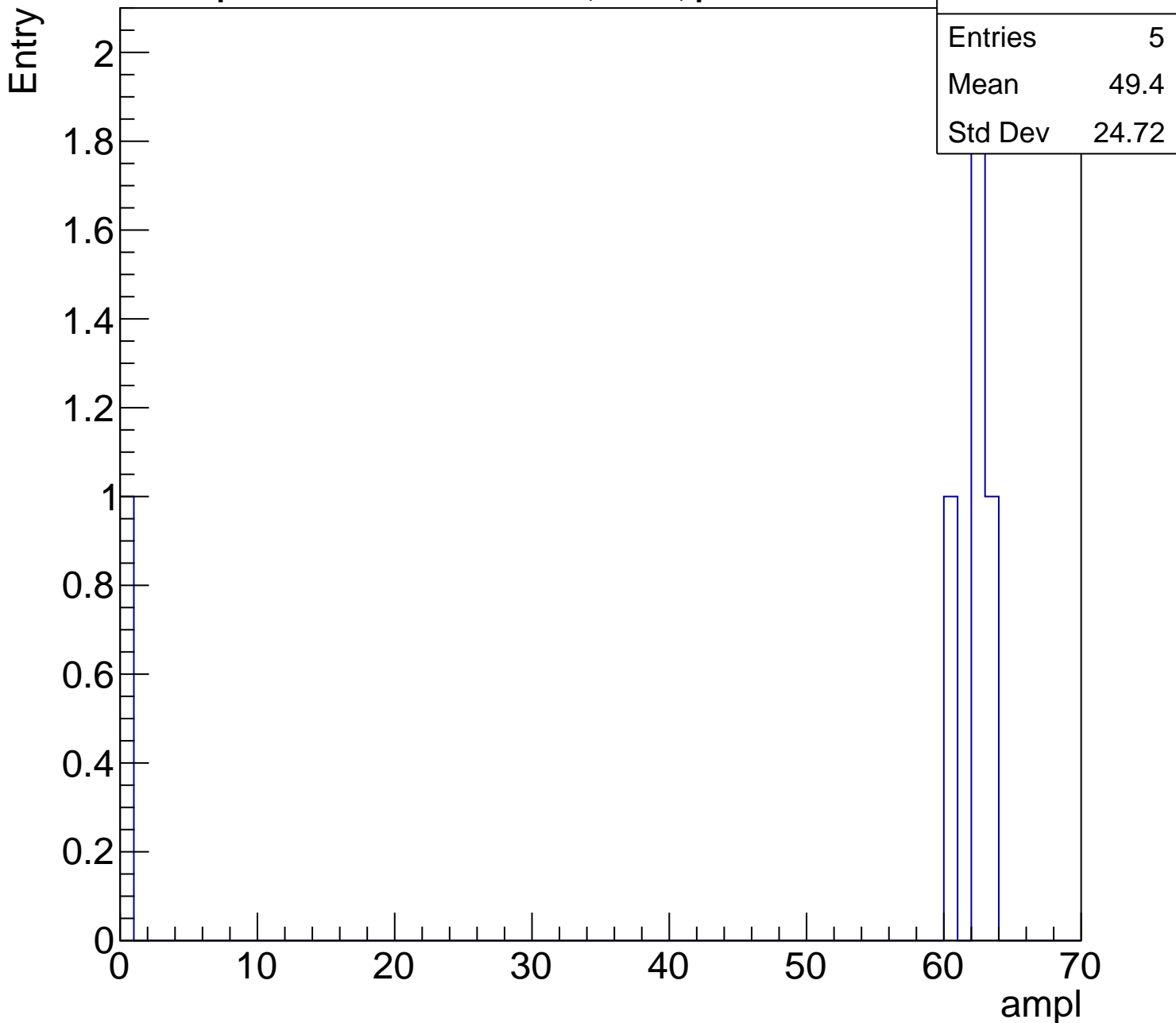
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.4
Std Dev	24.72

0 10 20 30 40 50 60 70

ampl





# B1L000S, U24-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	30
Std Dev	3.199

**Gaus mean : 30.1354**

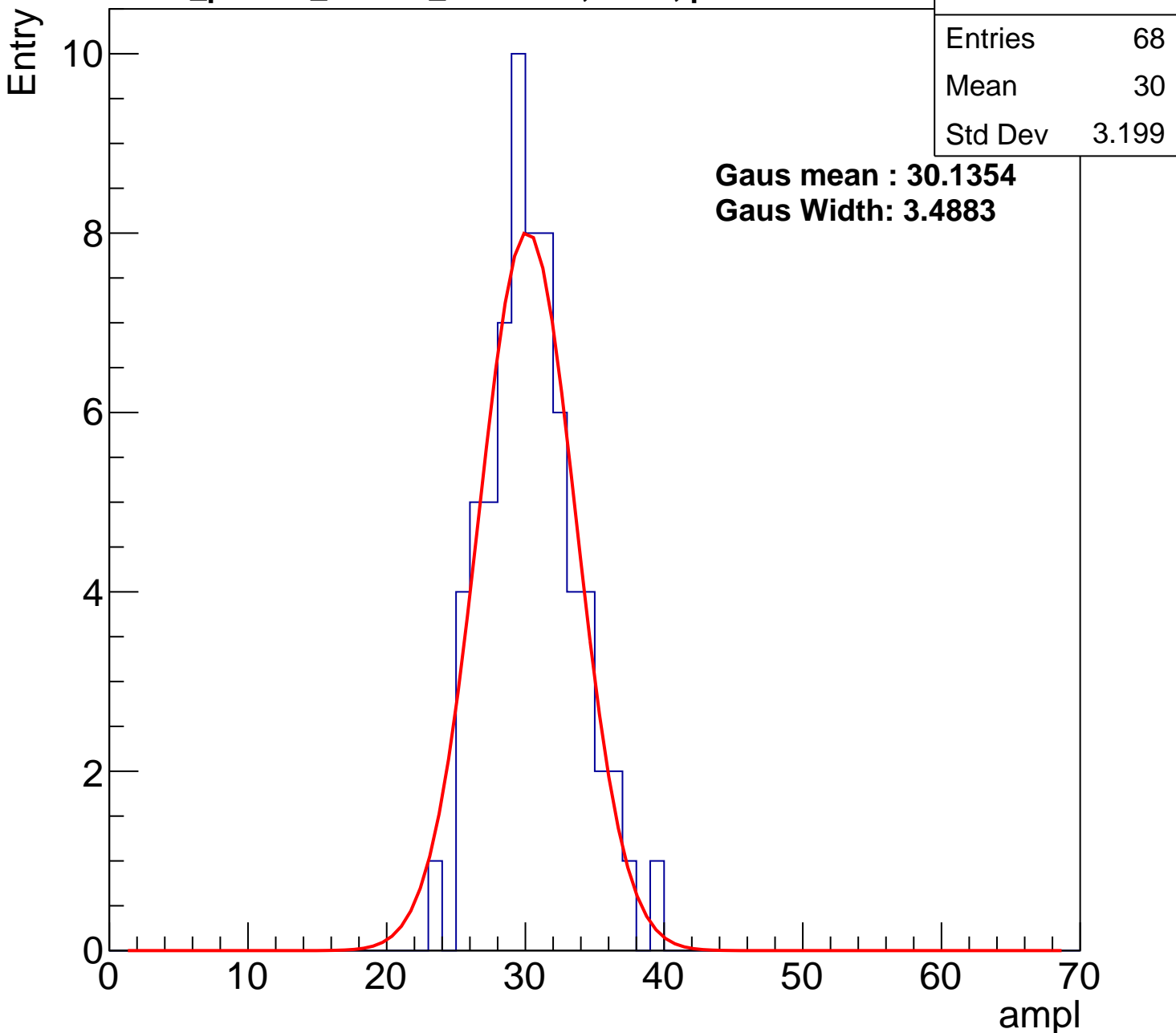
**Gaus Width: 3.4883**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch60, adc1

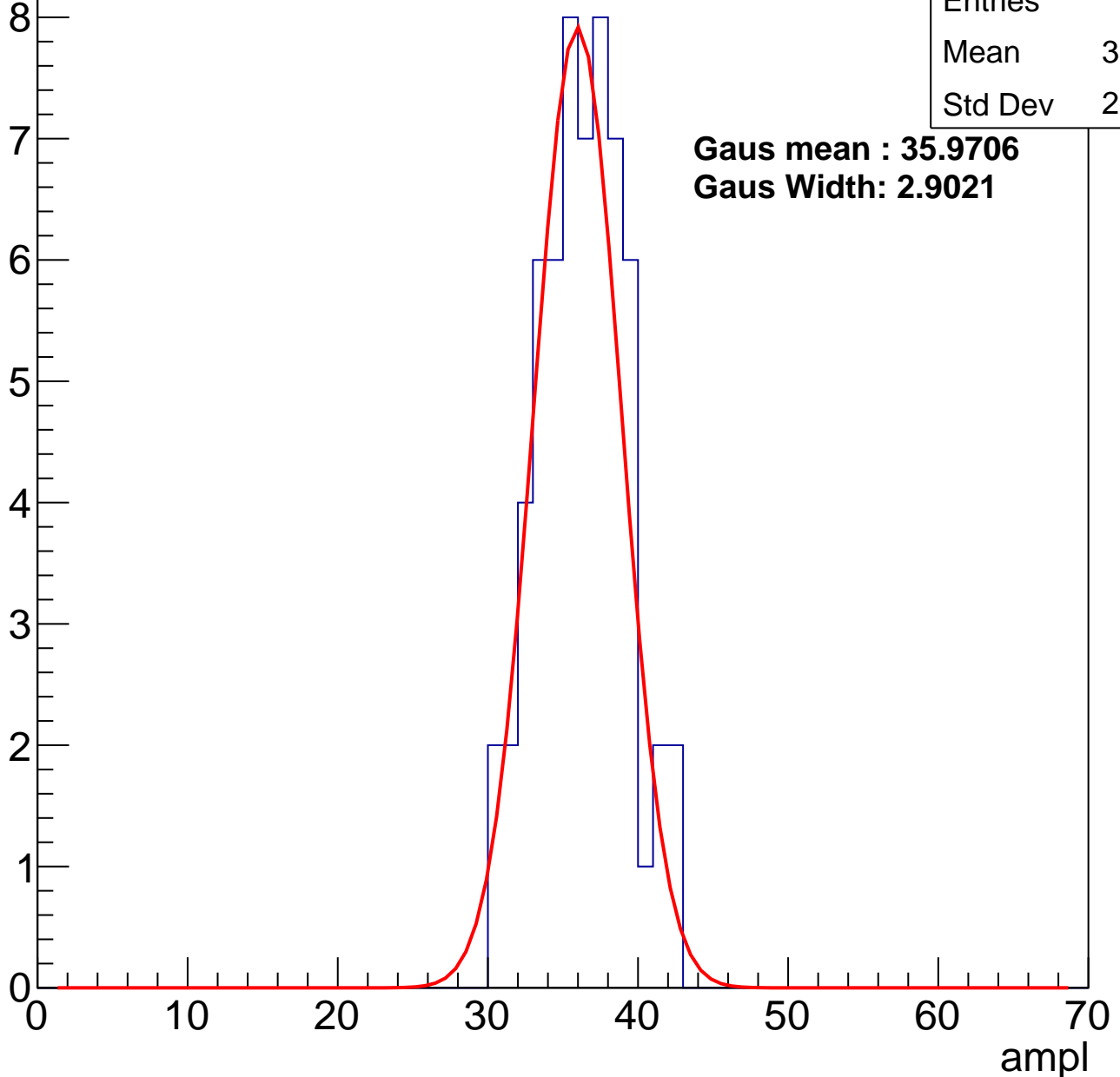
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.84
Std Dev	2.858

**Gaus mean : 35.9706**

**Gaus Width: 2.9021**



# B1L000S, U24-ch60, adc2

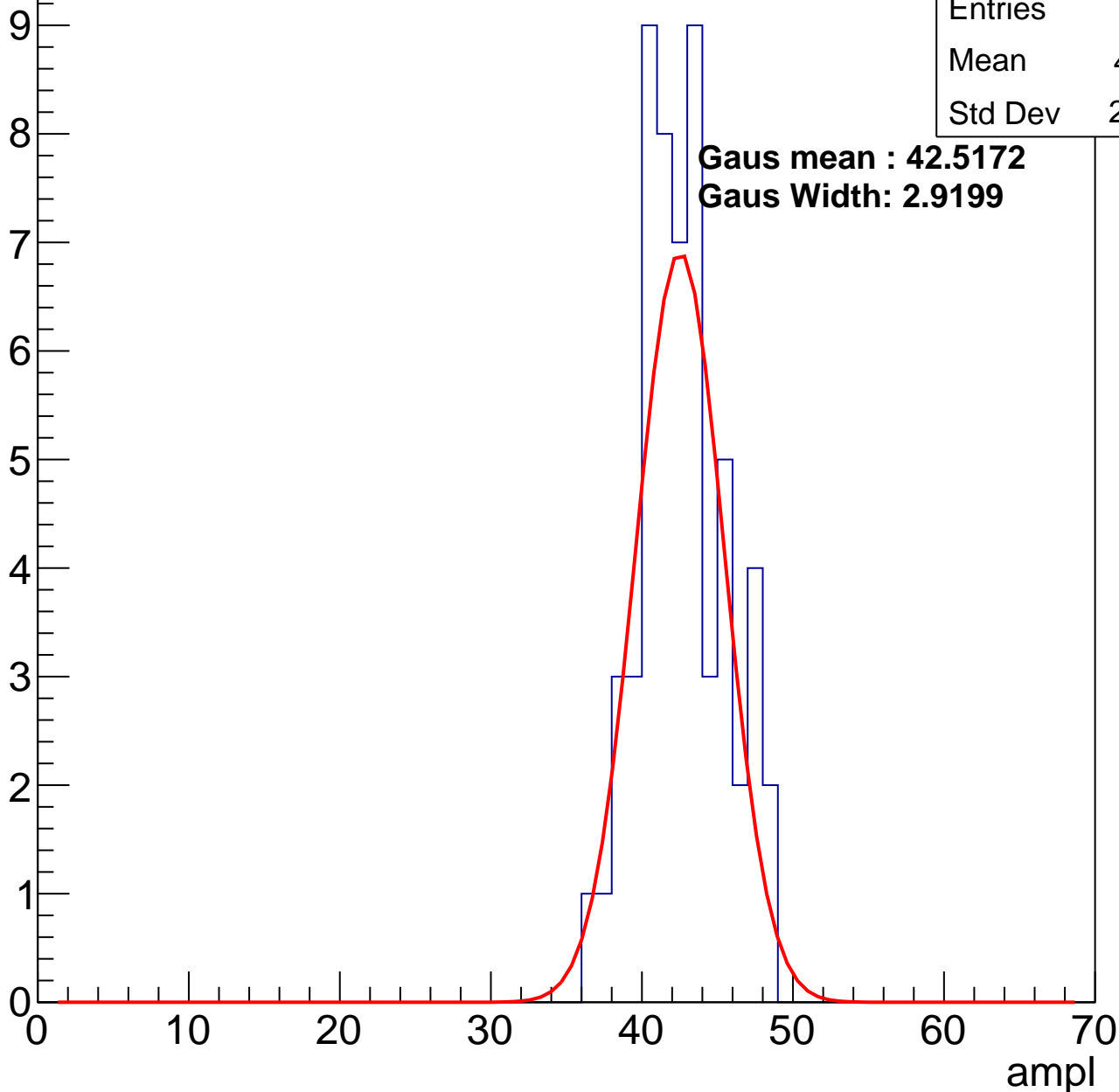
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42.21
Std Dev	2.802

**Gaus mean : 42.5172**

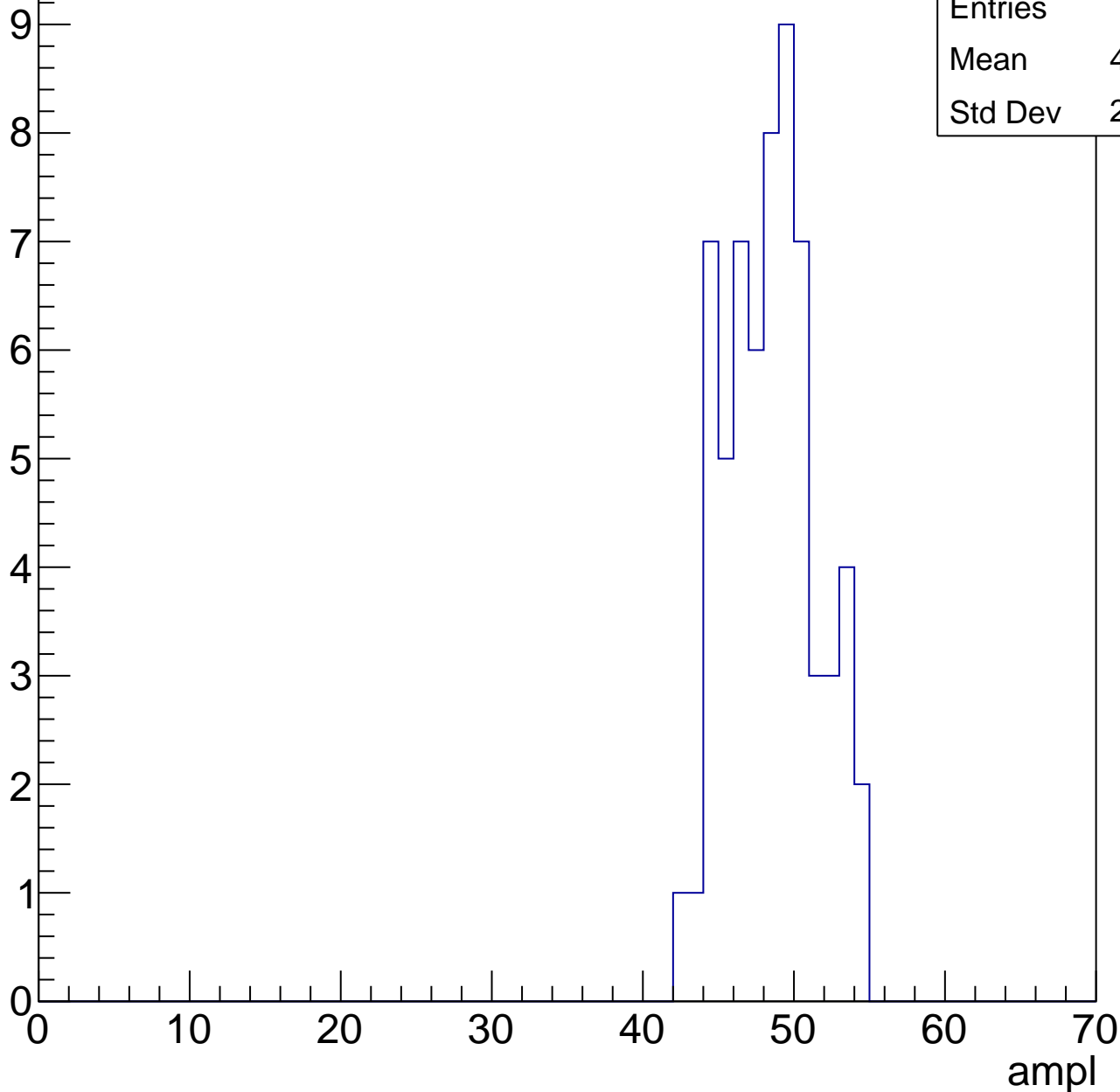
**Gaus Width: 2.9199**



# B1L000S, U24-ch60, adc3

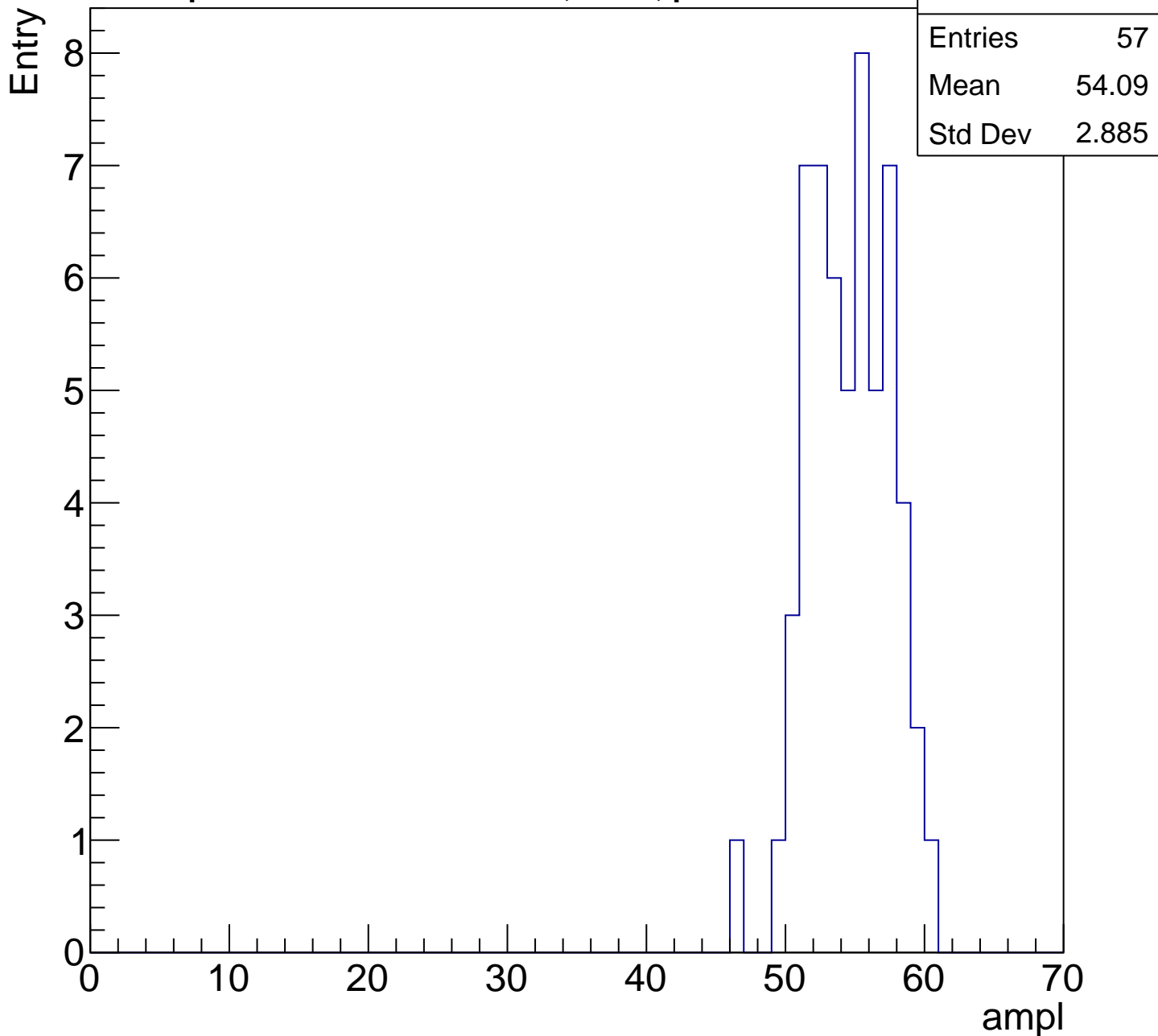
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U24-ch60, adc5

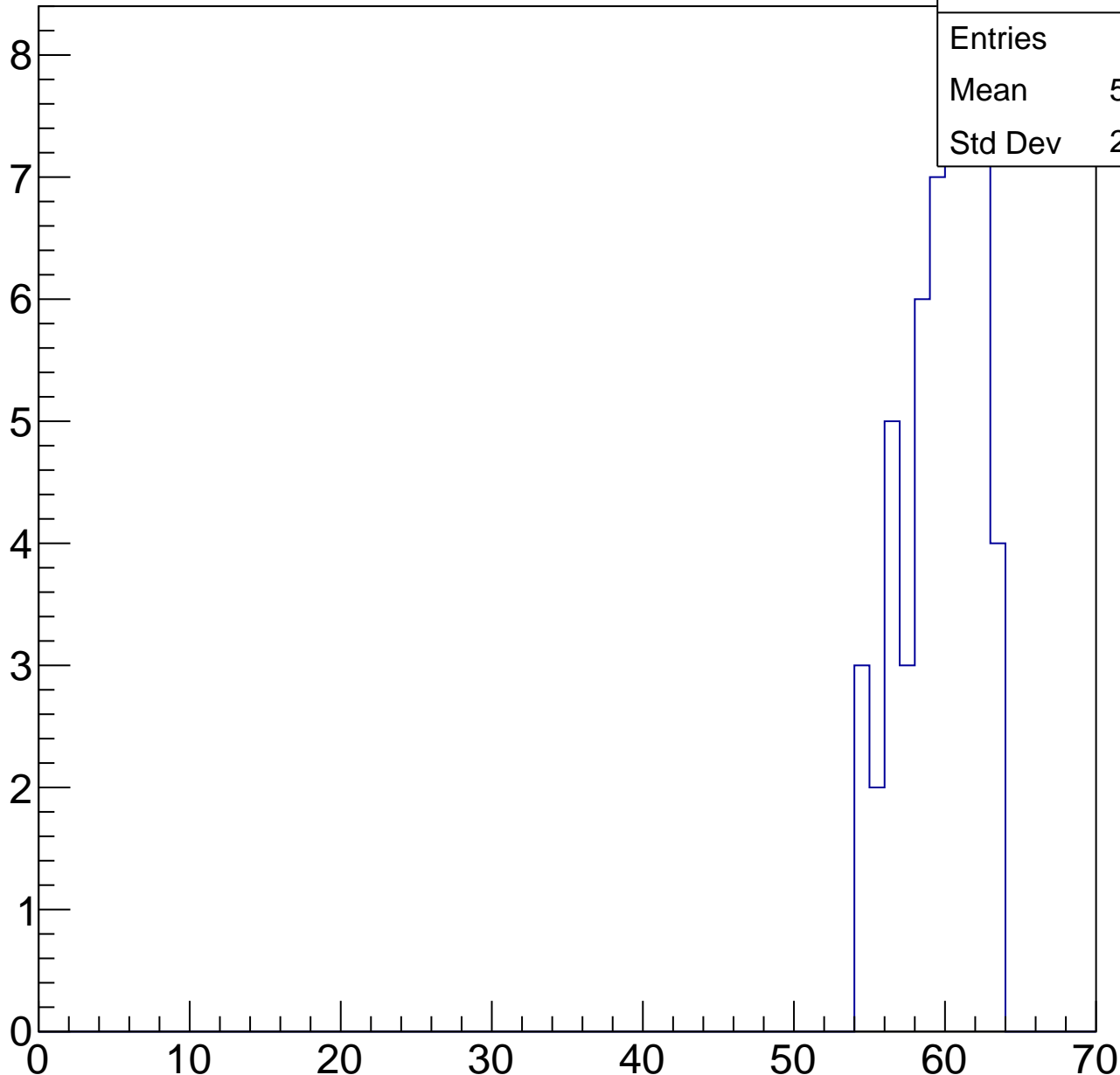
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	59.26
Std Dev	2.518

ampl

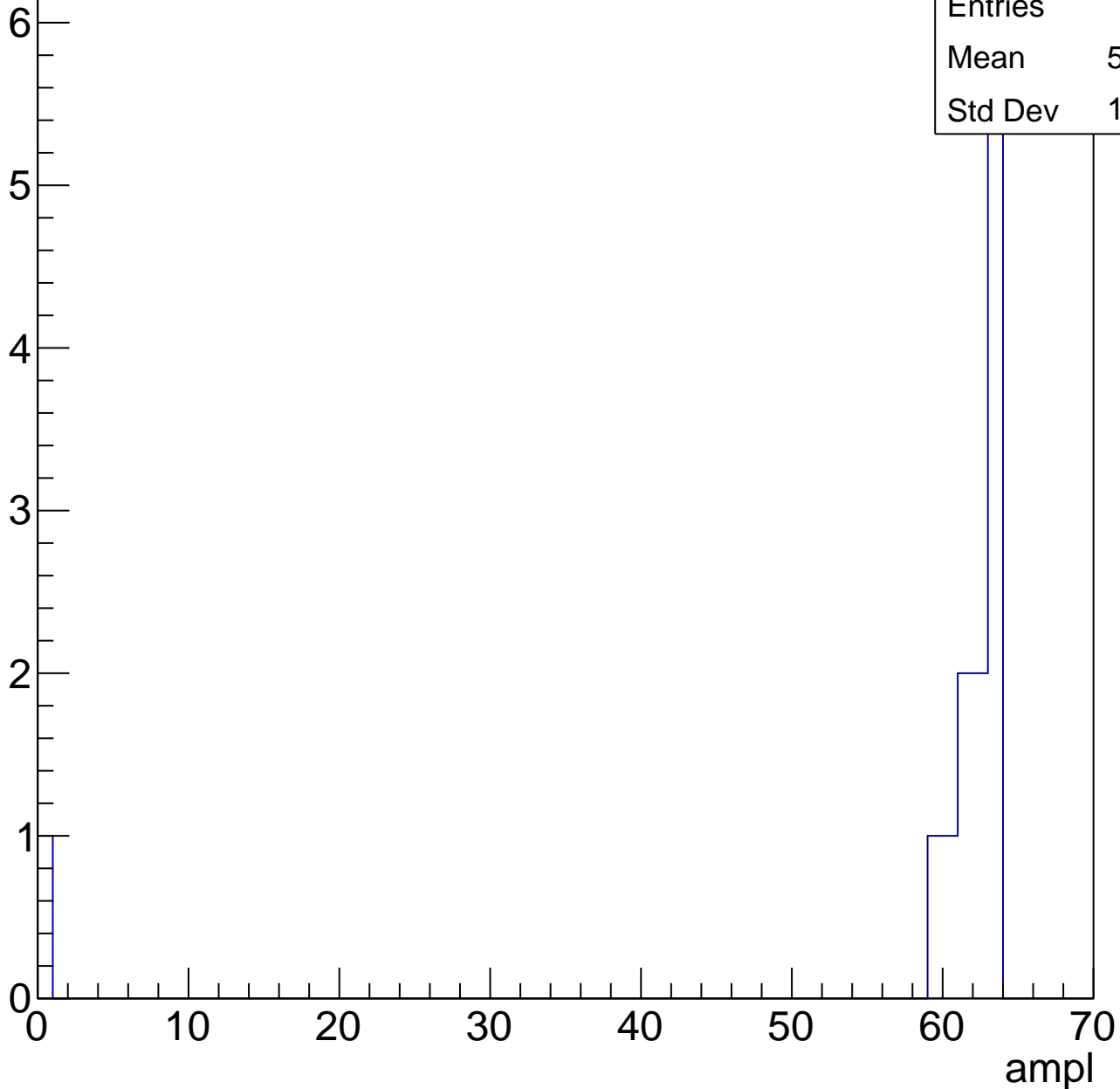


# B1L000S, U24-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.15
Std Dev	16.55





# B1L000S, U24-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U24-ch61, adc0

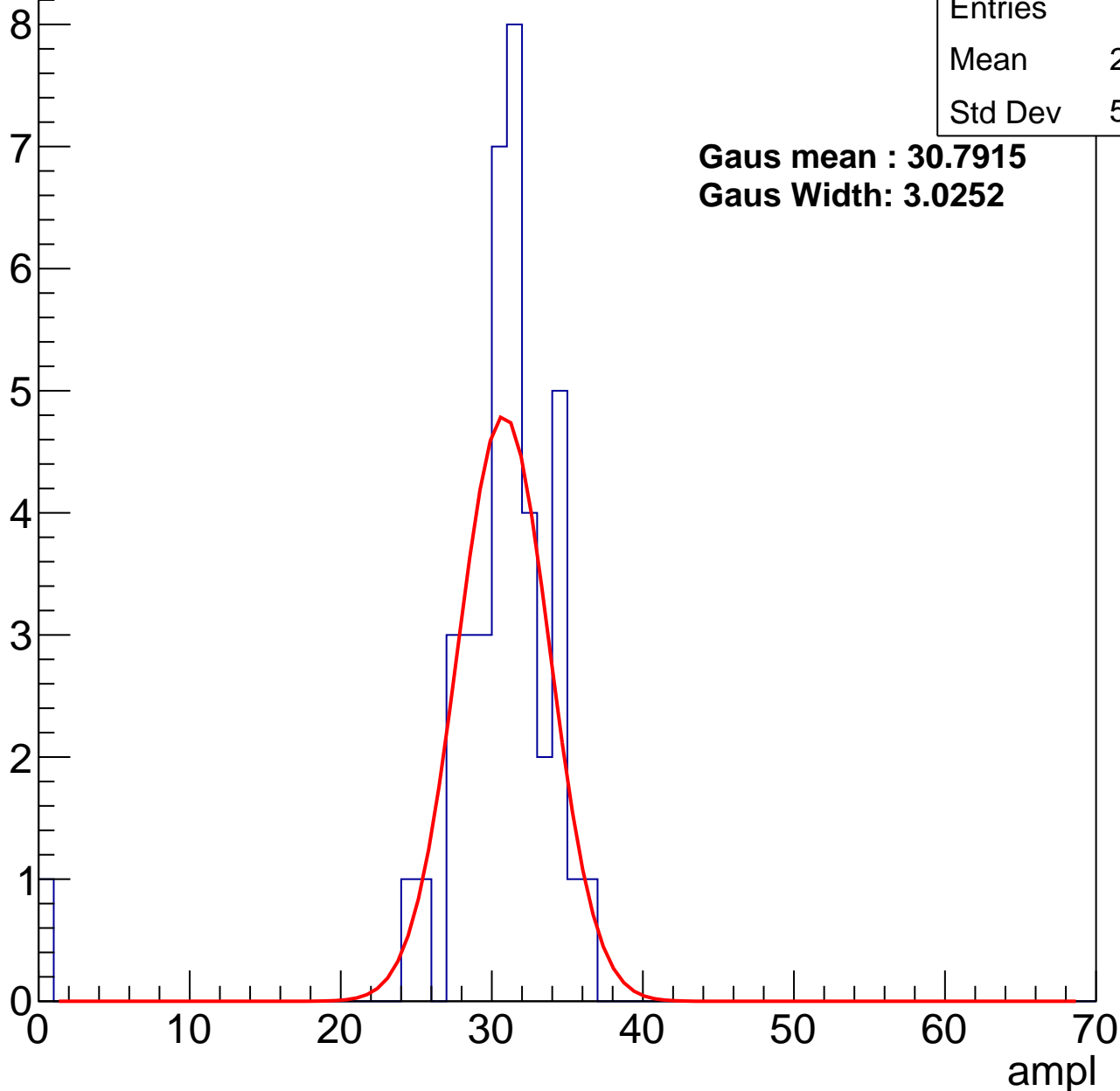
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	29.85
Std Dev	5.443

**Gaus mean : 30.7915**

**Gaus Width: 3.0252**



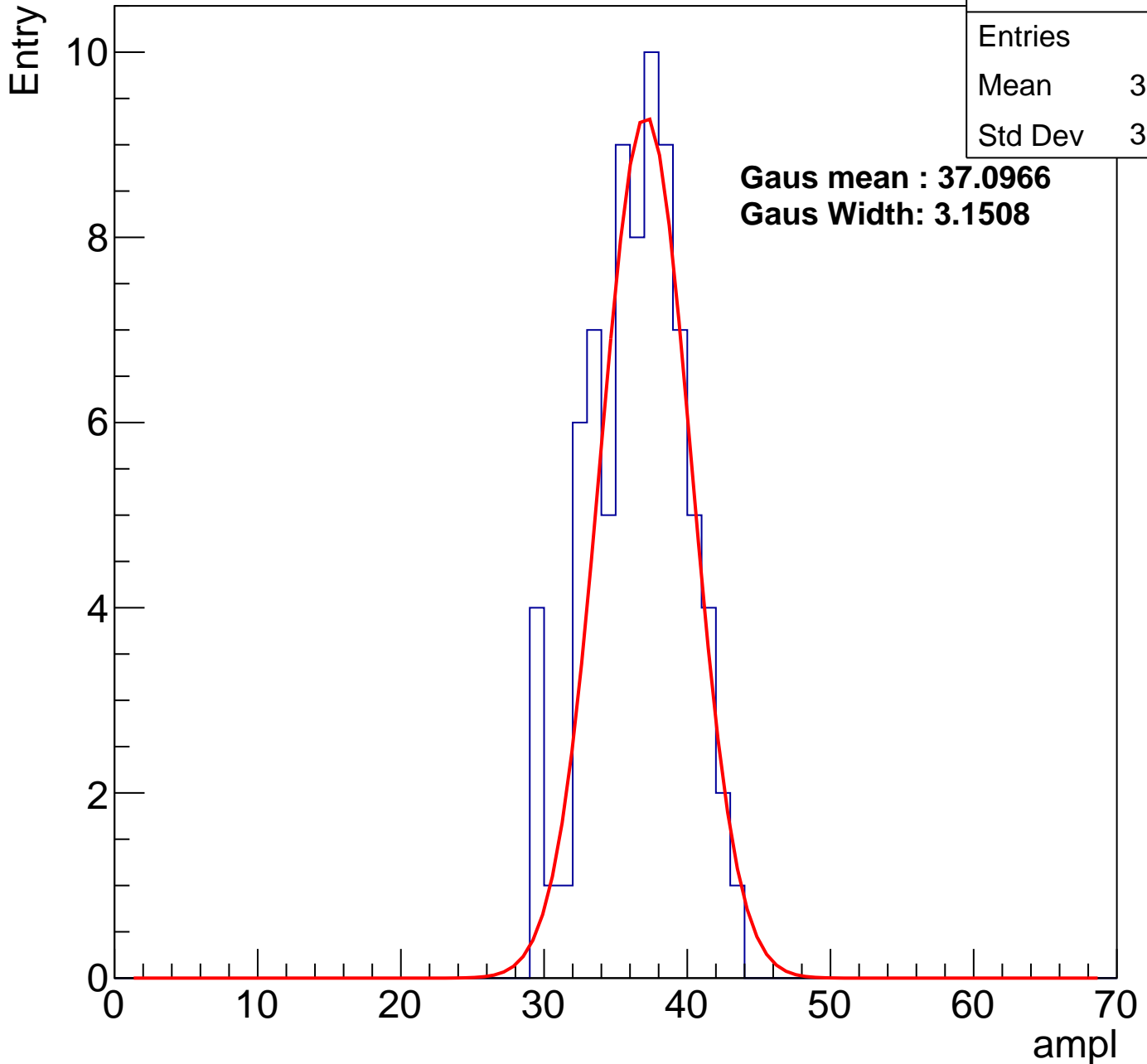
# B1L000S, U24-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	36.06
Std Dev	3.289

**Gaus mean : 37.0966**

**Gaus Width: 3.1508**



# B1L000S, U24-ch61, adc2

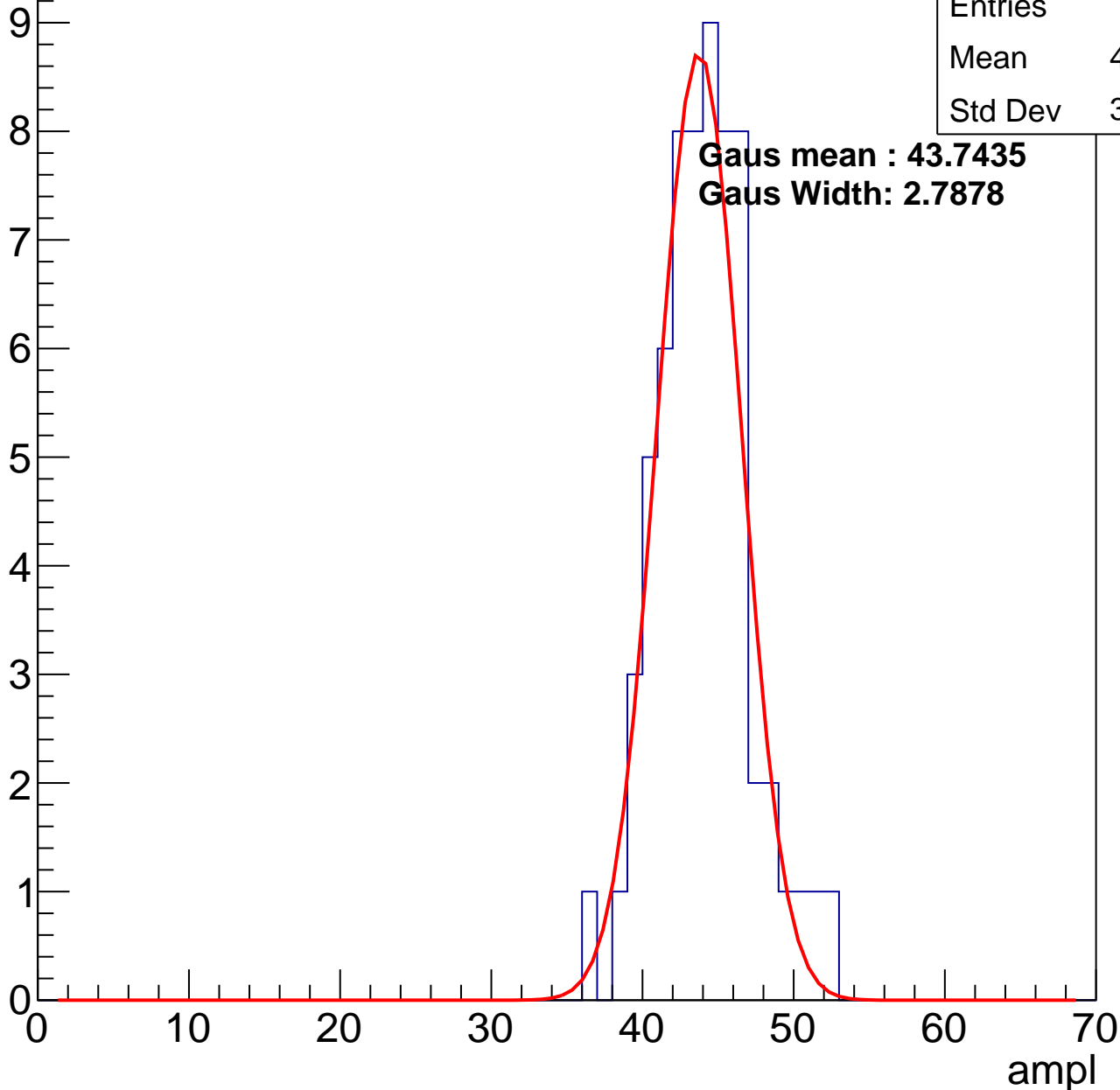
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.58
Std Dev	3.043

**Gaus mean : 43.7435**

**Gaus Width: 2.7878**

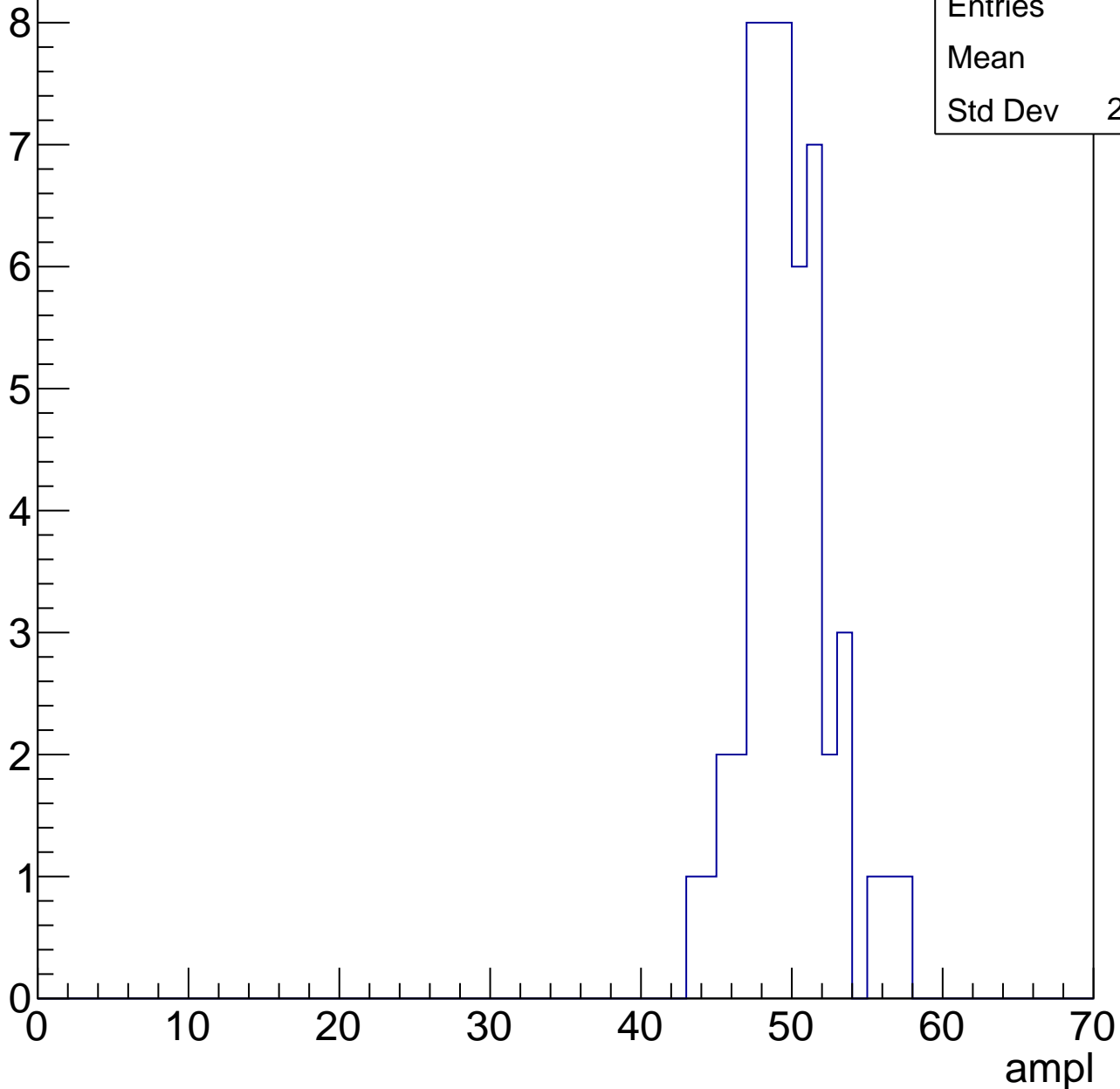


# B1L000S, U24-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	49.2
Std Dev	2.794

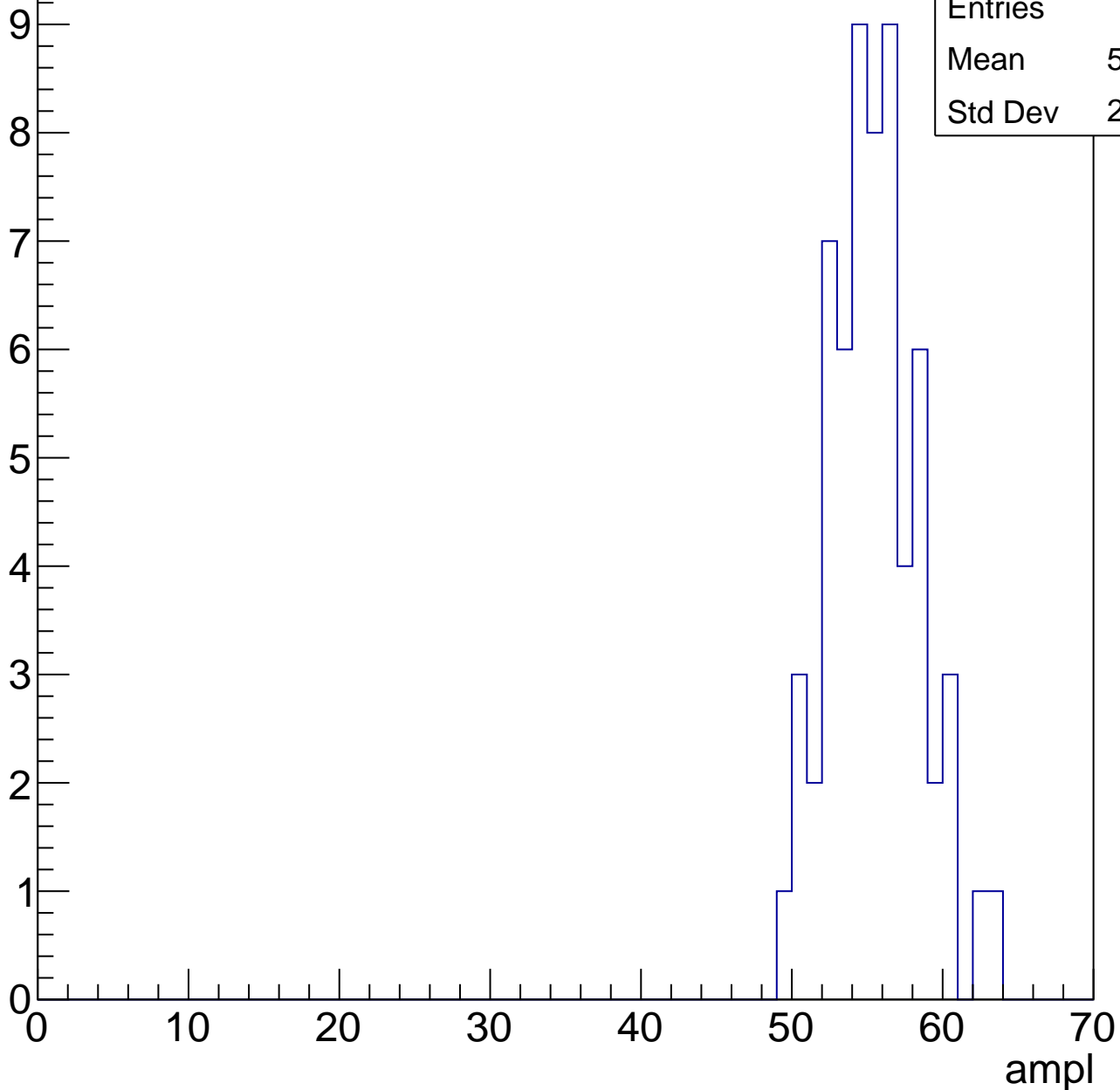


# B1L000S, U24-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	55.03
Std Dev	2.946

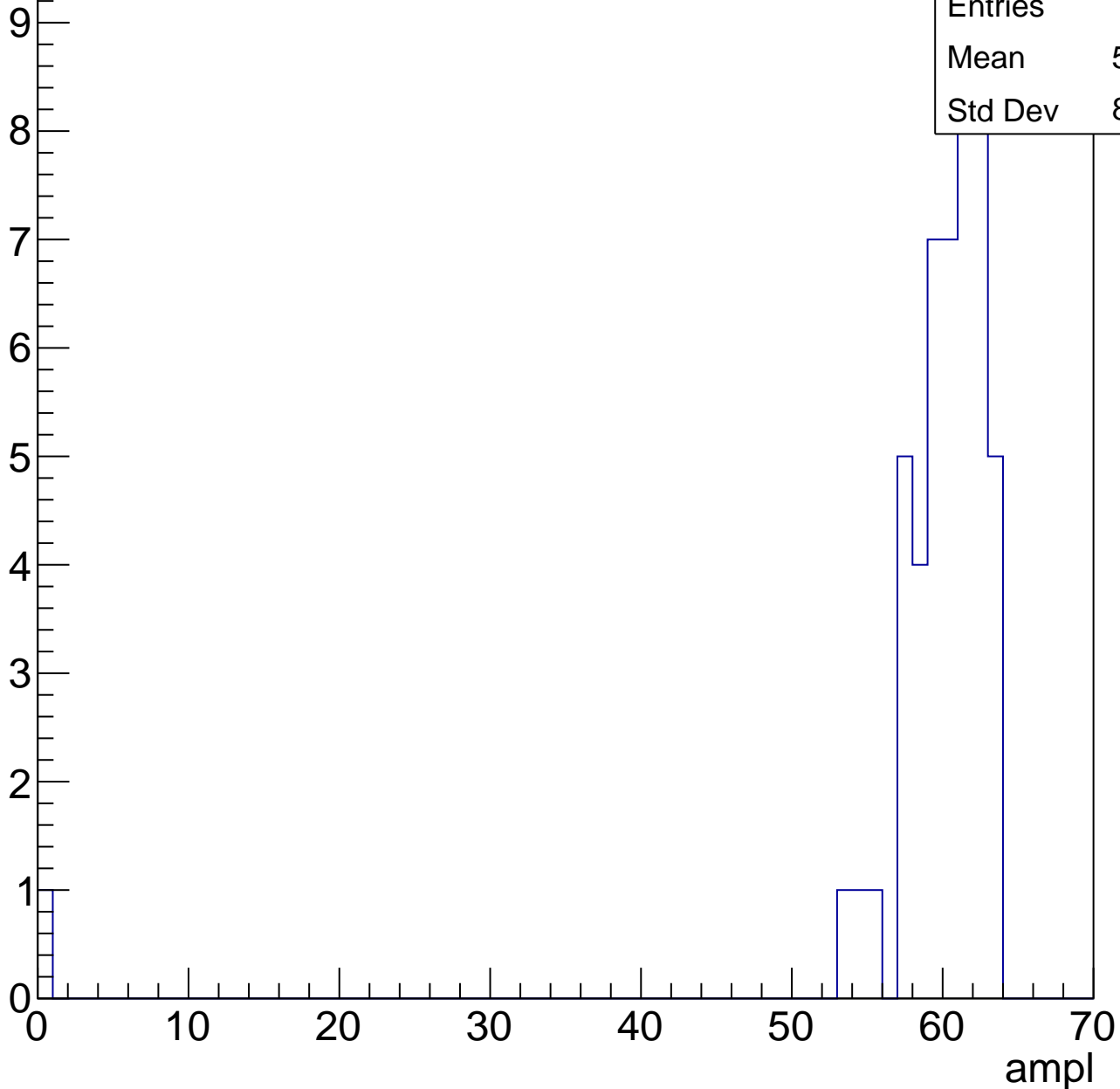


# B1L000S, U24-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

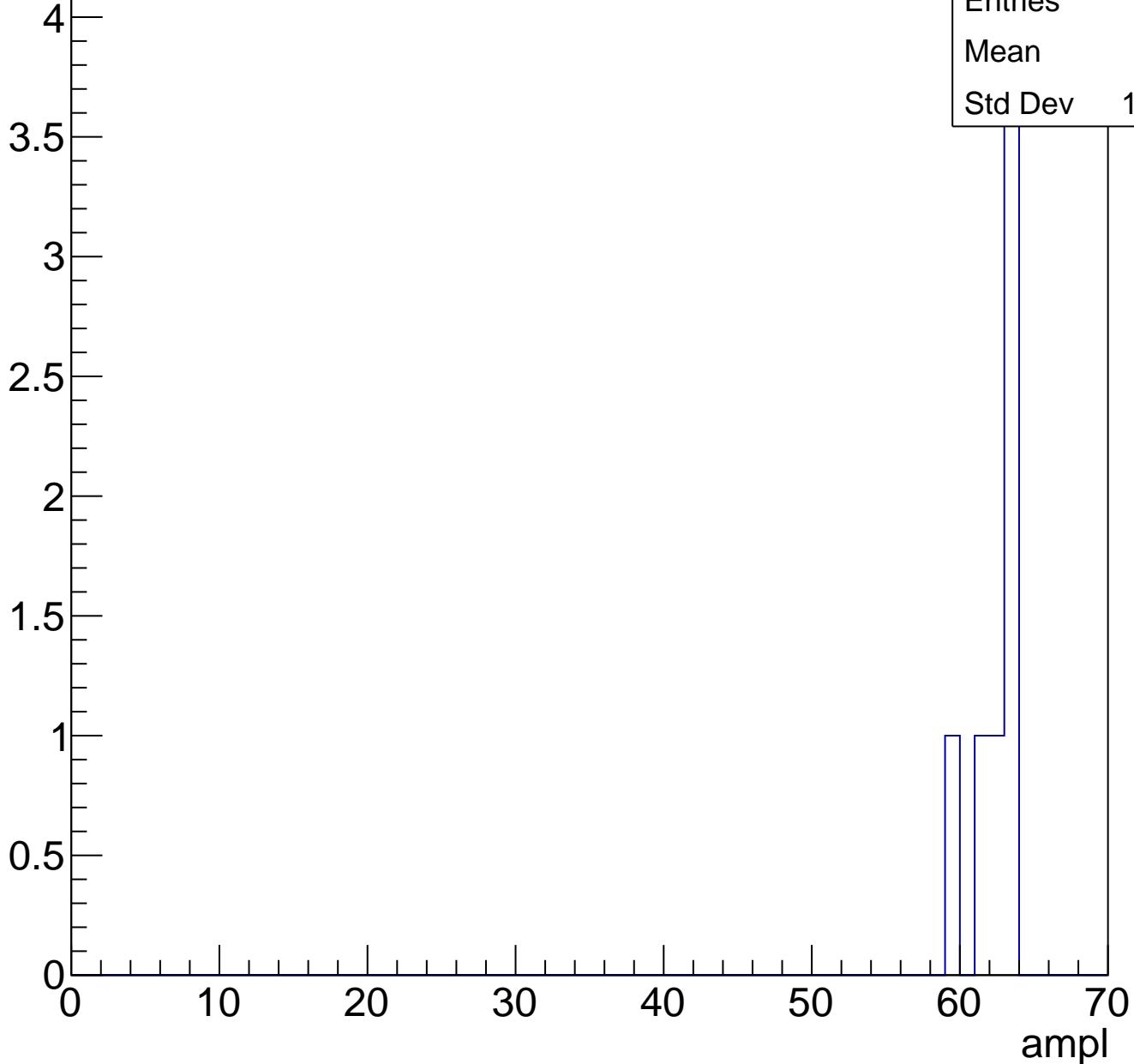
Entries	49
Mean	58.61
Std Dev	8.771



# B1L000S, U24-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch62, adc0

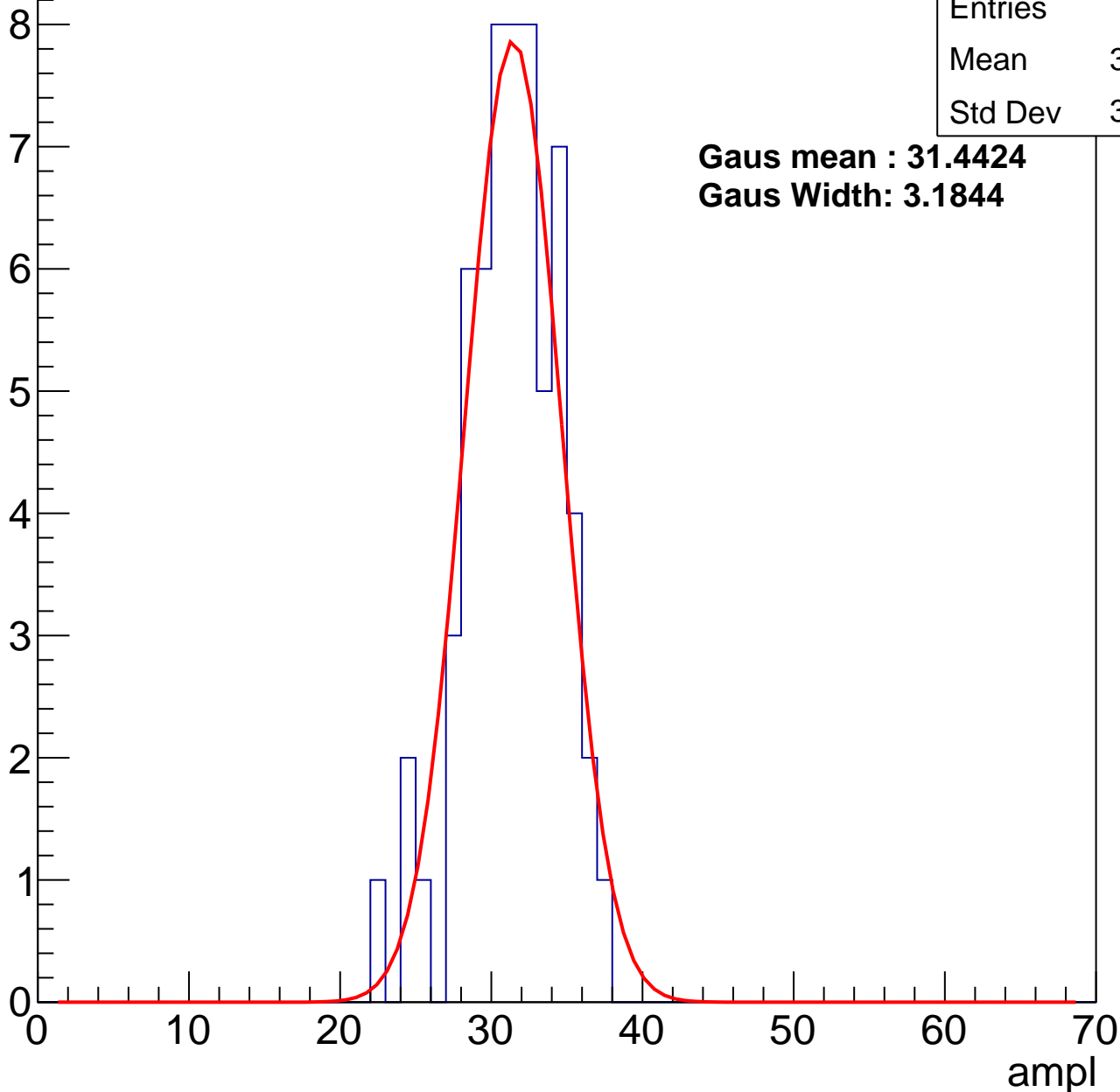
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	30.87
Std Dev	3.082

**Gaus mean : 31.4424**

**Gaus Width: 3.1844**



# B1L000S, U24-ch62, adc1

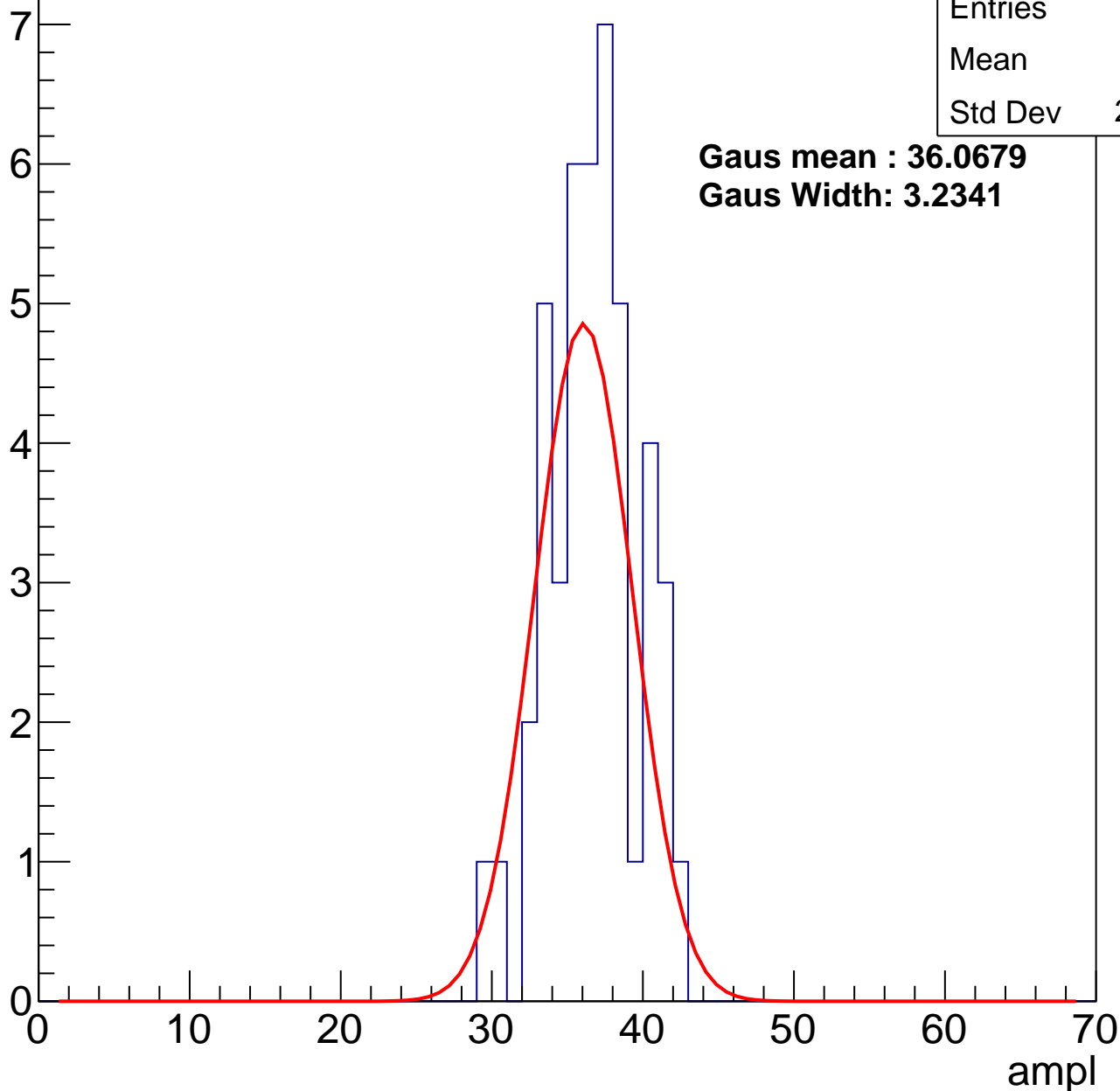
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	36.2
Std Dev	2.941

**Gaus mean : 36.0679**

**Gaus Width: 3.2341**



# B1L000S, U24-ch62, adc2

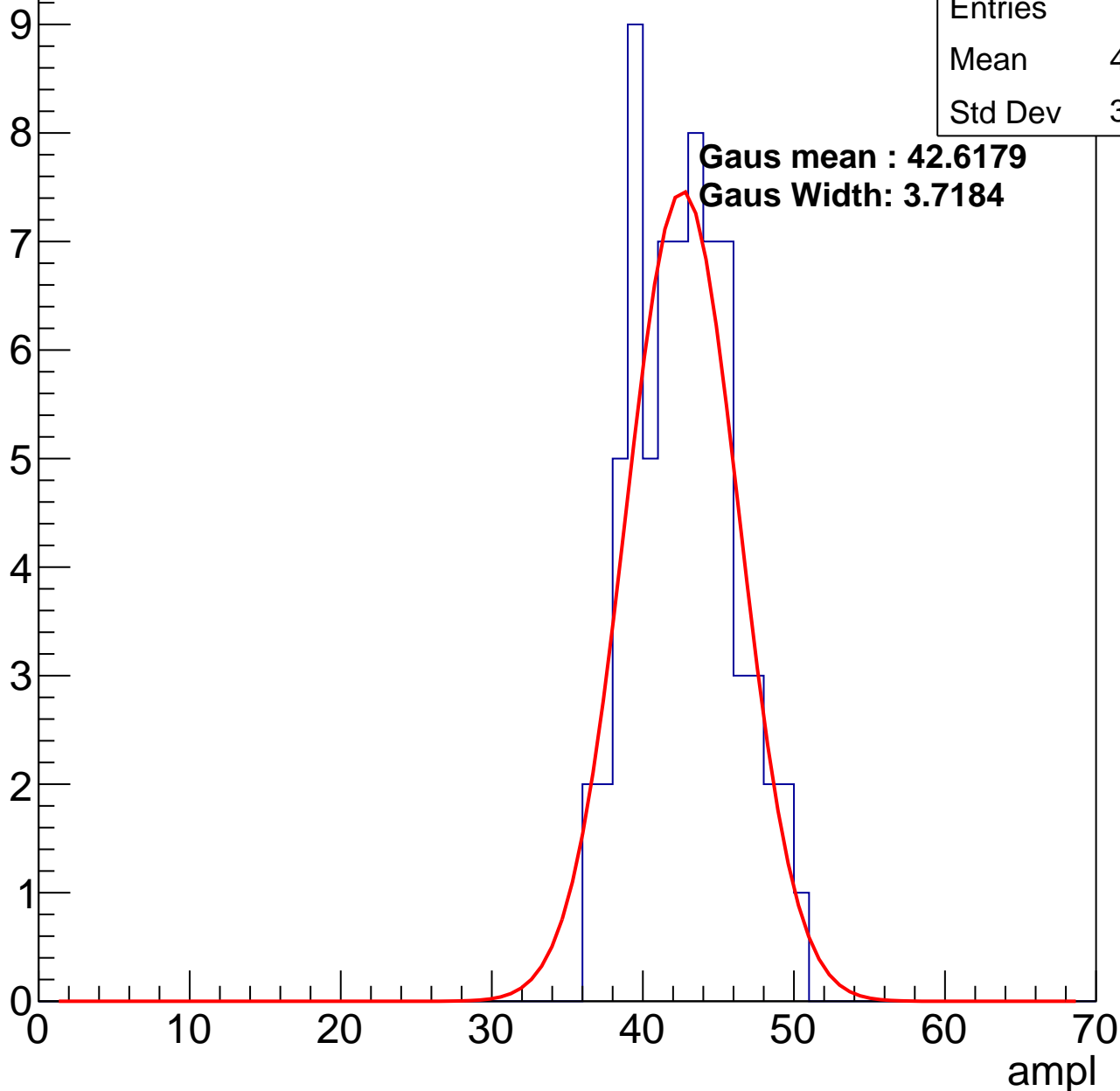
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.26
Std Dev	3.298

**Gaus mean : 42.6179**

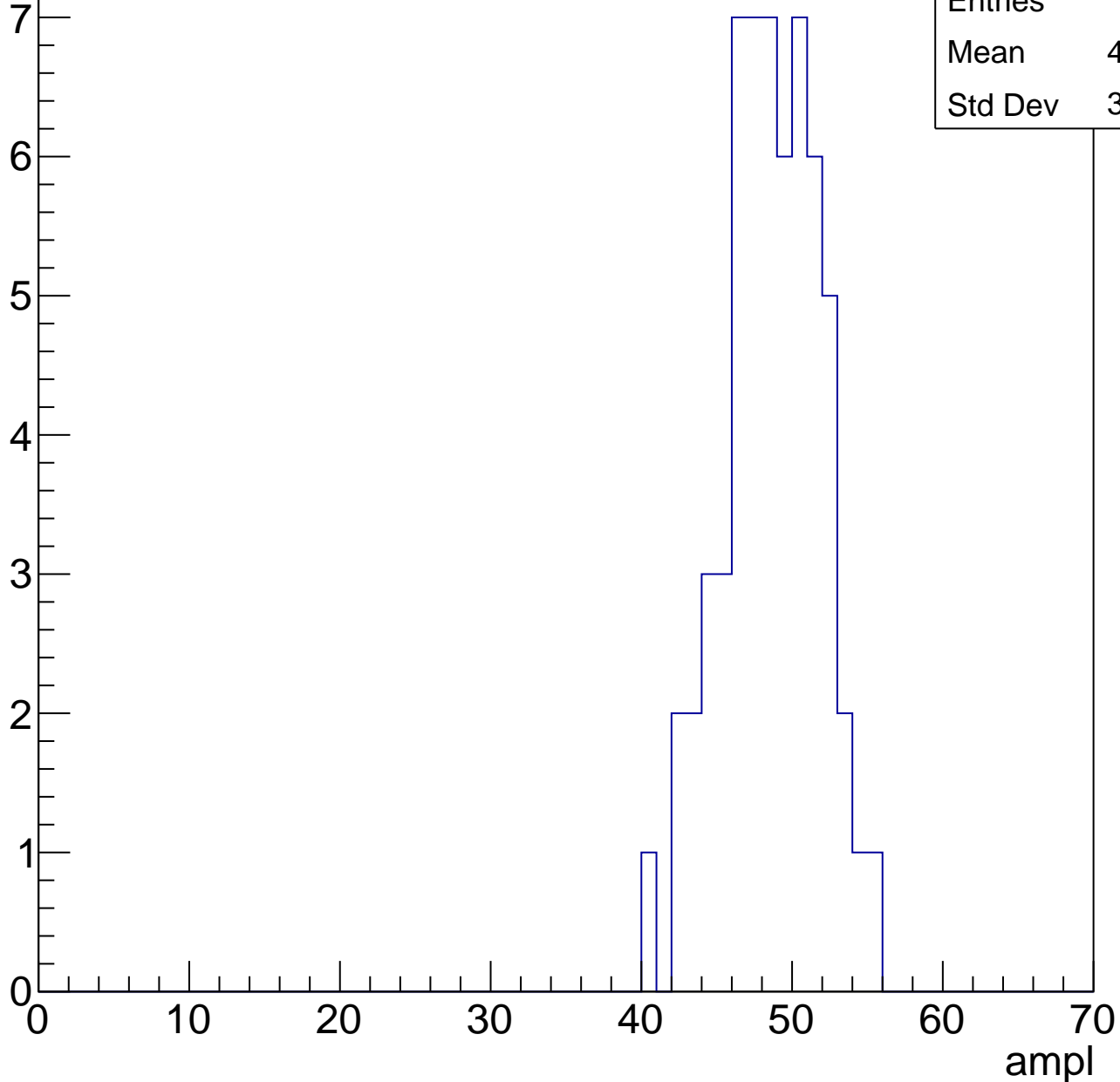
**Gaus Width: 3.7184**



# B1L000S, U24-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

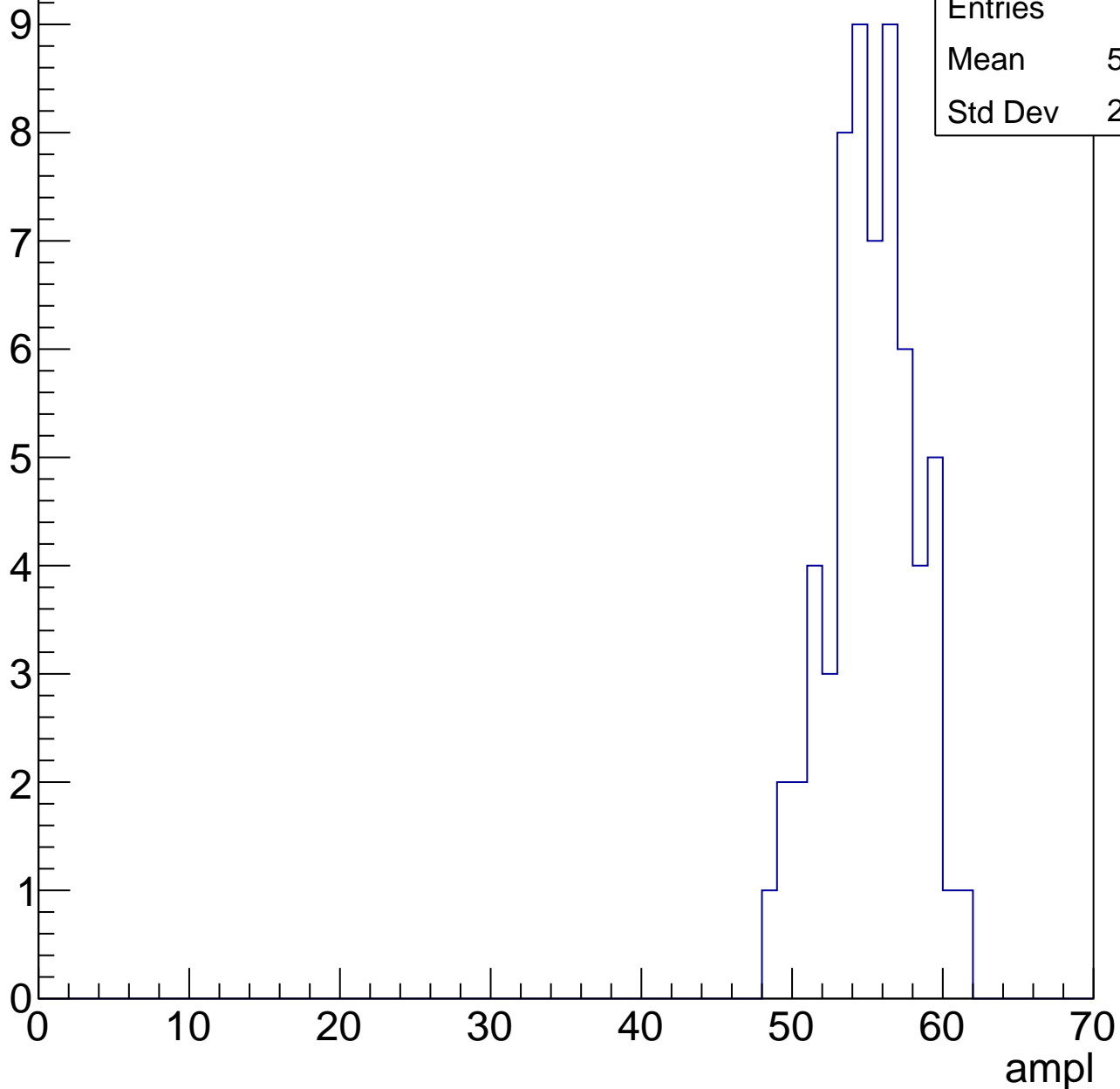


Entries	60
Mean	48.15
Std Dev	3.156

# B1L000S, U24-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

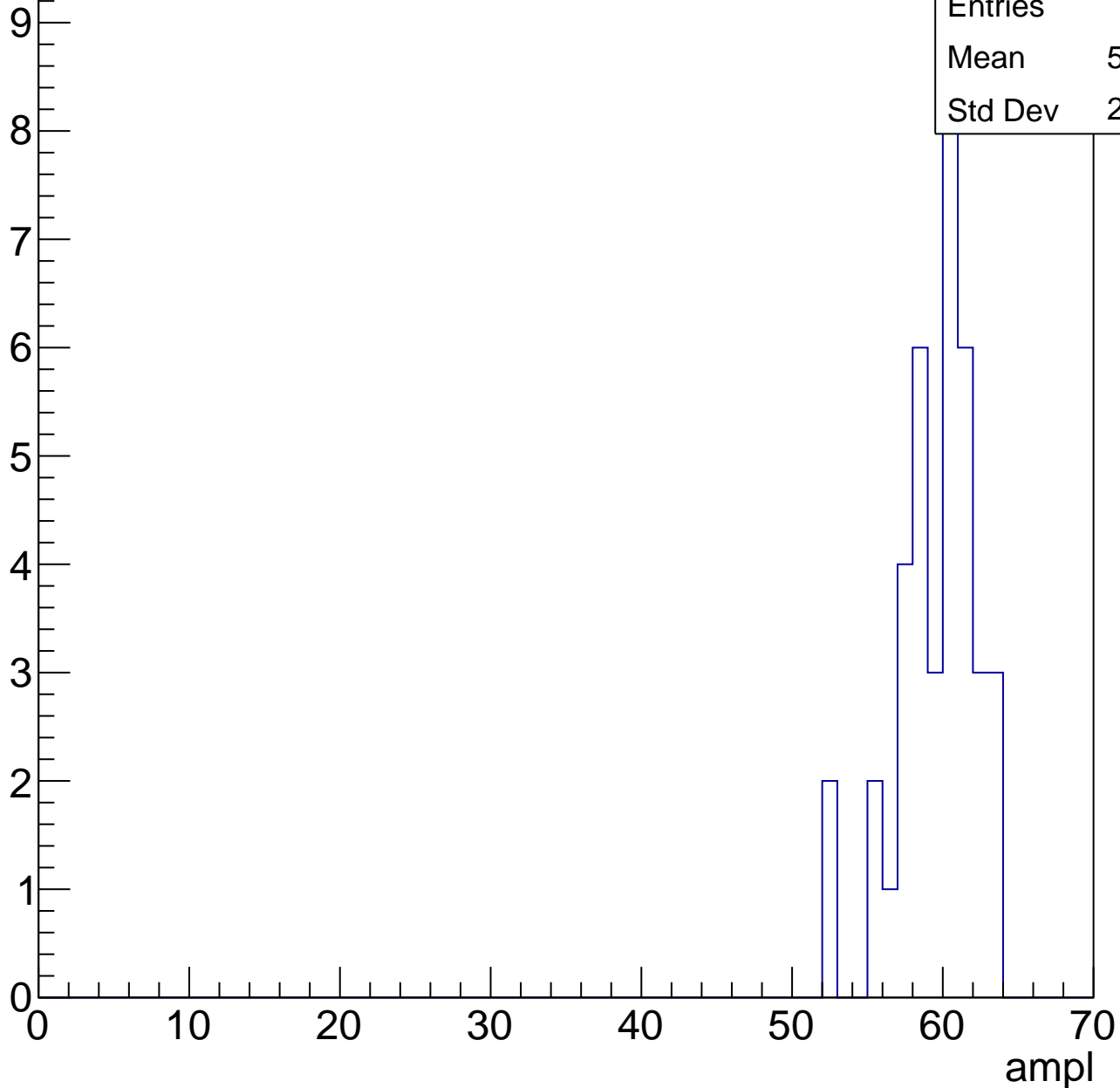
Entry



# B1L000S, U24-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

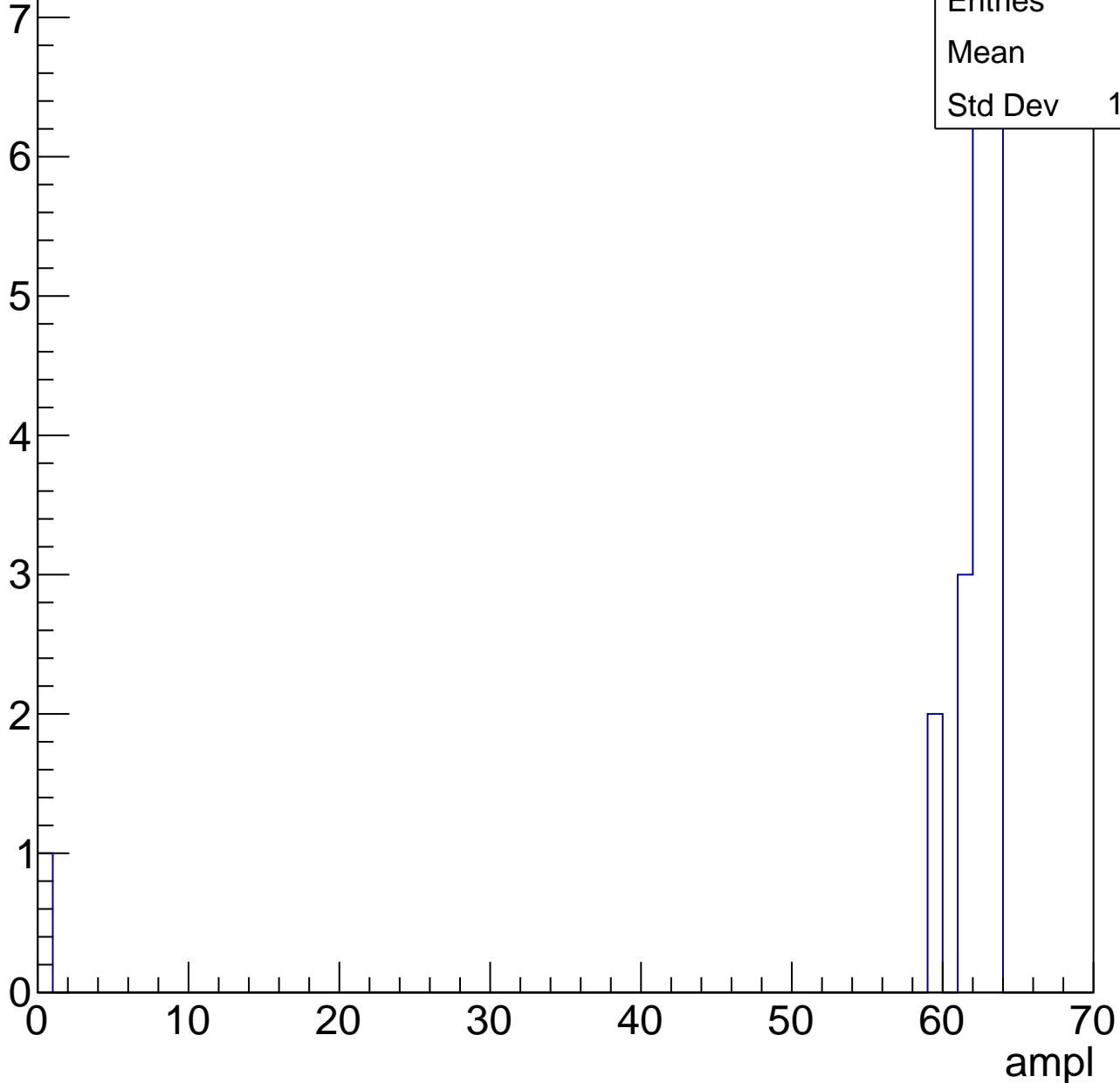


# B1L000S, U24-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.8
Std Dev	13.54

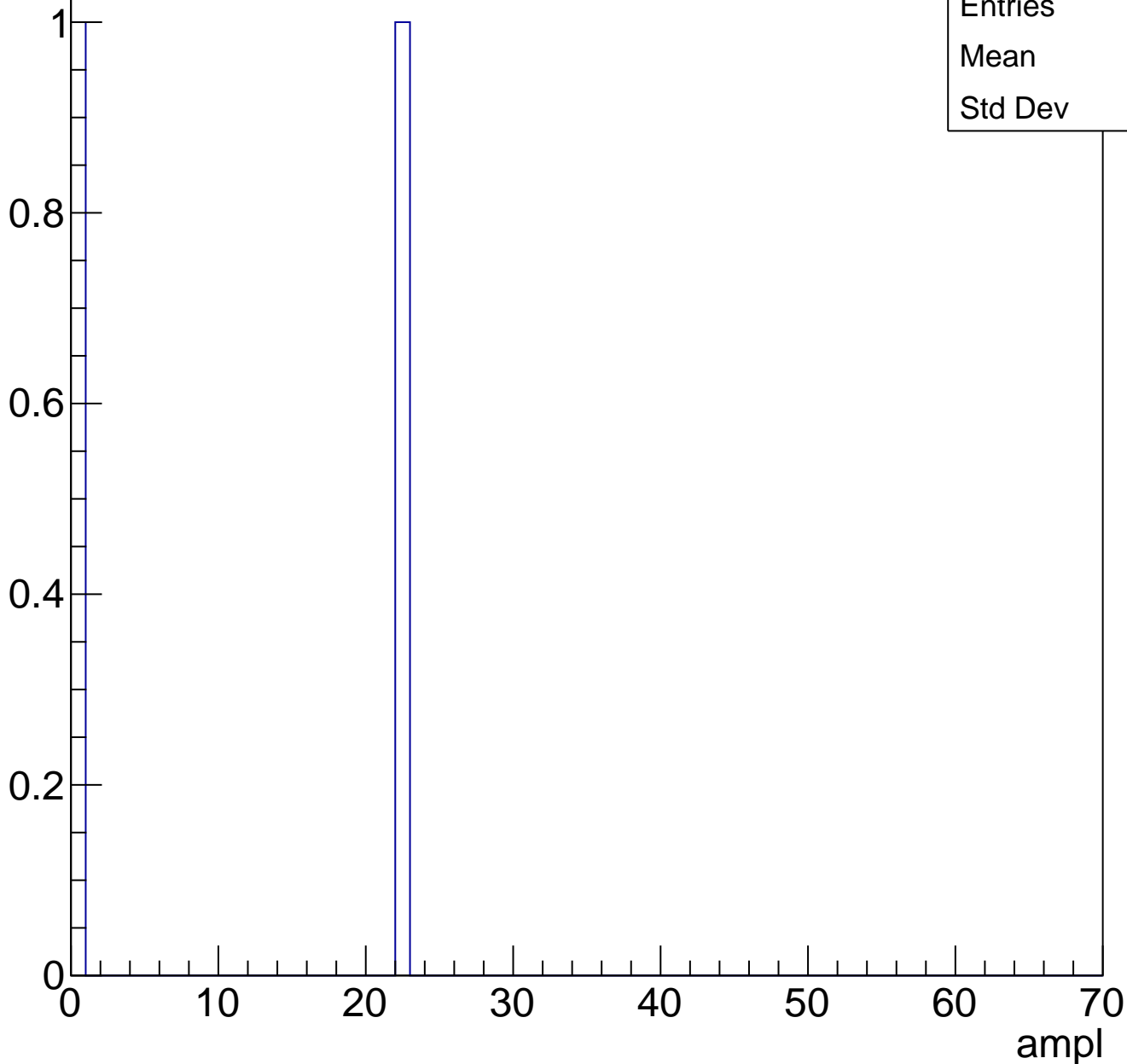




# B1L000S, U24-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	29.18
Std Dev	4.709

**Gaus mean : 30.1224**

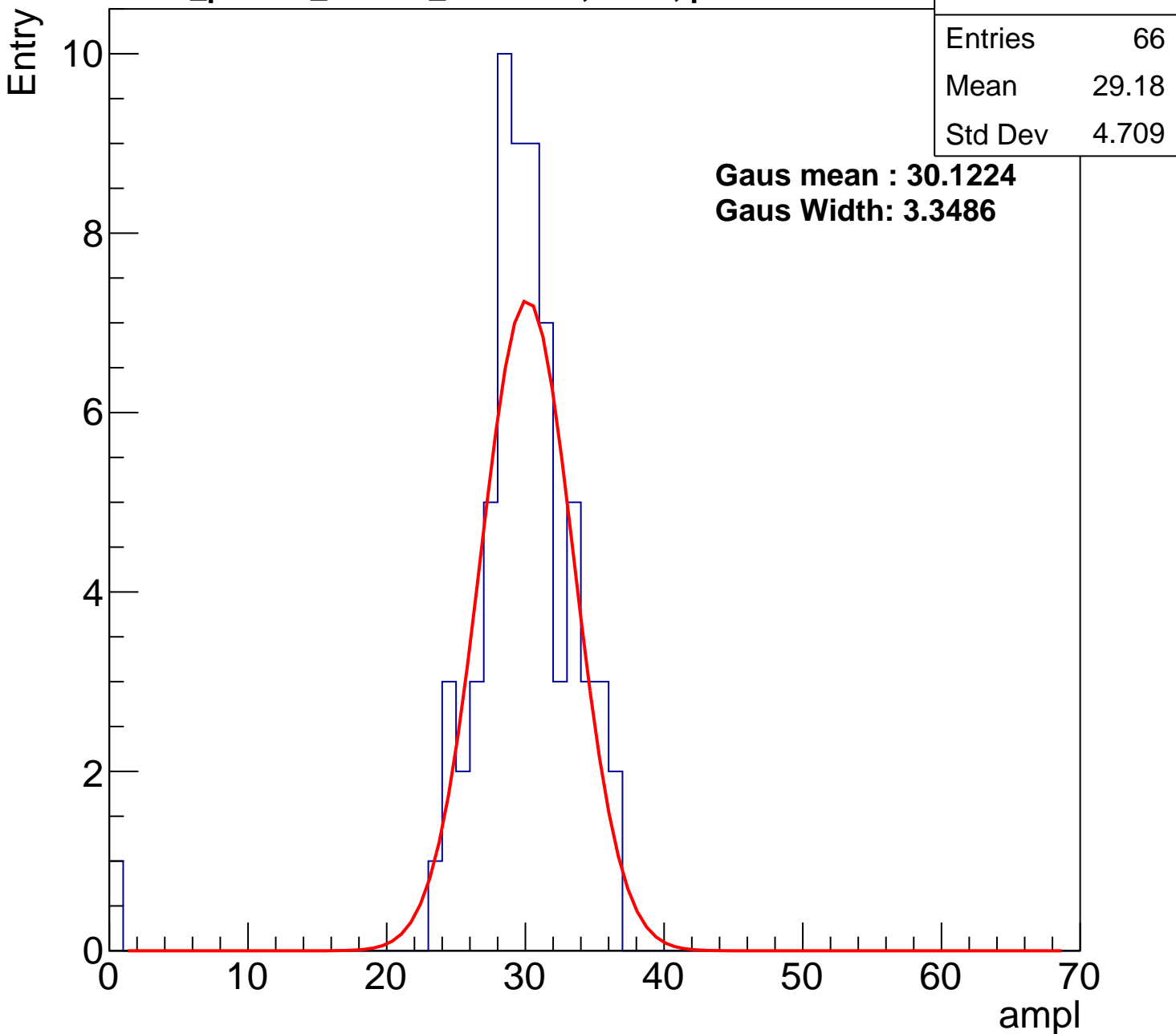
**Gaus Width: 3.3486**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch63, adc1

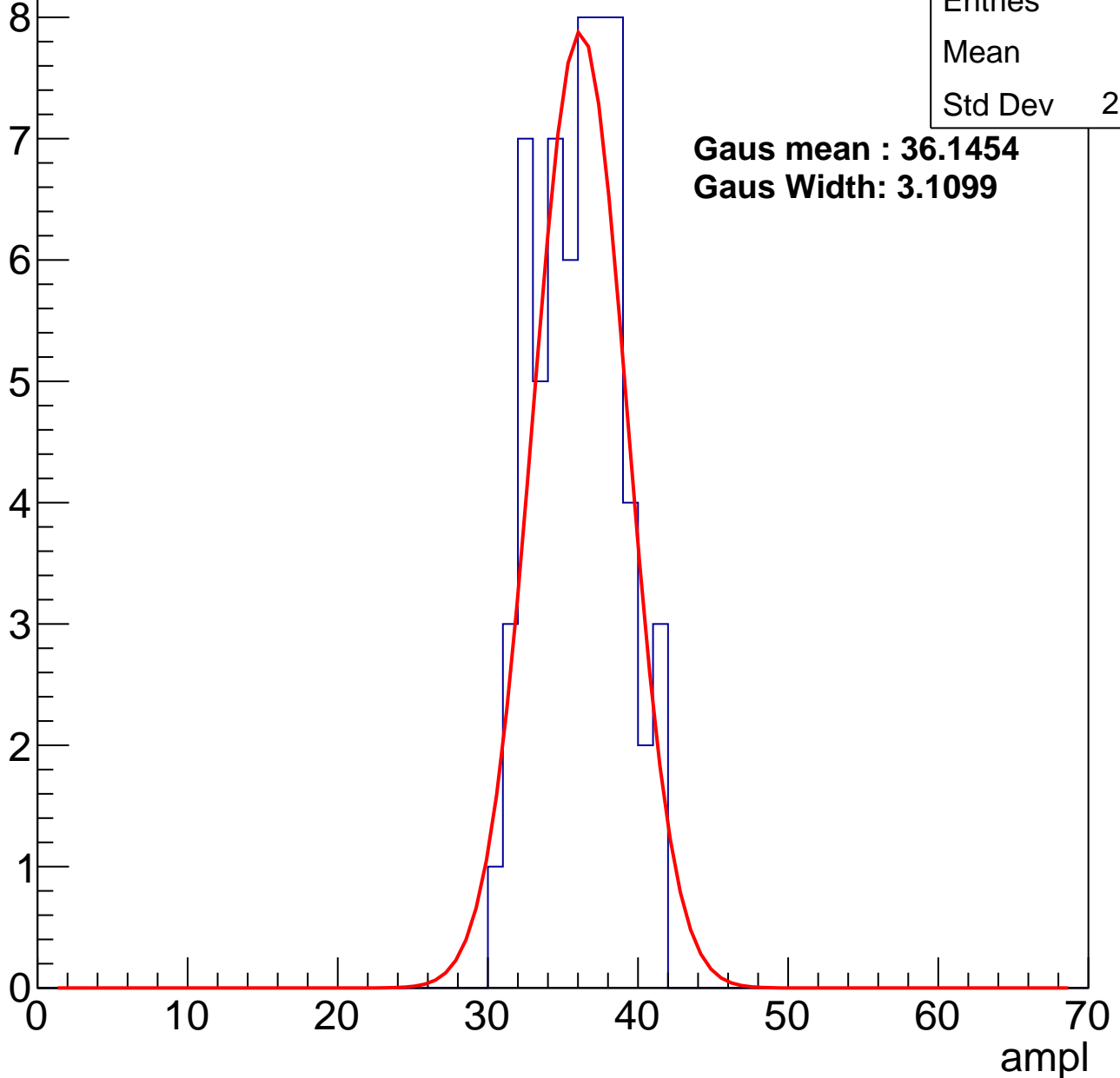
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.6
Std Dev	2.768

**Gaus mean : 36.1454**

**Gaus Width: 3.1099**



# B1L000S, U24-ch63, adc2

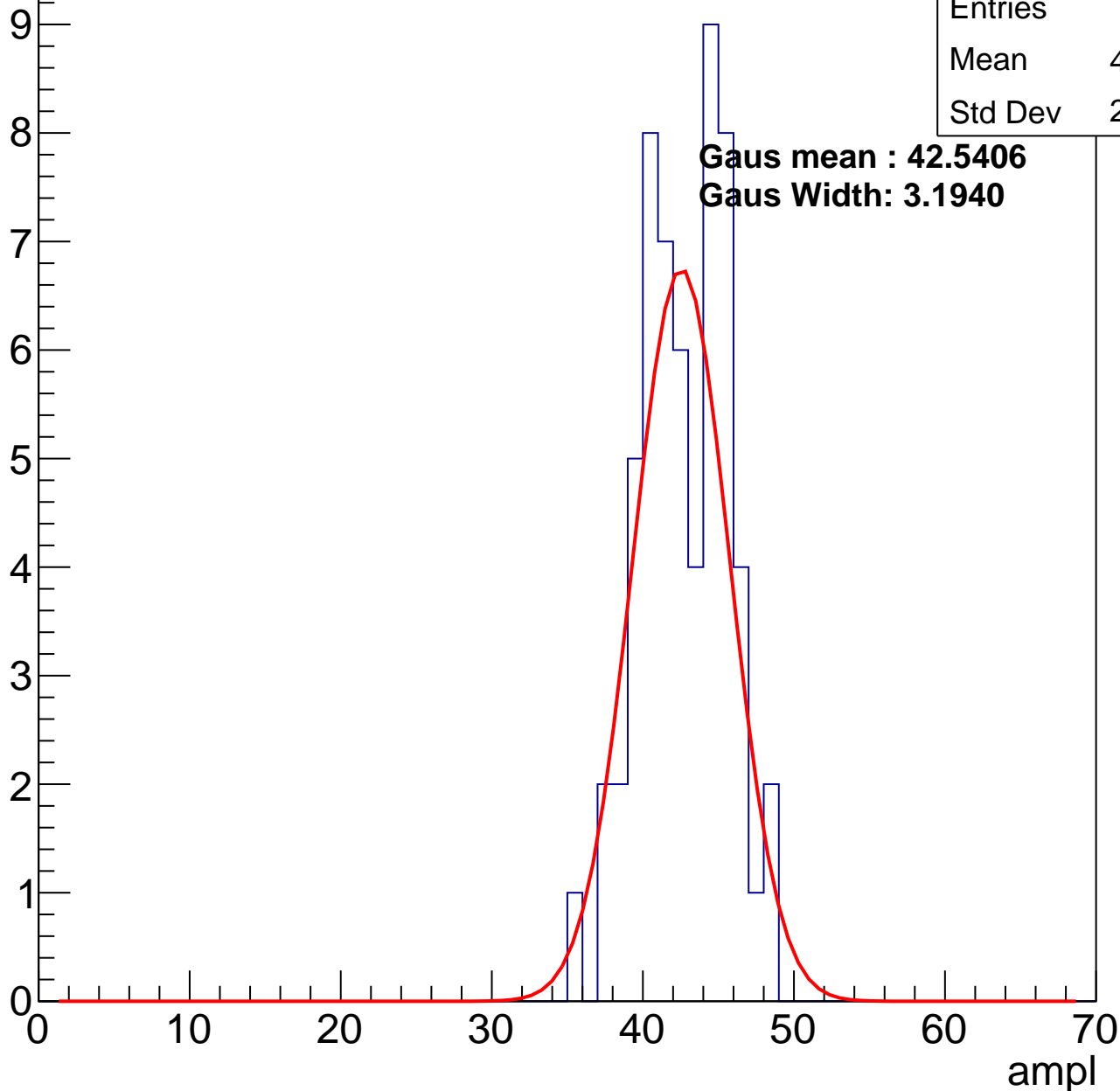
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.27
Std Dev	2.863

**Gaus mean : 42.5406**

**Gaus Width: 3.1940**

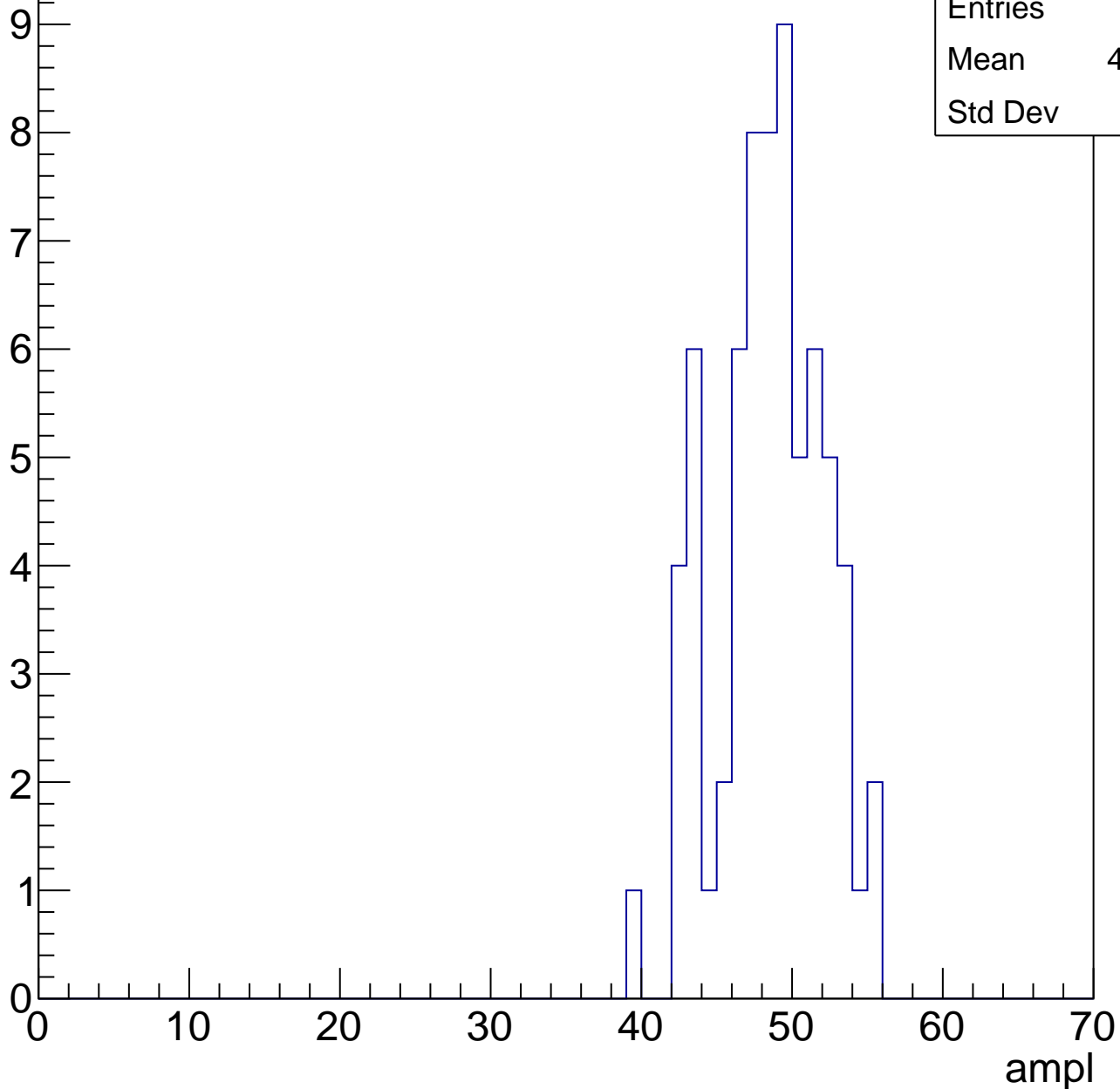


# B1L000S, U24-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	48.06
Std Dev	3.51

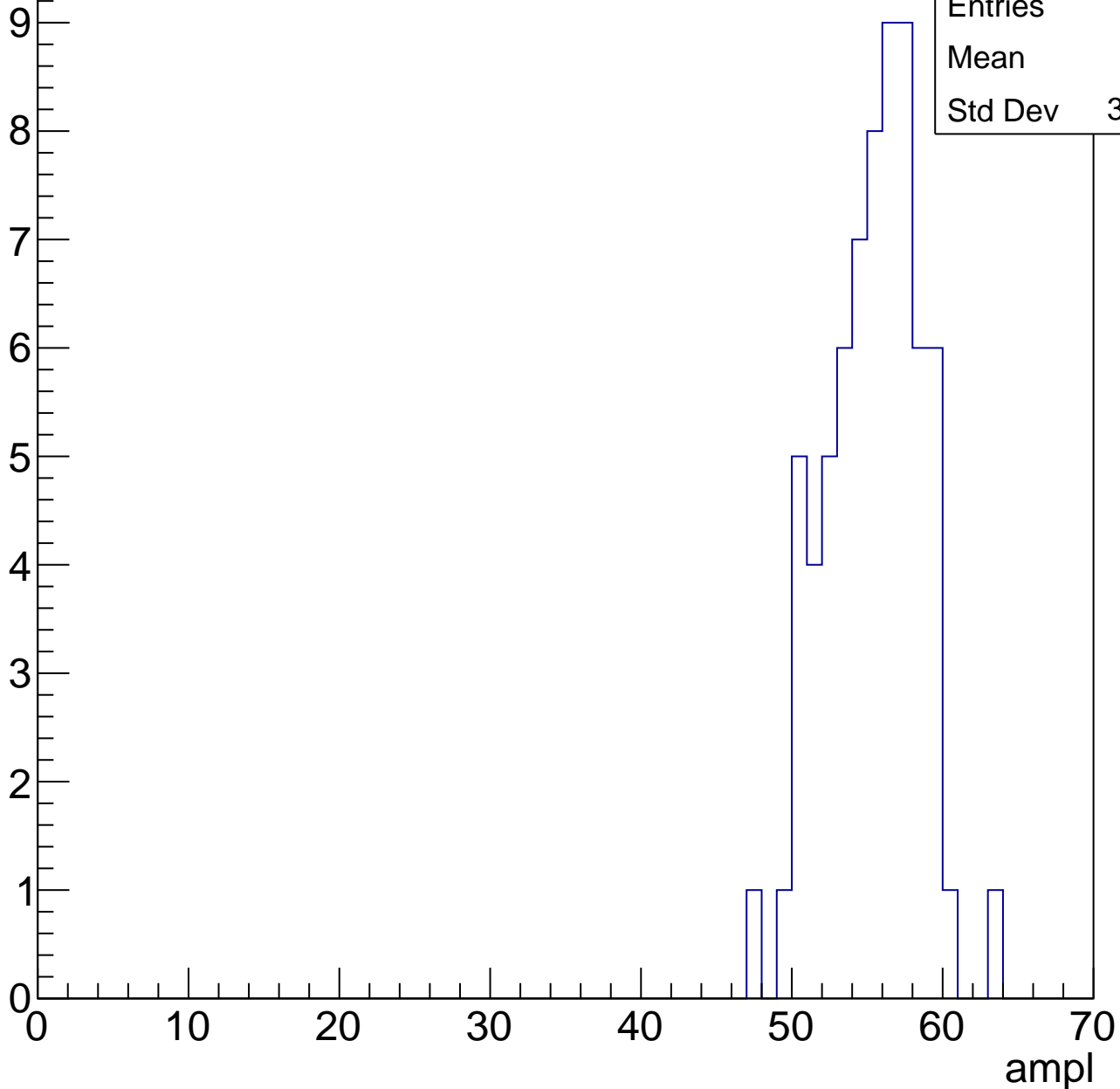


# B1L000S, U24-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

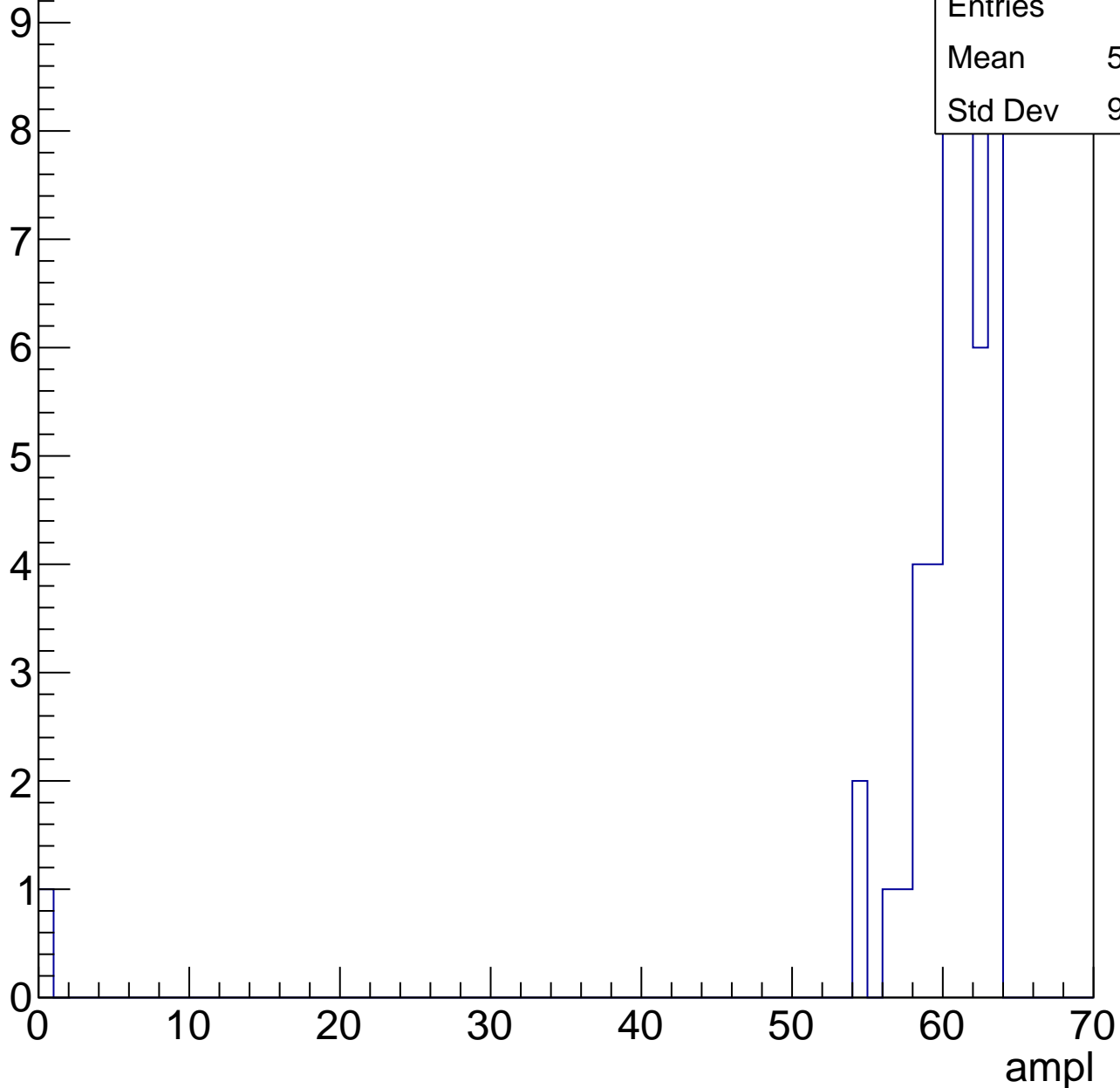
Entries	69
Mean	54.9
Std Dev	3.065



# B1L000S, U24-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

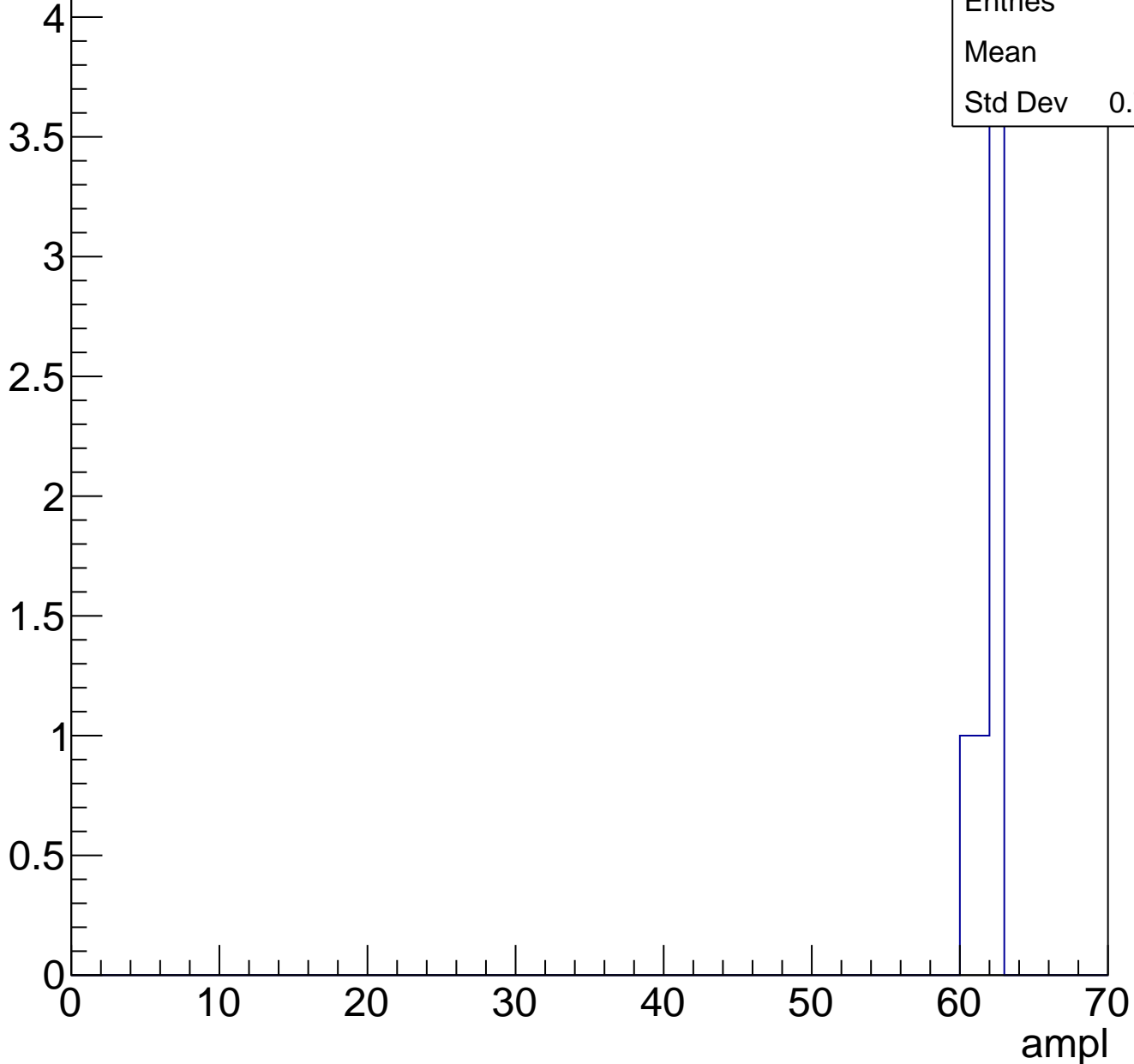
Entry



# B1L000S, U24-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch64, adc0

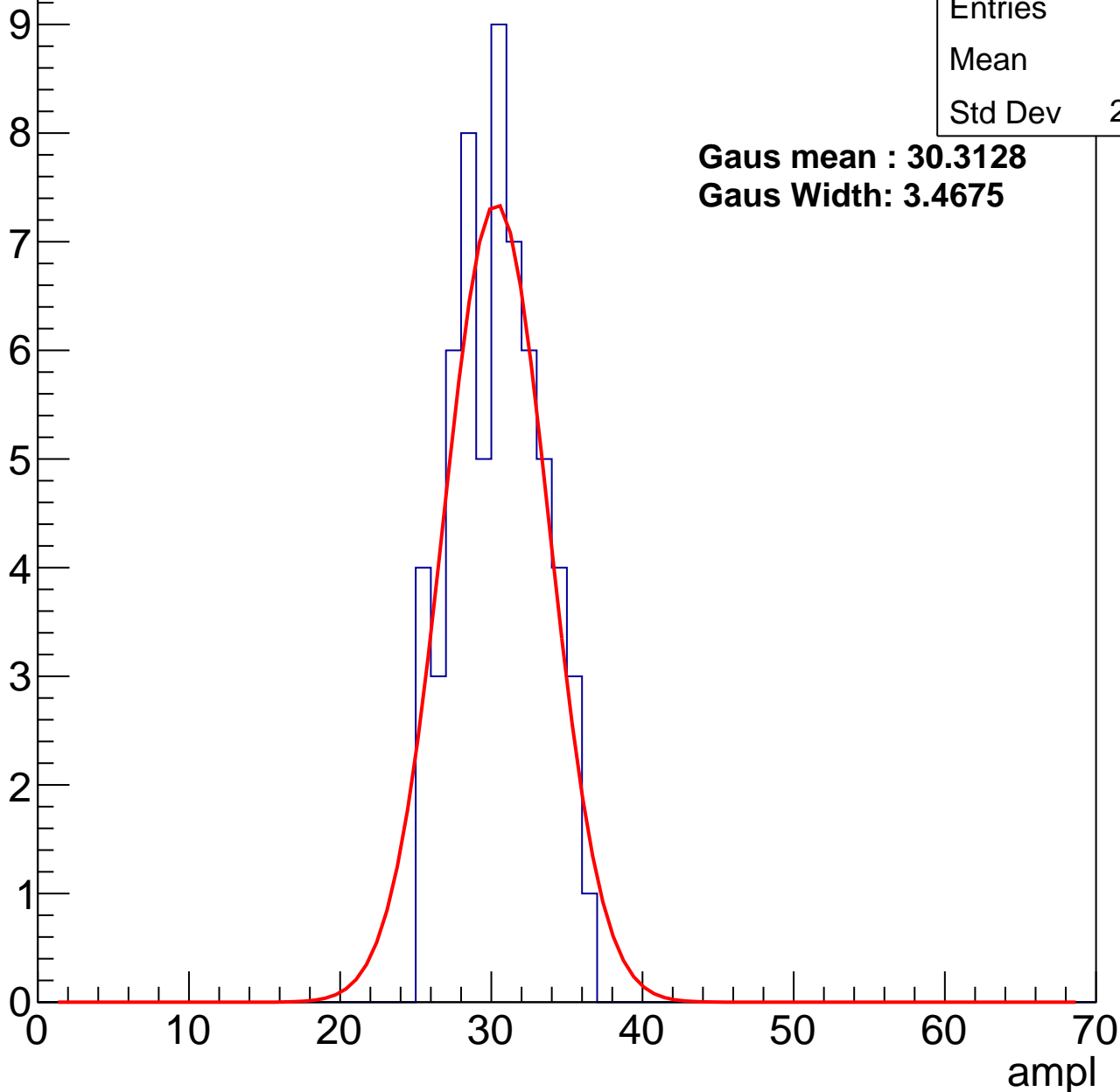
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	30
Std Dev	2.834

**Gaus mean : 30.3128**

**Gaus Width: 3.4675**



# B1L000S, U24-ch64, adc1

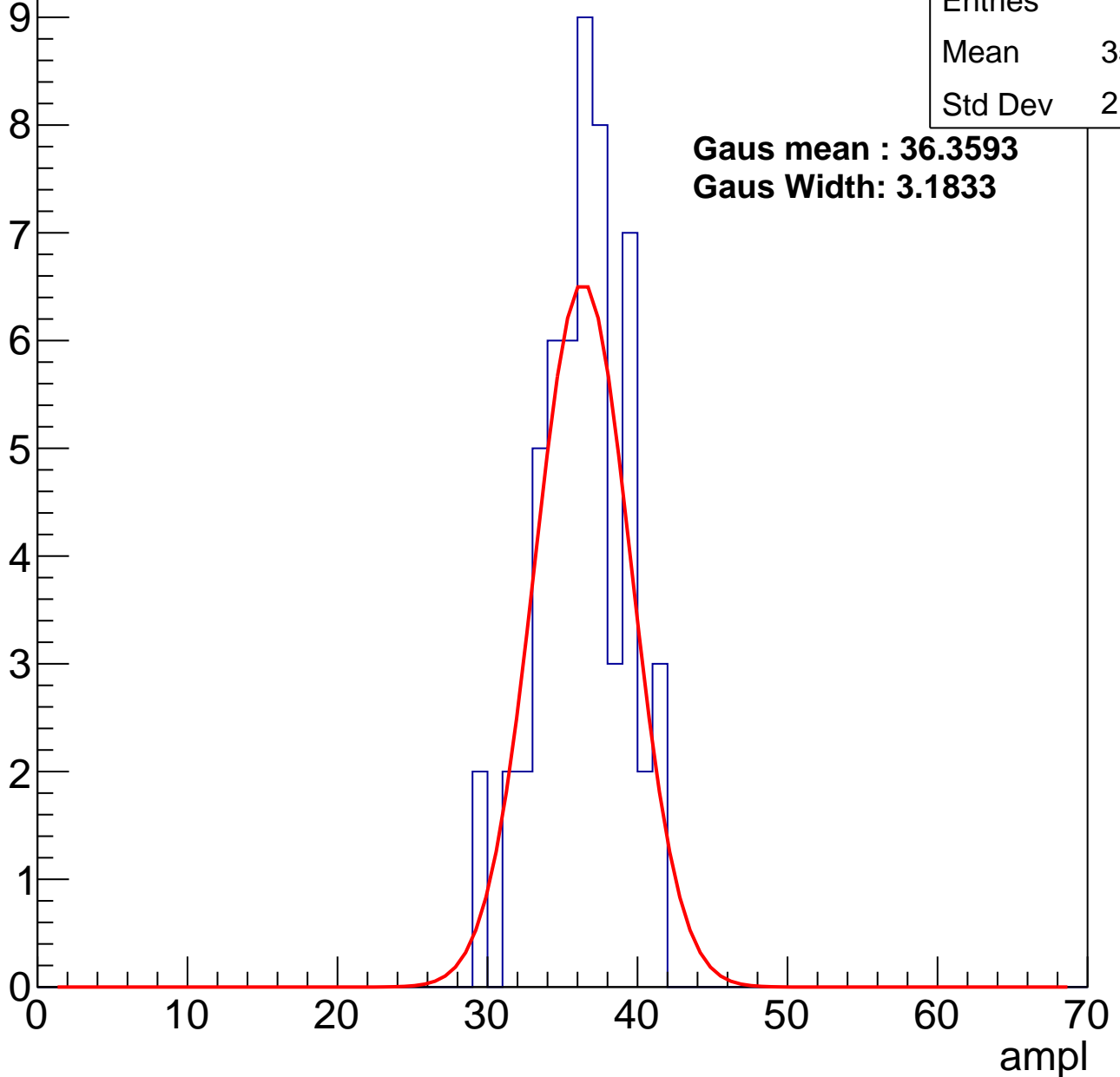
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	35.87
Std Dev	2.842

**Gaus mean : 36.3593**

**Gaus Width: 3.1833**



# B1L000S, U24-ch64, adc2

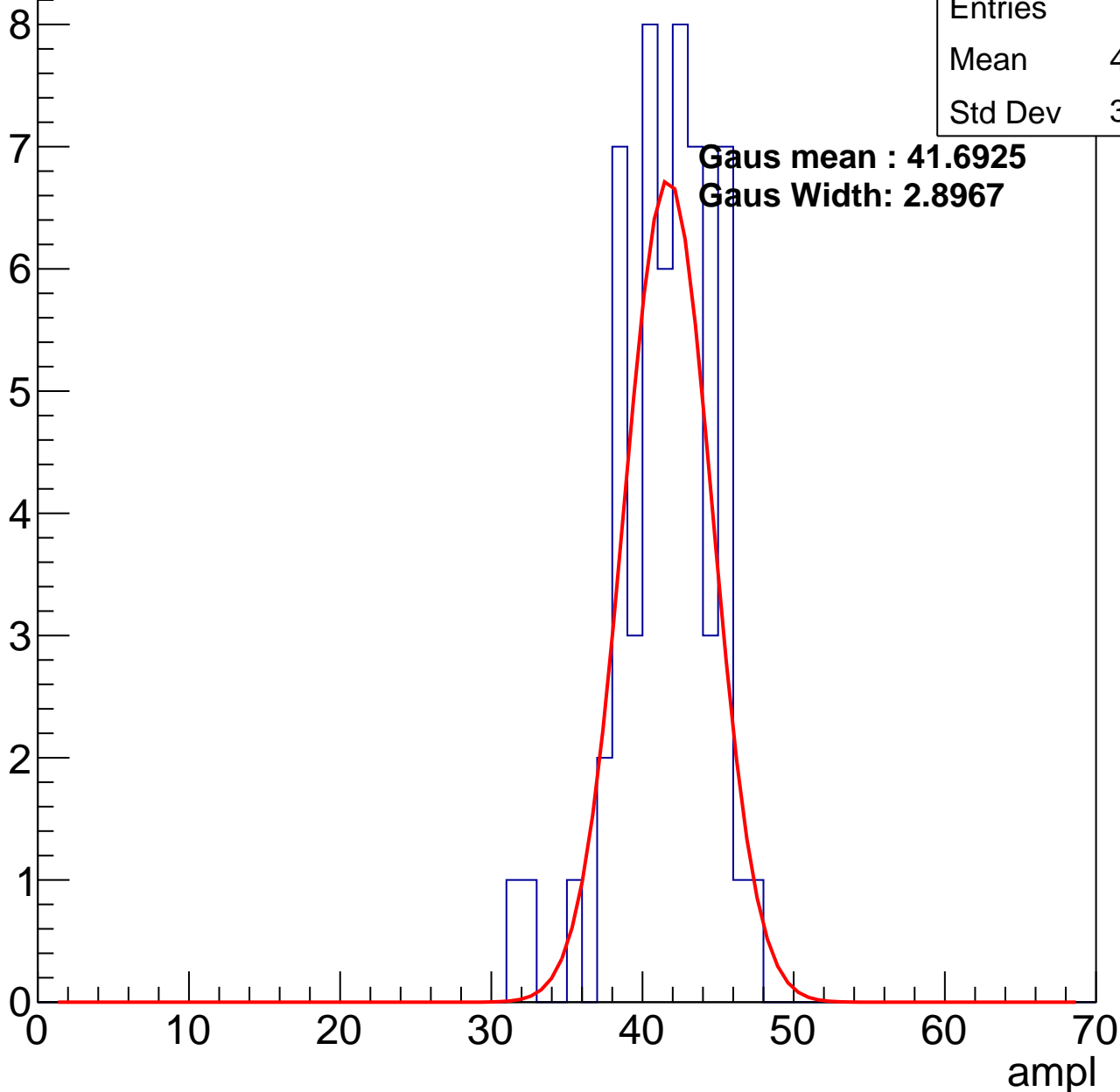
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.04
Std Dev	3.185

**Gaus mean : 41.6925**

**Gaus Width: 2.8967**

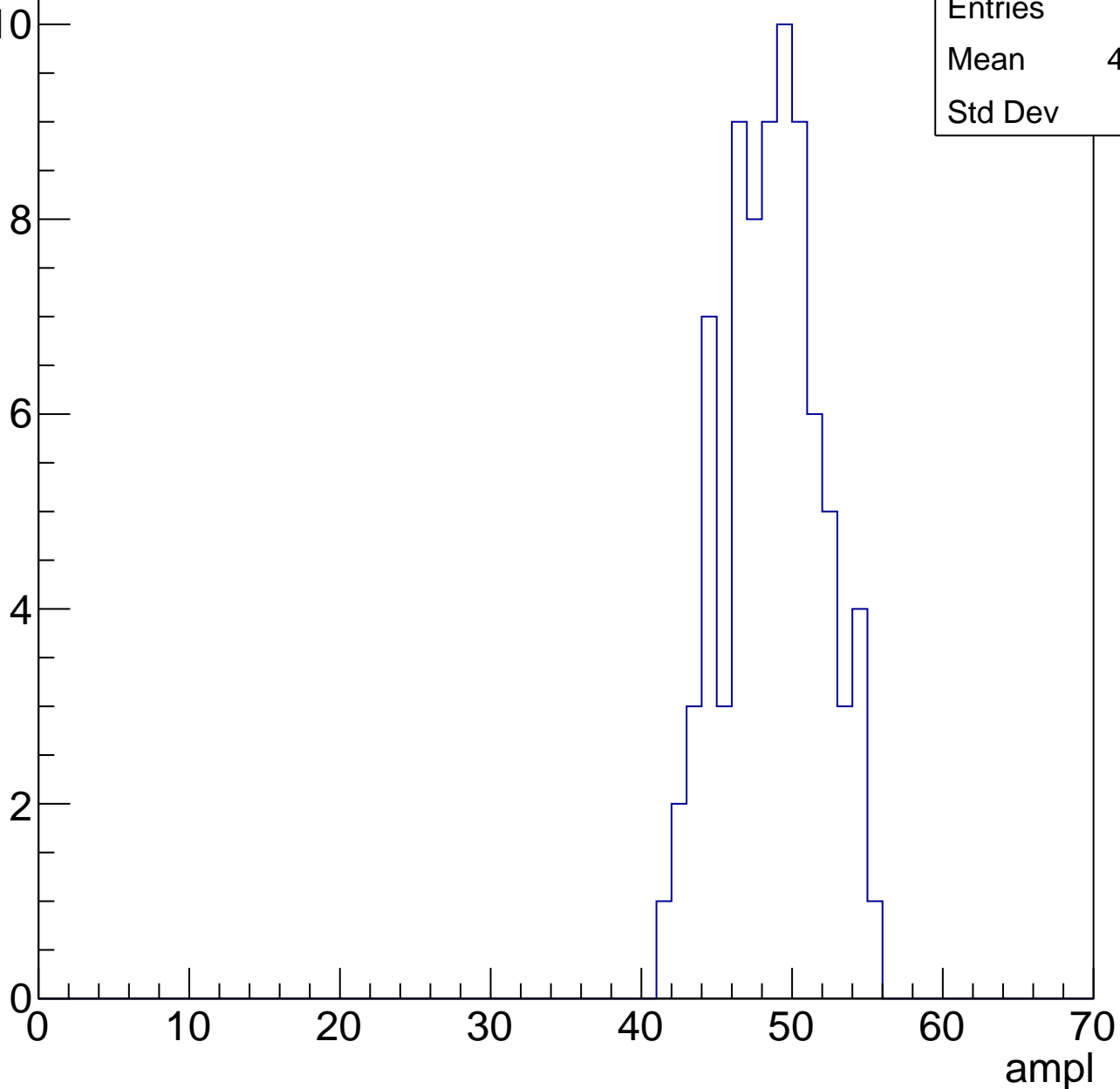


# B1L000S, U24-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

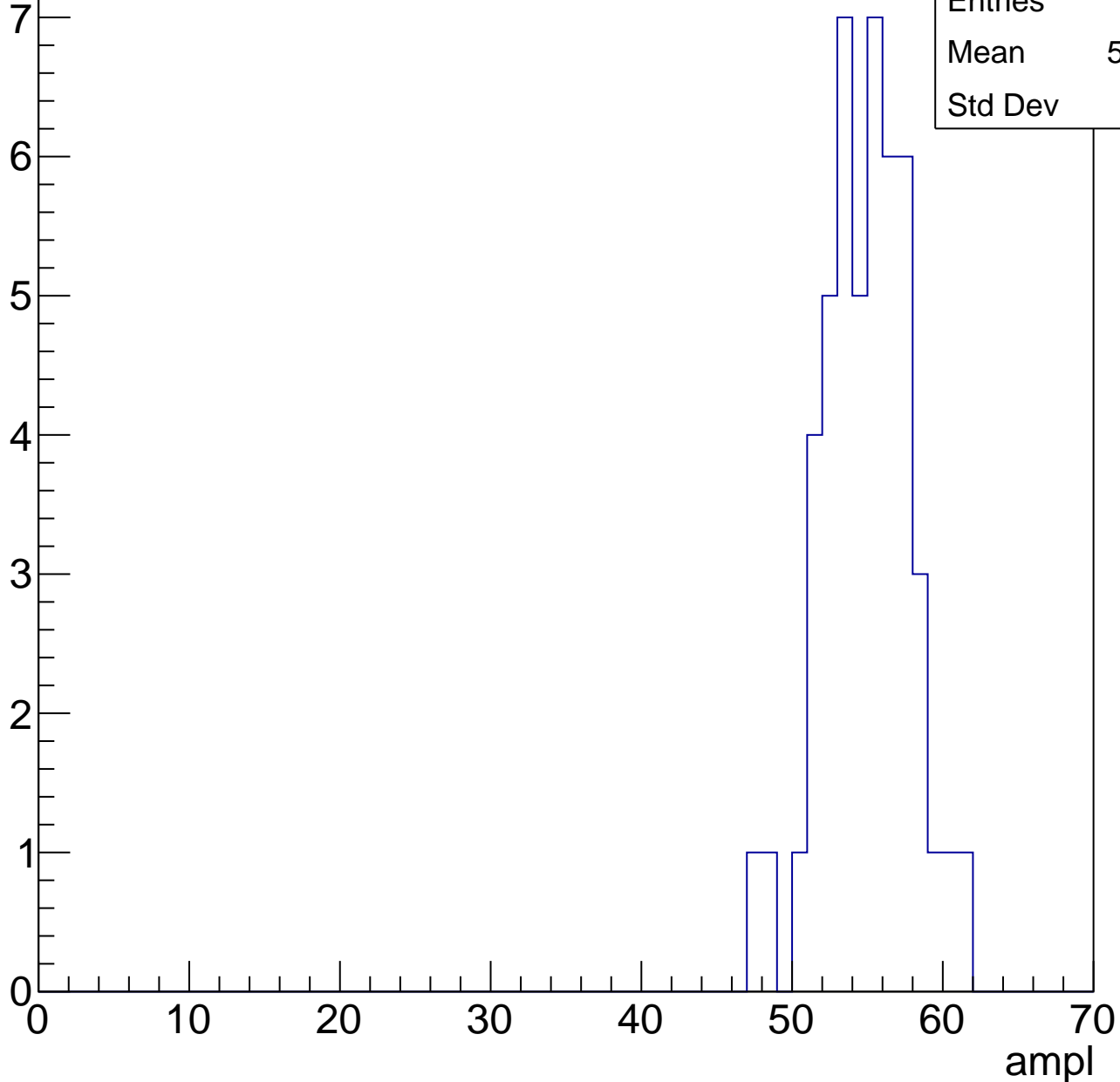
Entries	80
Mean	48.19
Std Dev	3.21



# B1L000S, U24-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



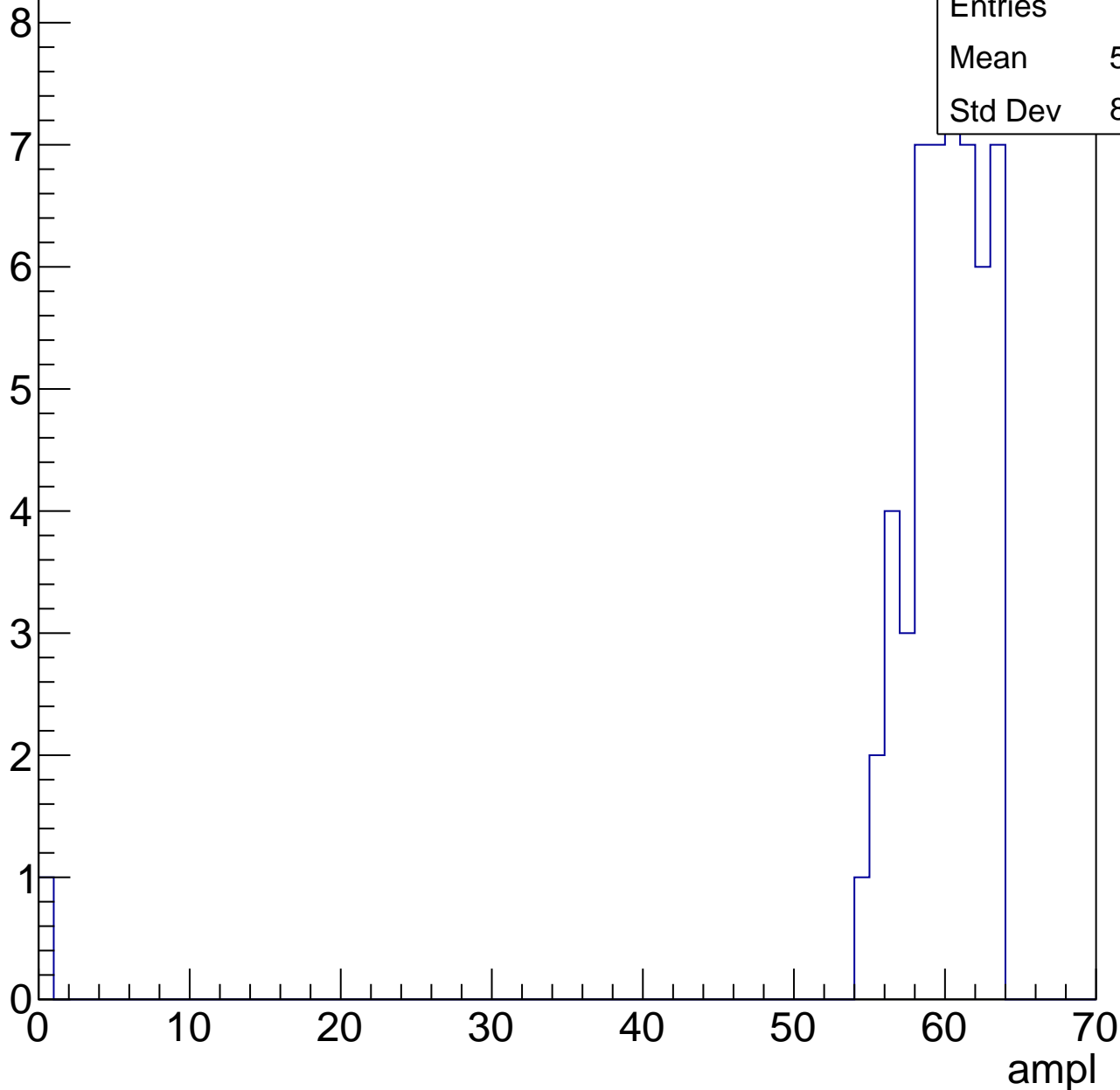
Entries	49
Mean	54.43
Std Dev	2.85

# B1L000S, U24-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

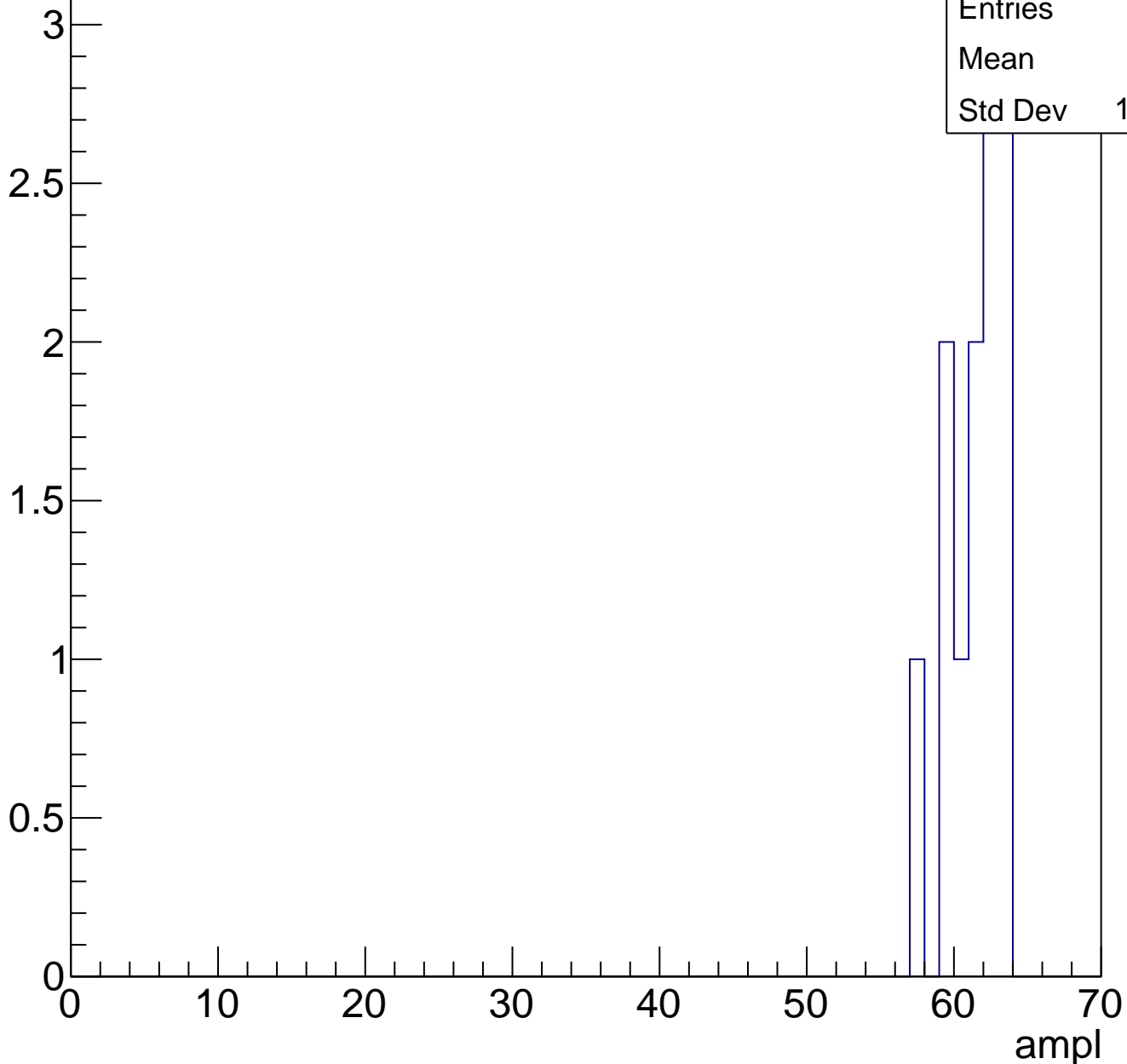
Entries	53
Mean	58.45
Std Dev	8.444



# B1L000S, U24-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

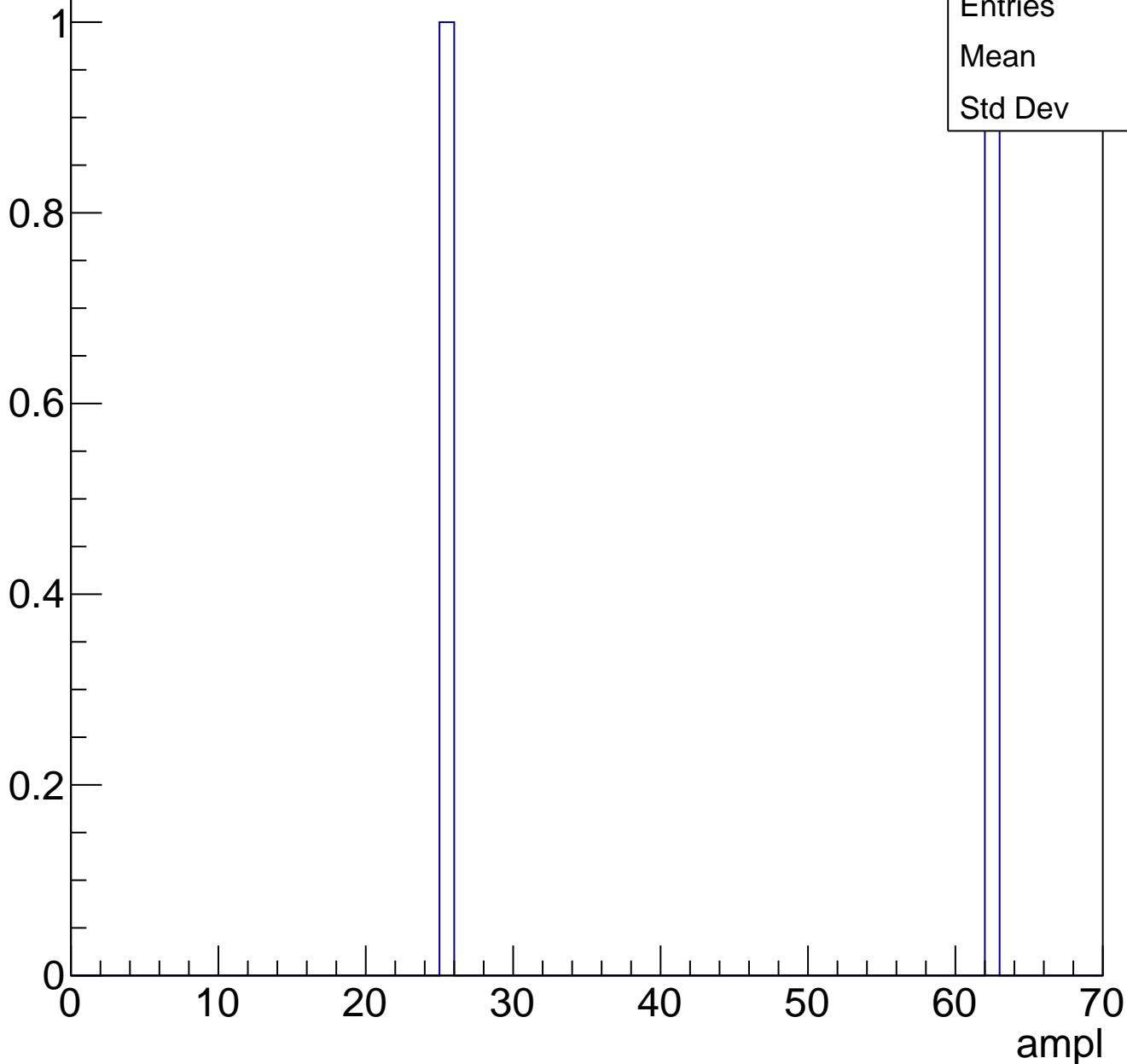




# B1L000S, U24-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.22
Std Dev	5.981

**Gaus mean : 29.6191**

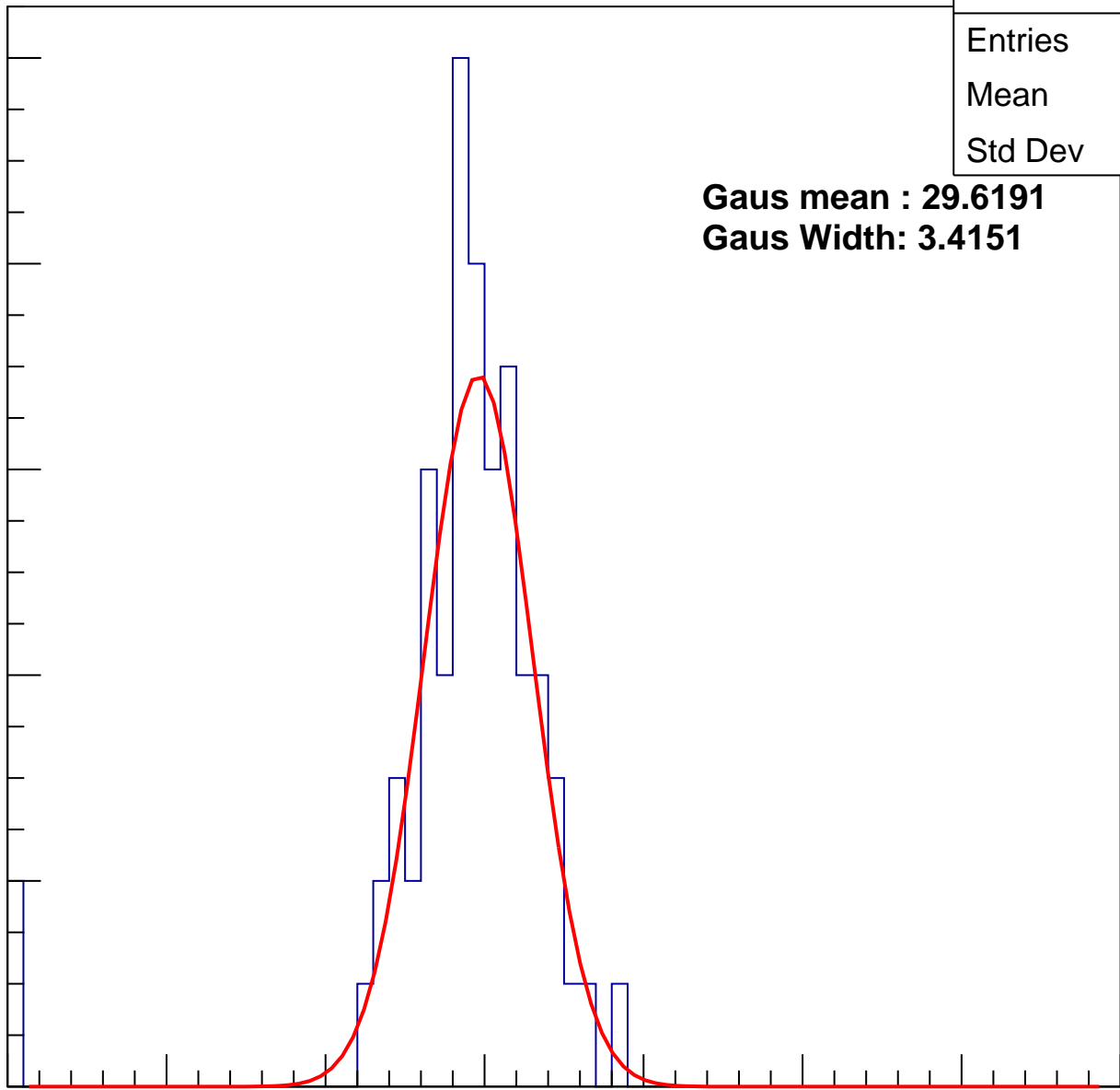
**Gaus Width: 3.4151**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch65, adc1

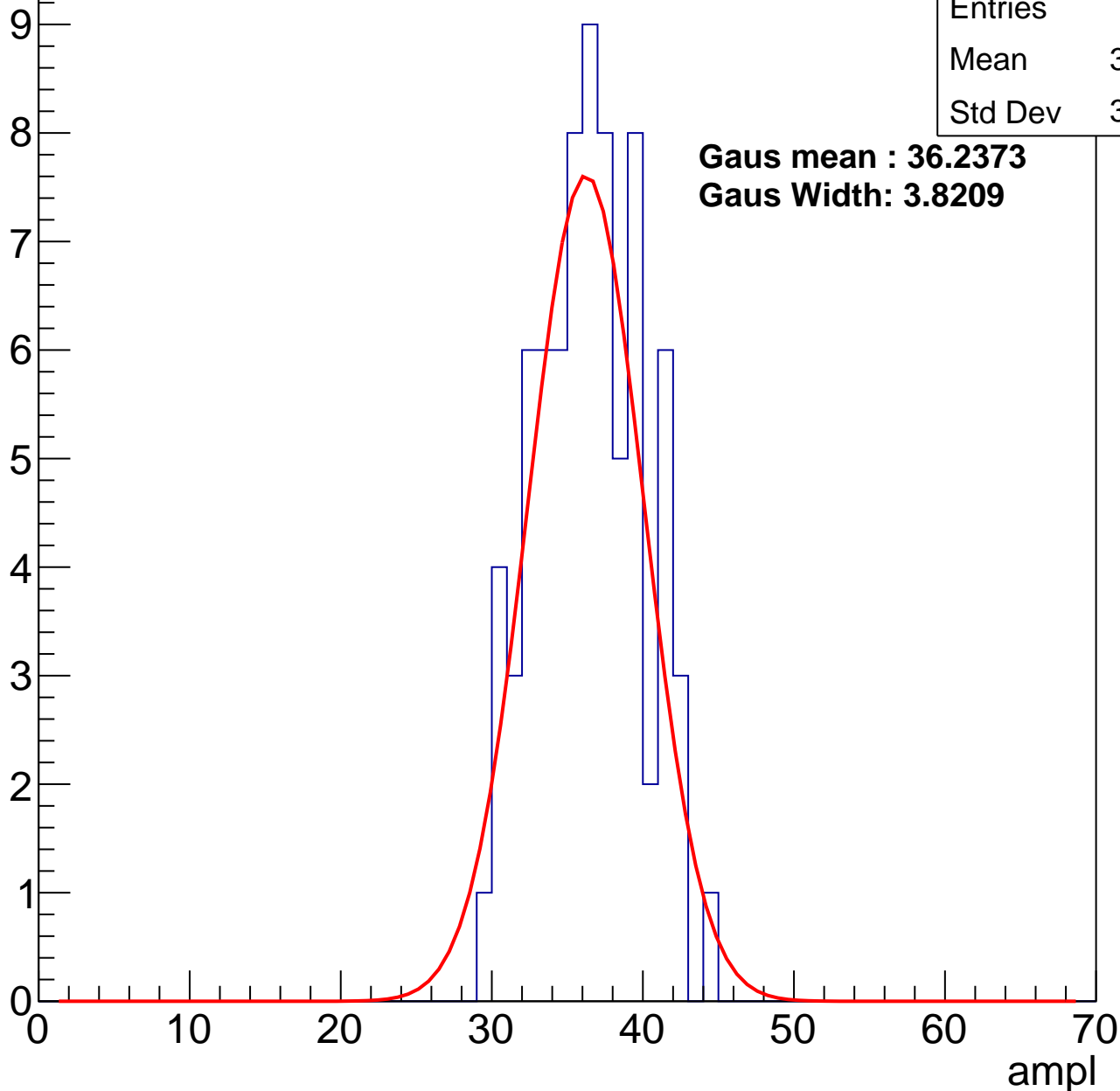
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	35.97
Std Dev	3.449

**Gaus mean : 36.2373**

**Gaus Width: 3.8209**



# B1L000S, U24-ch65, adc2

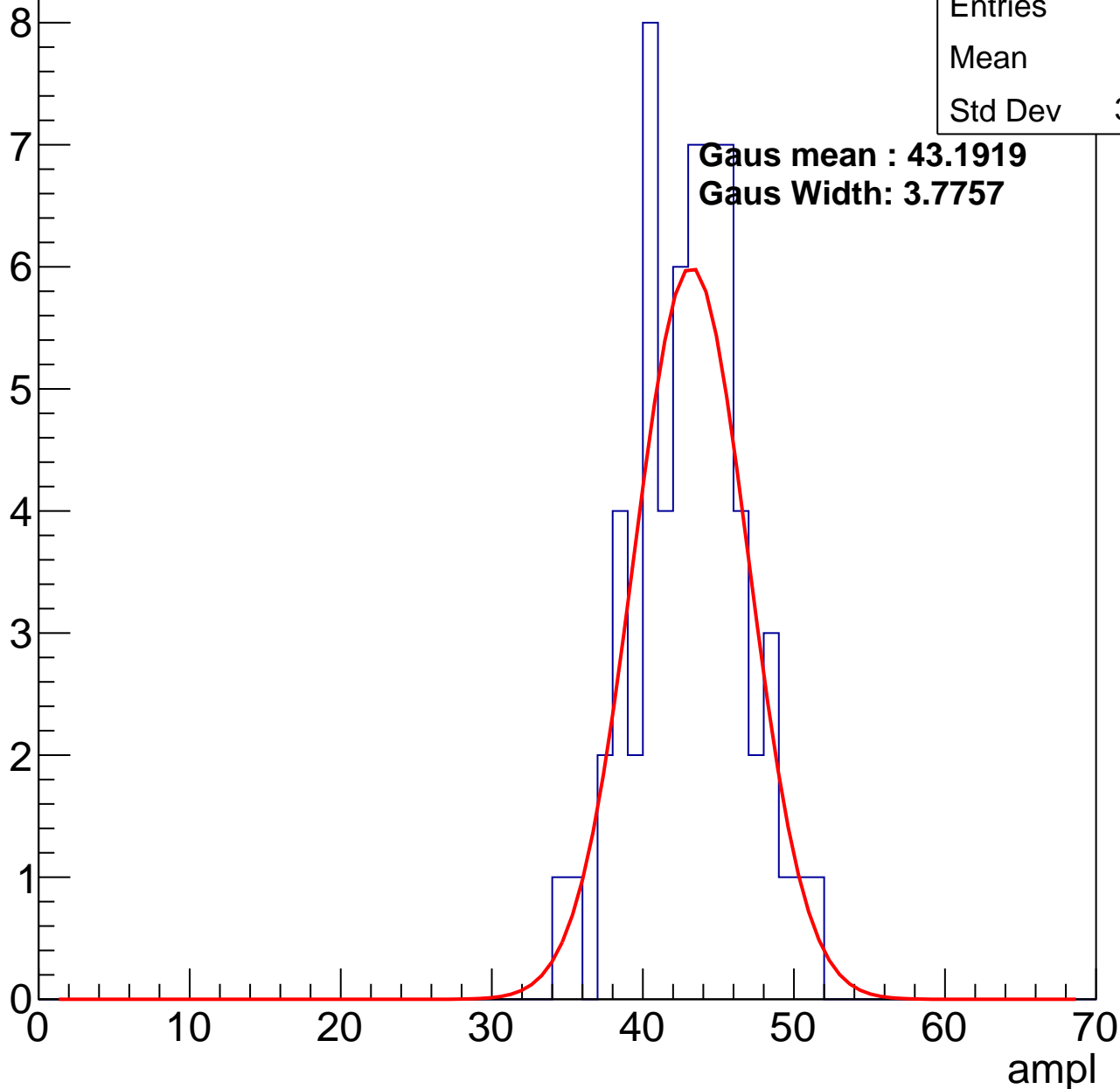
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.7
Std Dev	3.541

**Gaus mean : 43.1919**

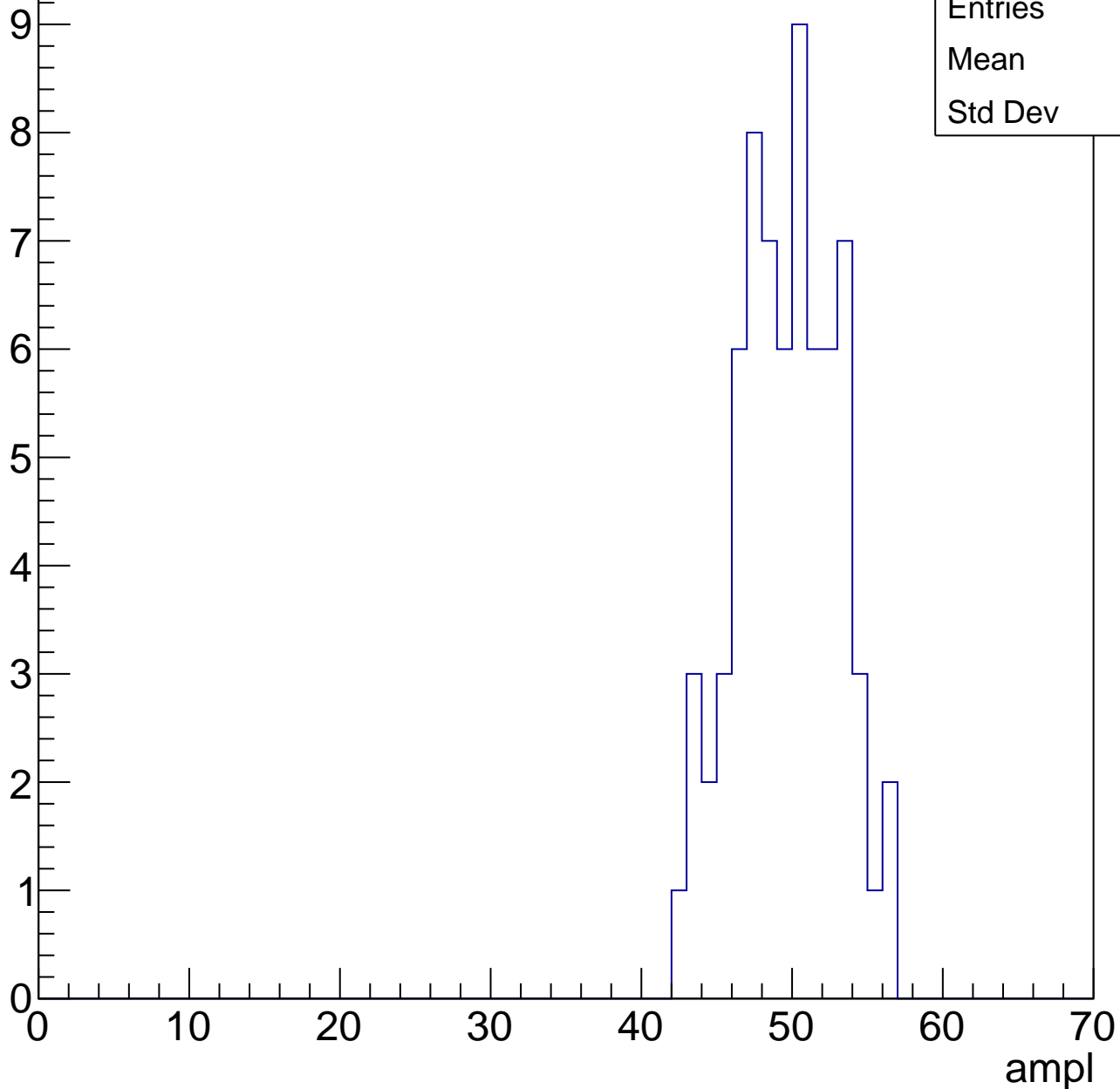
**Gaus Width: 3.7757**



# B1L000S, U24-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



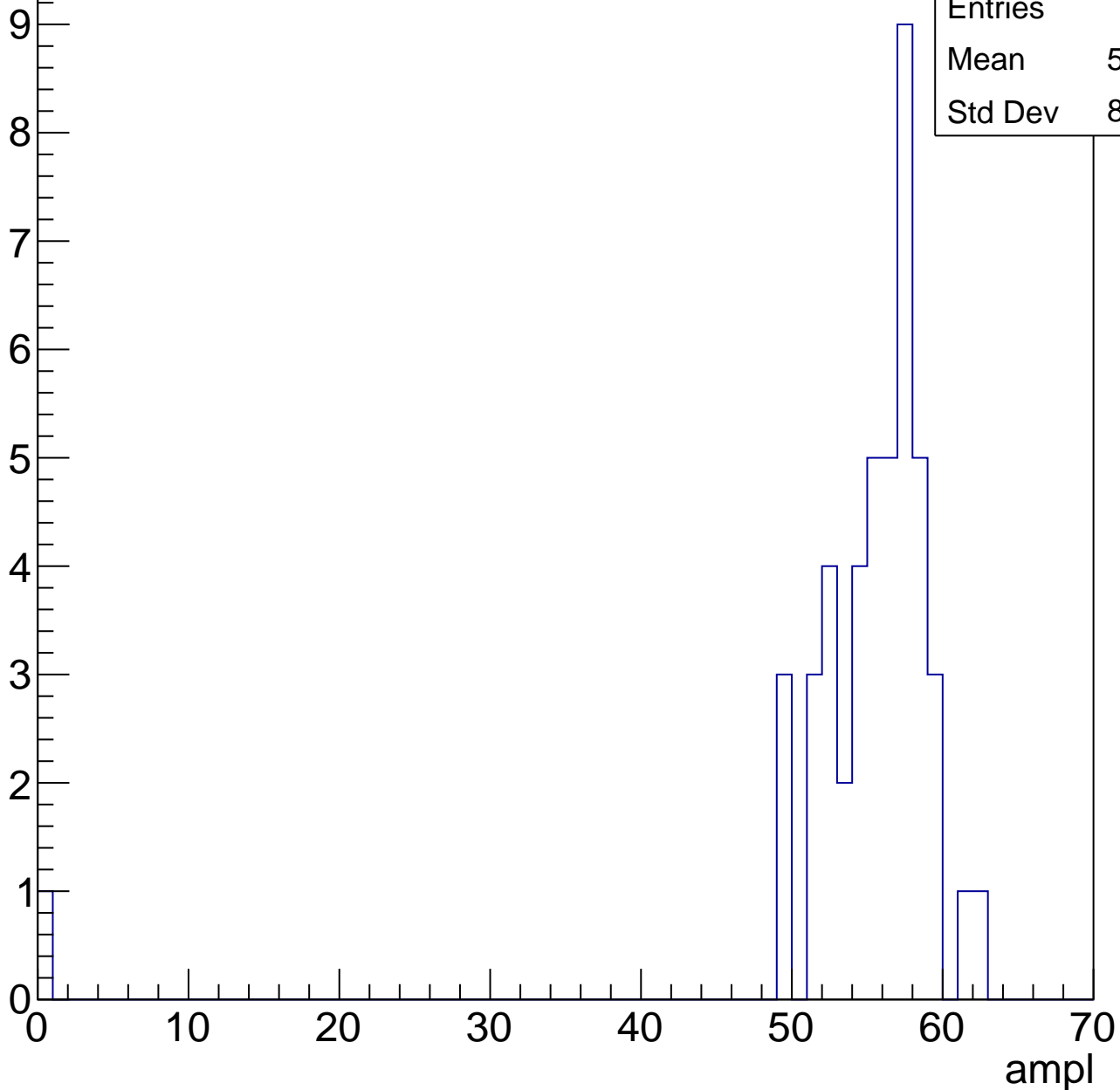
Entries	70
Mean	49.2
Std Dev	3.28

# B1L000S, U24-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	54.09
Std Dev	8.609



# B1L000S, U24-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	58
Mean	59.5
Std Dev	2.75

# B1L000S, U24-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch66, adc0

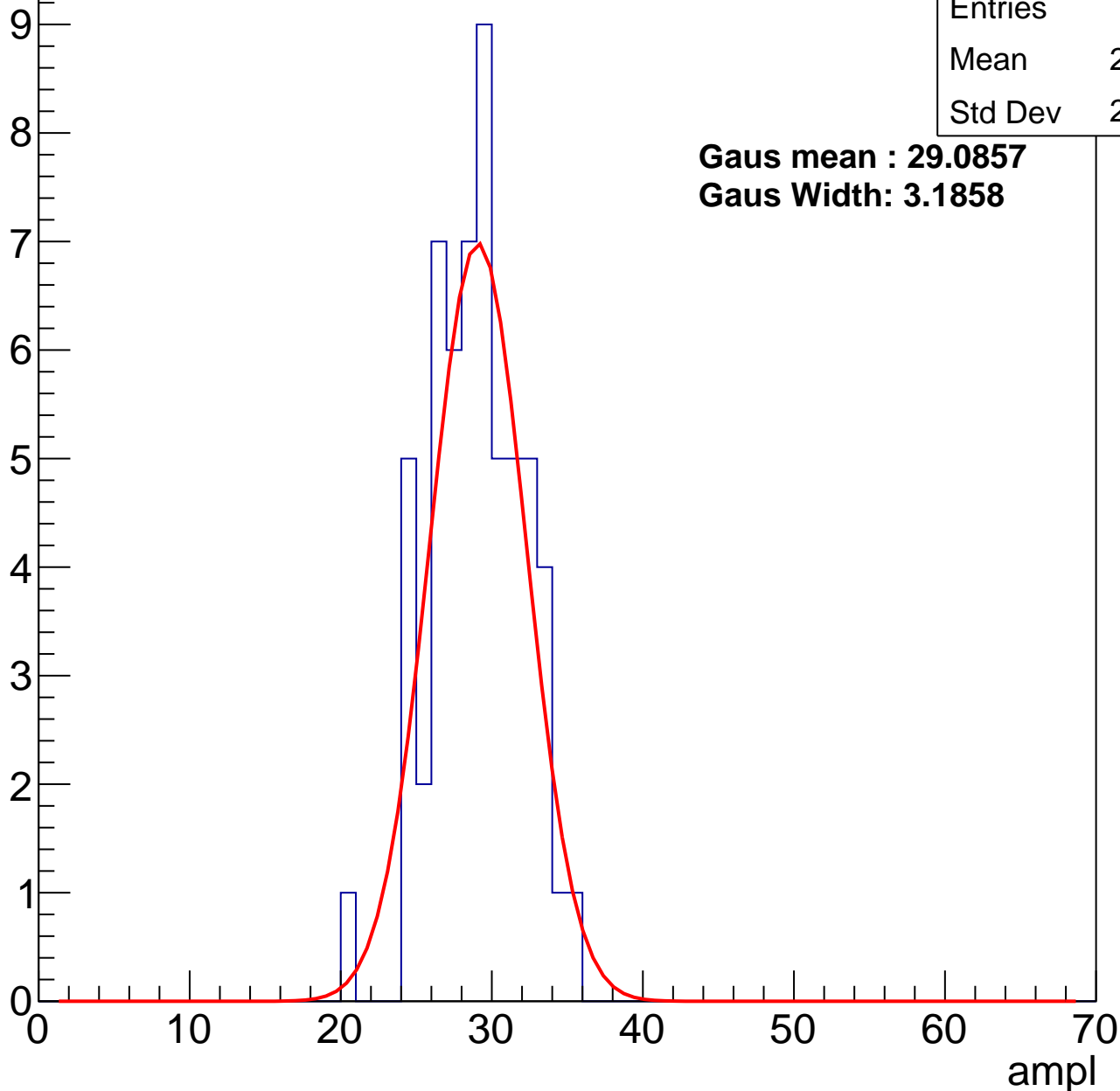
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	28.57
Std Dev	2.983

**Gaus mean : 29.0857**

**Gaus Width: 3.1858**



# B1L000S, U24-ch66, adc1

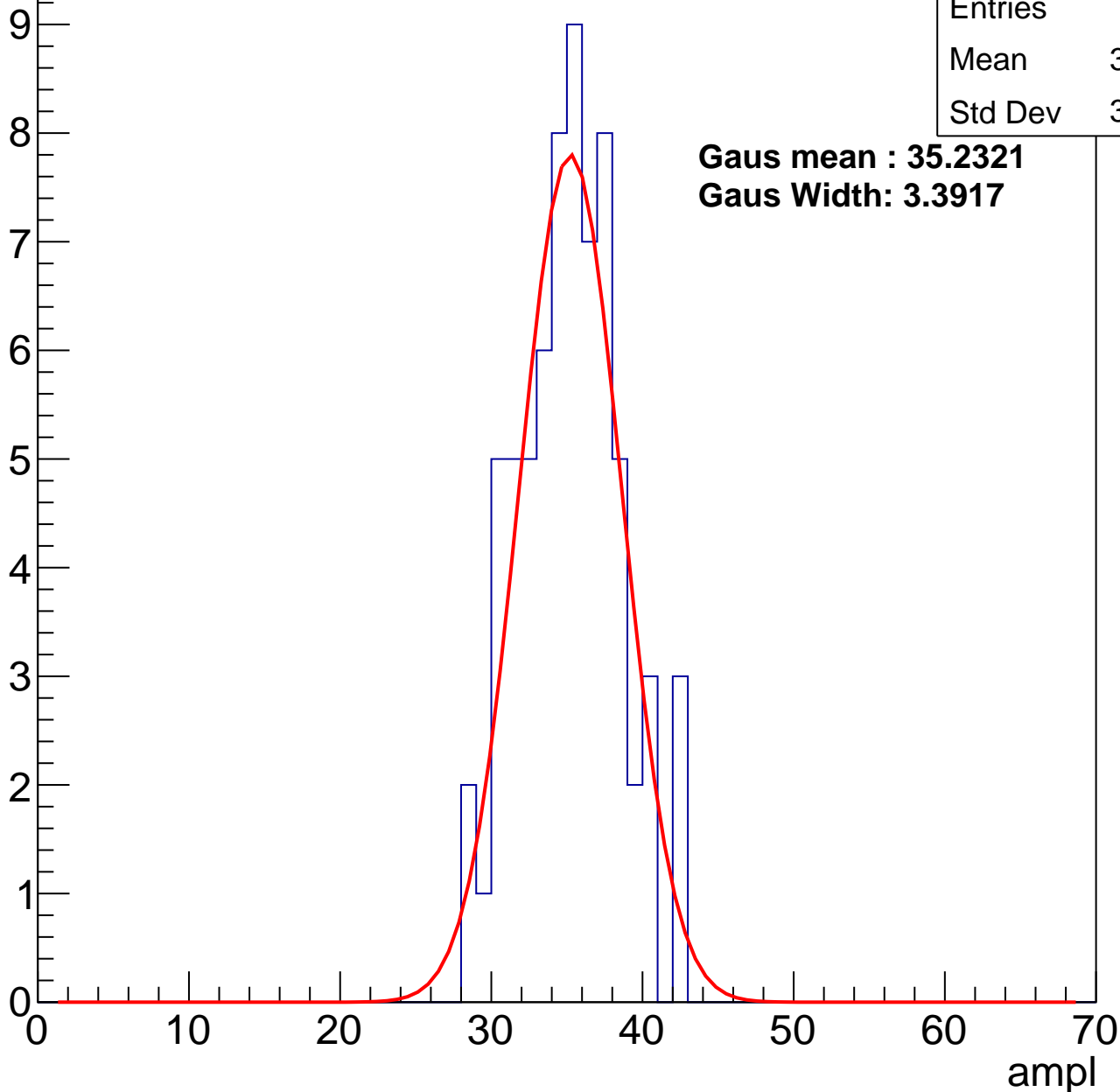
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.74
Std Dev	3.295

**Gaus mean : 35.2321**

**Gaus Width: 3.3917**



# B1L000S, U24-ch66, adc2

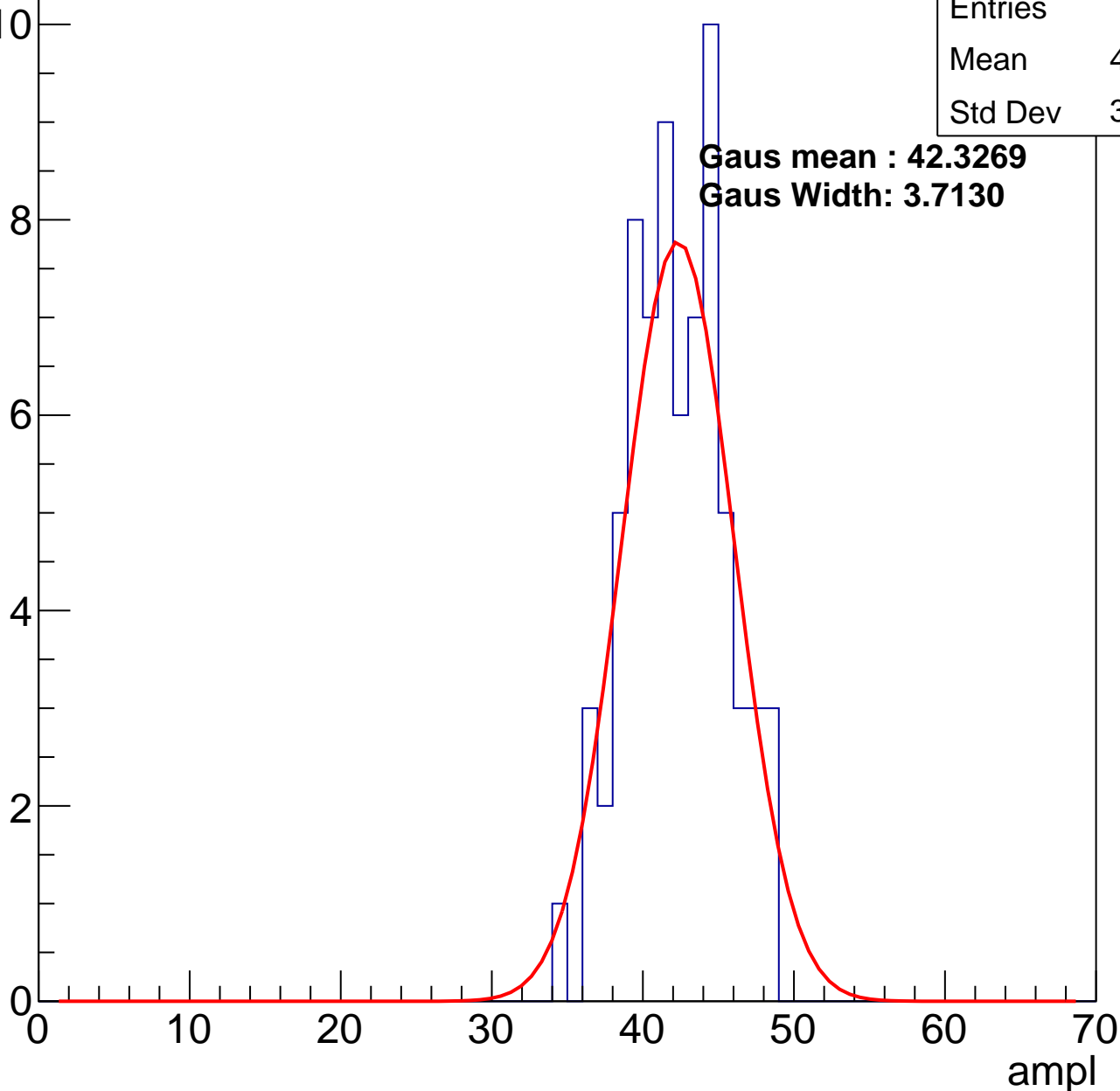
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.78
Std Dev	3.185

**Gaus mean : 42.3269**

**Gaus Width: 3.7130**

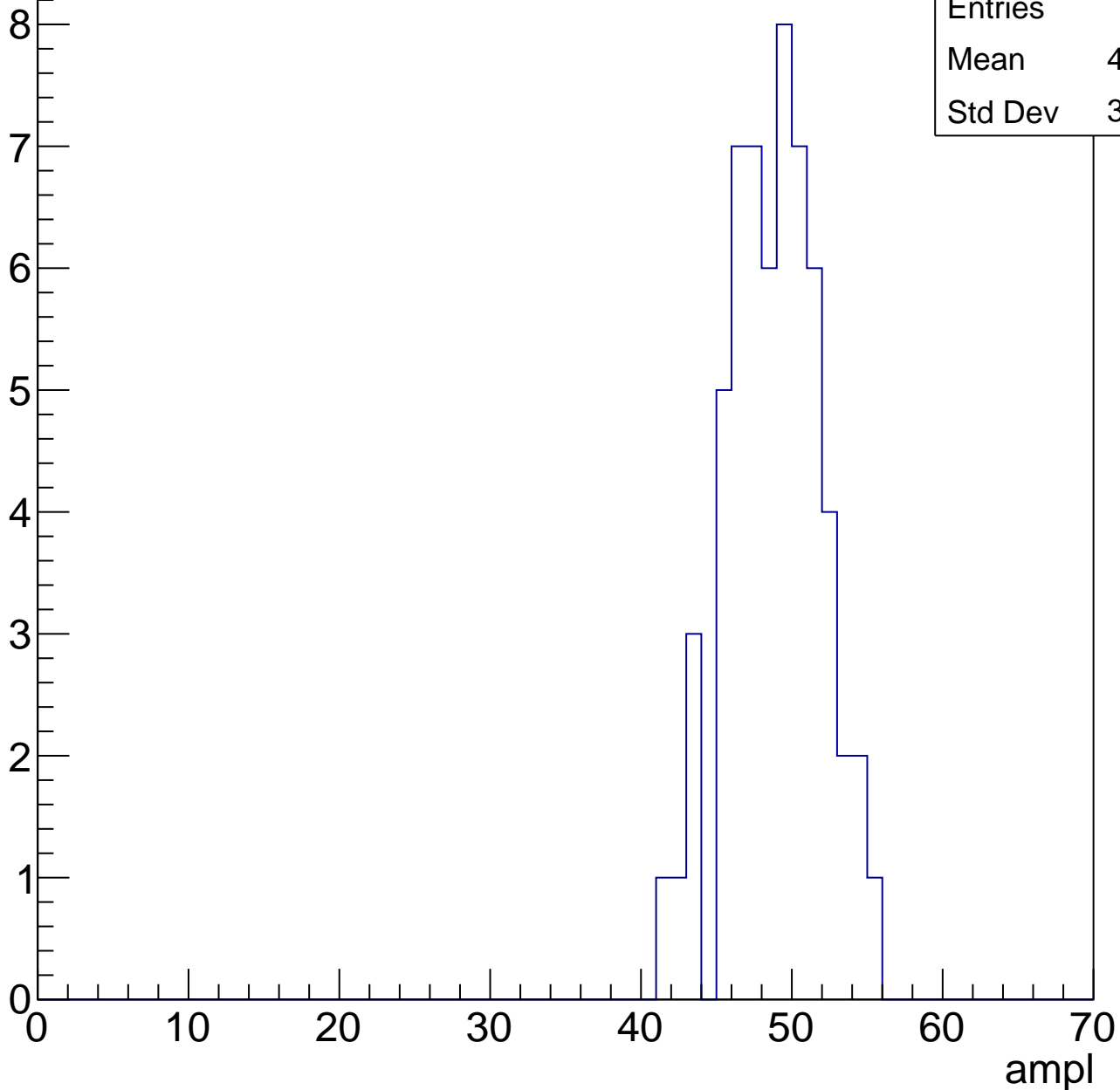


# B1L000S, U24-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	48.35
Std Dev	3.049

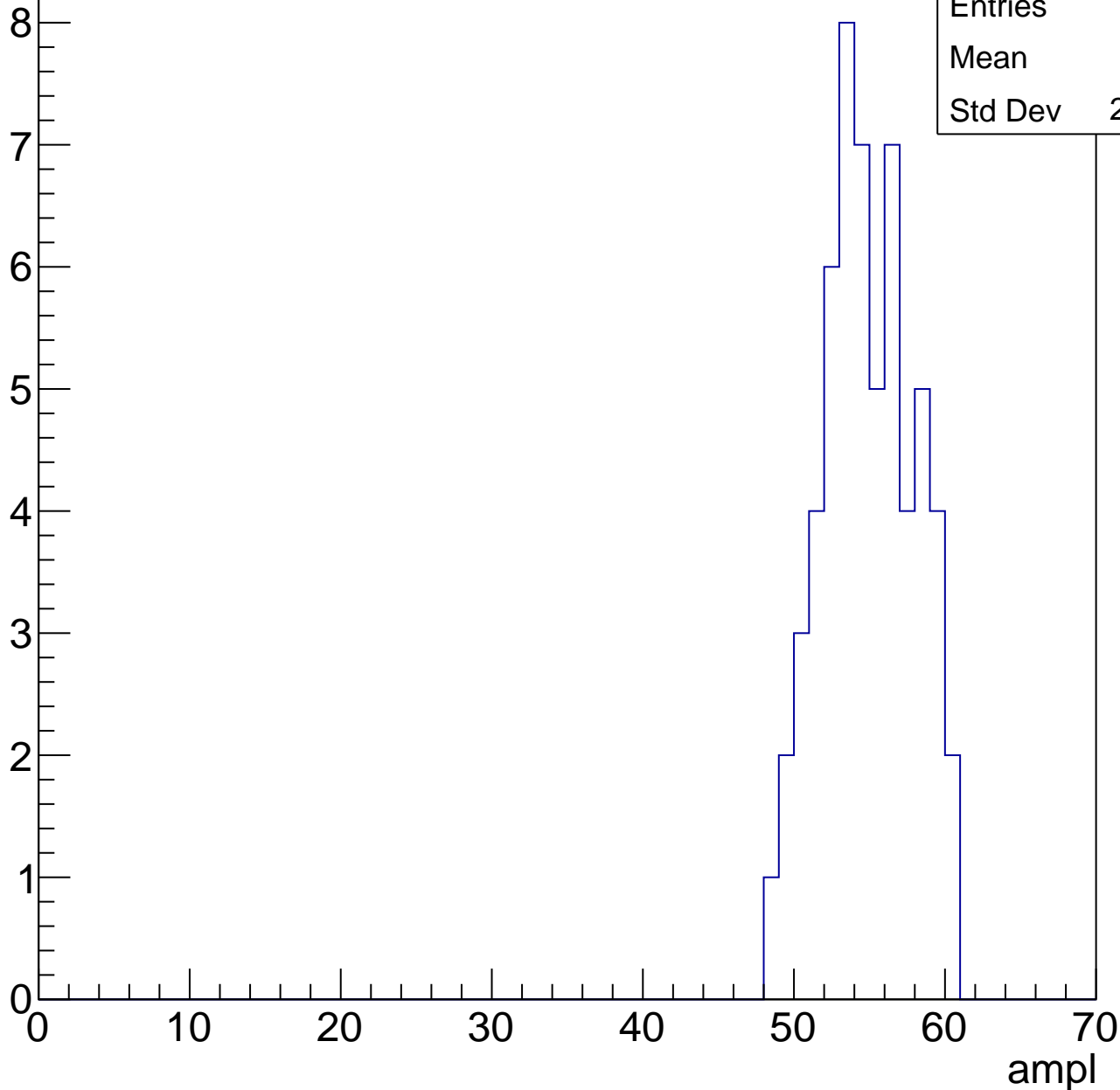


# B1L000S, U24-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	54.4
Std Dev	2.977



# B1L000S, U24-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

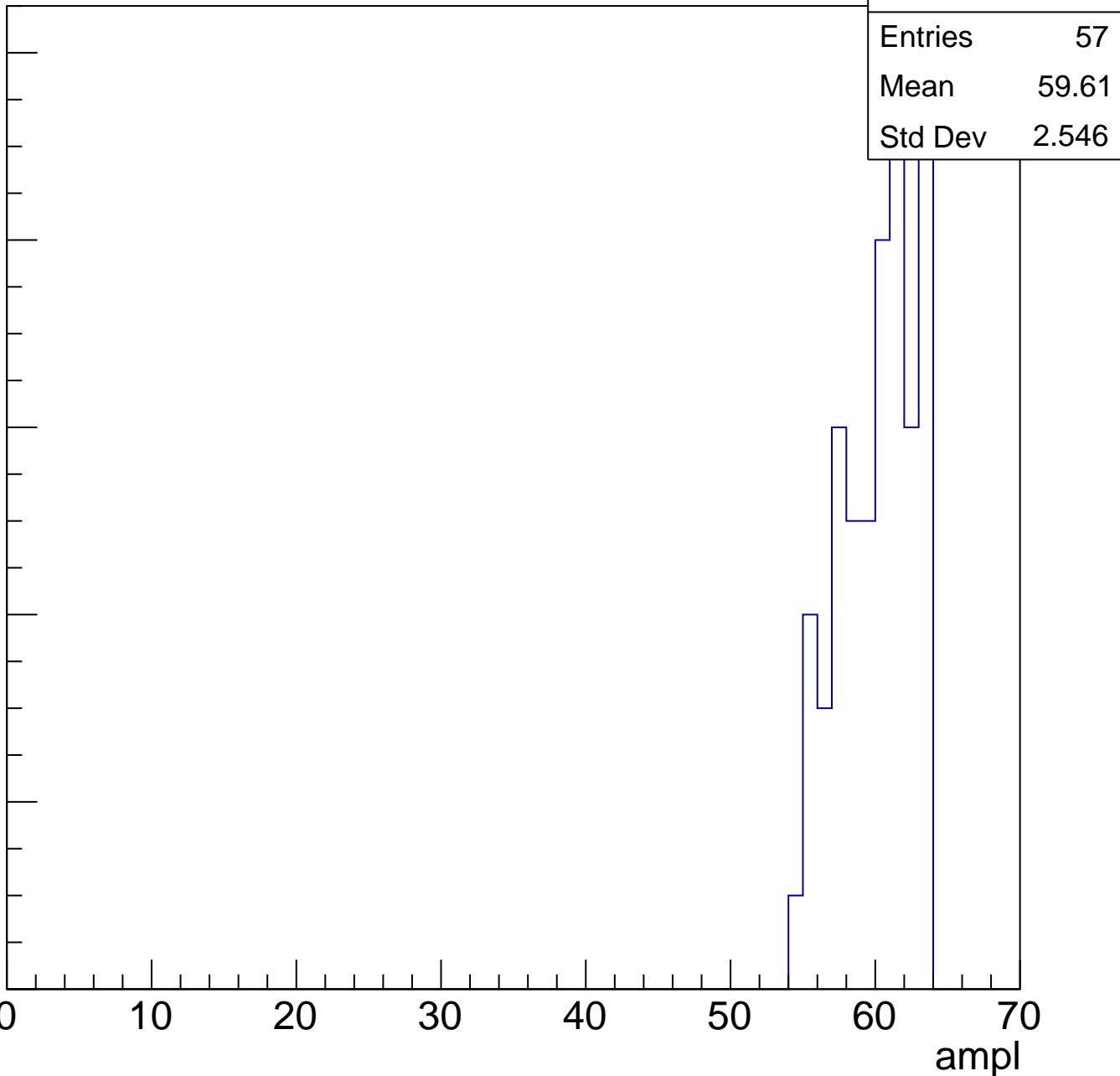
50

60

70

ampl

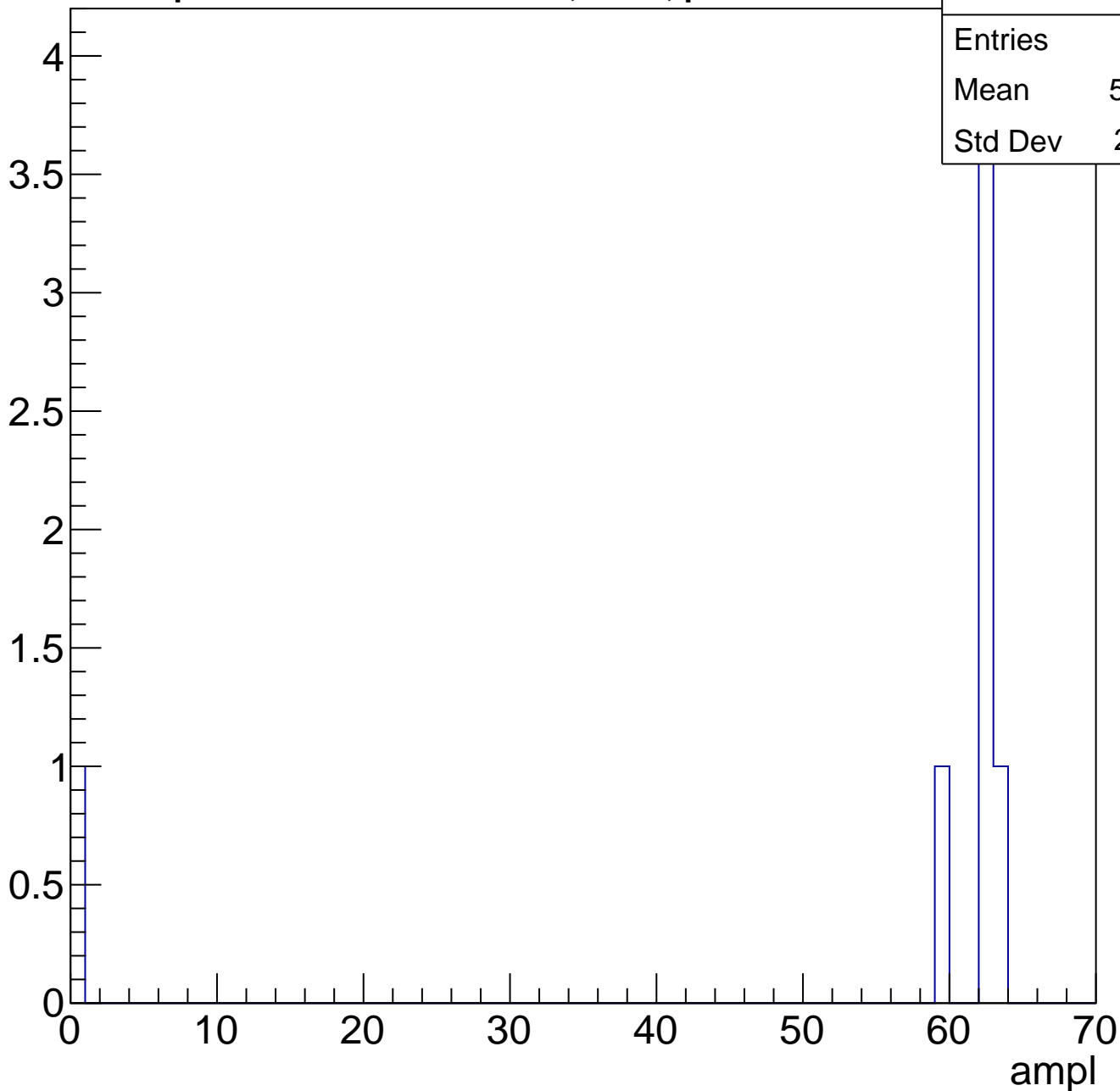
Entries	57
Mean	59.61
Std Dev	2.546



# B1L000S, U24-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch67, adc0

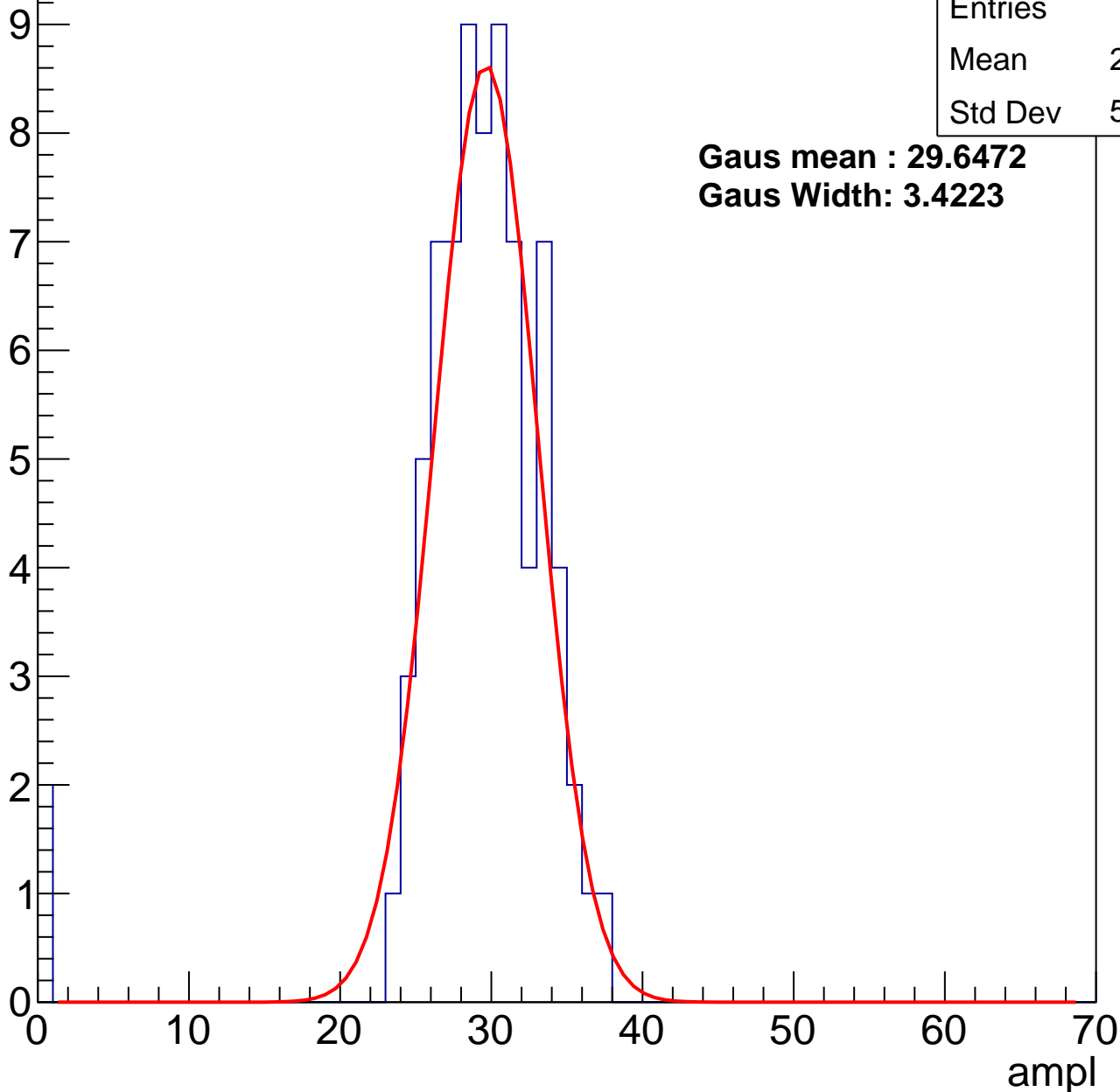
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	28.57
Std Dev	5.616

**Gaus mean : 29.6472**

**Gaus Width: 3.4223**



# B1L000S, U24-ch67, adc1

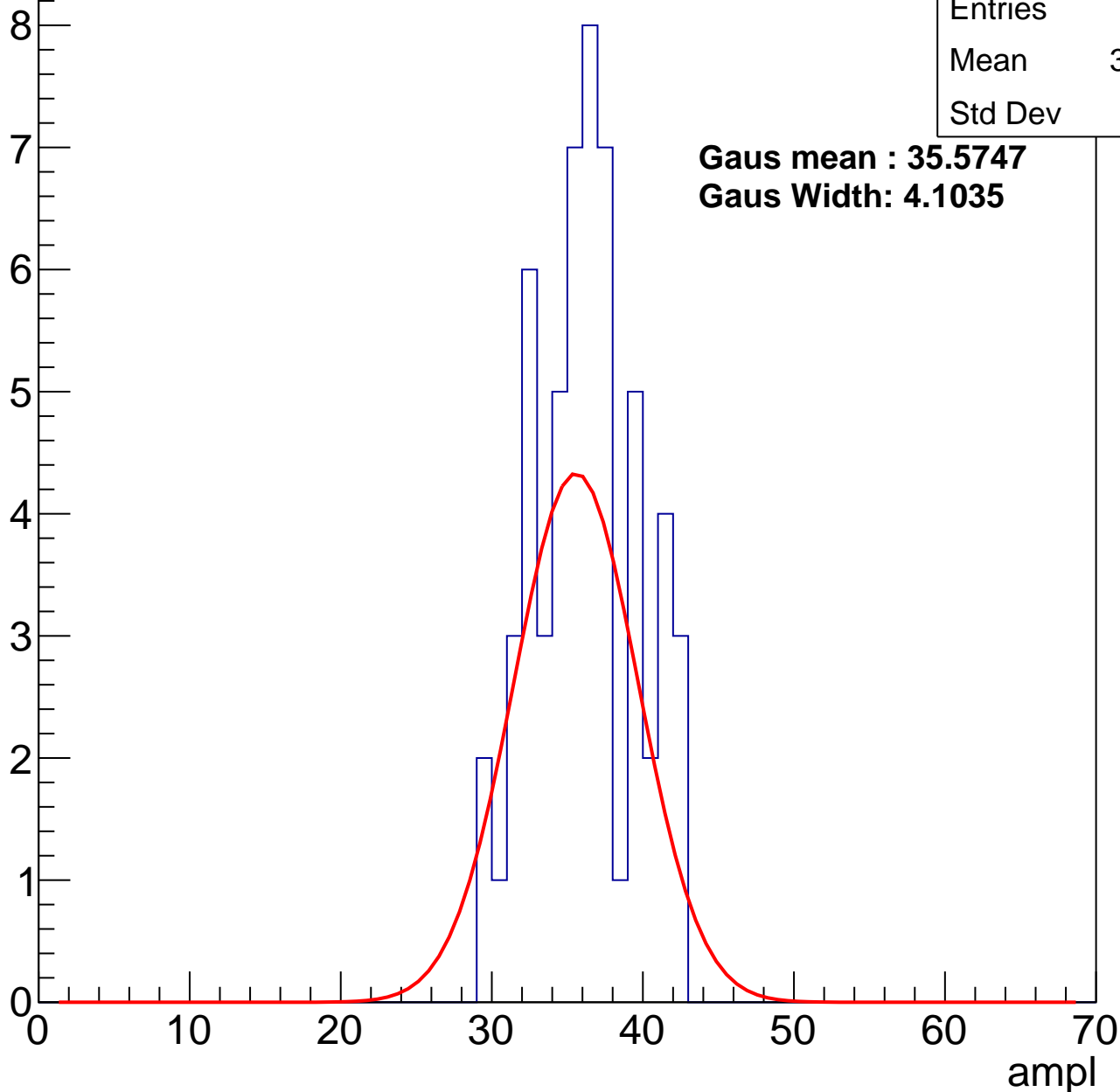
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.74
Std Dev	3.38

**Gaus mean : 35.5747**

**Gaus Width: 4.1035**



# B1L000S, U24-ch67, adc2

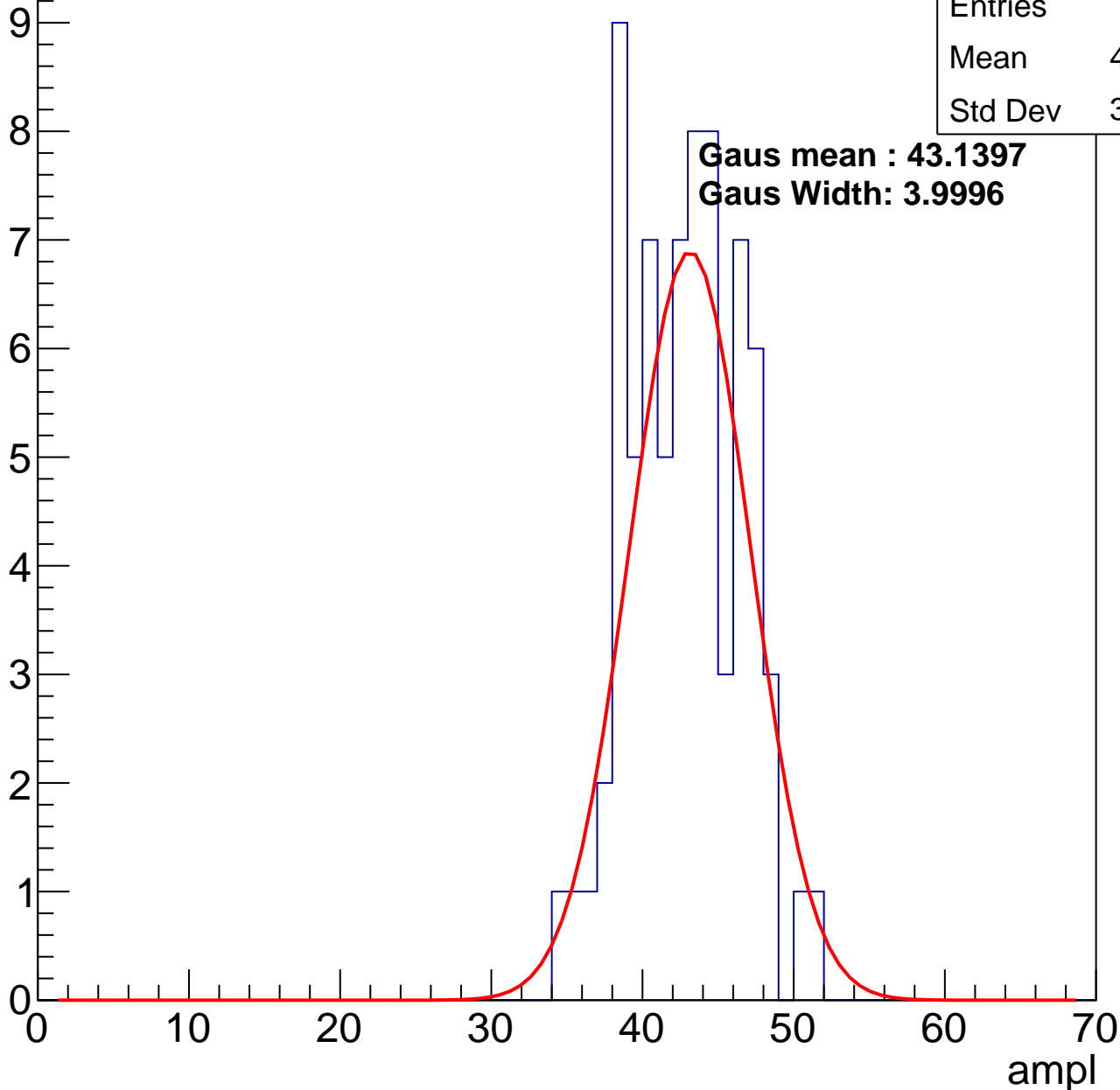
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.33
Std Dev	3.642

**Gaus mean : 43.1397**

**Gaus Width: 3.9996**



# B1L000S, U24-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

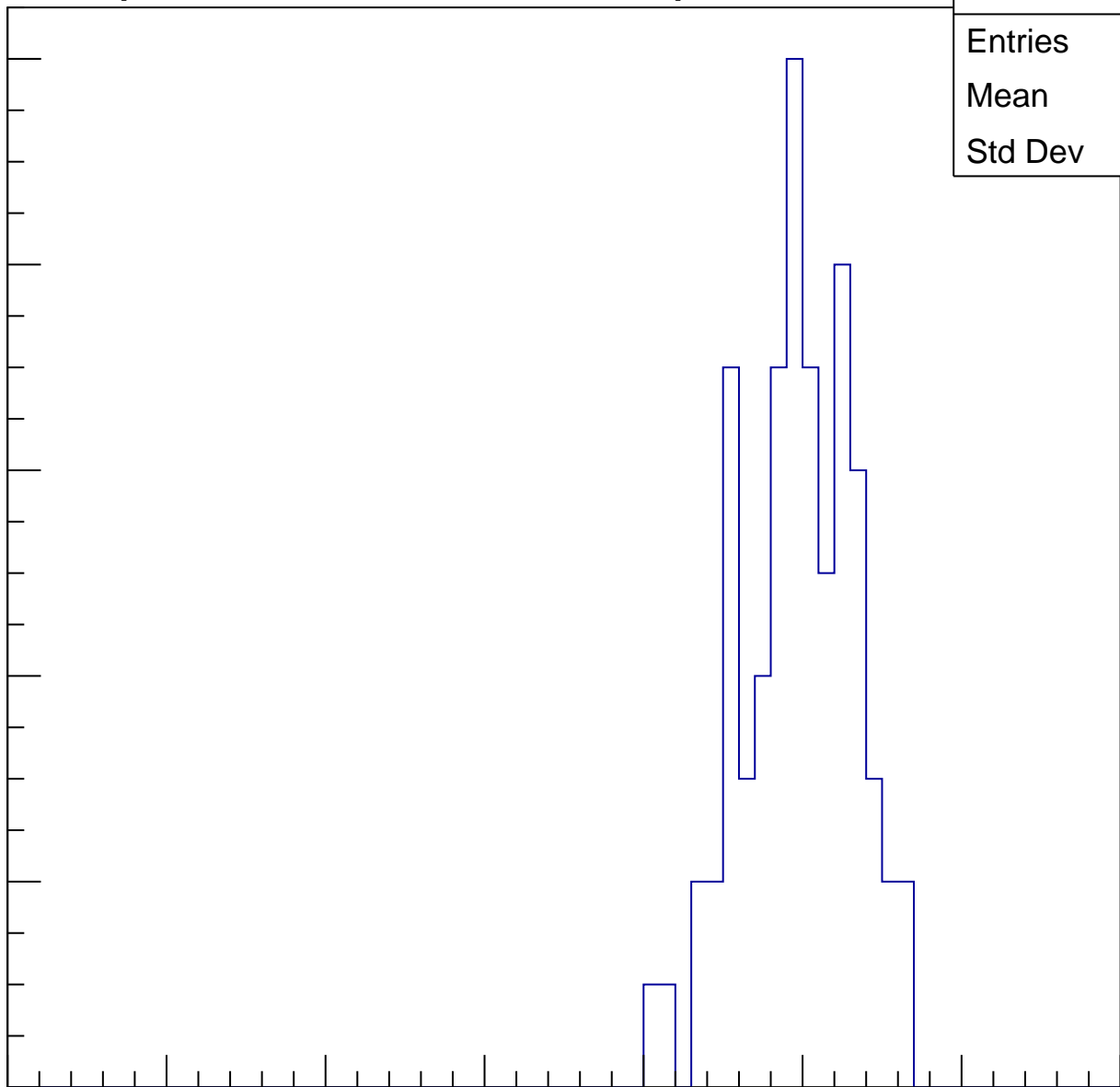
Entries	70
Mean	49.21
Std Dev	3.517

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

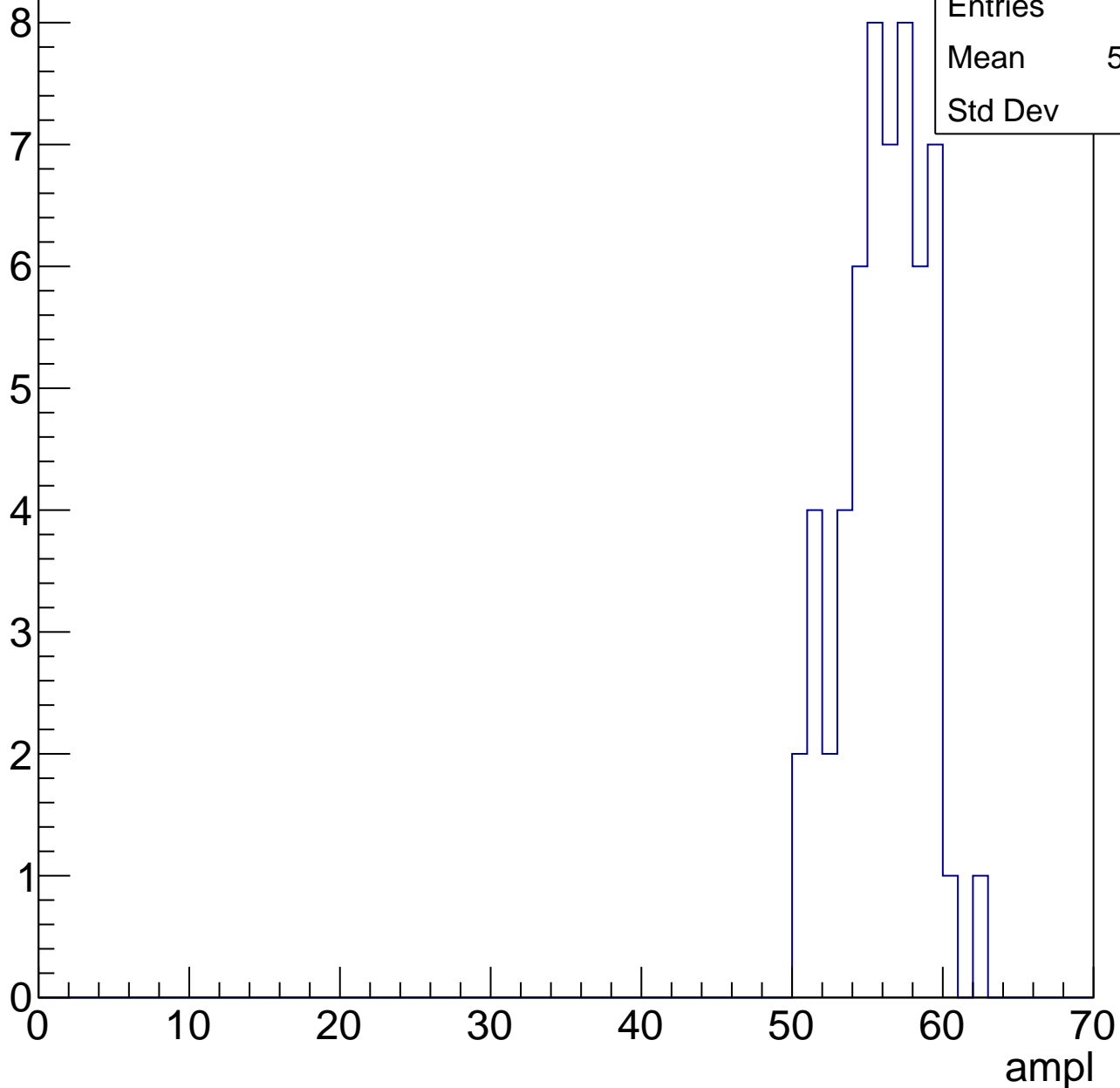
ampl



# B1L000S, U24-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



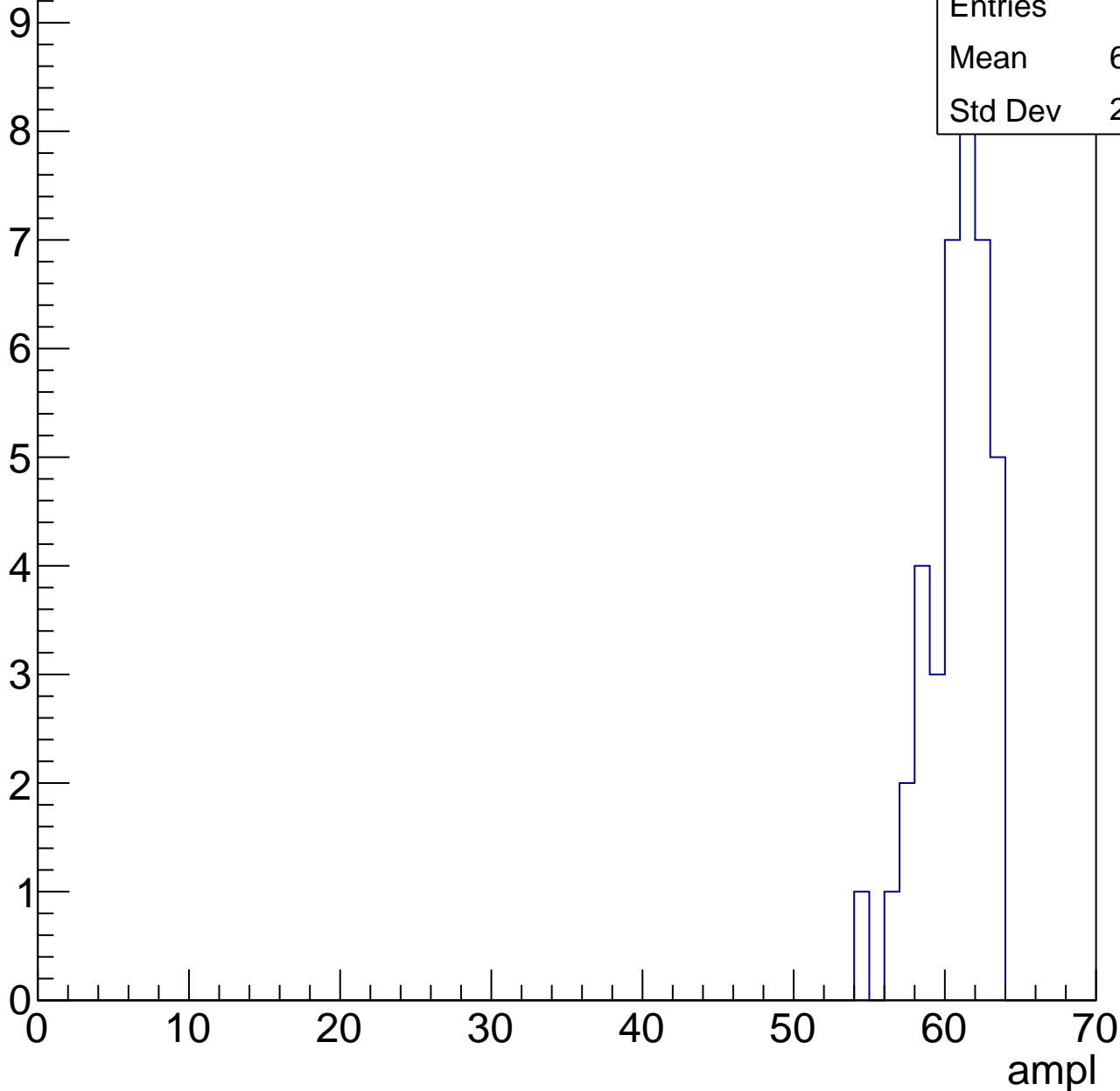
Entries	56
Mean	55.62
Std Dev	2.71

# B1L000S, U24-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

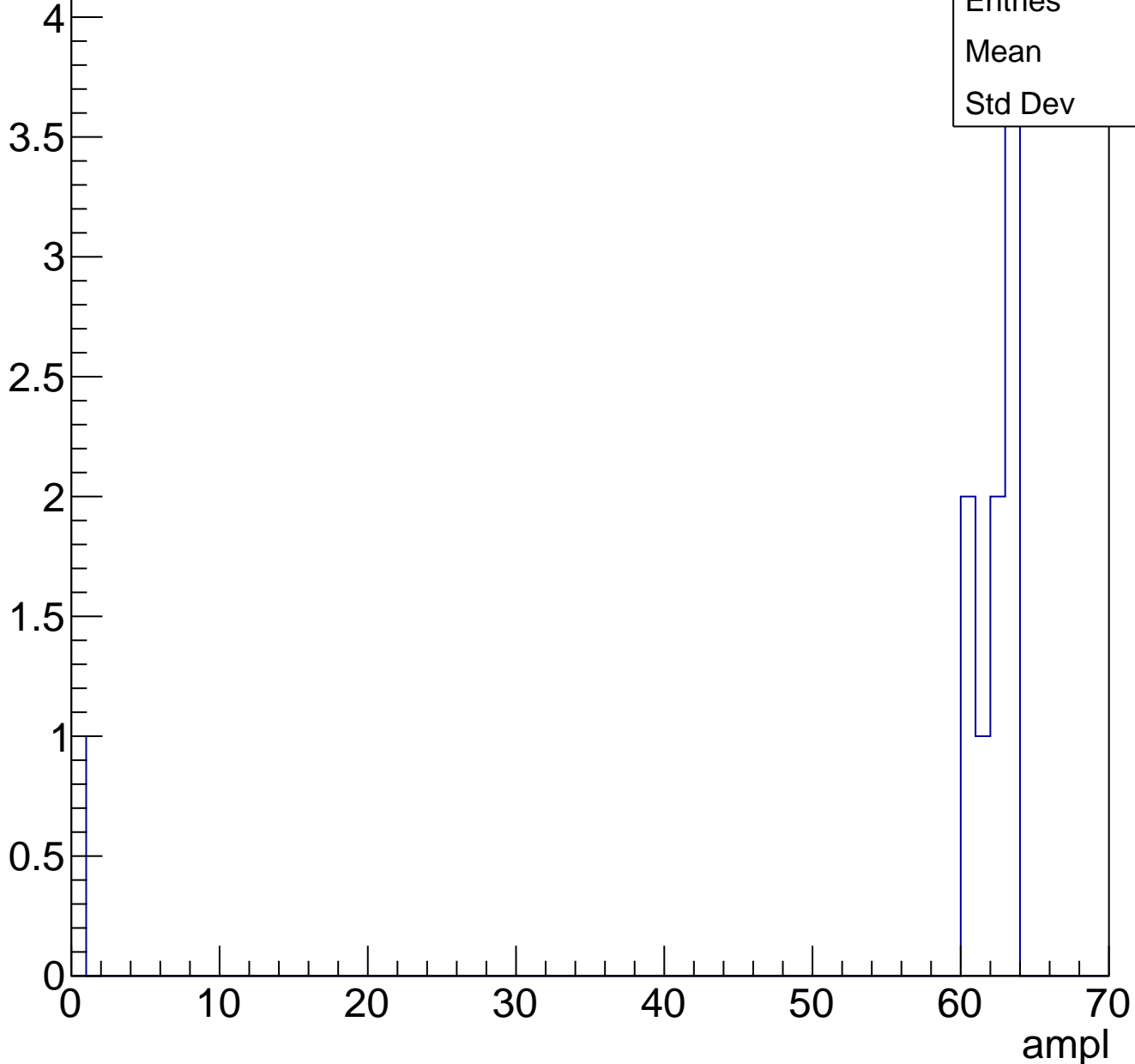
Entries	39
Mean	60.28
Std Dev	2.075



# B1L000S, U24-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U24-ch68, adc0

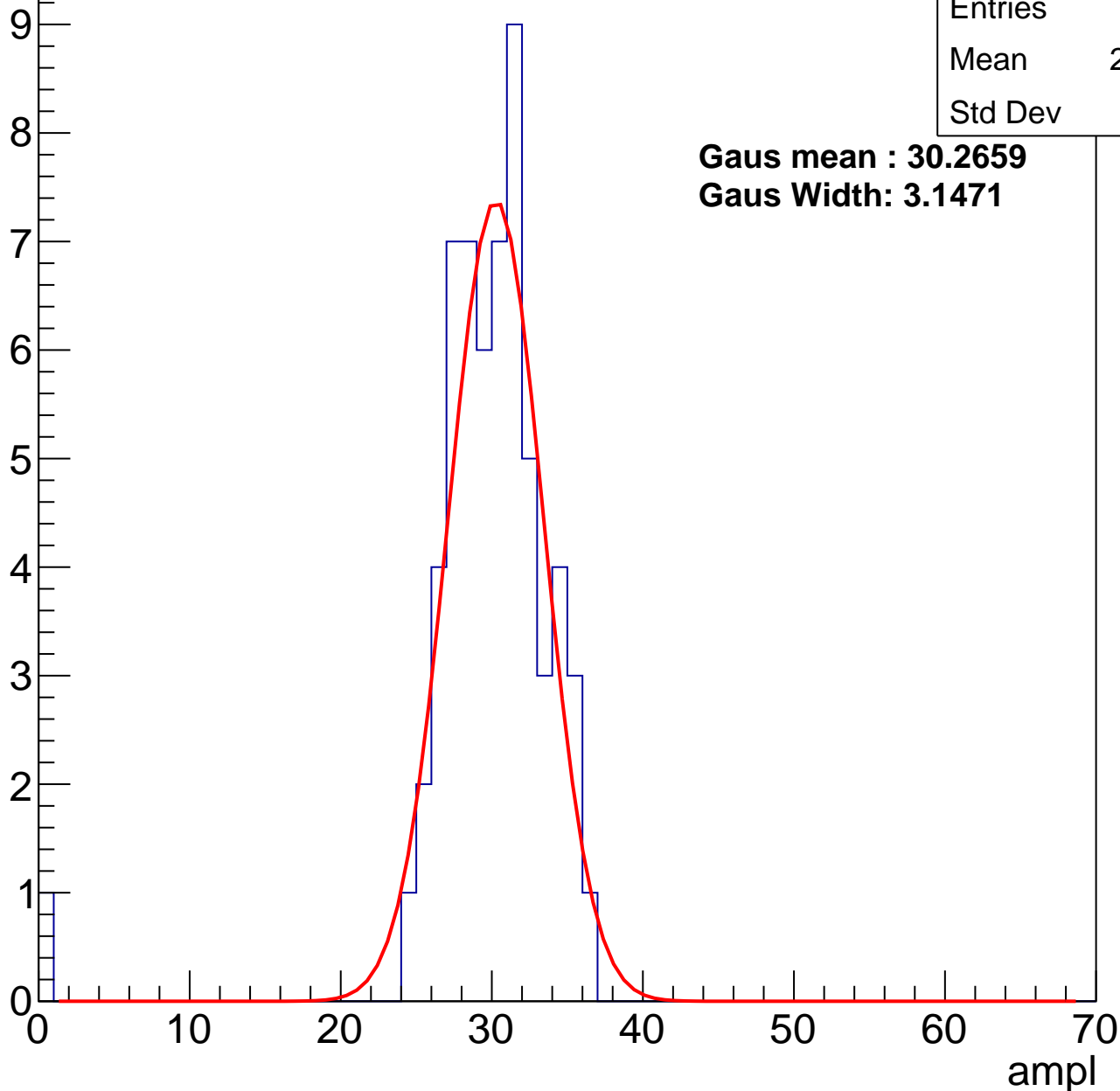
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	29.37
Std Dev	4.75

**Gaus mean : 30.2659**

**Gaus Width: 3.1471**



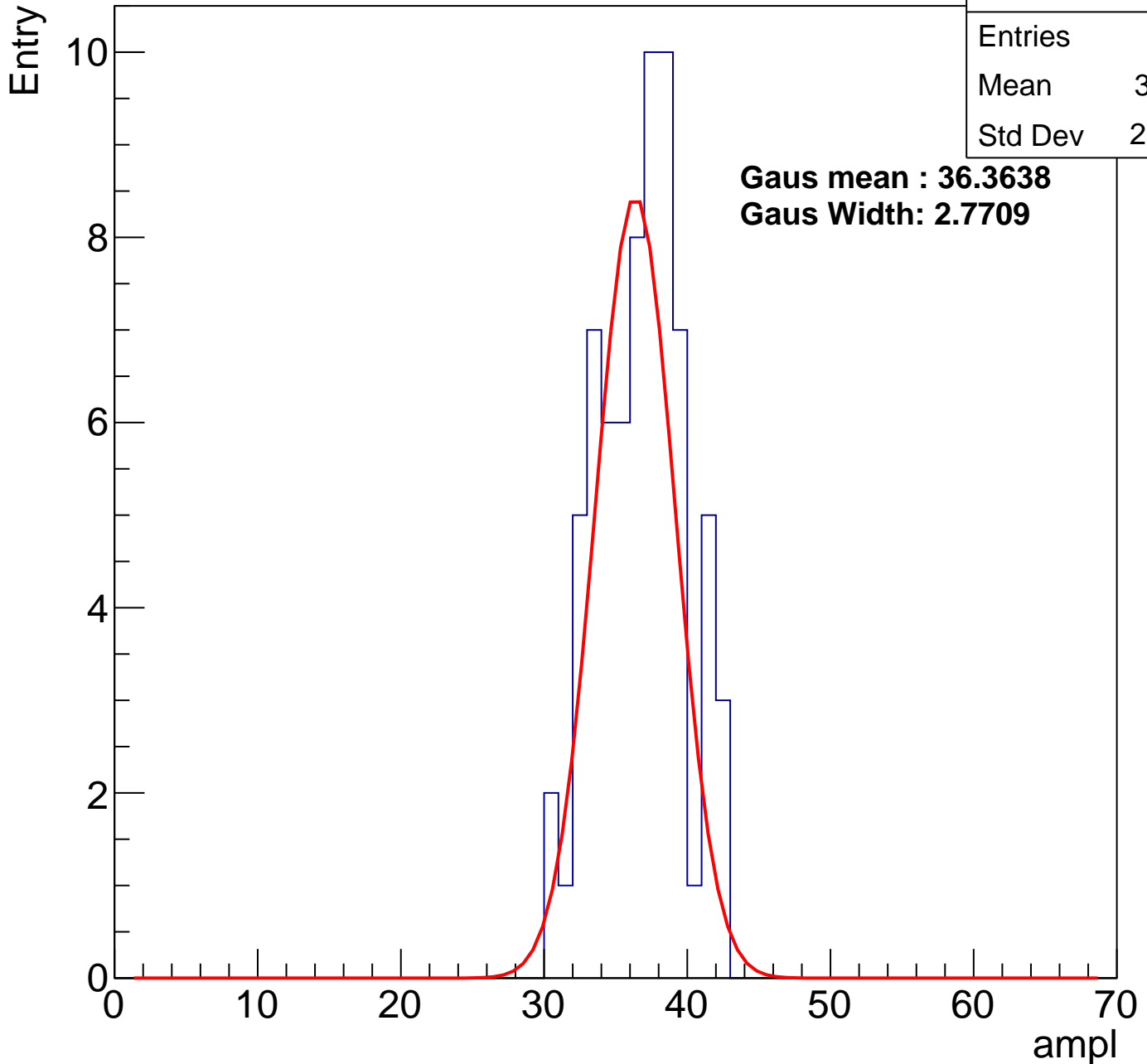
# B1L000S, U24-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	36.31
Std Dev	2.967

**Gaus mean : 36.3638**

**Gaus Width: 2.7709**



# B1L000S, U24-ch68, adc2

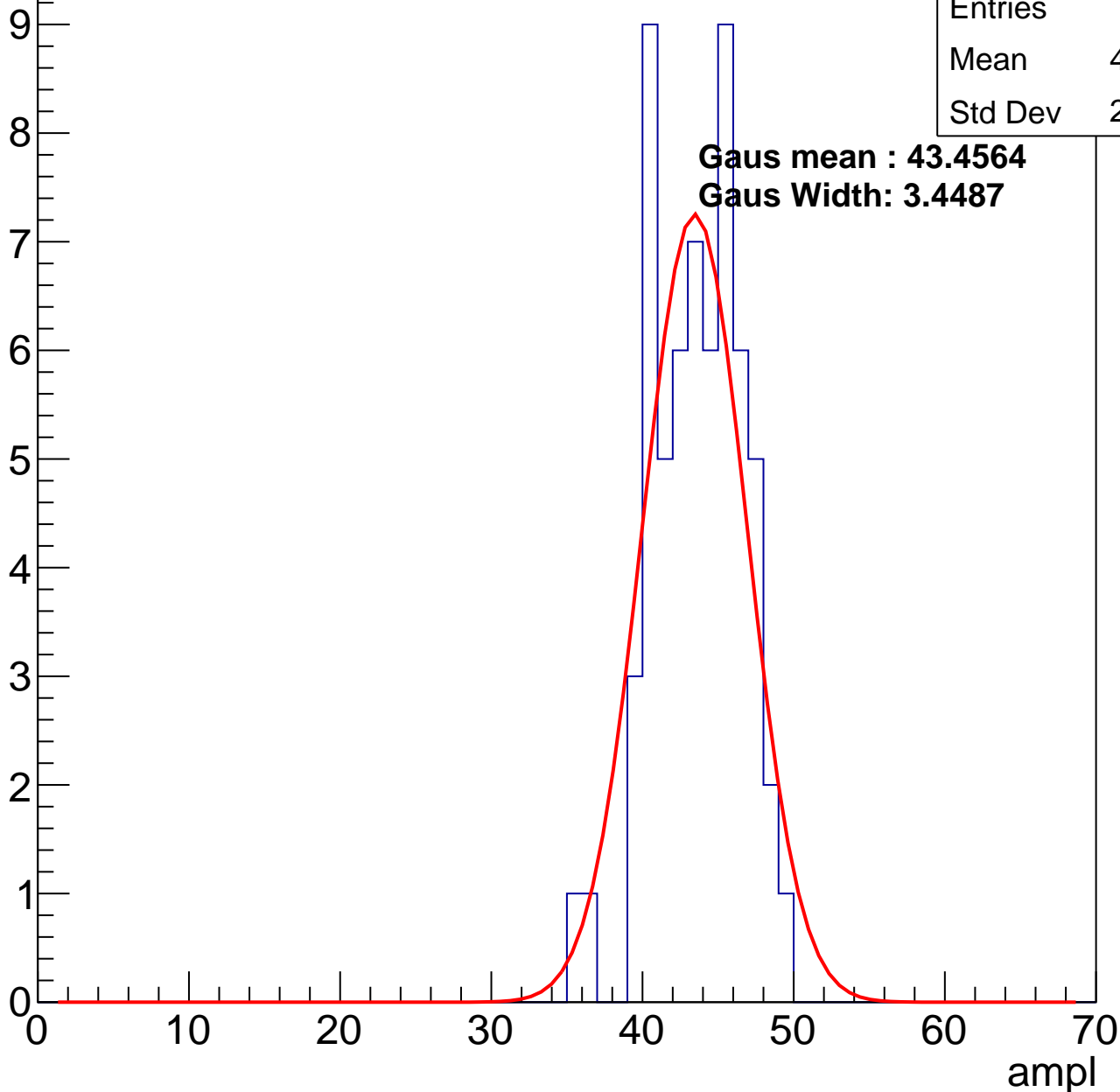
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	43.13
Std Dev	2.945

**Gaus mean : 43.4564**

**Gaus Width: 3.4487**

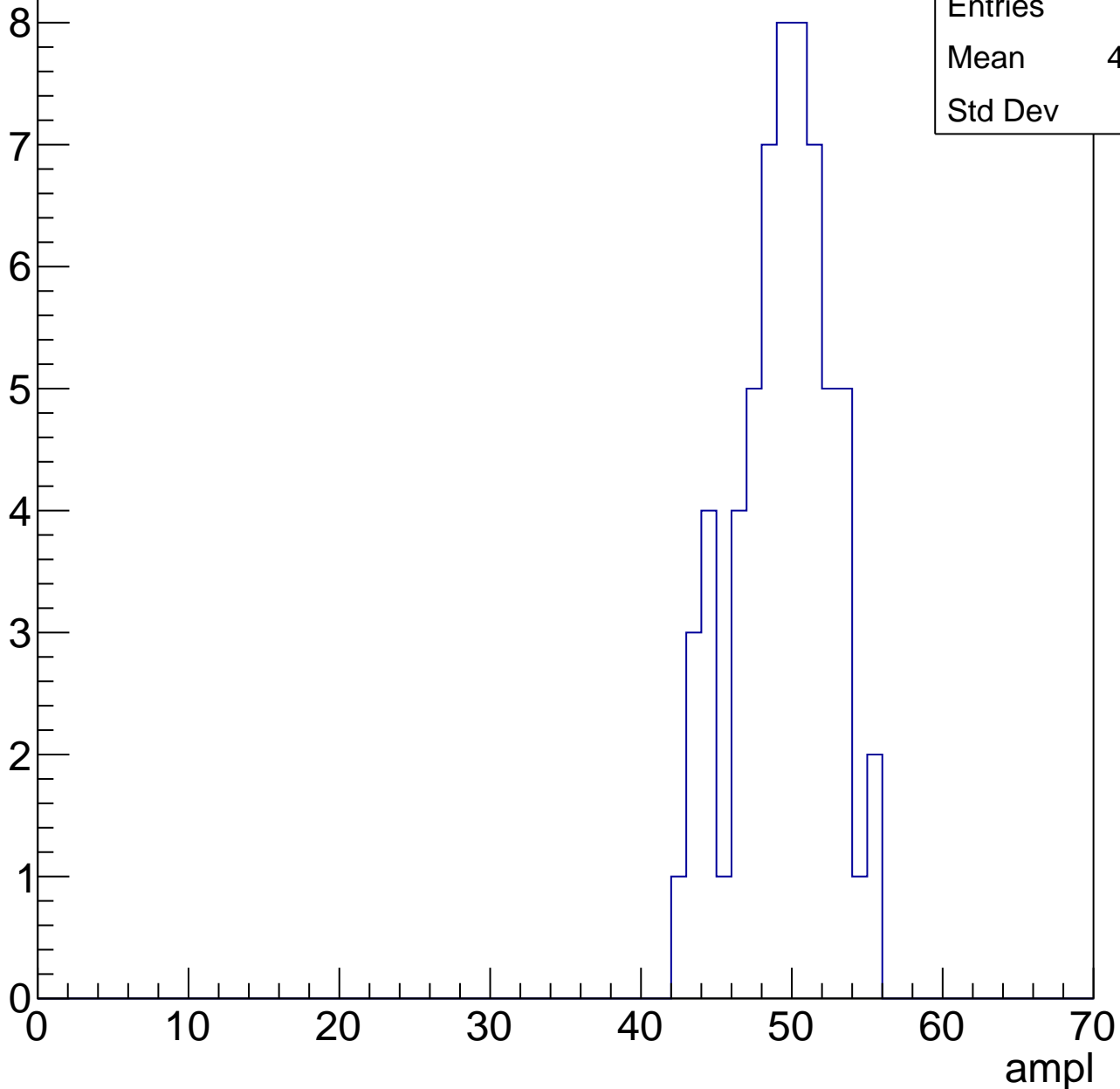


# B1L000S, U24-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

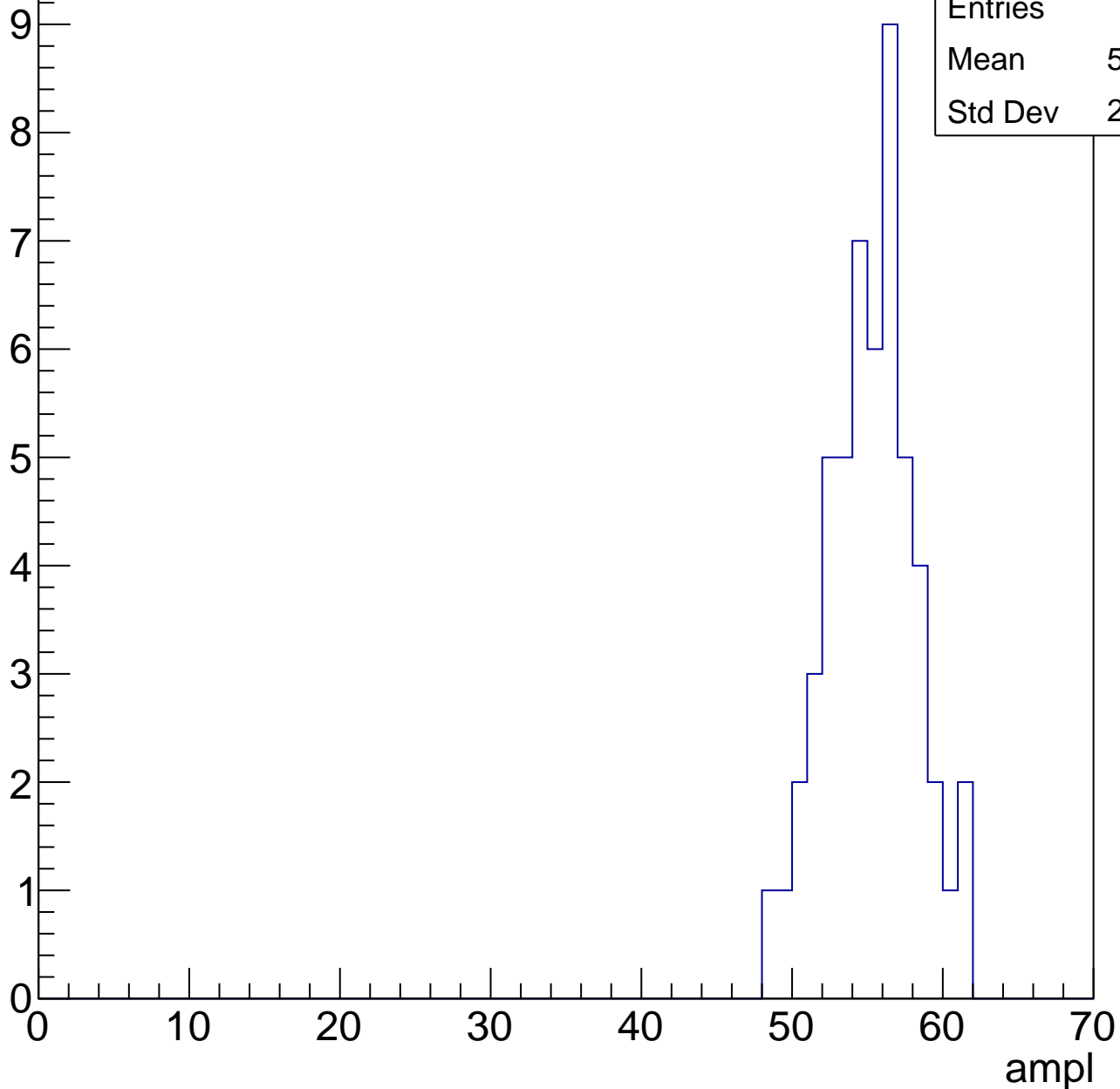
Entries	61
Mean	48.93
Std Dev	3.12



# B1L000S, U24-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	53
Mean	54.79
Std Dev	2.903

# B1L000S, U24-ch68, adc5

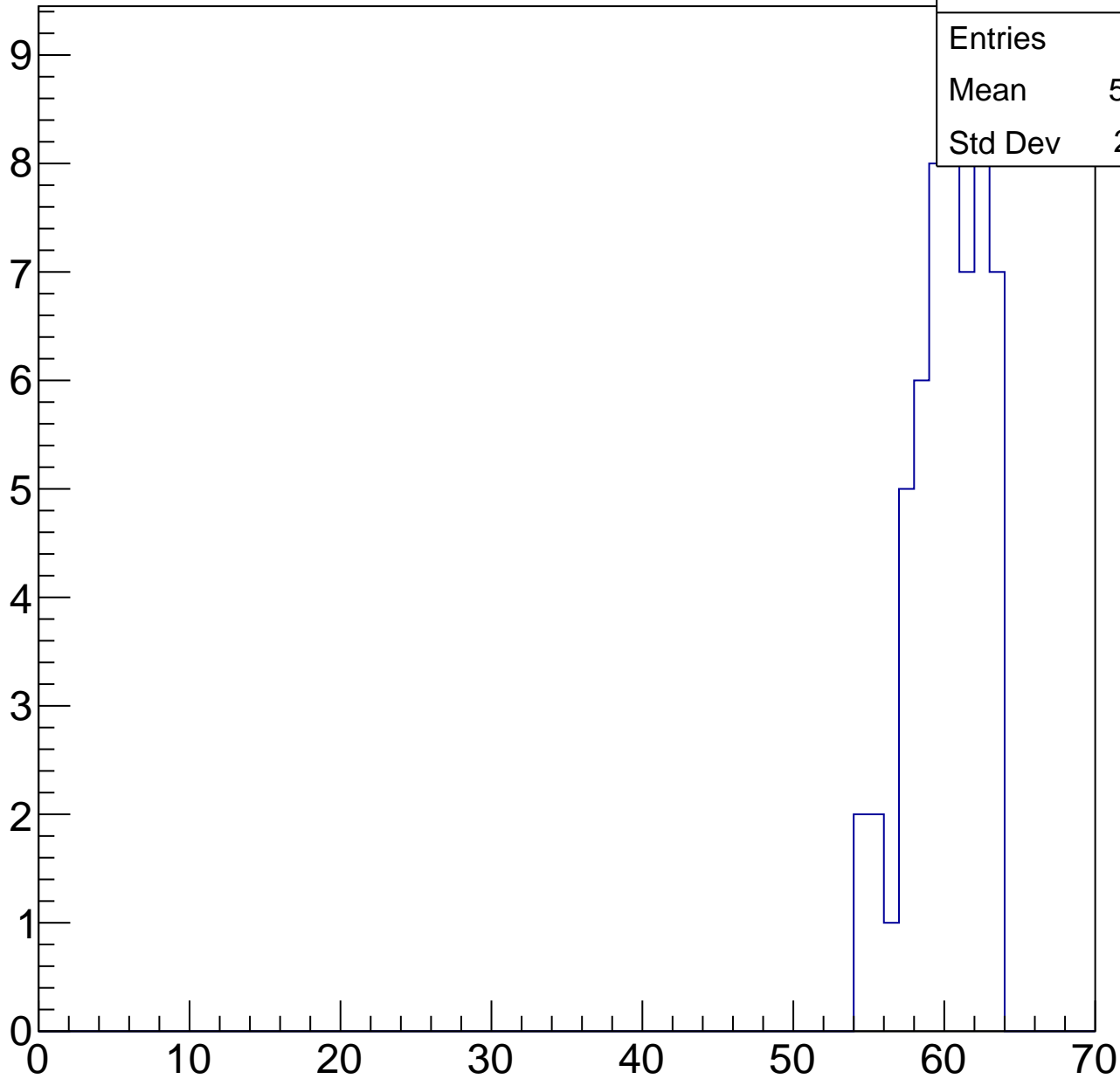
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	59.69
Std Dev	2.381

ampl



# B1L000S, U24-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



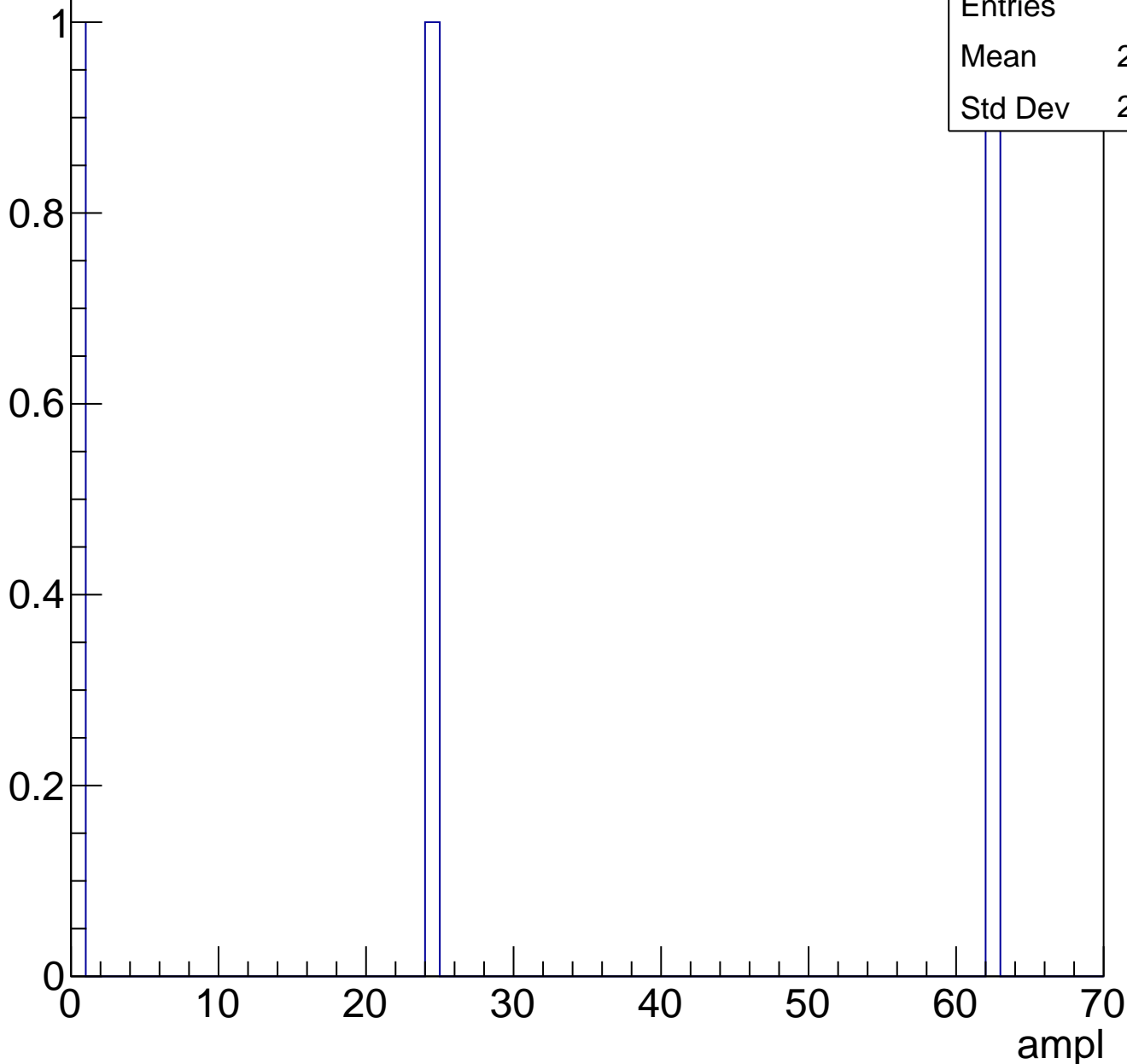
Entries	6
Mean	52
Std Dev	23.27



# B1L000S, U24-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.67
Std Dev	25.53

# B1L000S, U24-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	28.99
Std Dev	4.846

**Gaus mean : 29.8568**

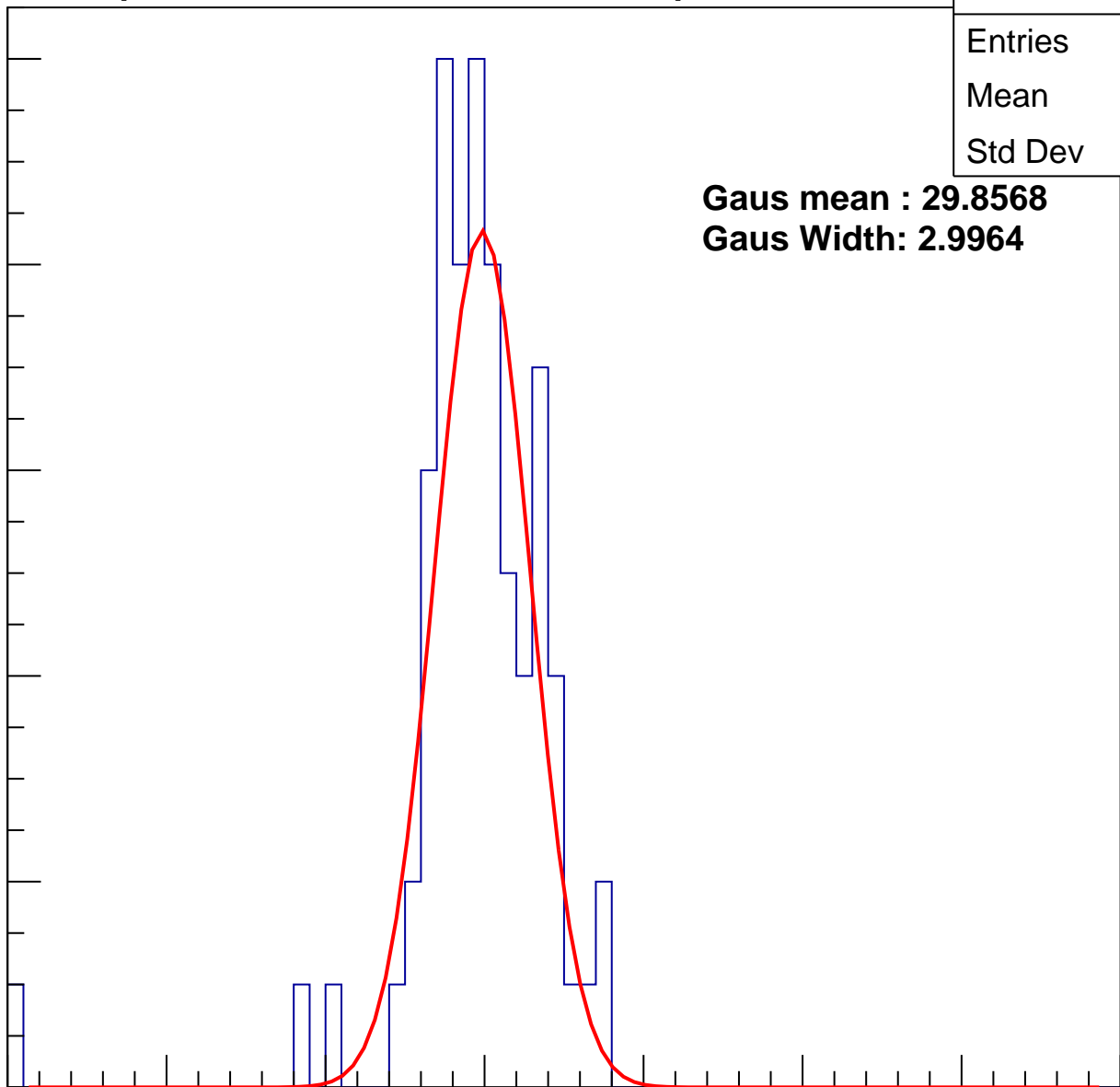
**Gaus Width: 2.9964**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch69, adc1

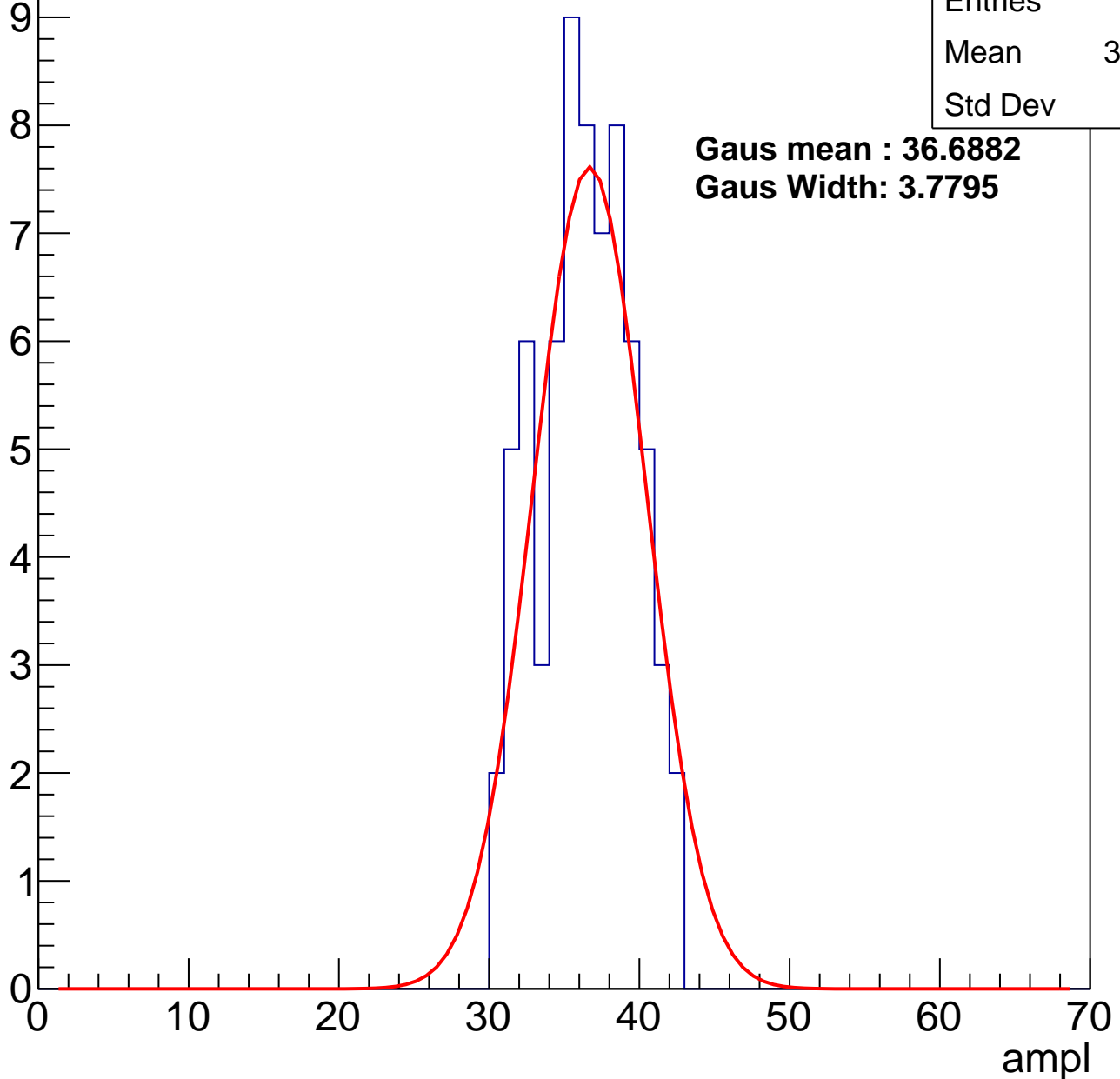
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	35.96
Std Dev	3.1

**Gaus mean : 36.6882**

**Gaus Width: 3.7795**



# B1L000S, U24-ch69, adc2

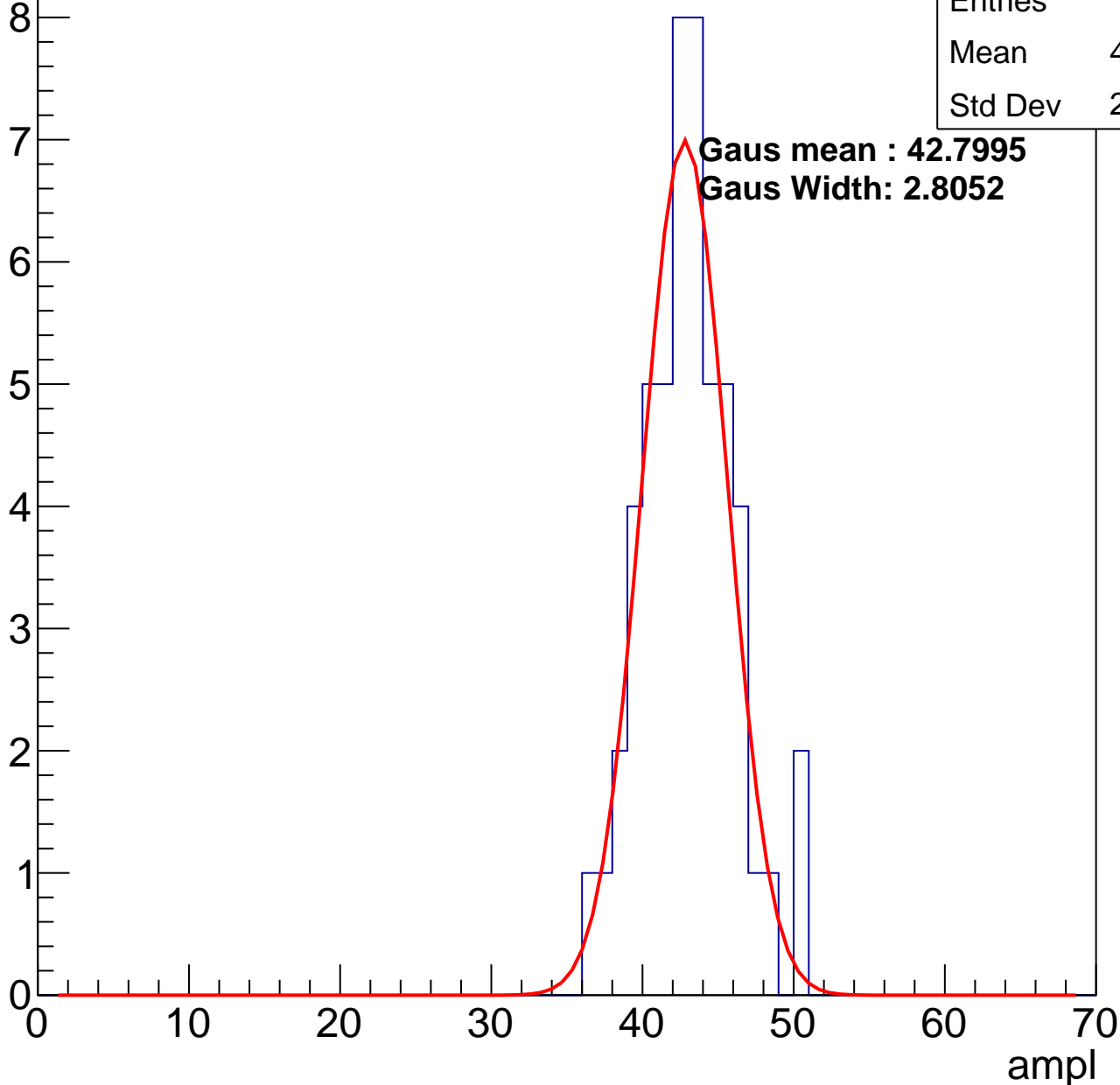
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	42.58
Std Dev	2.983

**Gaus mean : 42.7995**

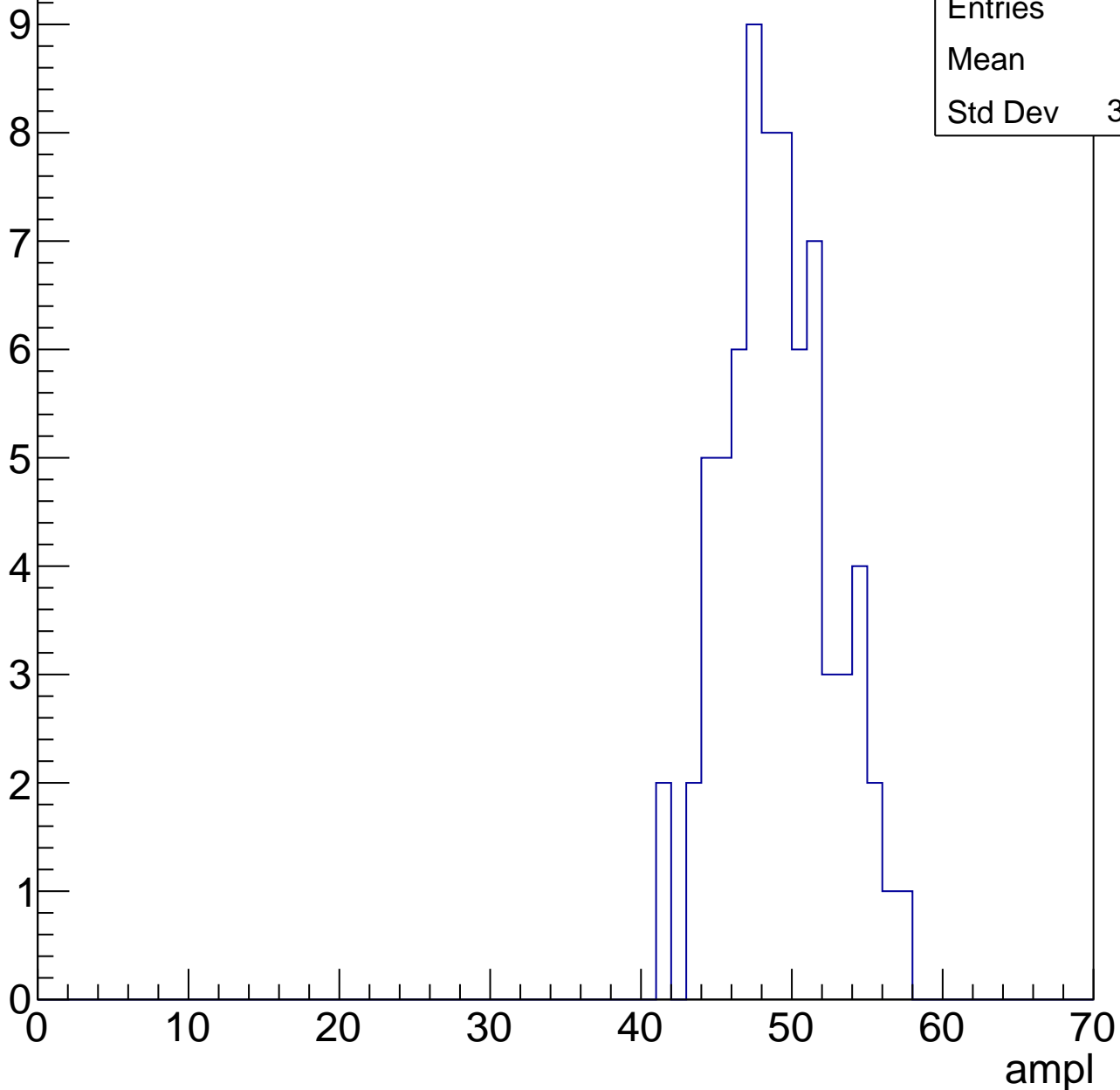
**Gaus Width: 2.8052**



# B1L000S, U24-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



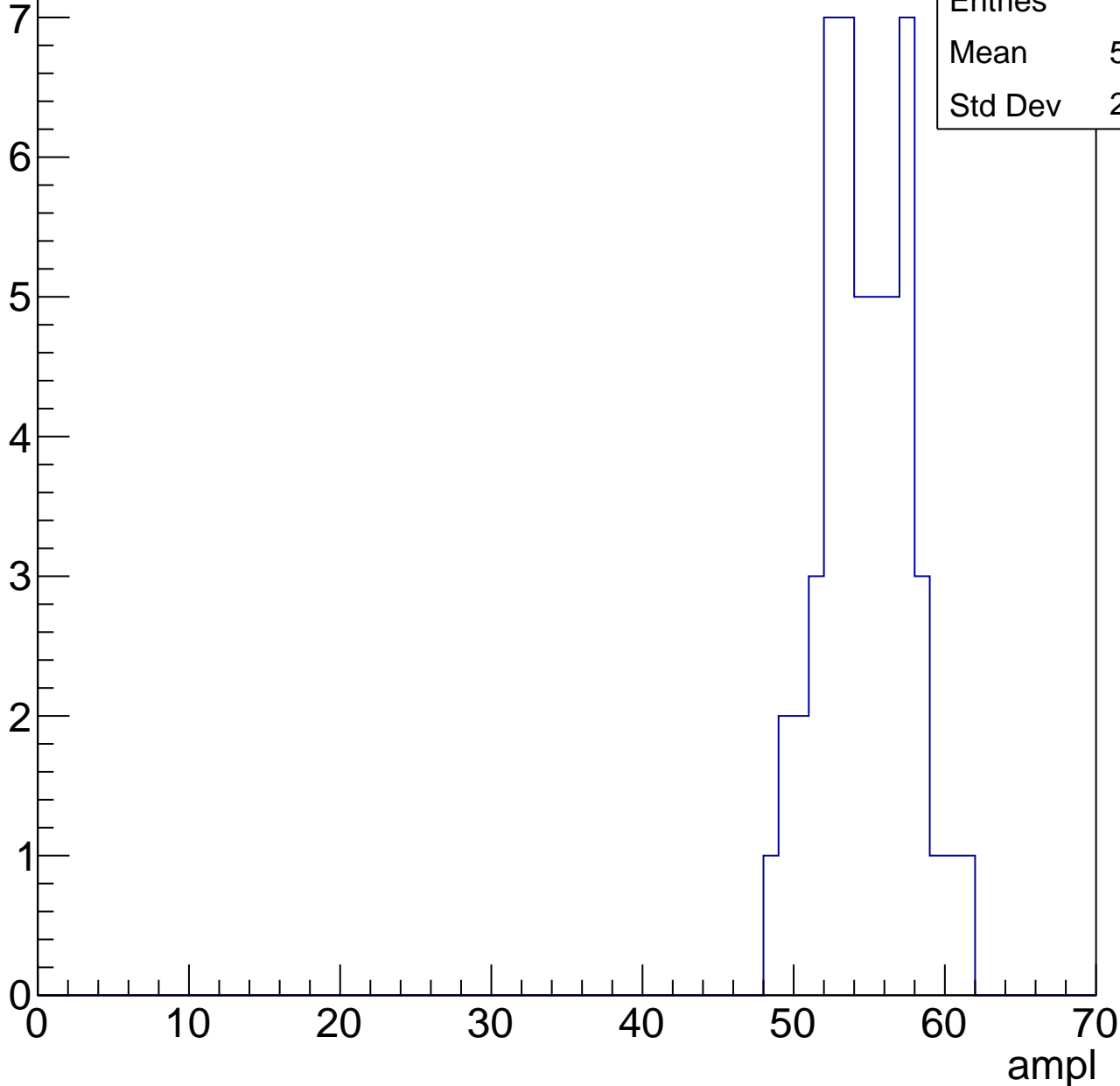
Entries	72
Mean	48.6
Std Dev	3.507

# B1L000S, U24-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

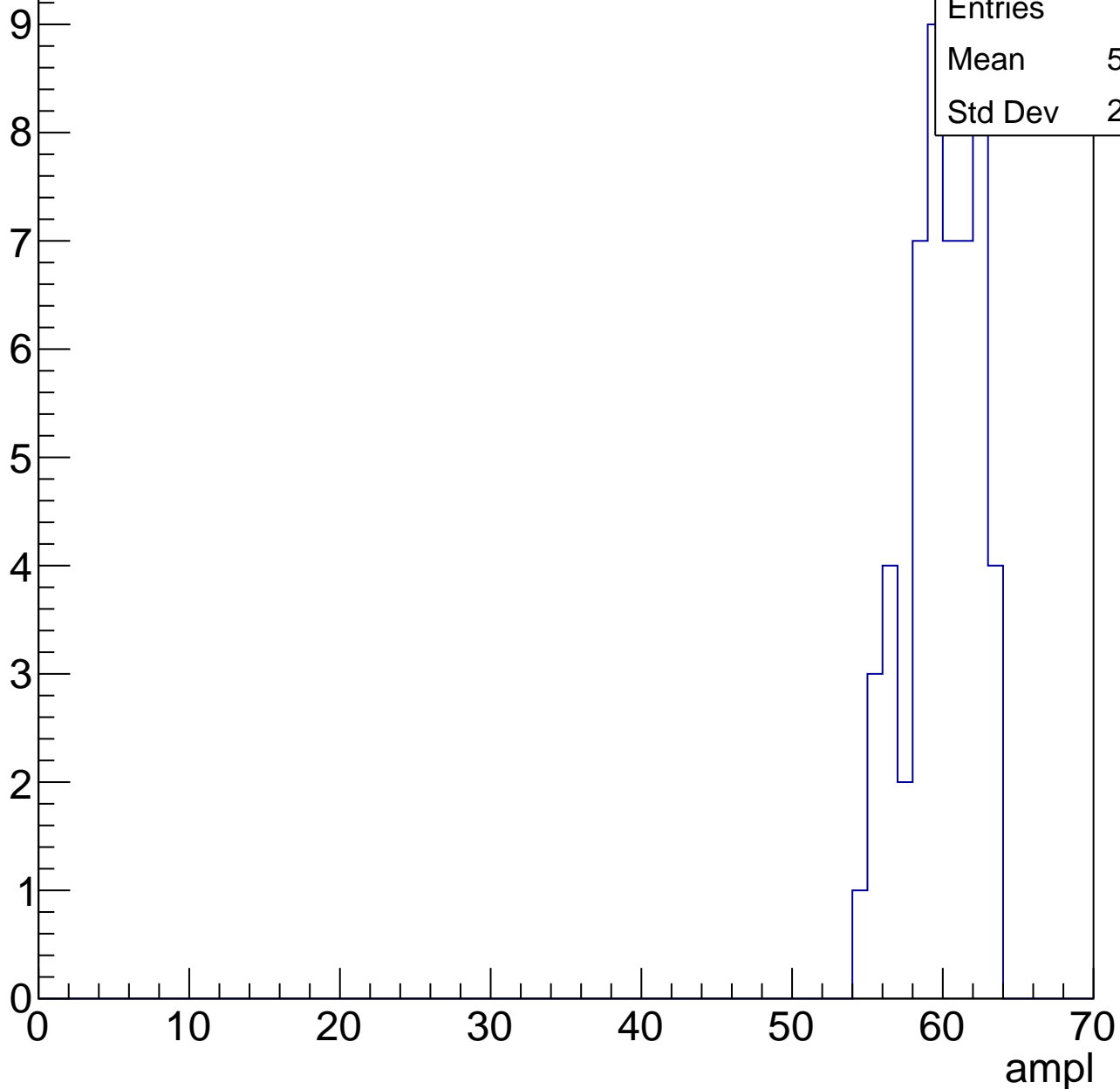
Entries	50
Mean	54.24
Std Dev	2.909



# B1L000S, U24-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



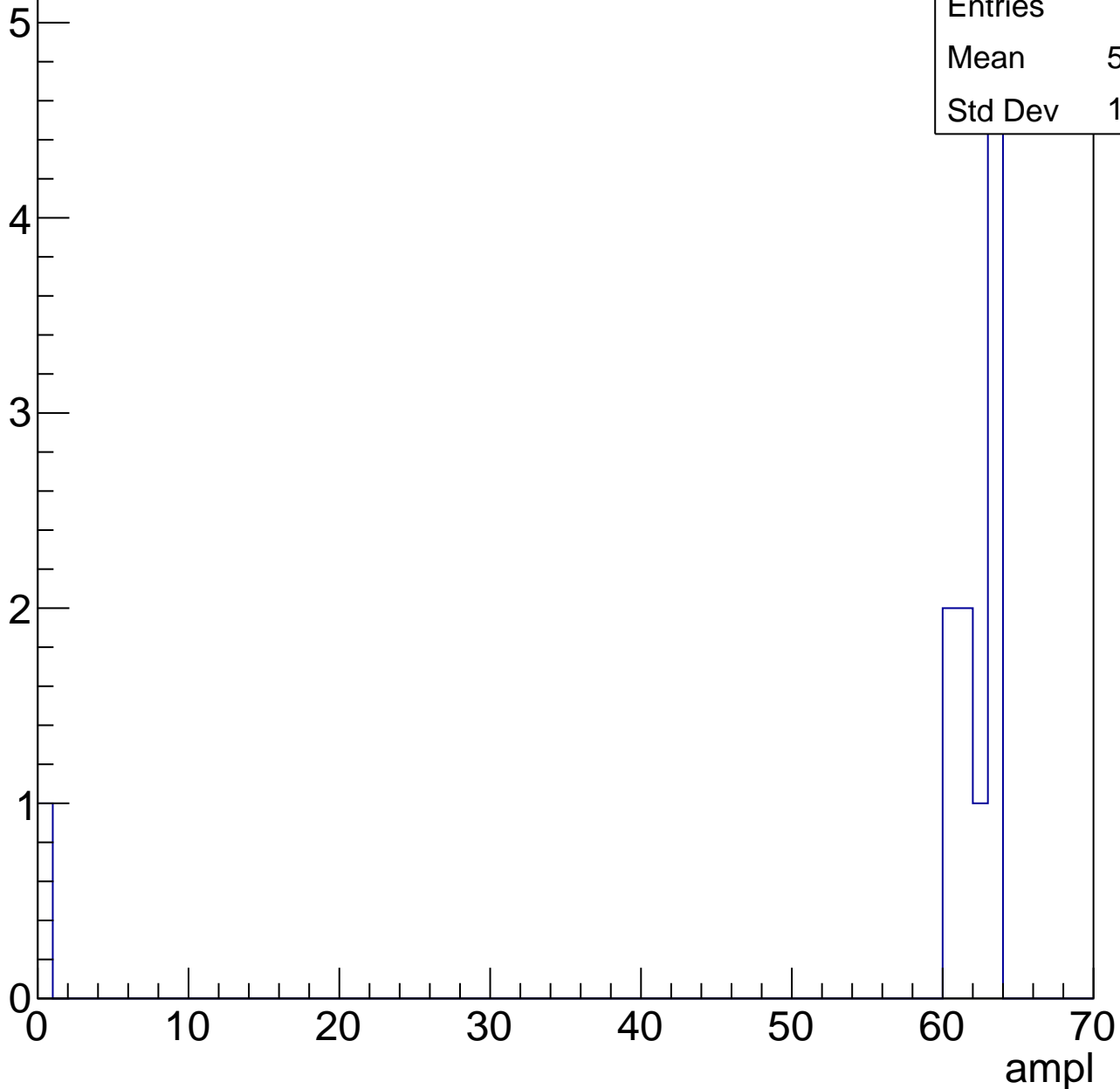
Entries	53
Mean	59.45
Std Dev	2.352

# B1L000S, U24-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	56.27
Std Dev	17.83





# B1L000S, U24-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch70, adc0

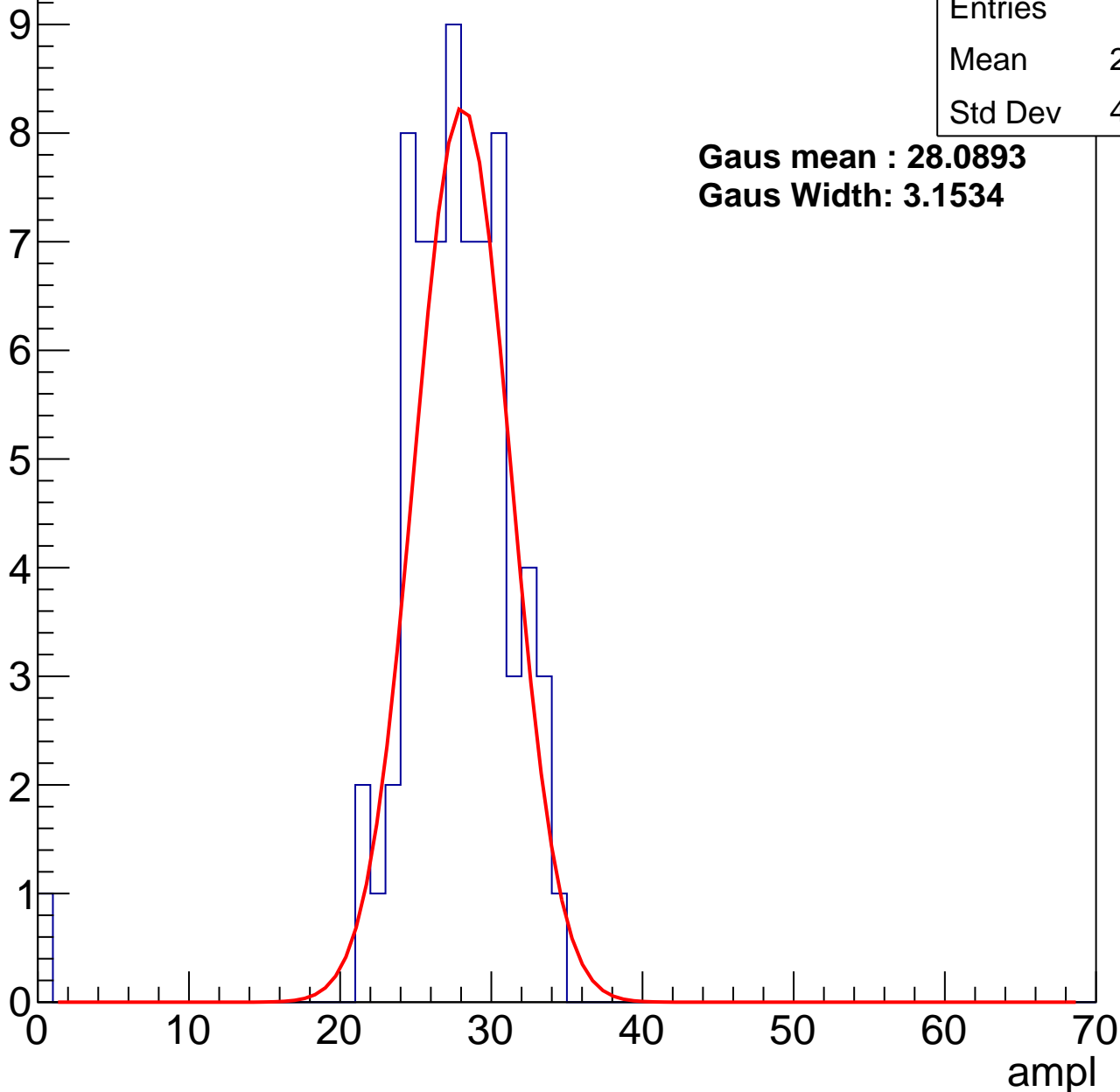
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	27.07
Std Dev	4.435

**Gaus mean : 28.0893**

**Gaus Width: 3.1534**



# B1L000S, U24-ch70, adc1

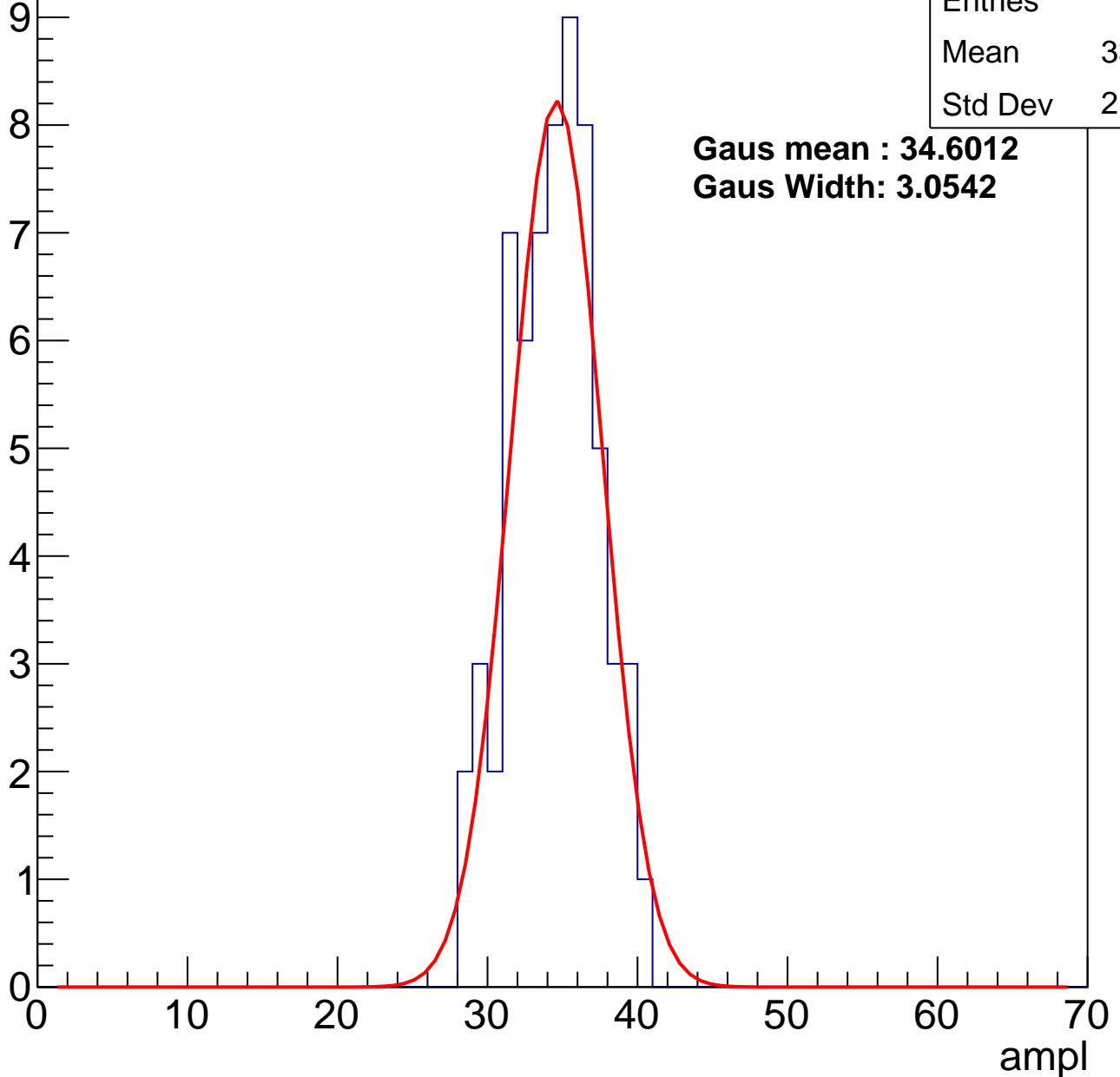
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	33.97
Std Dev	2.845

**Gaus mean : 34.6012**

**Gaus Width: 3.0542**



# B1L000S, U24-ch70, adc2

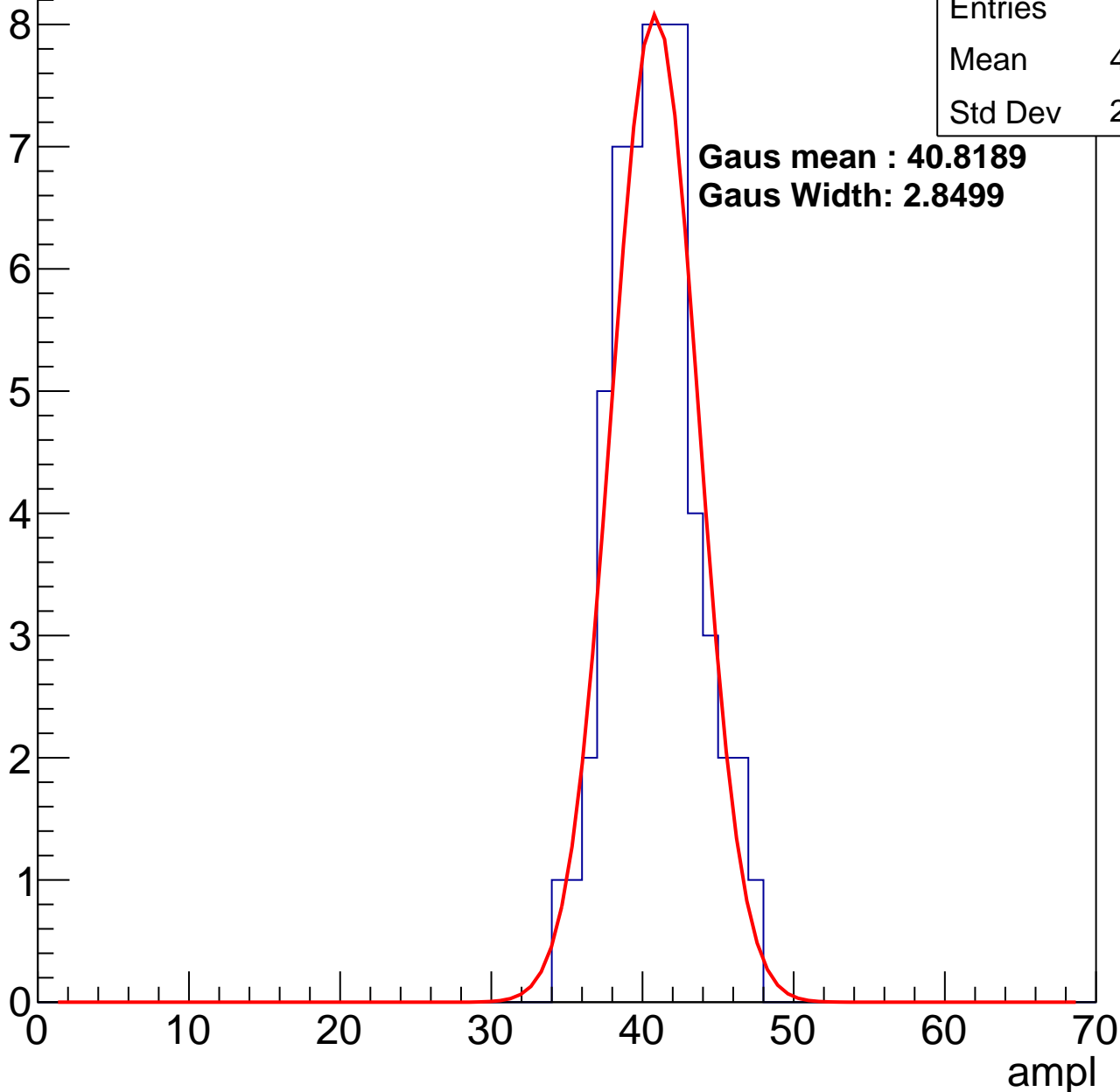
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	40.37
Std Dev	2.792

**Gaus mean : 40.8189**

**Gaus Width: 2.8499**

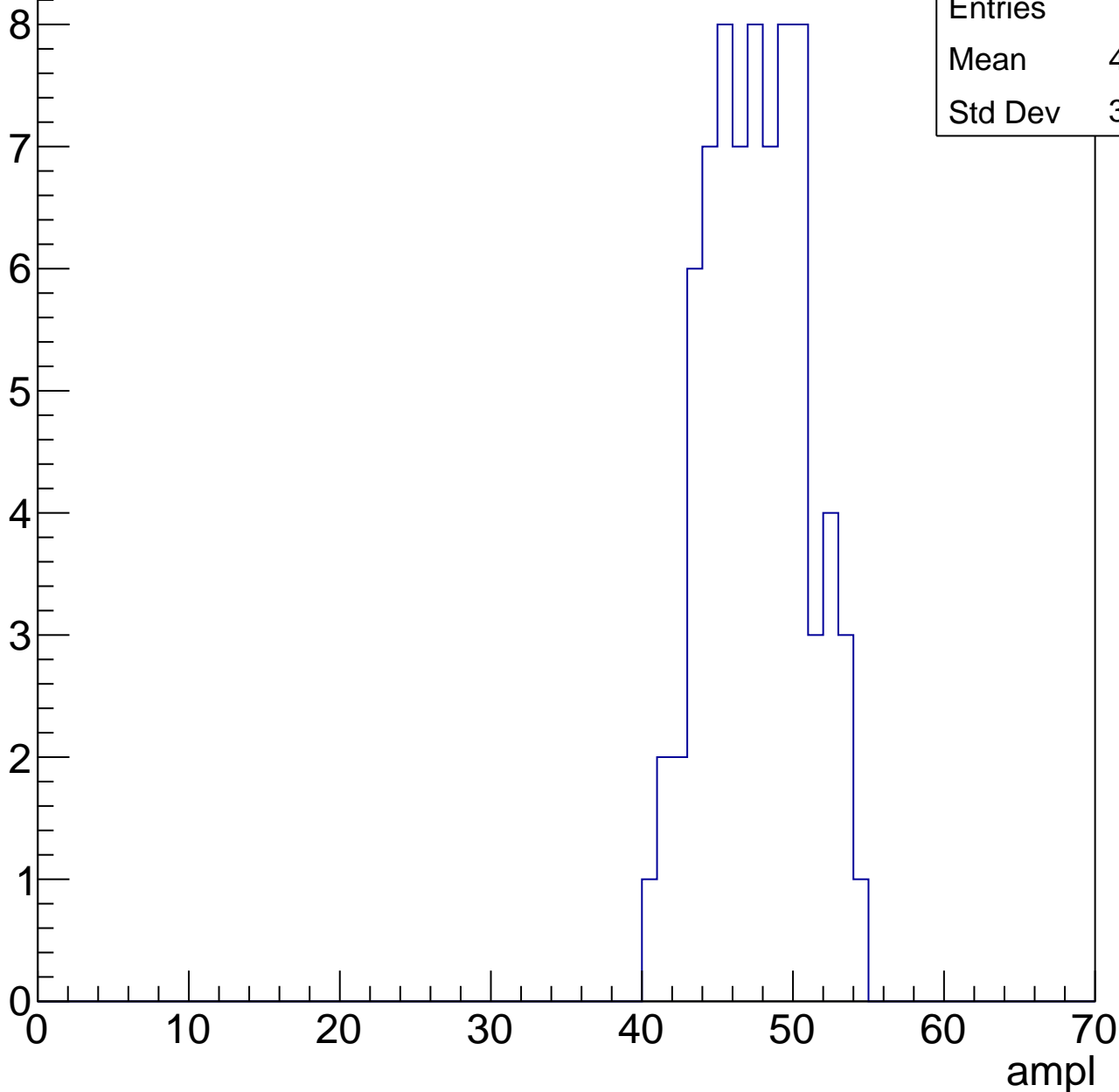


# B1L000S, U24-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	47.09
Std Dev	3.234

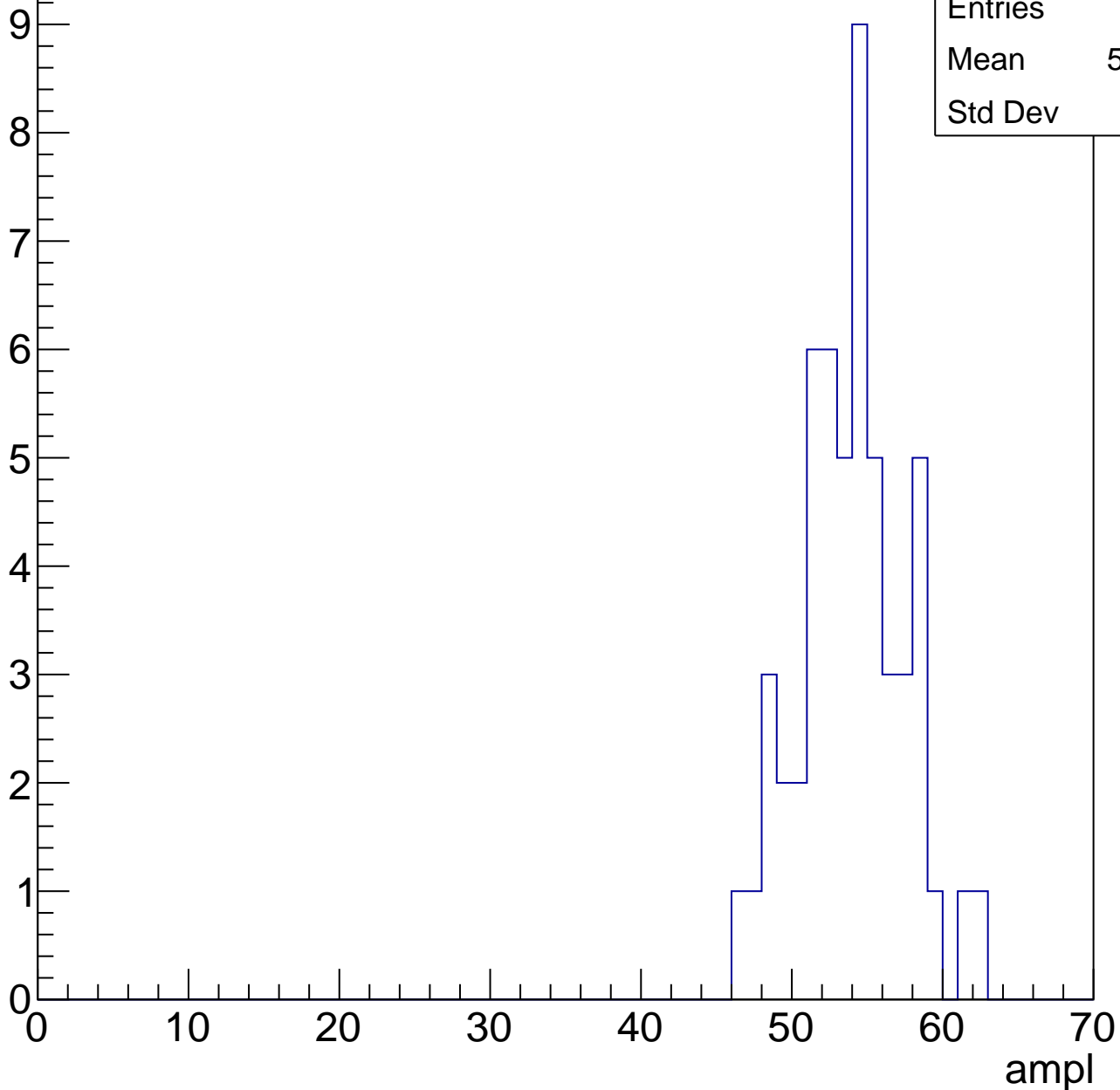


# B1L000S, U24-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	53.52
Std Dev	3.43

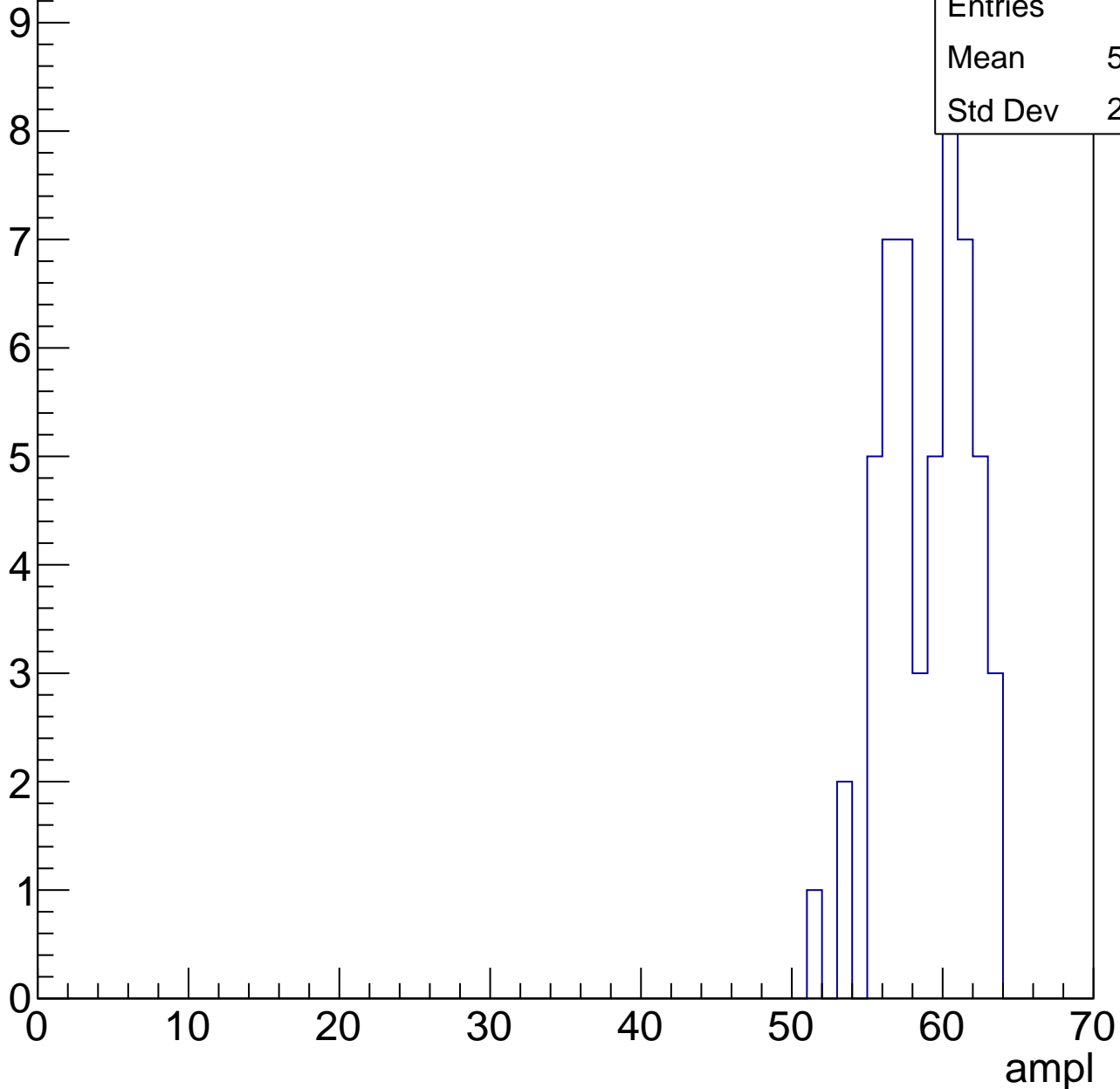


# B1L000S, U24-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	58.48
Std Dev	2.807

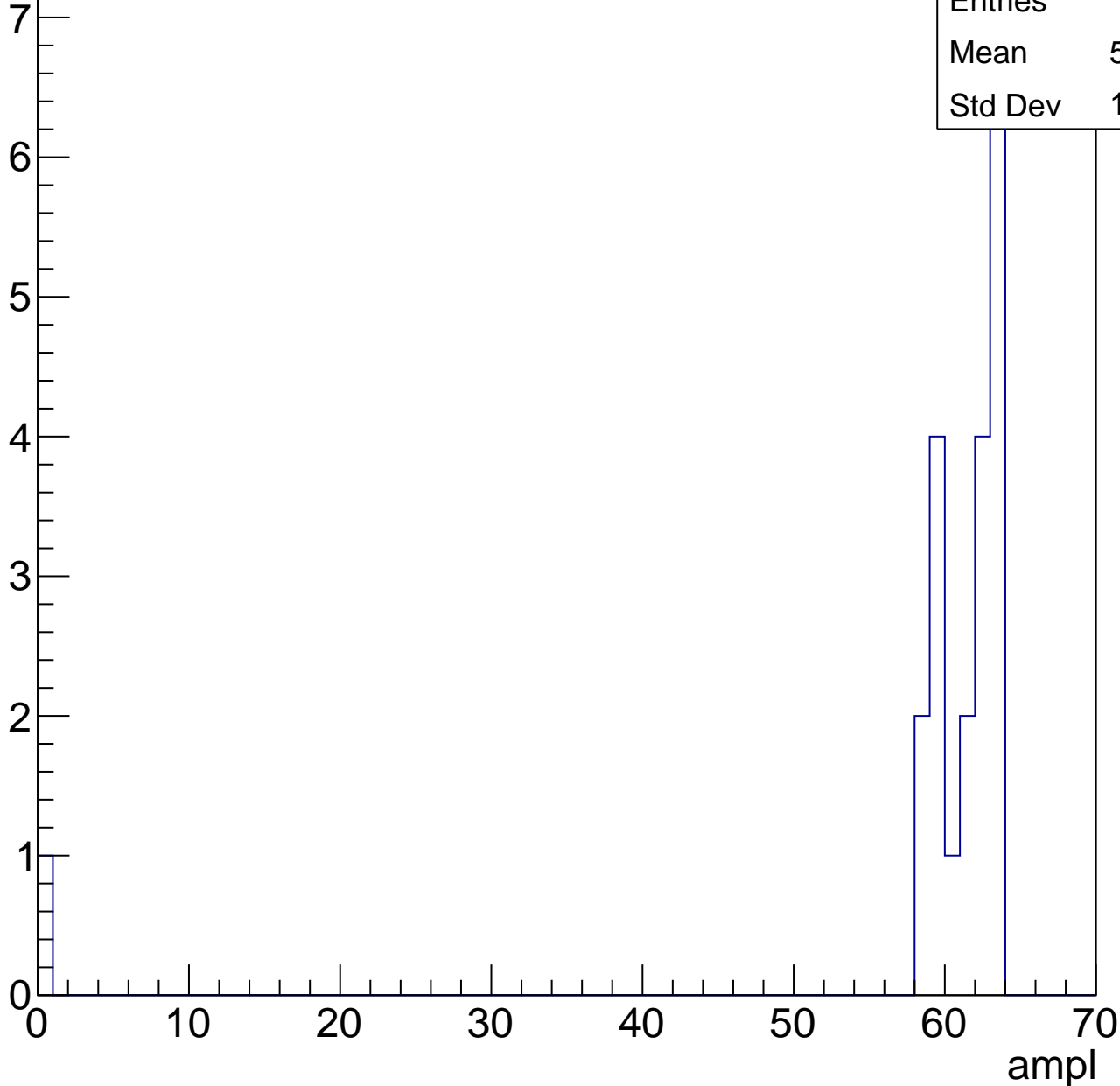


# B1L000S, U24-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.24
Std Dev	13.14





# B1L000S, U24-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch71, adc0

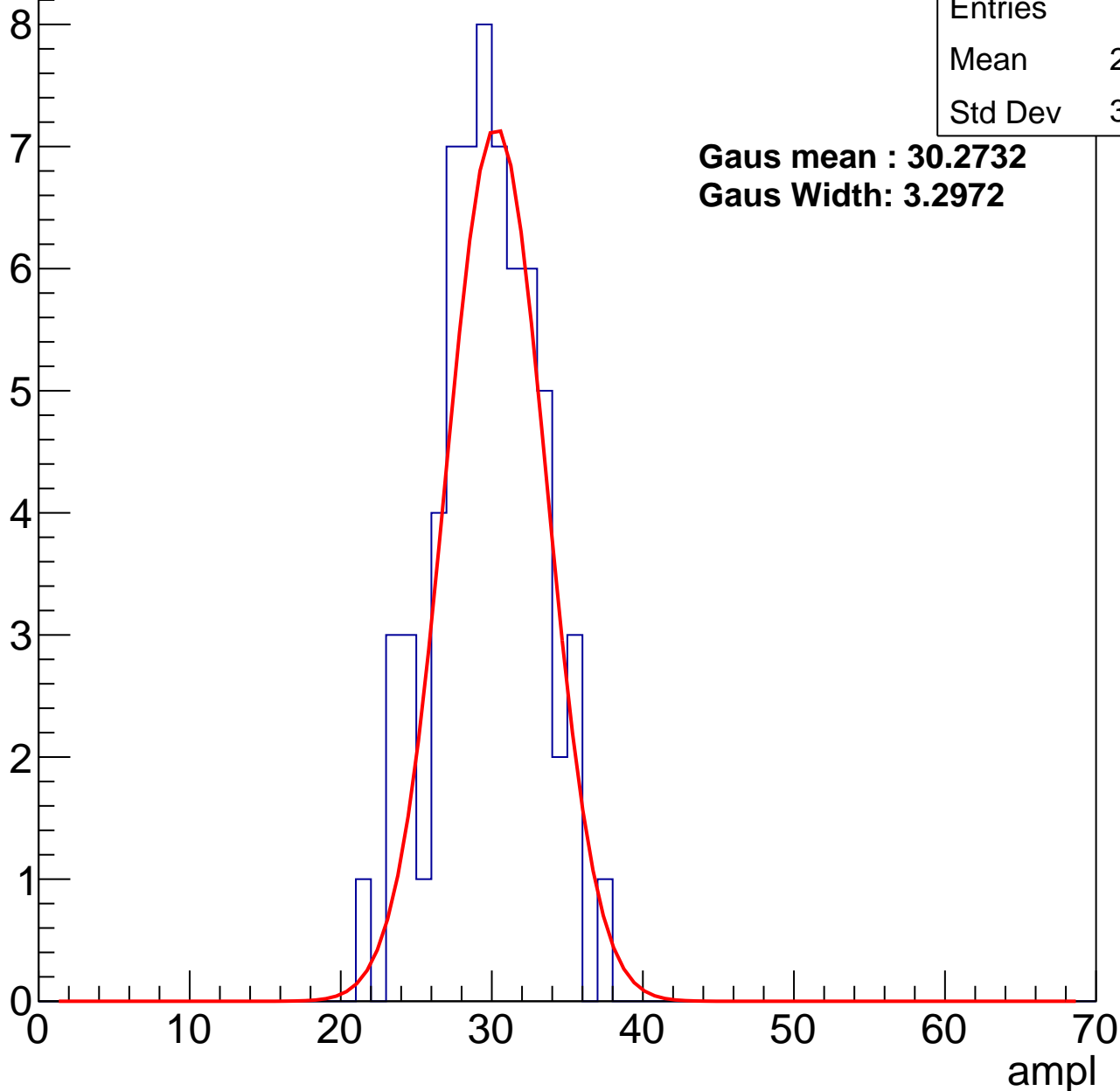
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.23
Std Dev	3.348

**Gaus mean : 30.2732**

**Gaus Width: 3.2972**



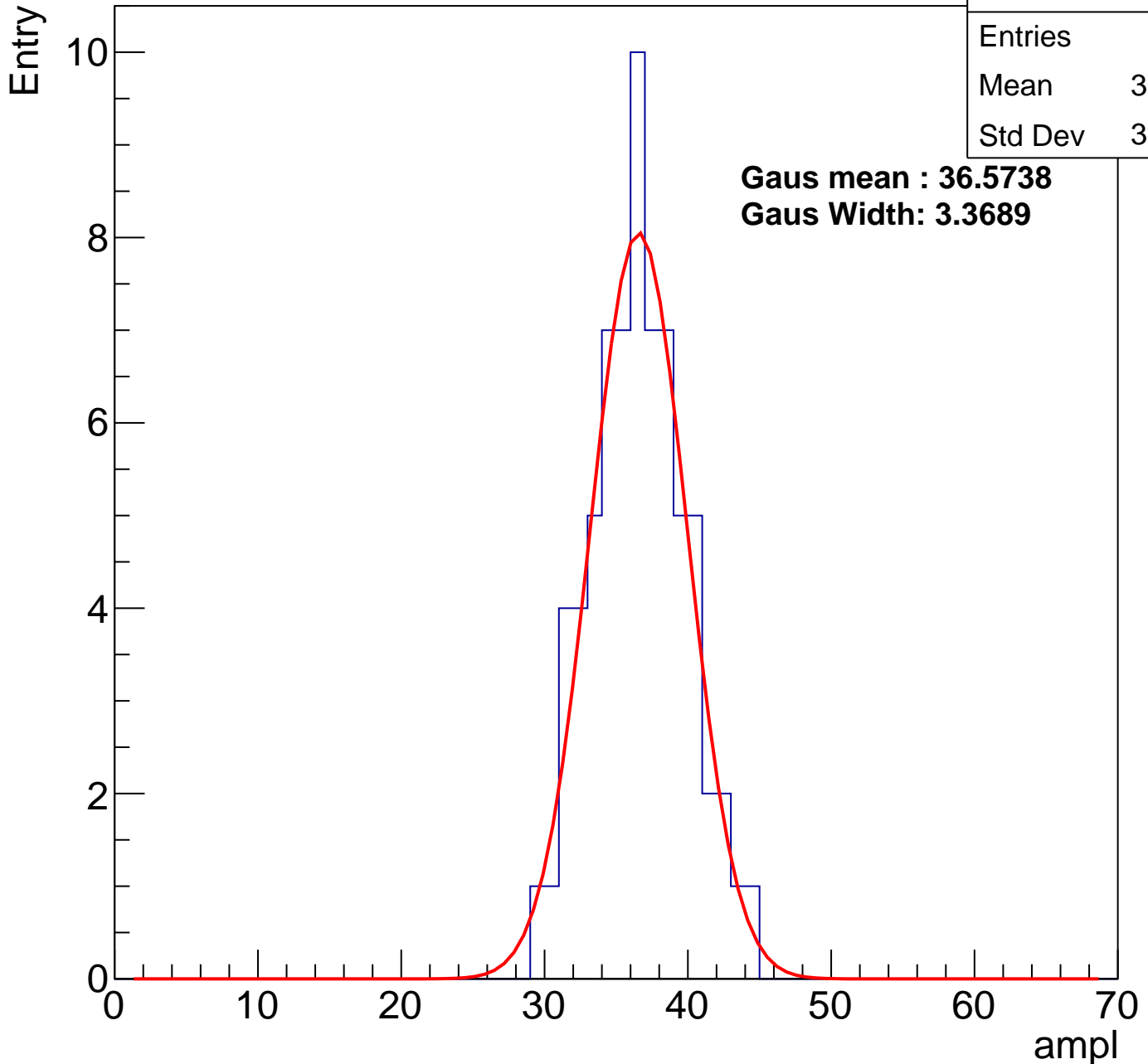
# B1L000S, U24-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	36.12
Std Dev	3.237

**Gaus mean : 36.5738**

**Gaus Width: 3.3689**



# B1L000S, U24-ch71, adc2

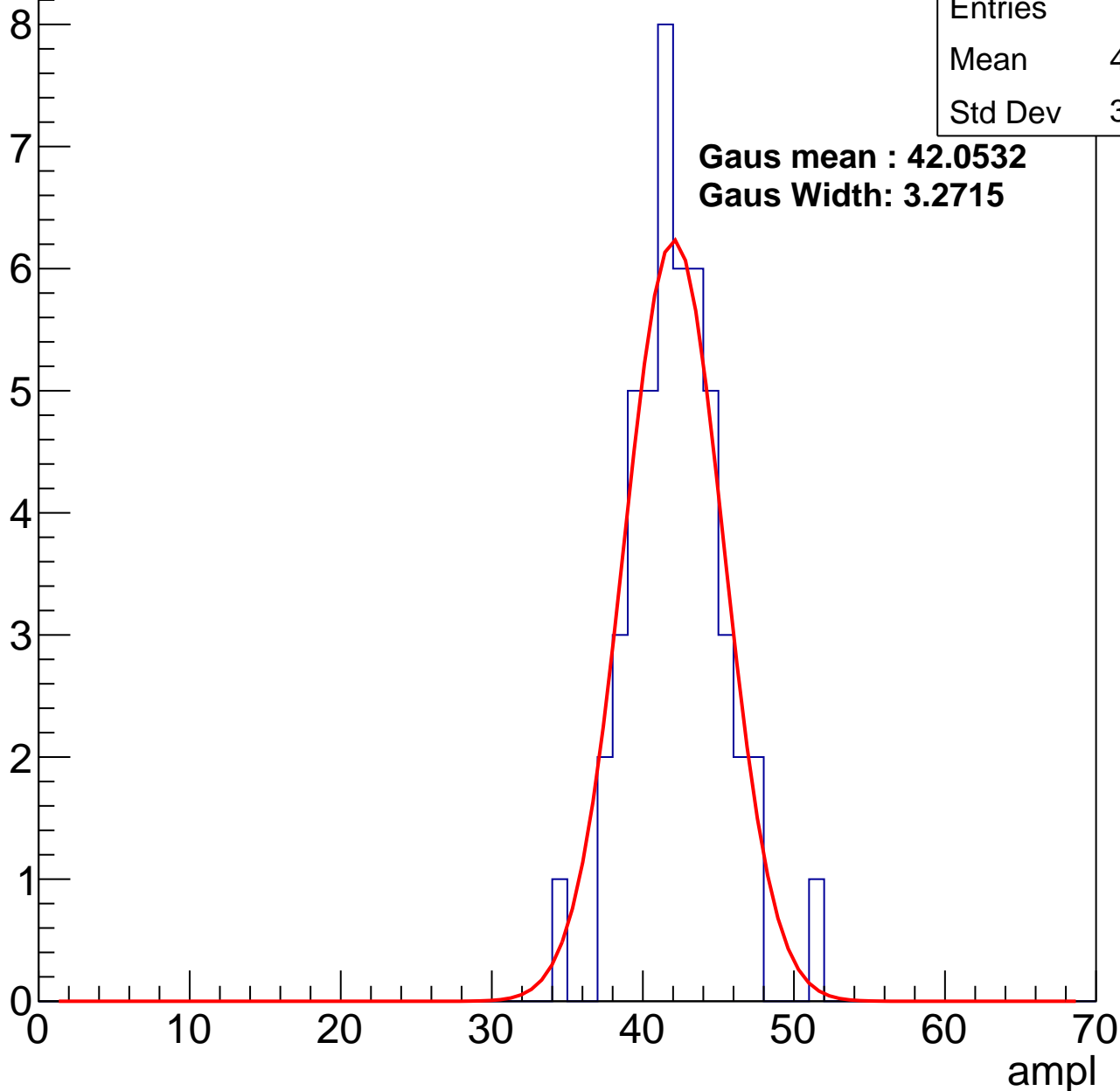
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	41.78
Std Dev	3.026

**Gaus mean : 42.0532**

**Gaus Width: 3.2715**

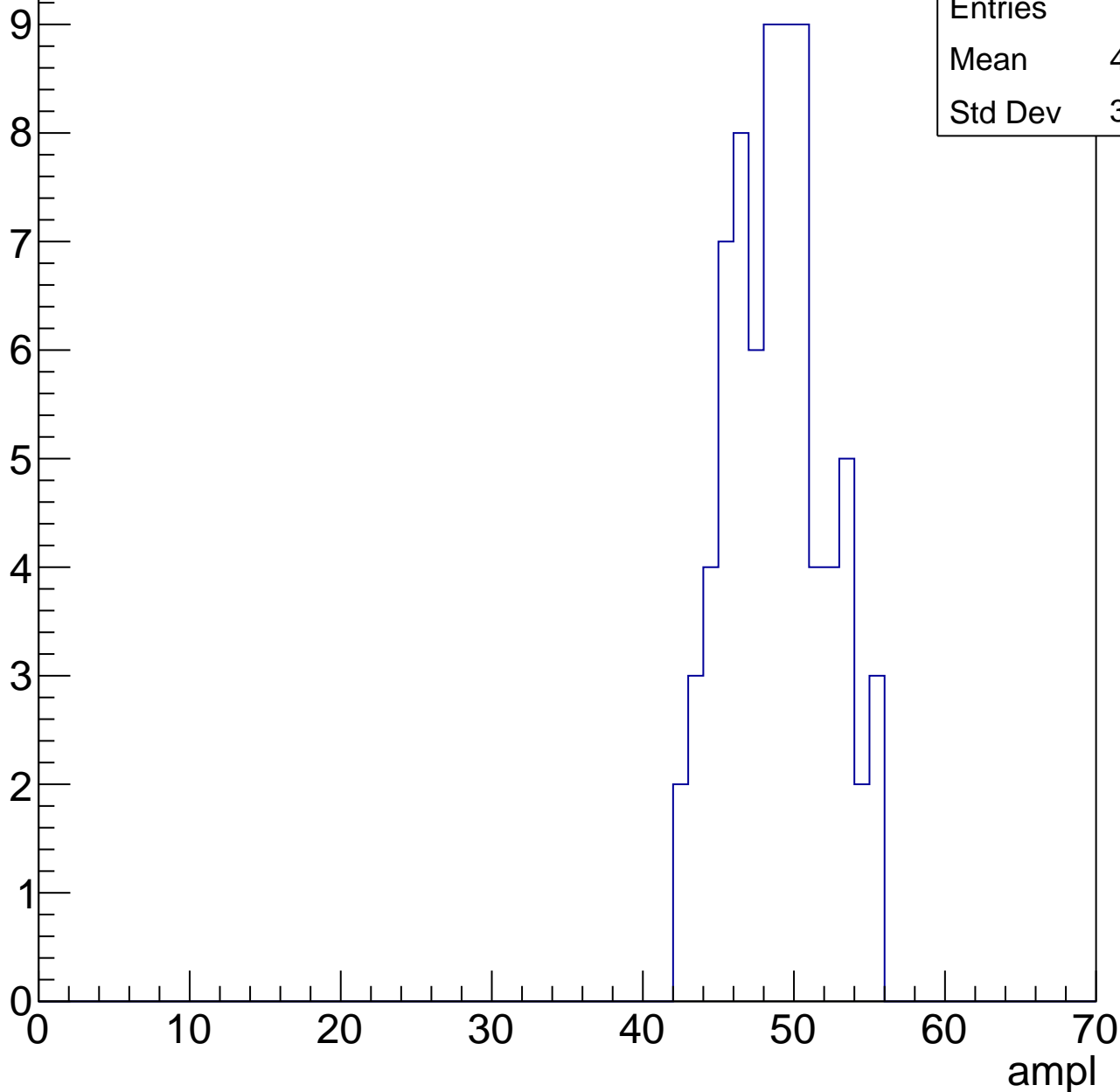


# B1L000S, U24-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	48.36
Std Dev	3.248

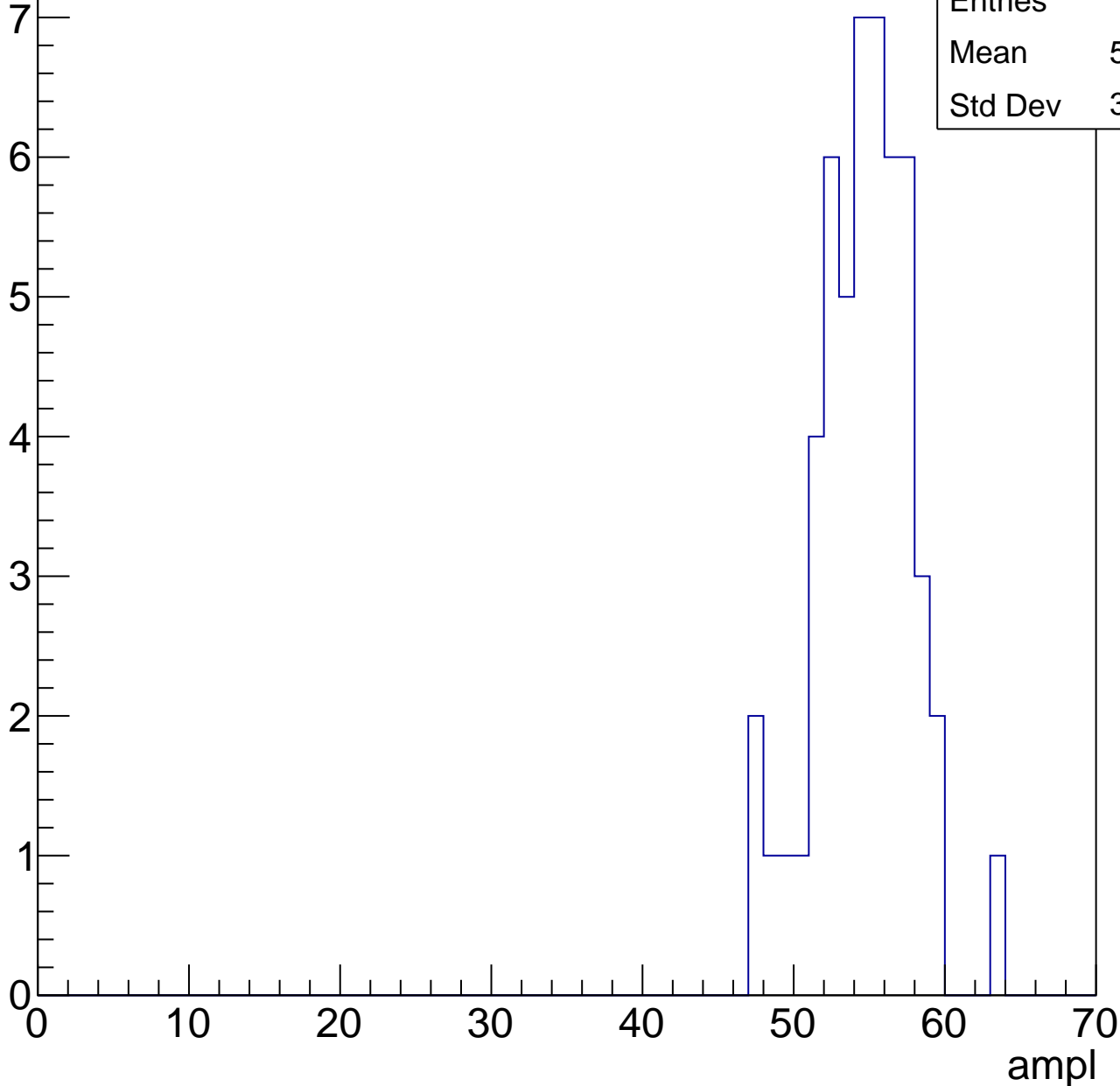


# B1L000S, U24-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	54.19
Std Dev	3.107

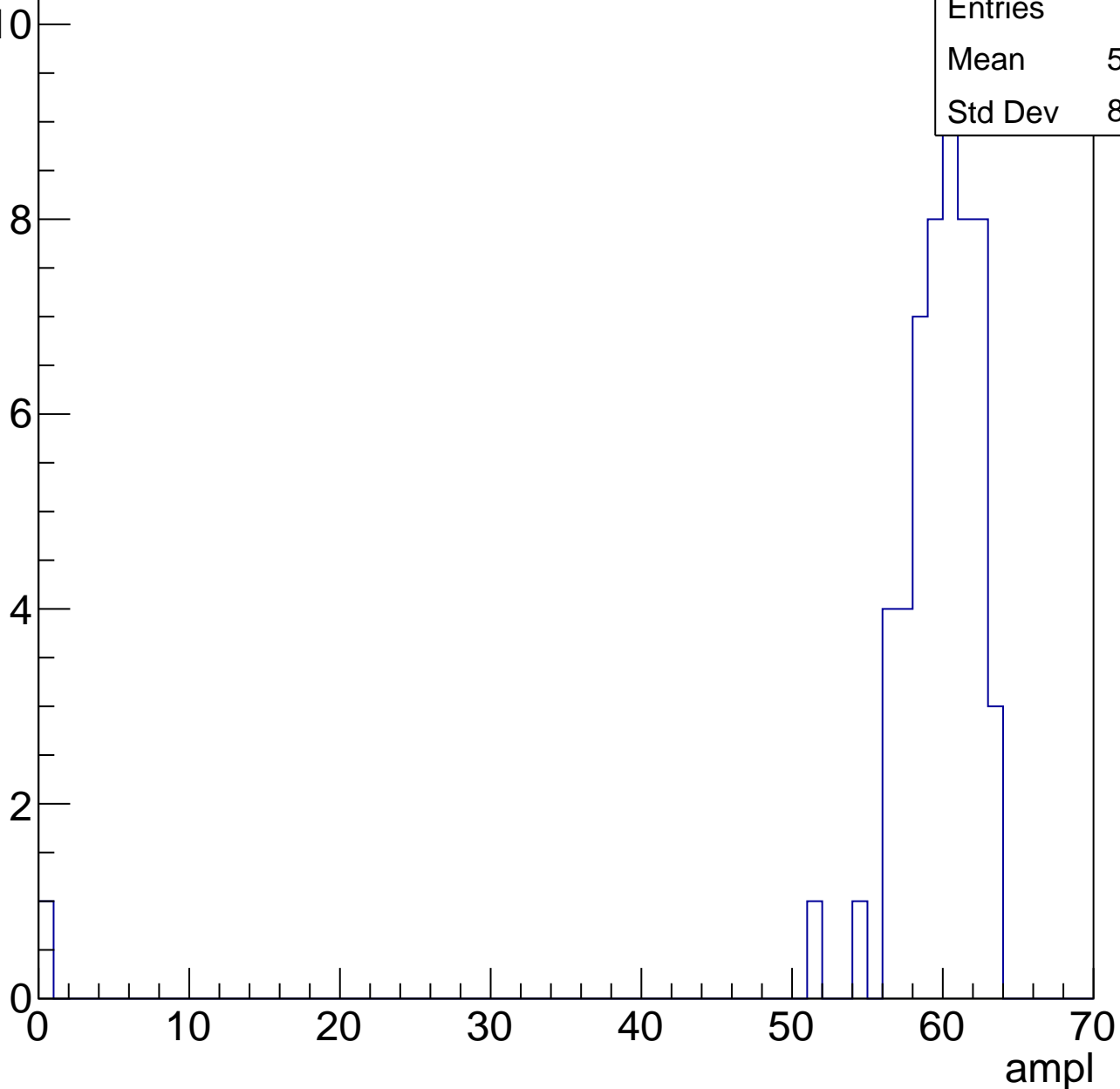


# B1L000S, U24-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.33
Std Dev	8.275

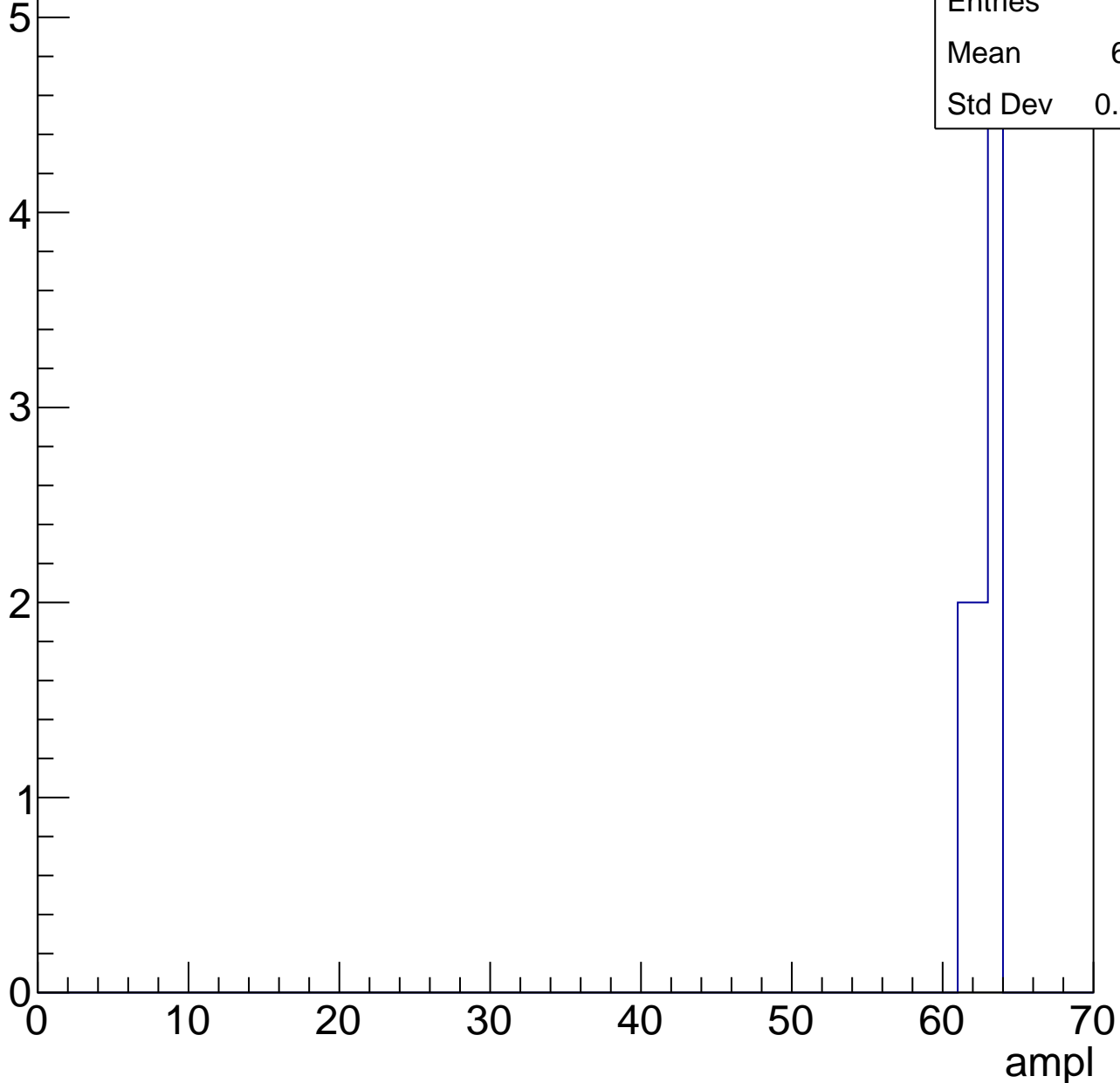


# B1L000S, U24-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	62.33
Std Dev	0.8165





# B1L000S, U24-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U24-ch72, adc0

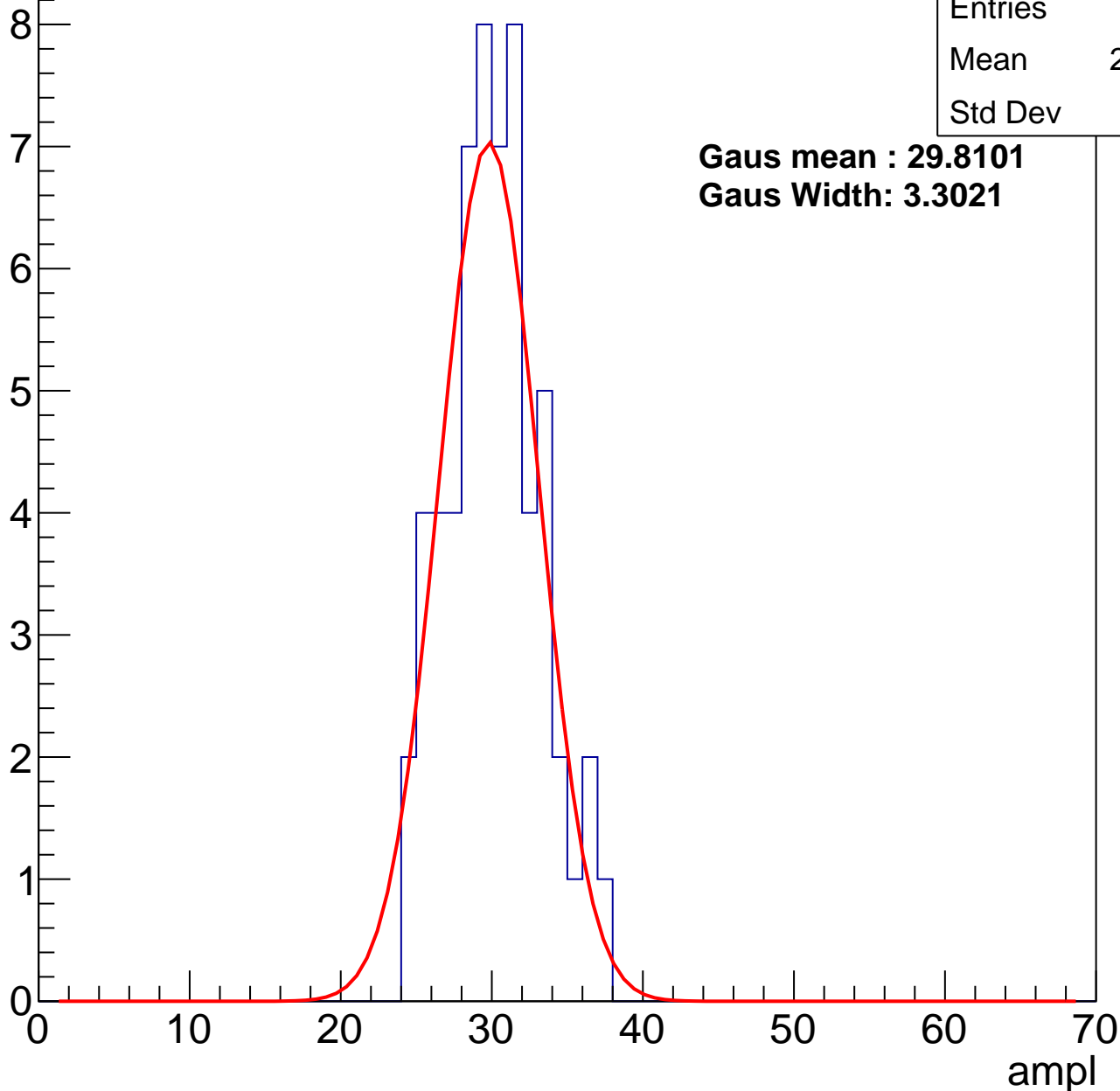
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.68
Std Dev	3.05

**Gaus mean : 29.8101**

**Gaus Width: 3.3021**



# B1L000S, U24-ch72, adc1

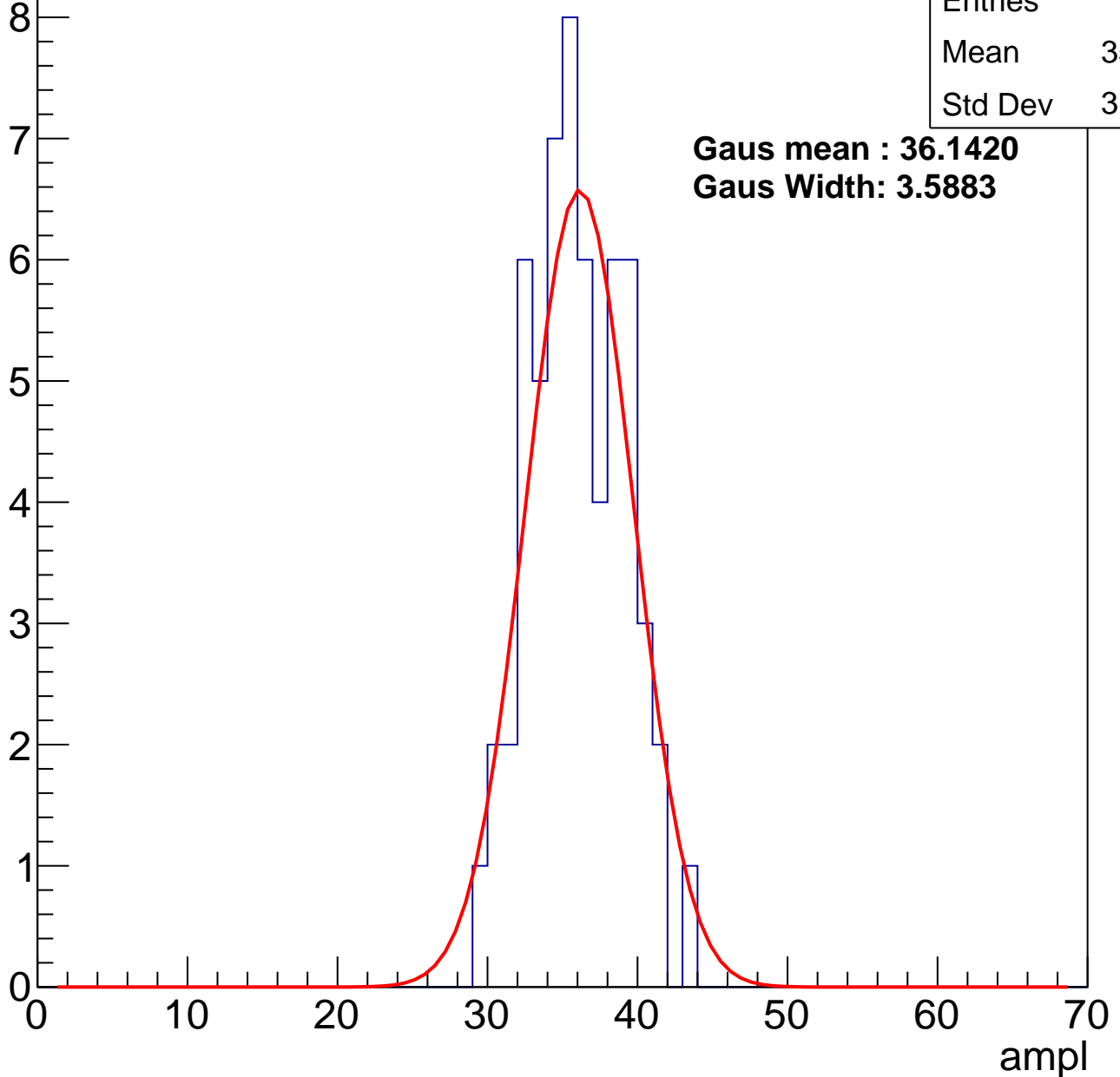
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	35.54
Std Dev	3.094

**Gaus mean : 36.1420**

**Gaus Width: 3.5883**



# B1L000S, U24-ch72, adc2

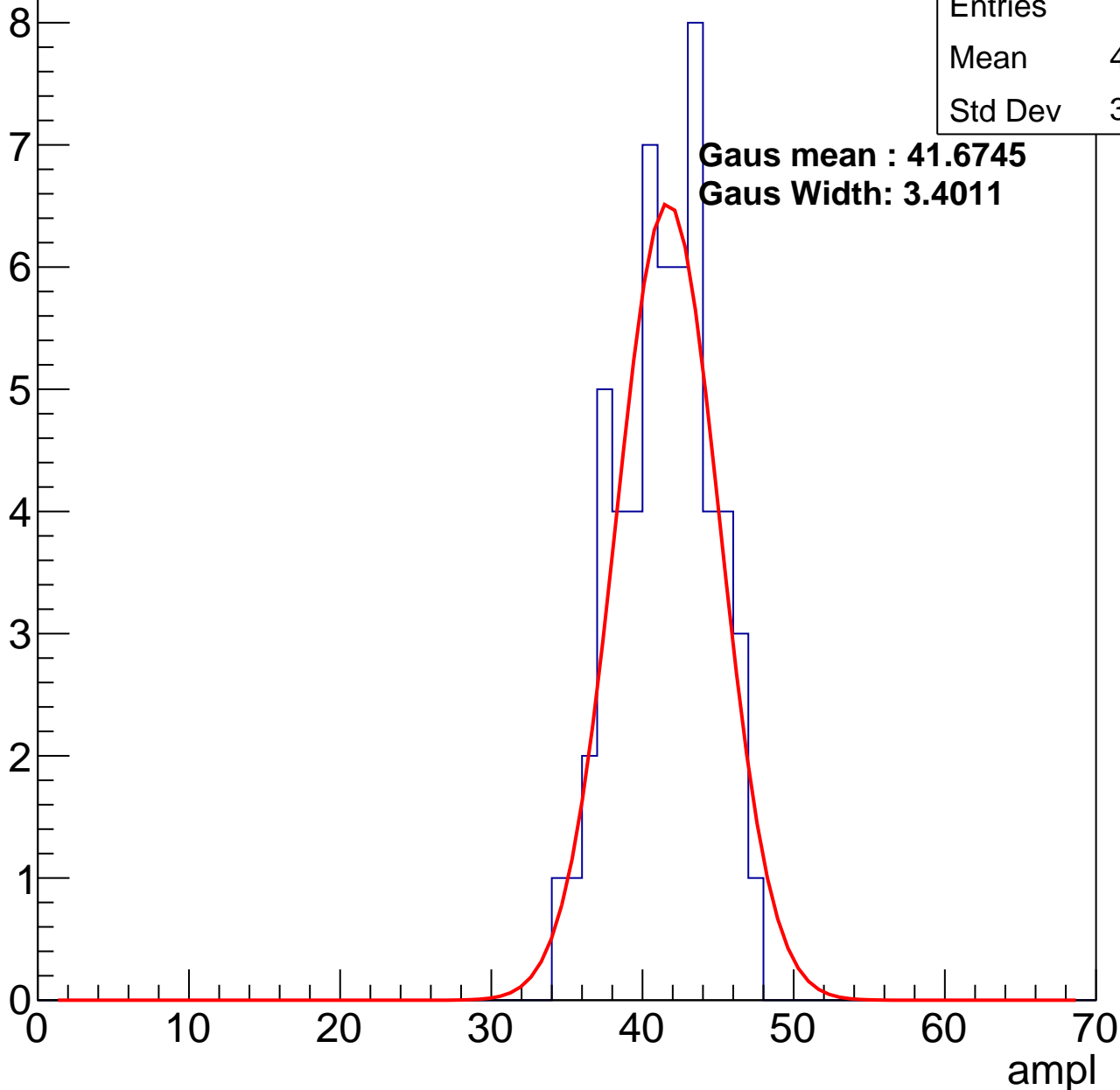
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.02
Std Dev	3.056

**Gaus mean : 41.6745**

**Gaus Width: 3.4011**



# B1L000S, U24-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	47.86
Std Dev	3.616

Entry

10

8

6

4

2

0

0

10

20

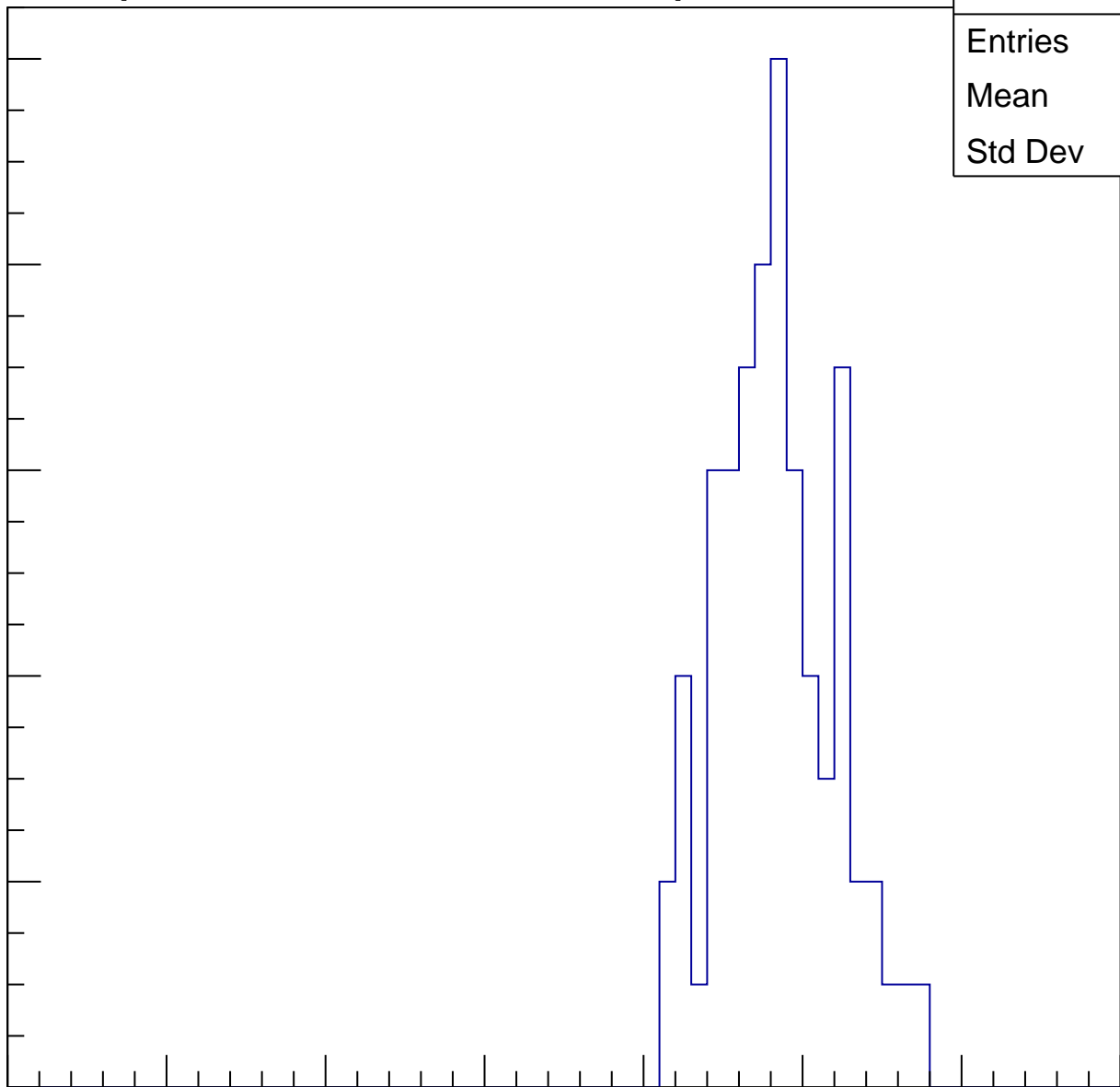
30

40

50

60

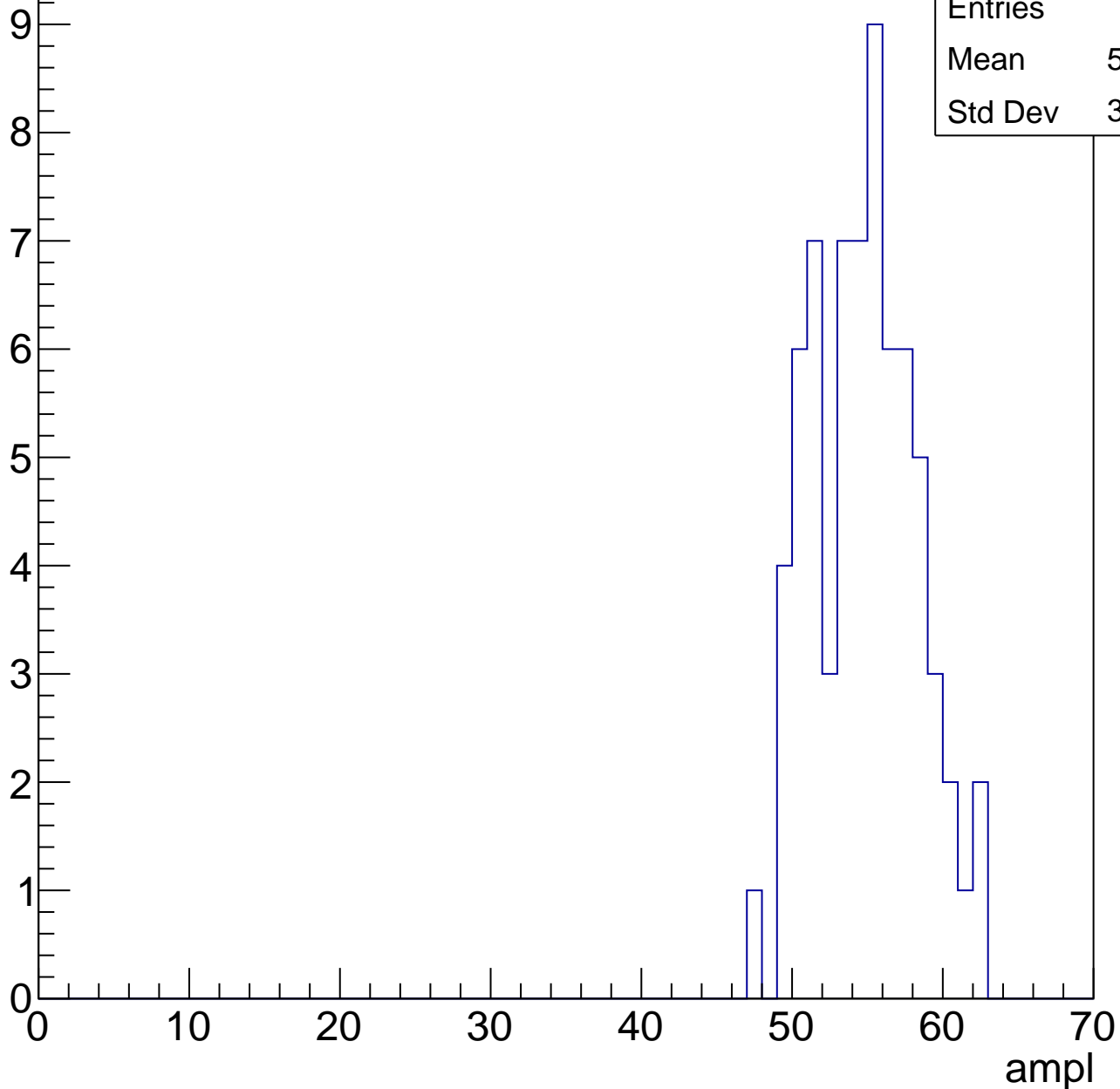
ampl



# B1L000S, U24-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

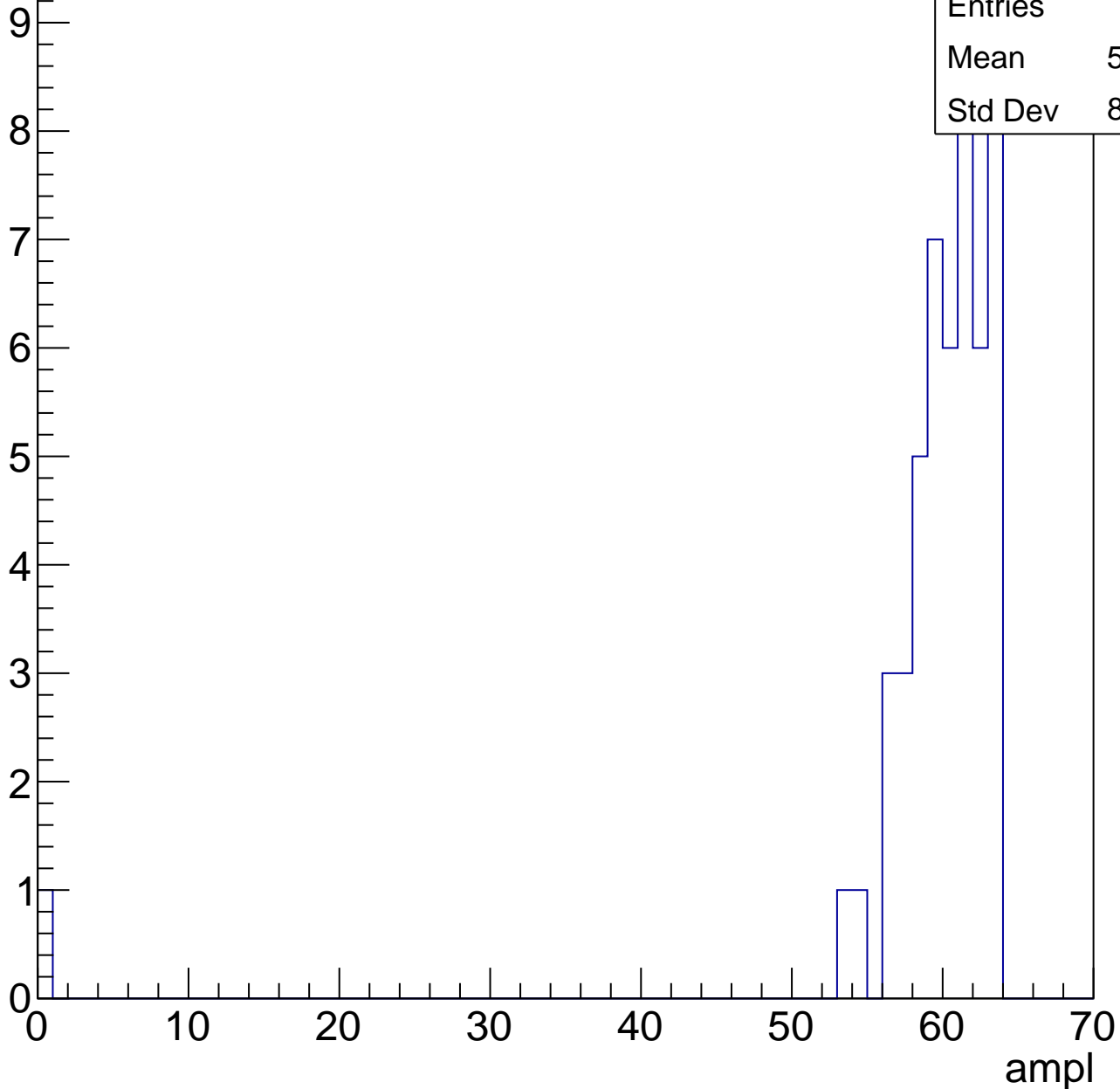


# B1L000S, U24-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.68
Std Dev	8.723



# B1L000S, U24-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

0

10

20

30

40

50

60

70

ampl

Entries	6
Mean	61.67
Std Dev	1.247



# B1L000S, U24-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch73, adc0

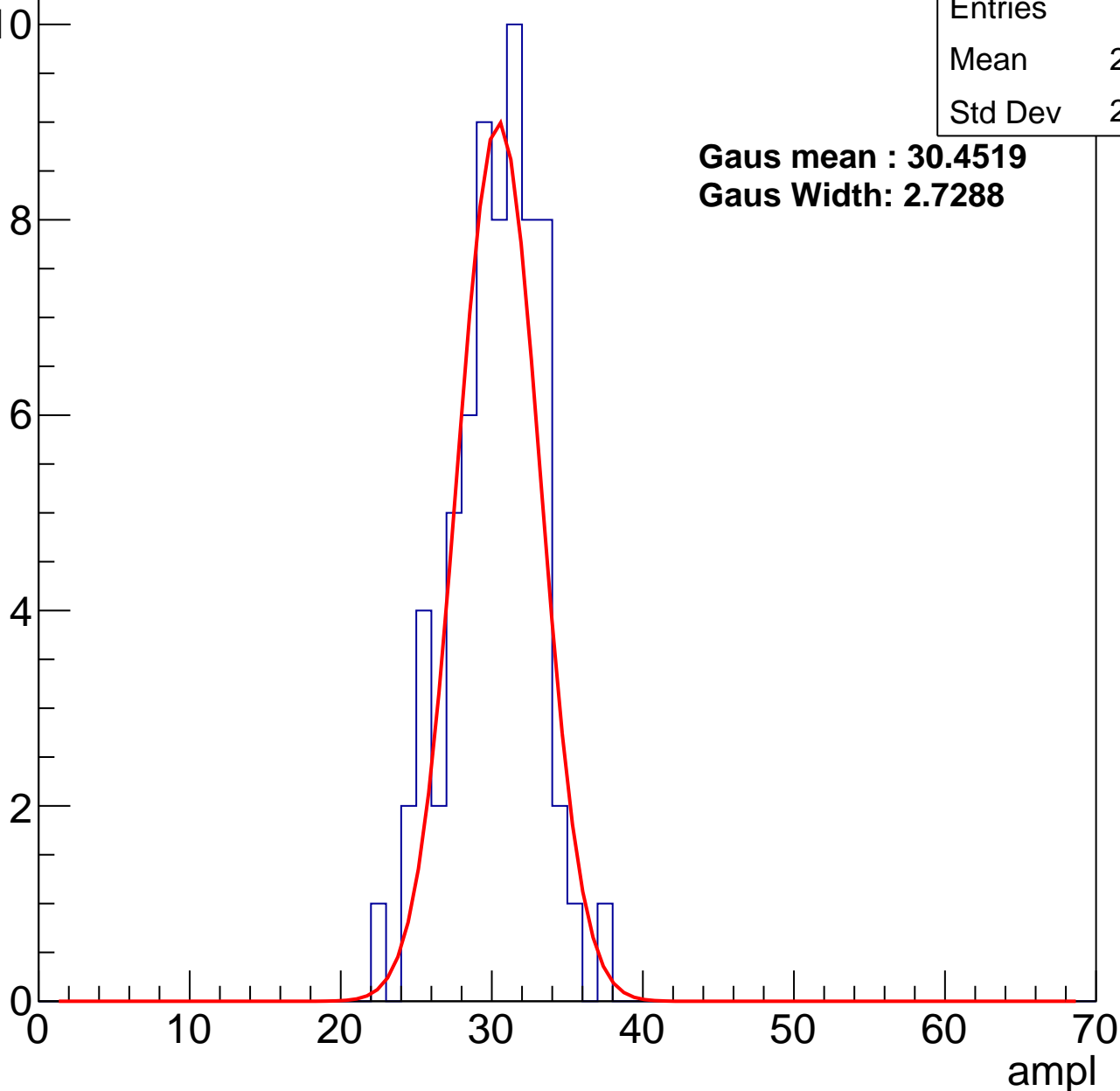
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.79
Std Dev	2.899

**Gaus mean : 30.4519**

**Gaus Width: 2.7288**



# B1L000S, U24-ch73, adc1

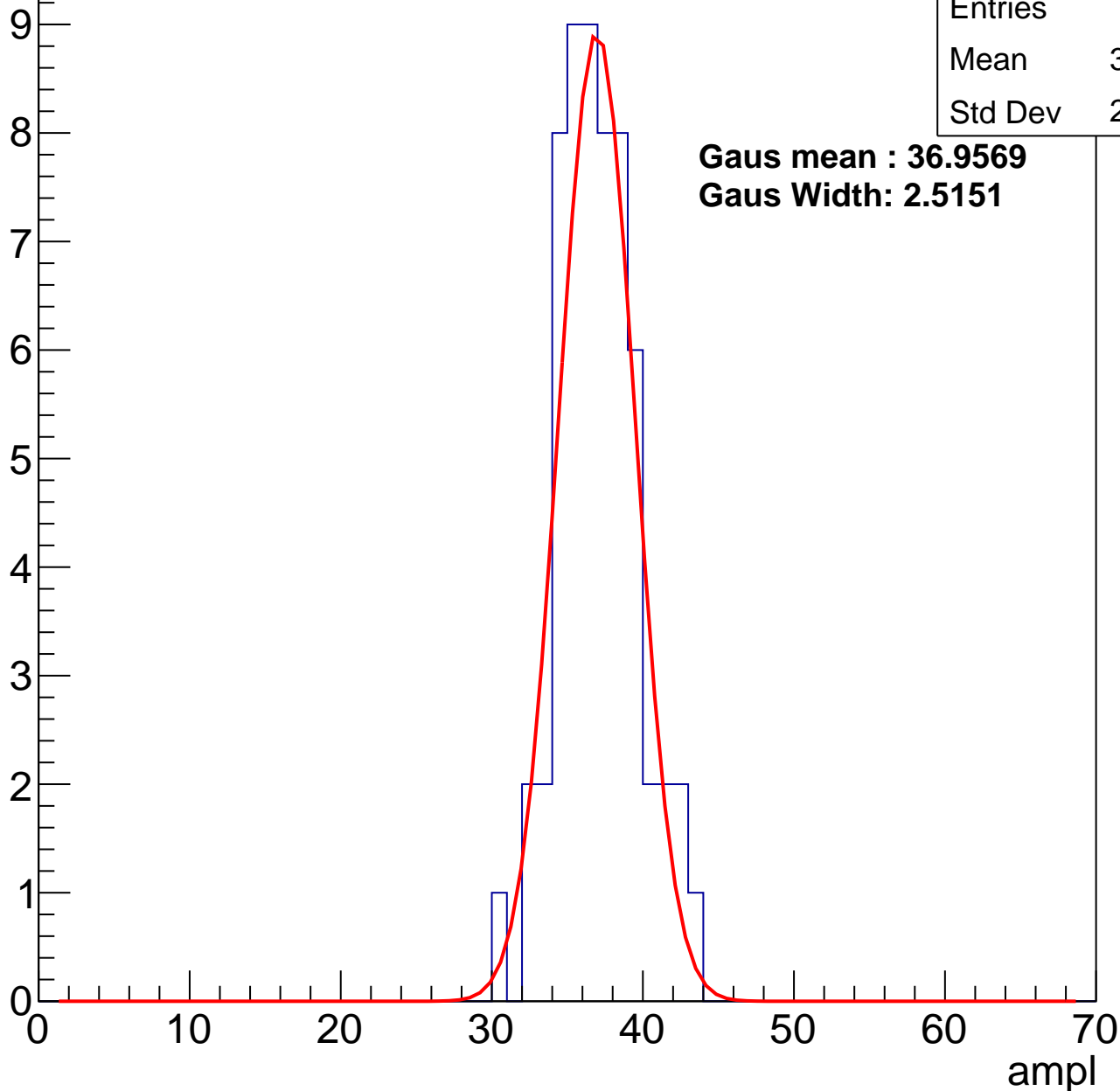
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	36.57
Std Dev	2.597

**Gaus mean : 36.9569**

**Gaus Width: 2.5151**



# B1L000S, U24-ch73, adc2

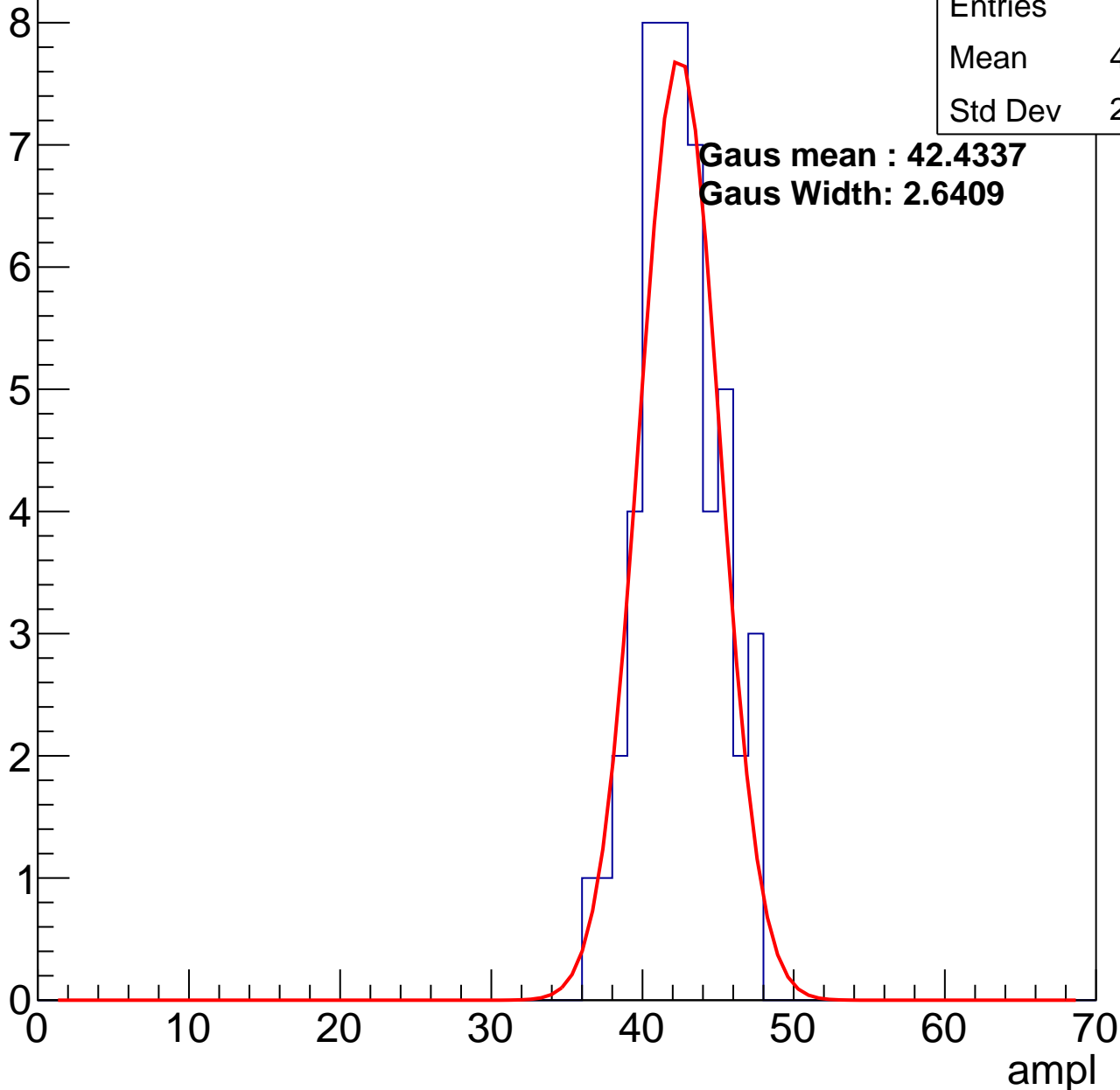
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	41.96
Std Dev	2.547

**Gaus mean : 42.4337**

**Gaus Width: 2.6409**



# B1L000S, U24-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

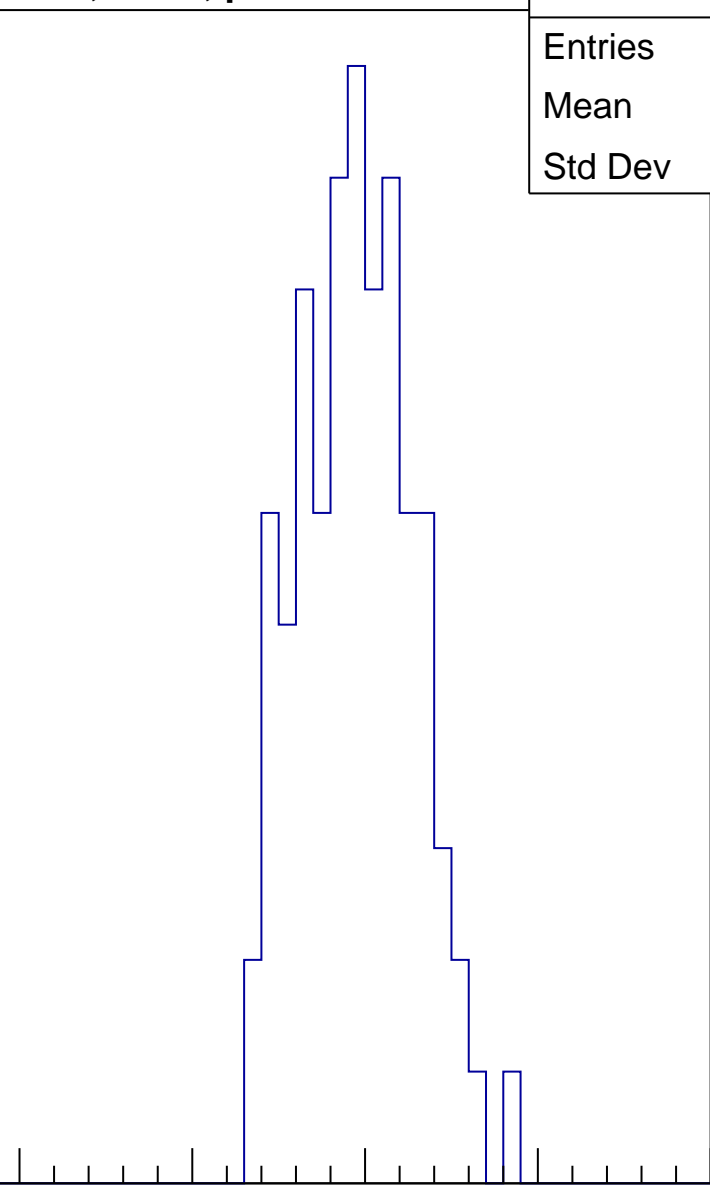
Entries	82
Mean	49.05
Std Dev	3.272

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

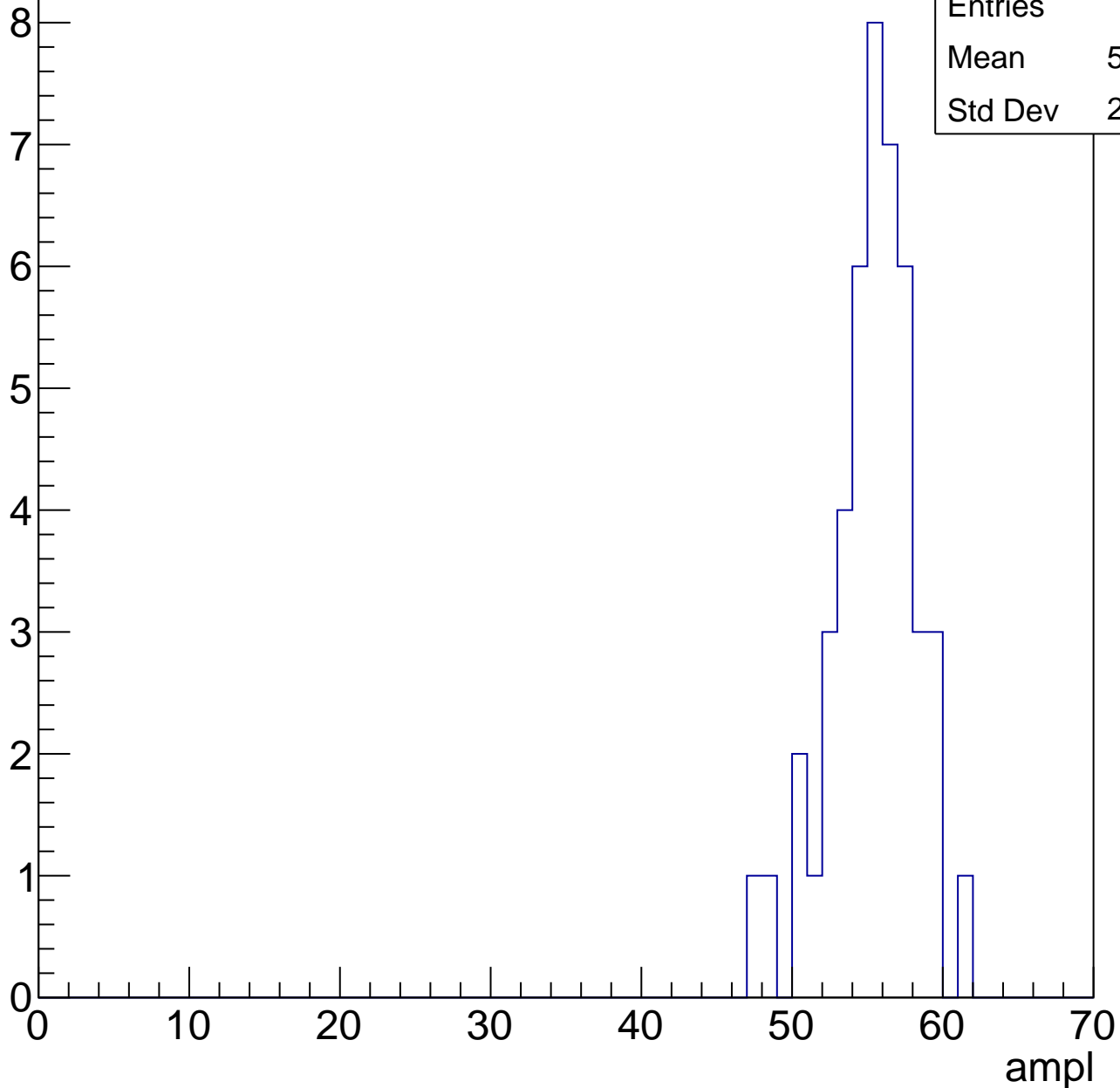
ampl



# B1L000S, U24-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	46
Mean	54.87
Std Dev	2.833

# B1L000S, U24-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

Entries

57

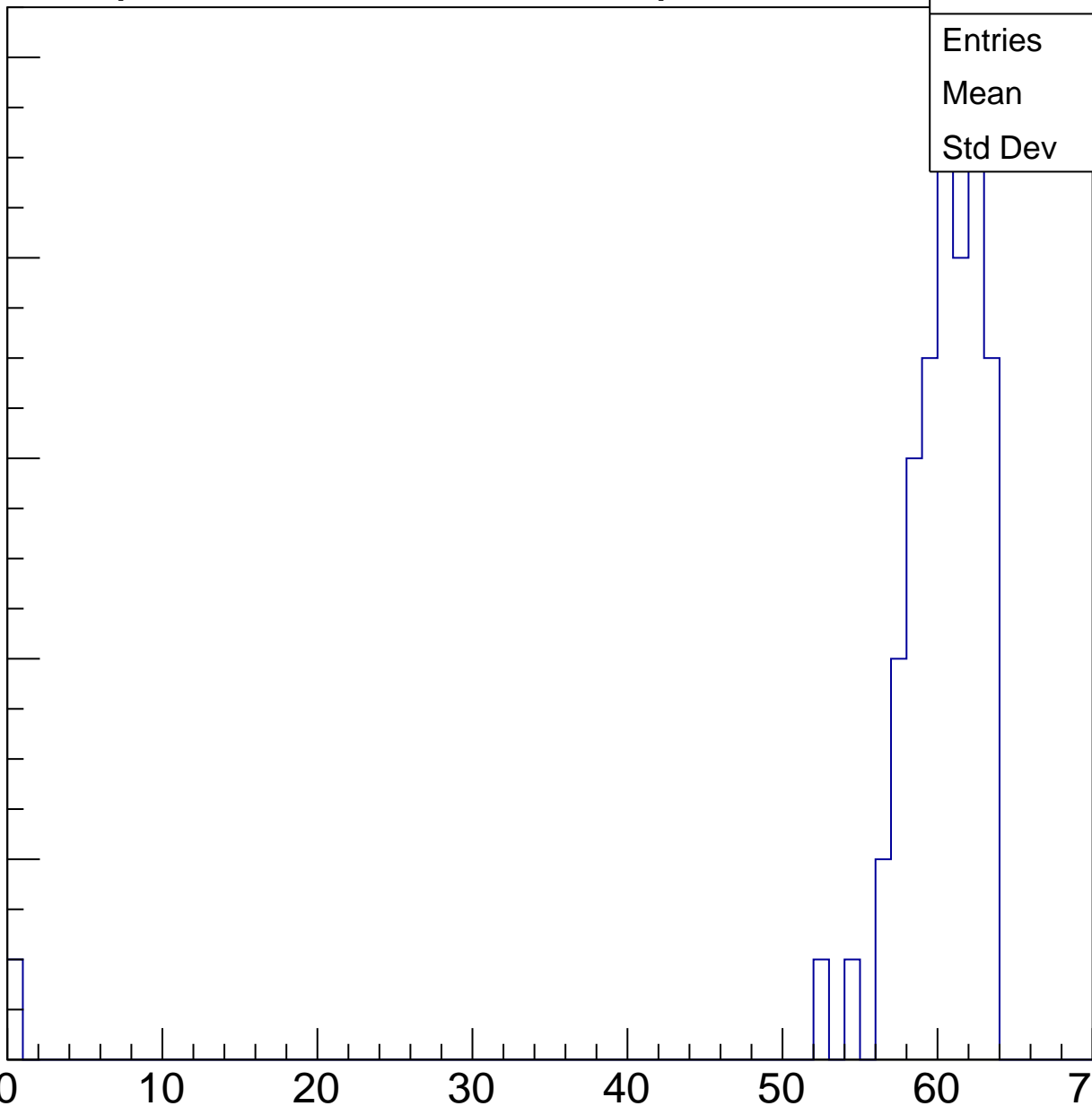
Mean

58.88

Std Dev

8.206

ampl



# B1L000S, U24-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U24-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	27.91
Std Dev	3.043

**Gaus mean : 28.0660**

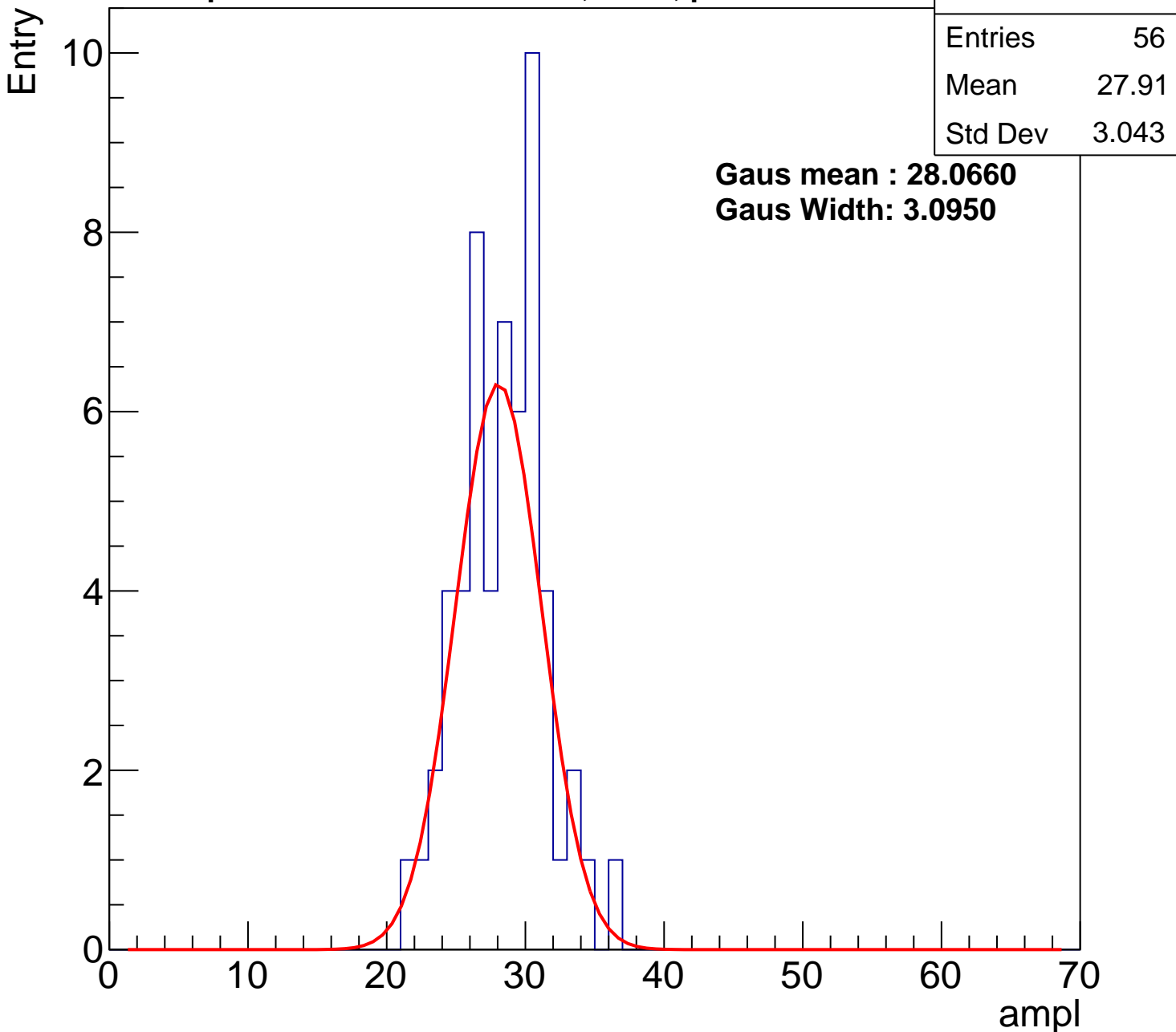
**Gaus Width: 3.0950**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



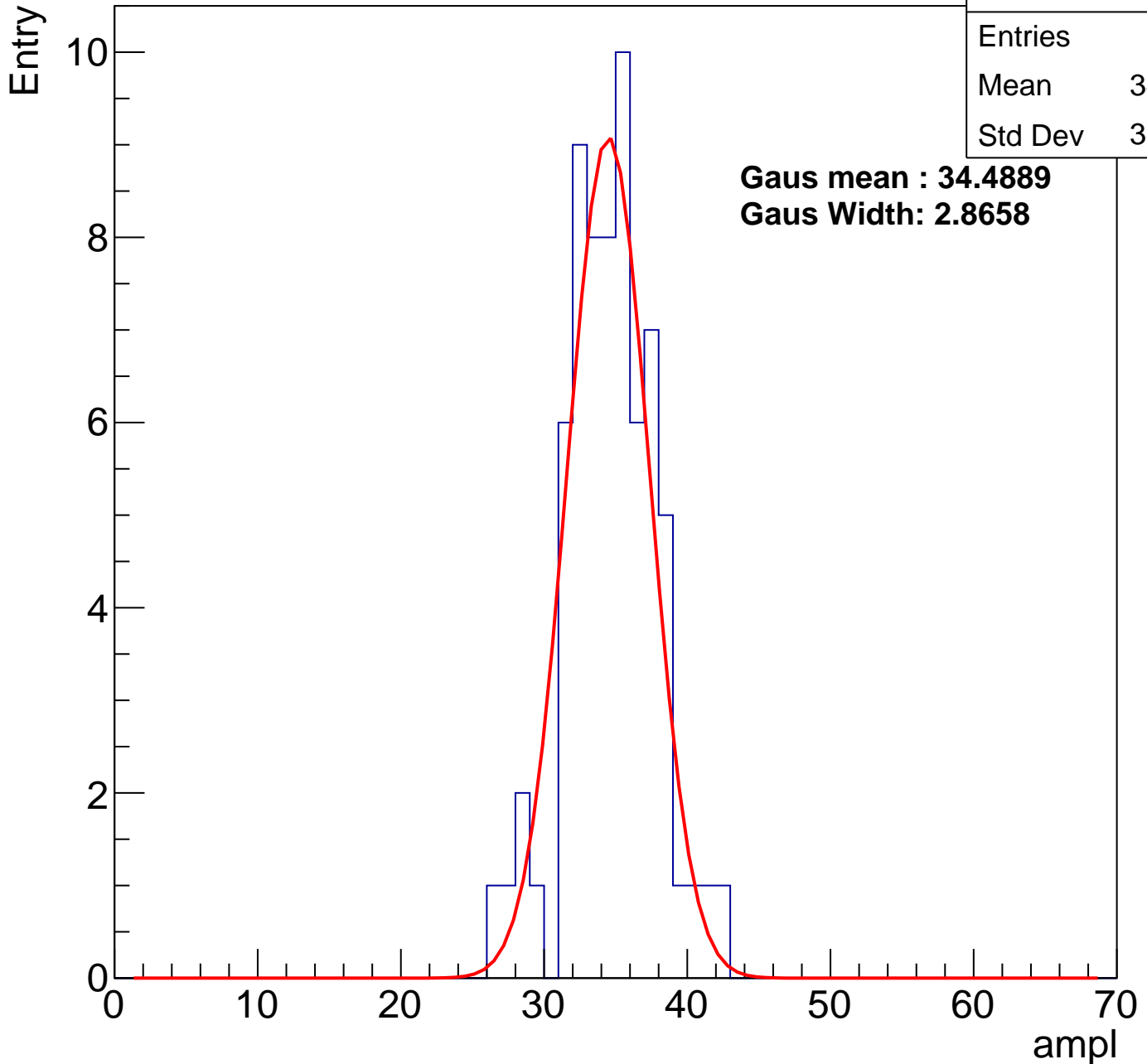
# B1L000S, U24-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	34.19
Std Dev	3.107

**Gaus mean : 34.4889**

**Gaus Width: 2.8658**



# B1L000S, U24-ch74, adc2

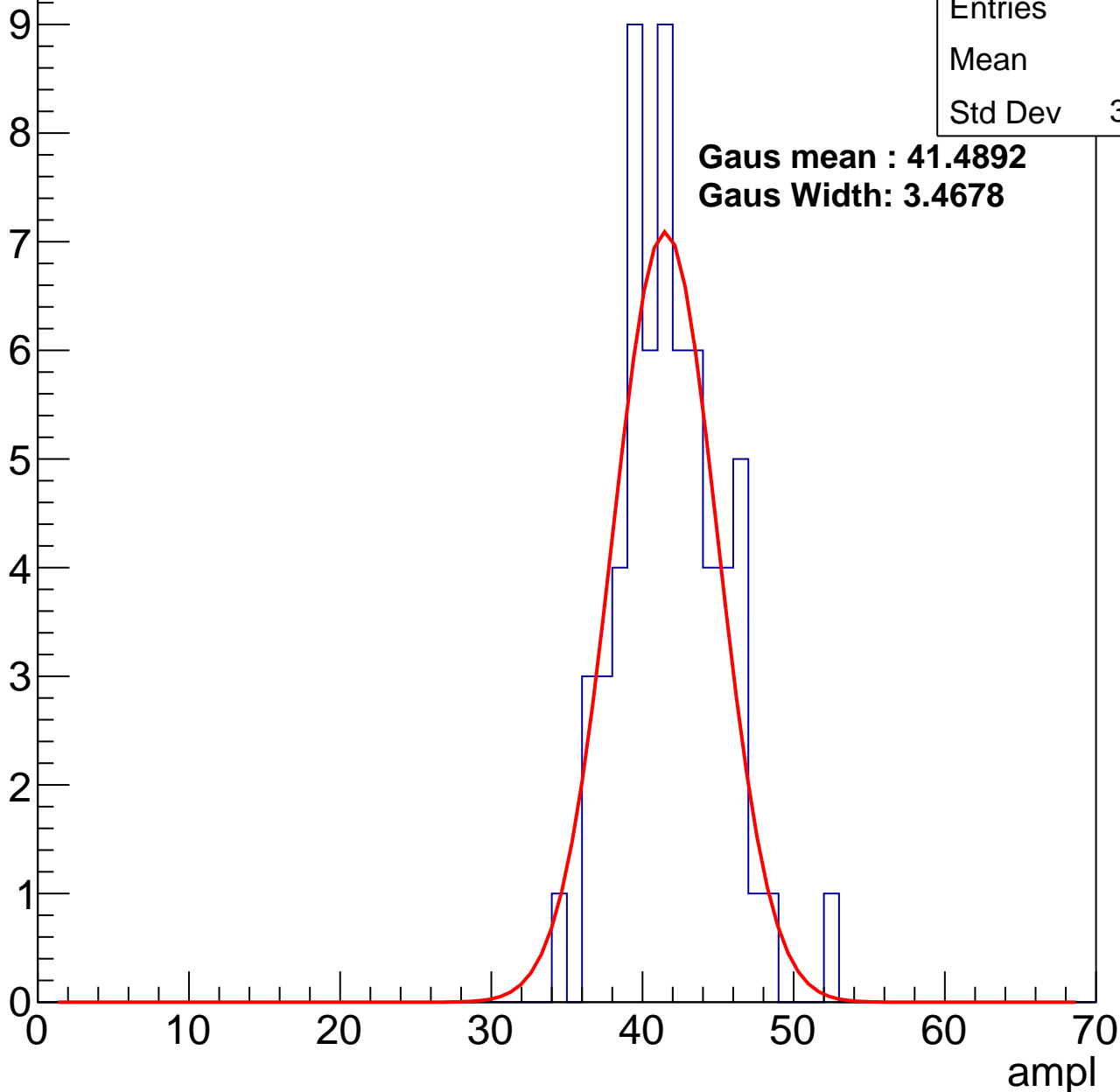
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.4
Std Dev	3.336

**Gaus mean : 41.4892**

**Gaus Width: 3.4678**

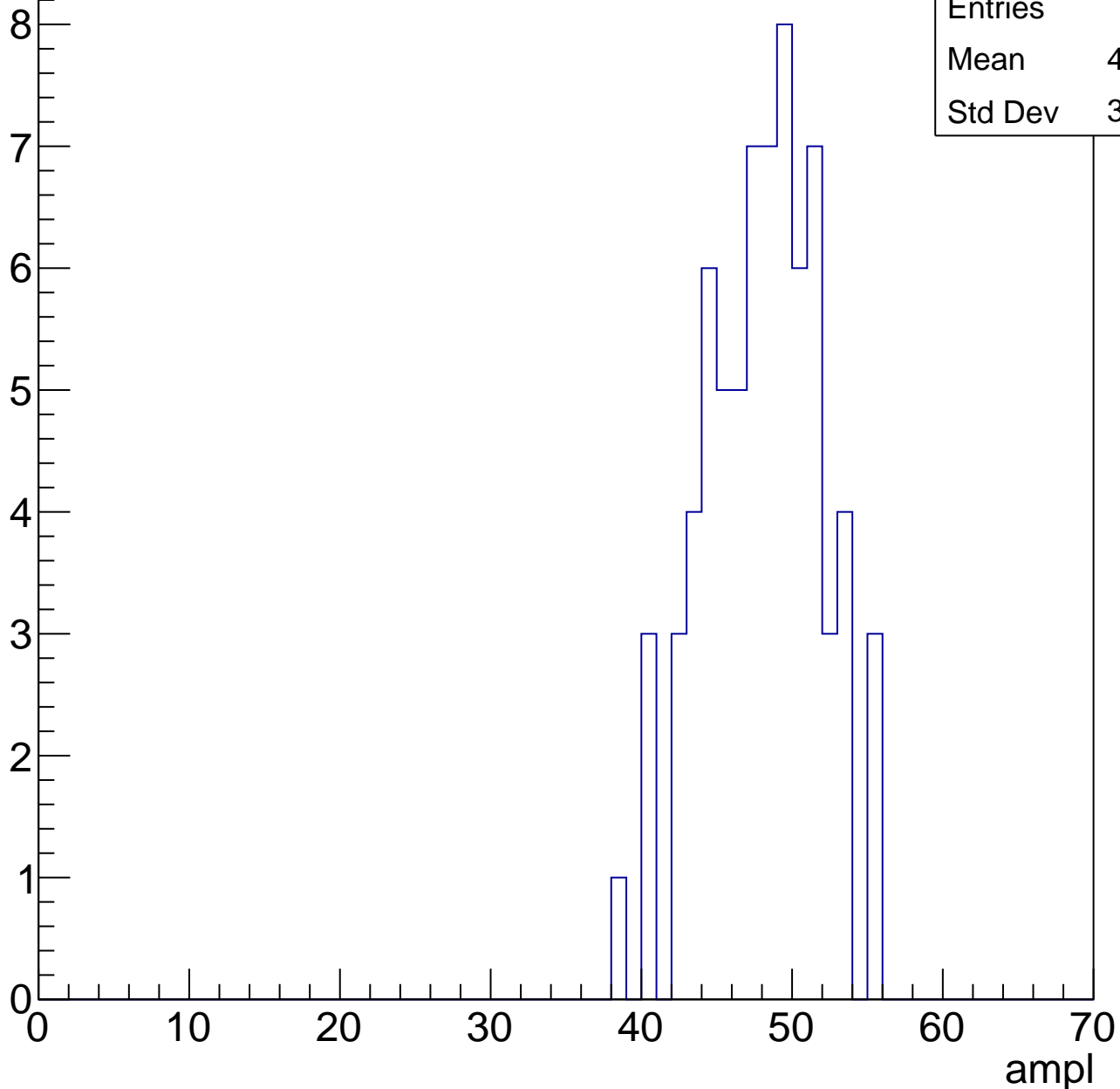


# B1L000S, U24-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	47.53
Std Dev	3.804

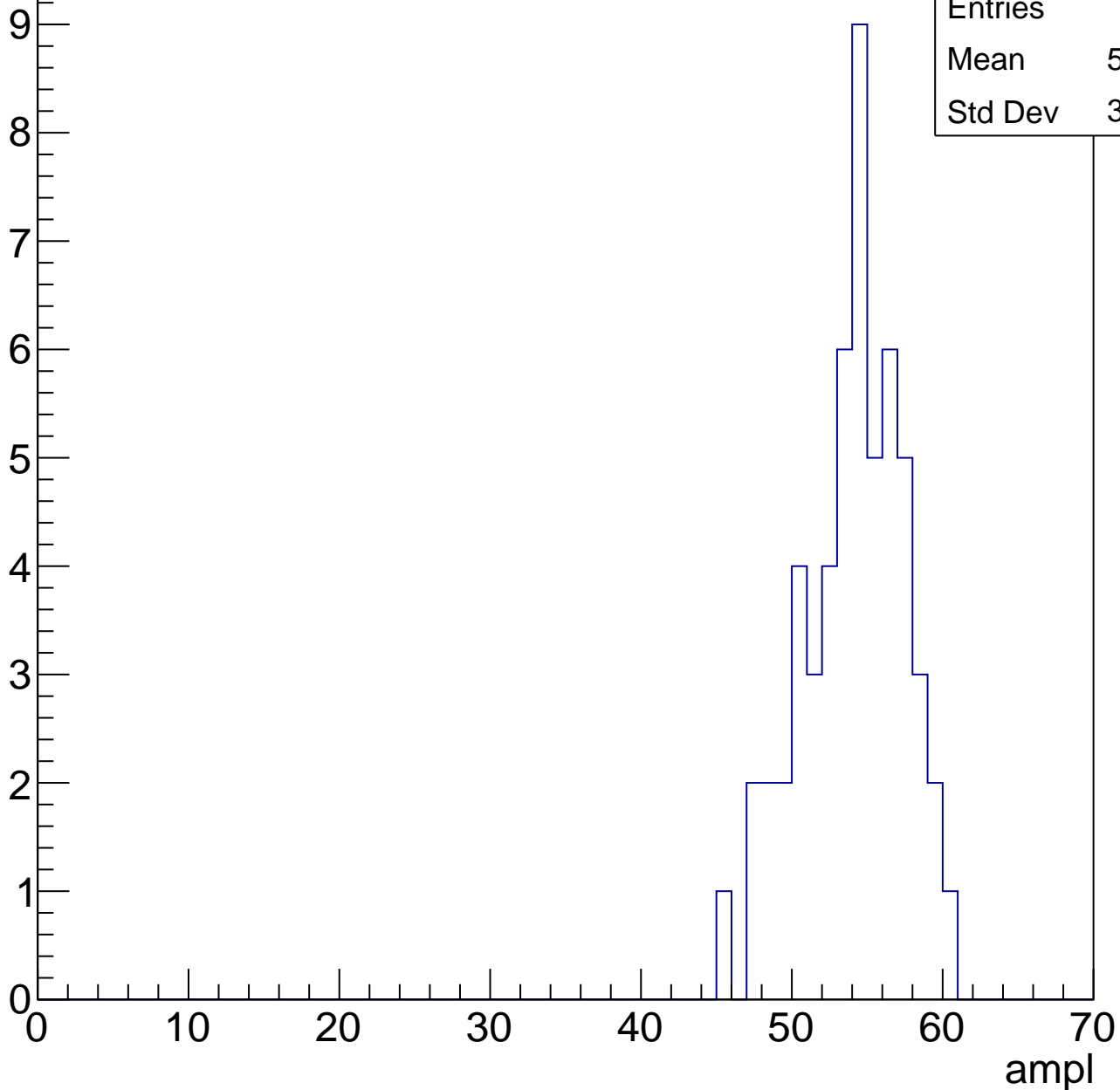


# B1L000S, U24-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	53.56
Std Dev	3.334

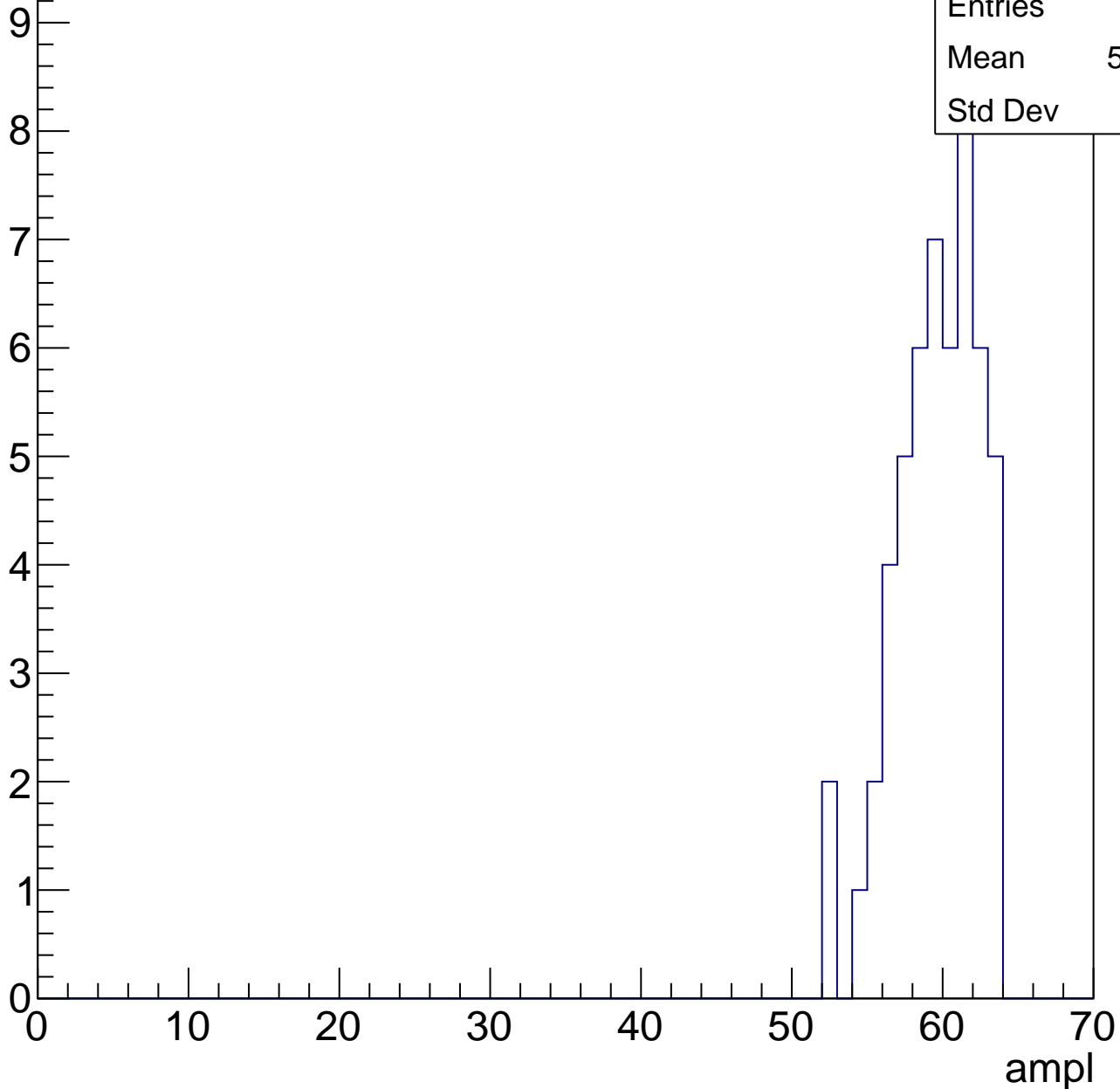


# B1L000S, U24-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	59.13
Std Dev	2.72

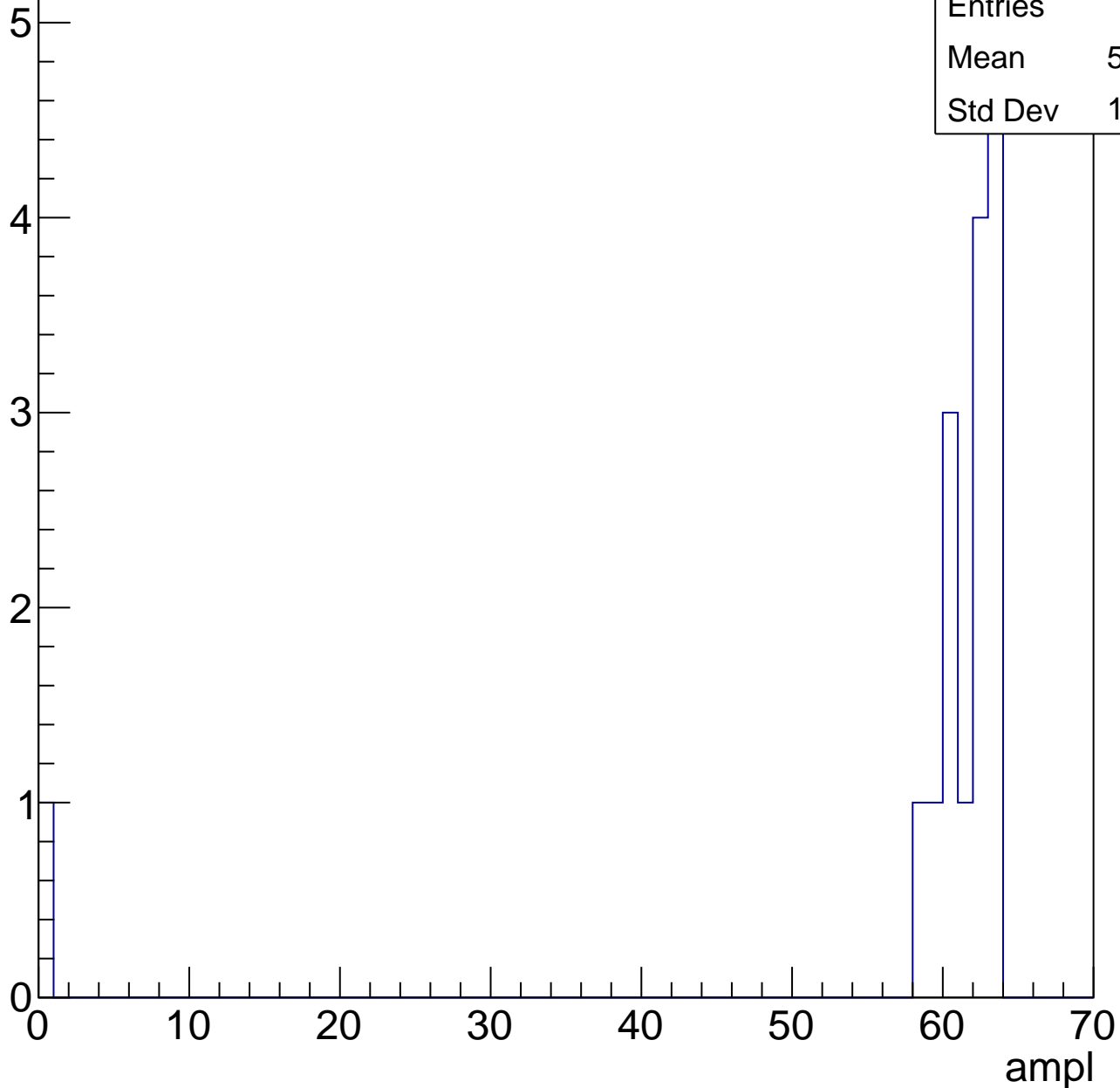


# B1L000S, U24-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.56
Std Dev	14.94





# B1L000S, U24-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	30.36
Std Dev	4.664

**Gaus mean : 31.1342**

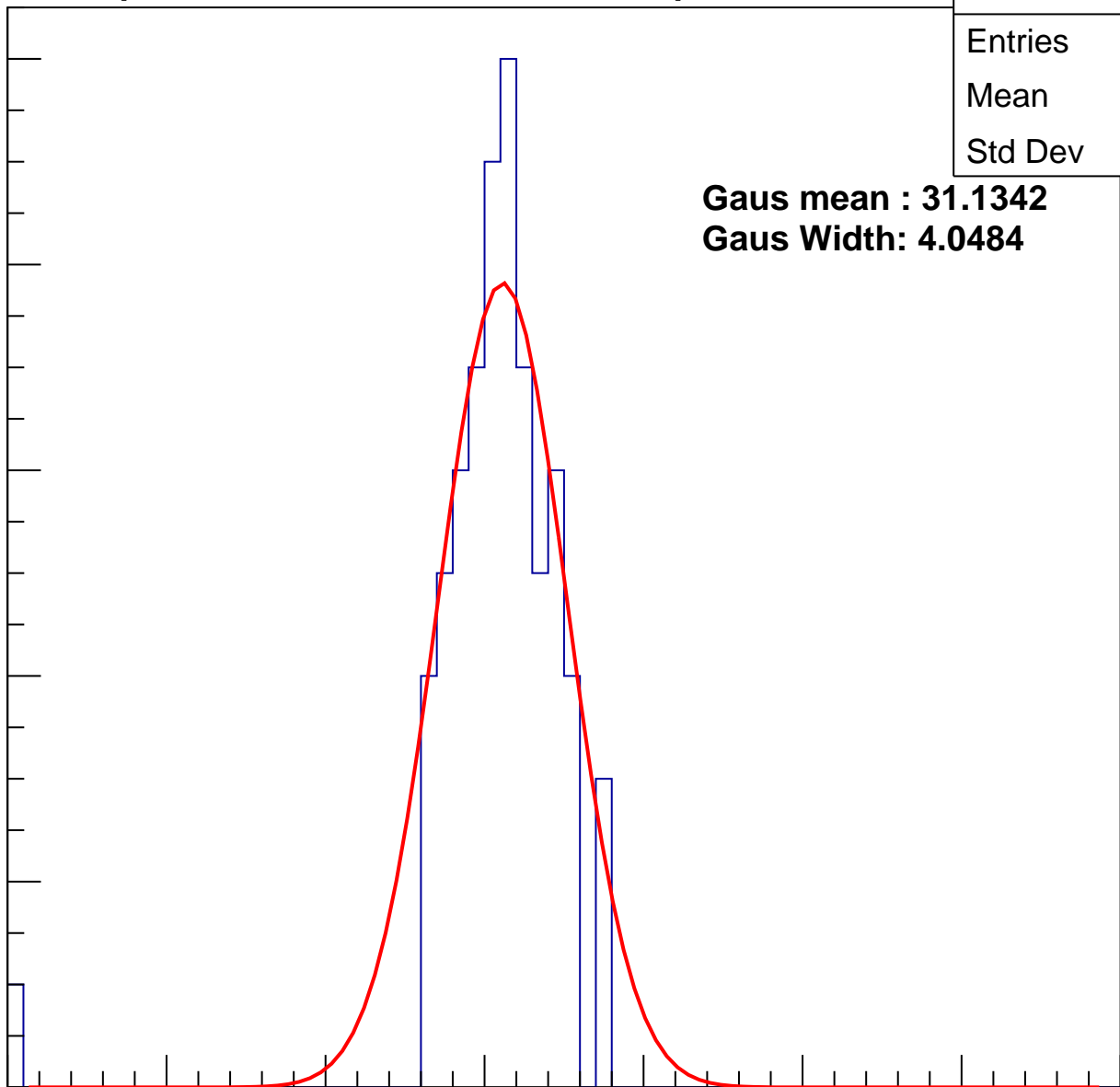
**Gaus Width: 4.0484**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch75, adc1

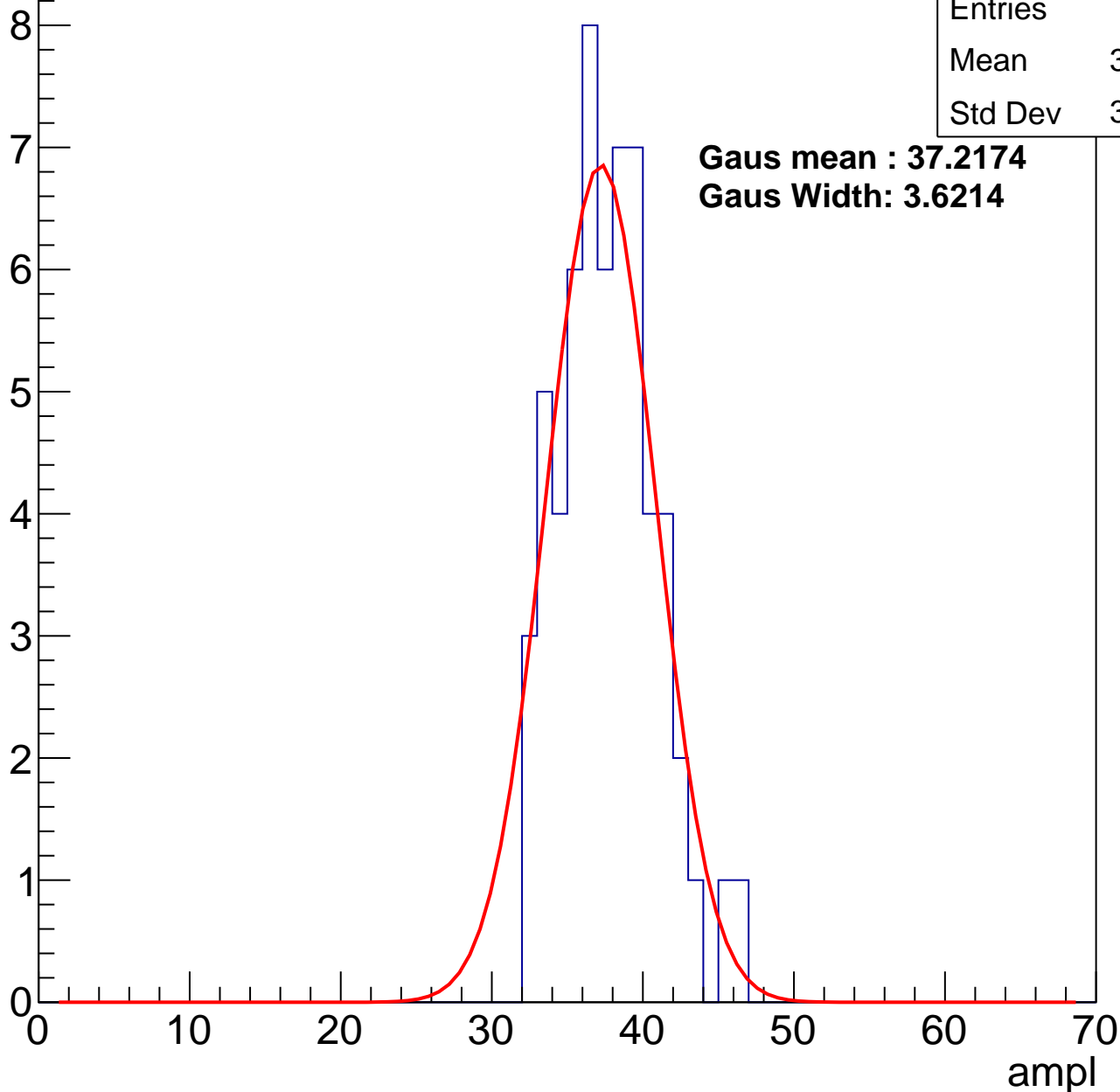
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	37.25
Std Dev	3.149

**Gaus mean : 37.2174**

**Gaus Width: 3.6214**



# B1L000S, U24-ch75, adc2

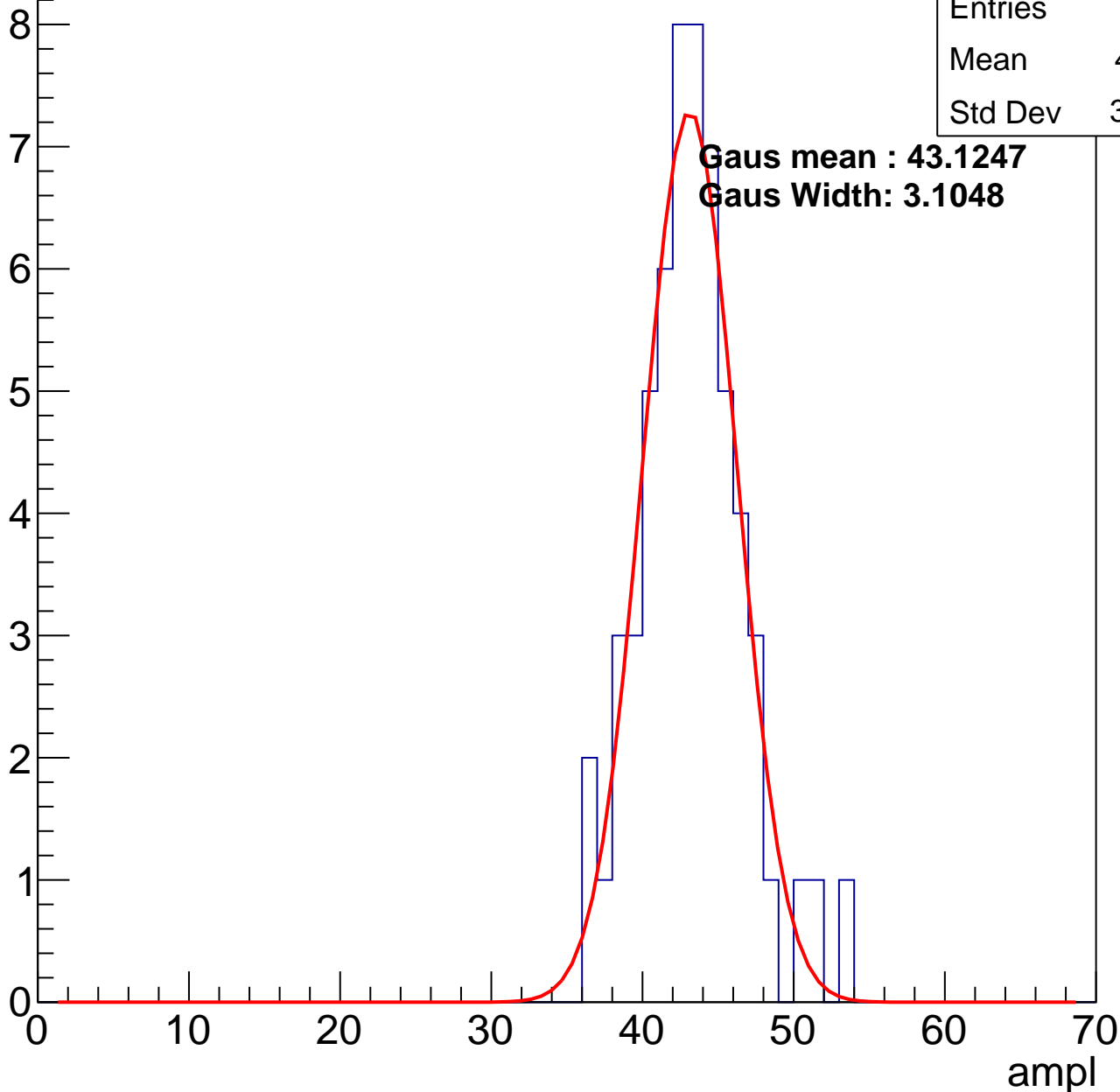
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.81
Std Dev	3.397

**Gaus mean : 43.1247**

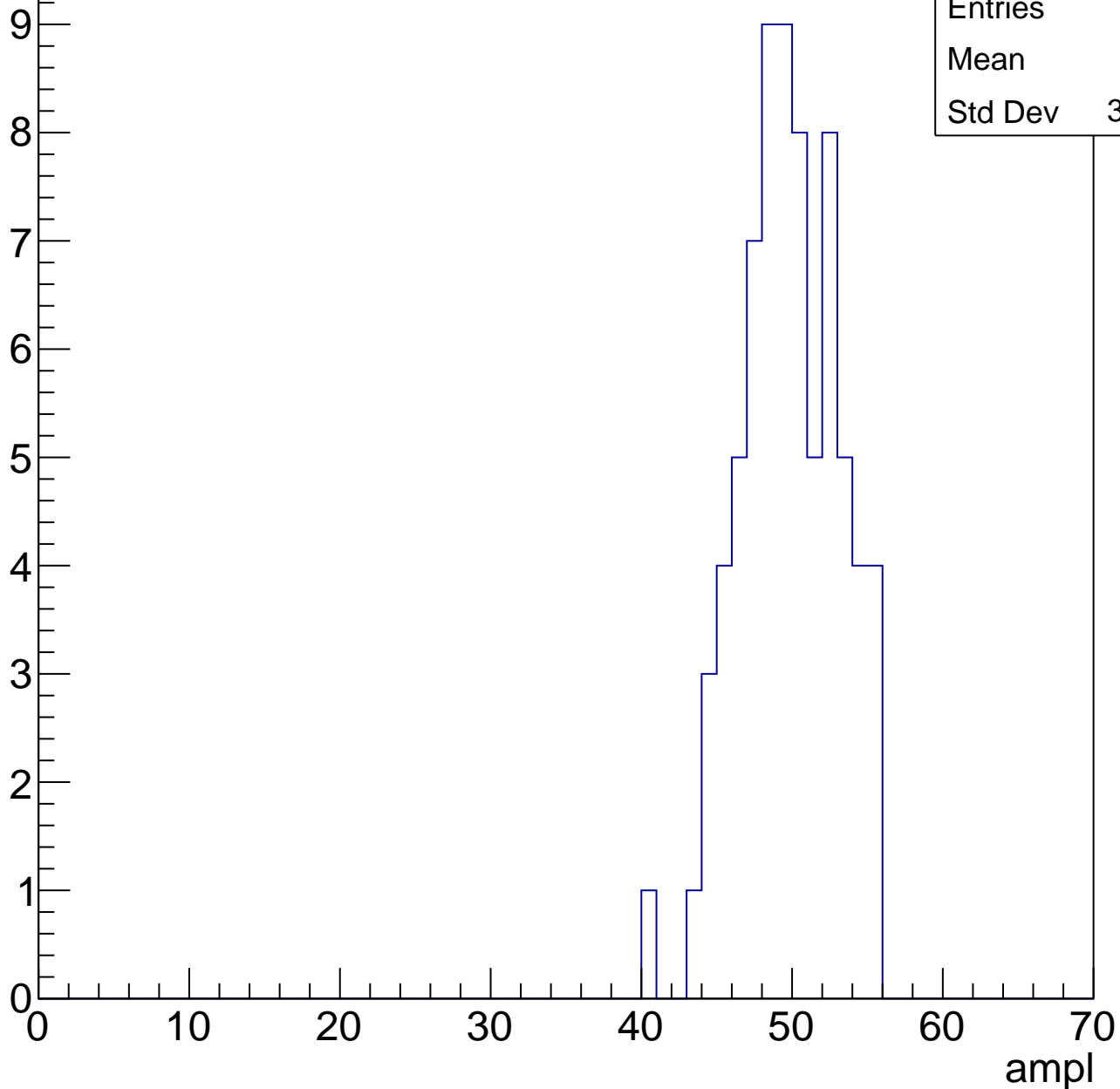
**Gaus Width: 3.1048**



# B1L000S, U24-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

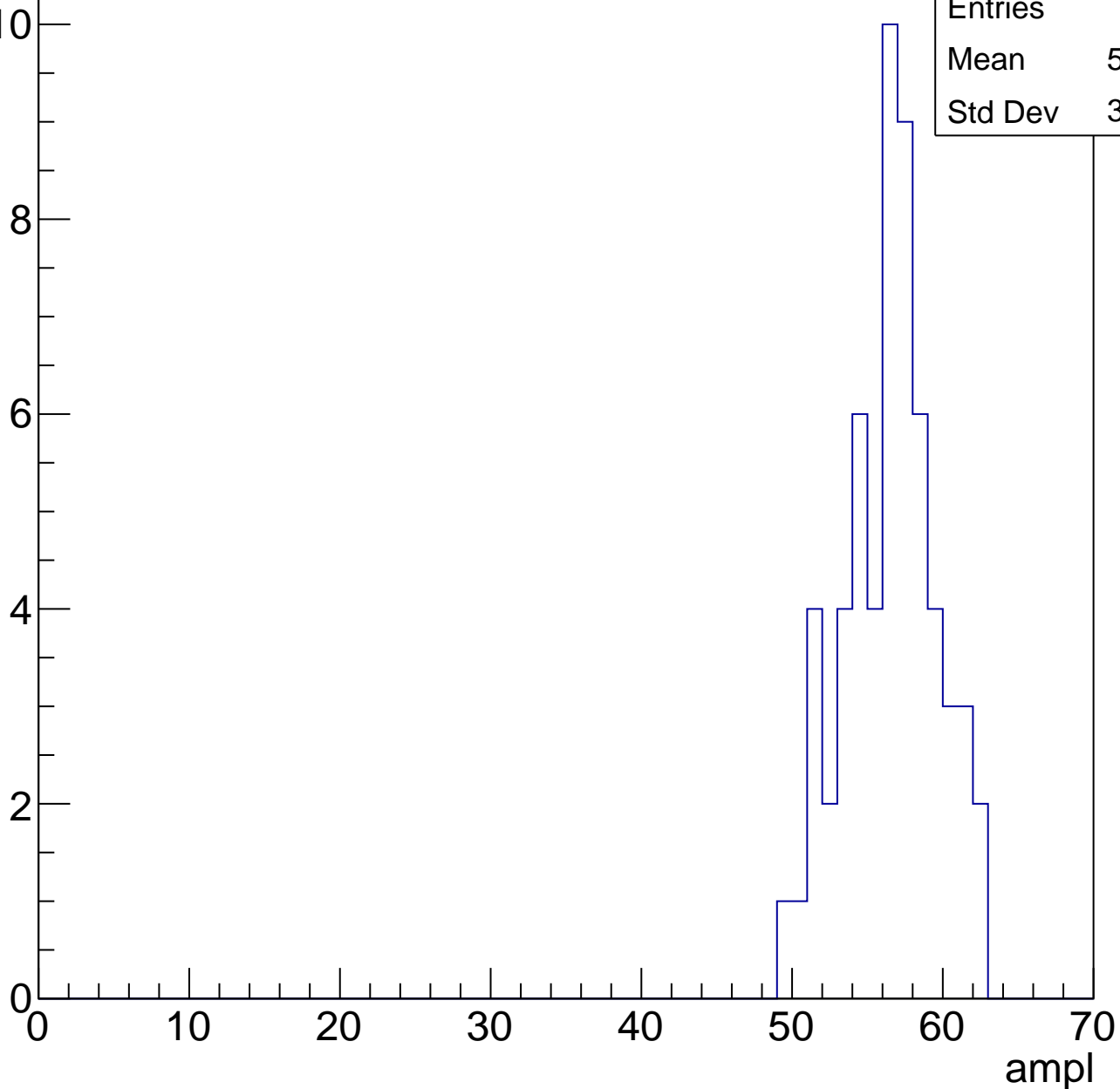


# B1L000S, U24-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	56.05
Std Dev	3.039



# B1L000S, U24-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	60.39
Std Dev	2.082

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

# B1L000S, U24-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B1L000S, U24-ch76, adc0

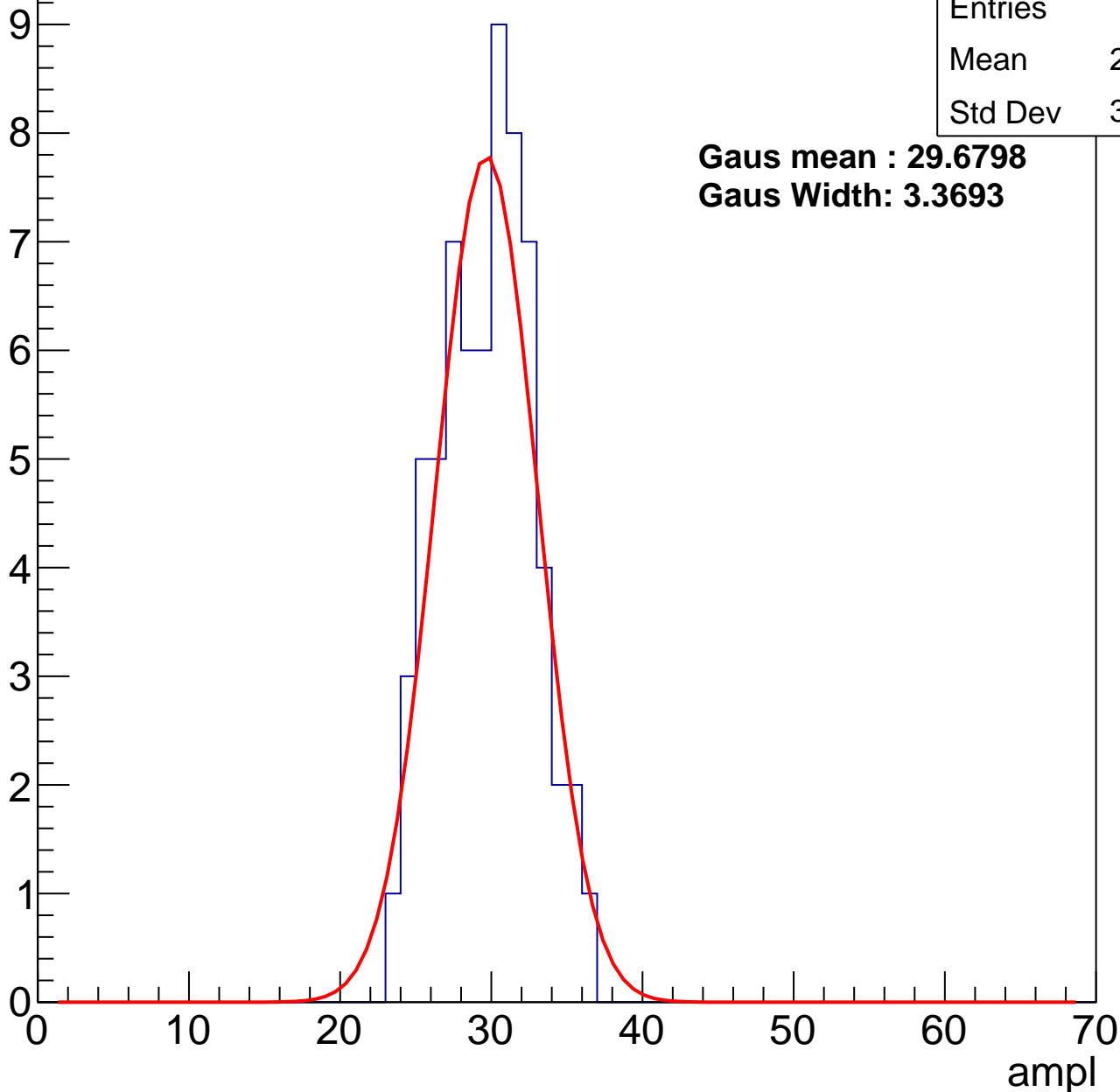
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.23
Std Dev	3.029

**Gaus mean : 29.6798**

**Gaus Width: 3.3693**



# B1L000S, U24-ch76, adc1

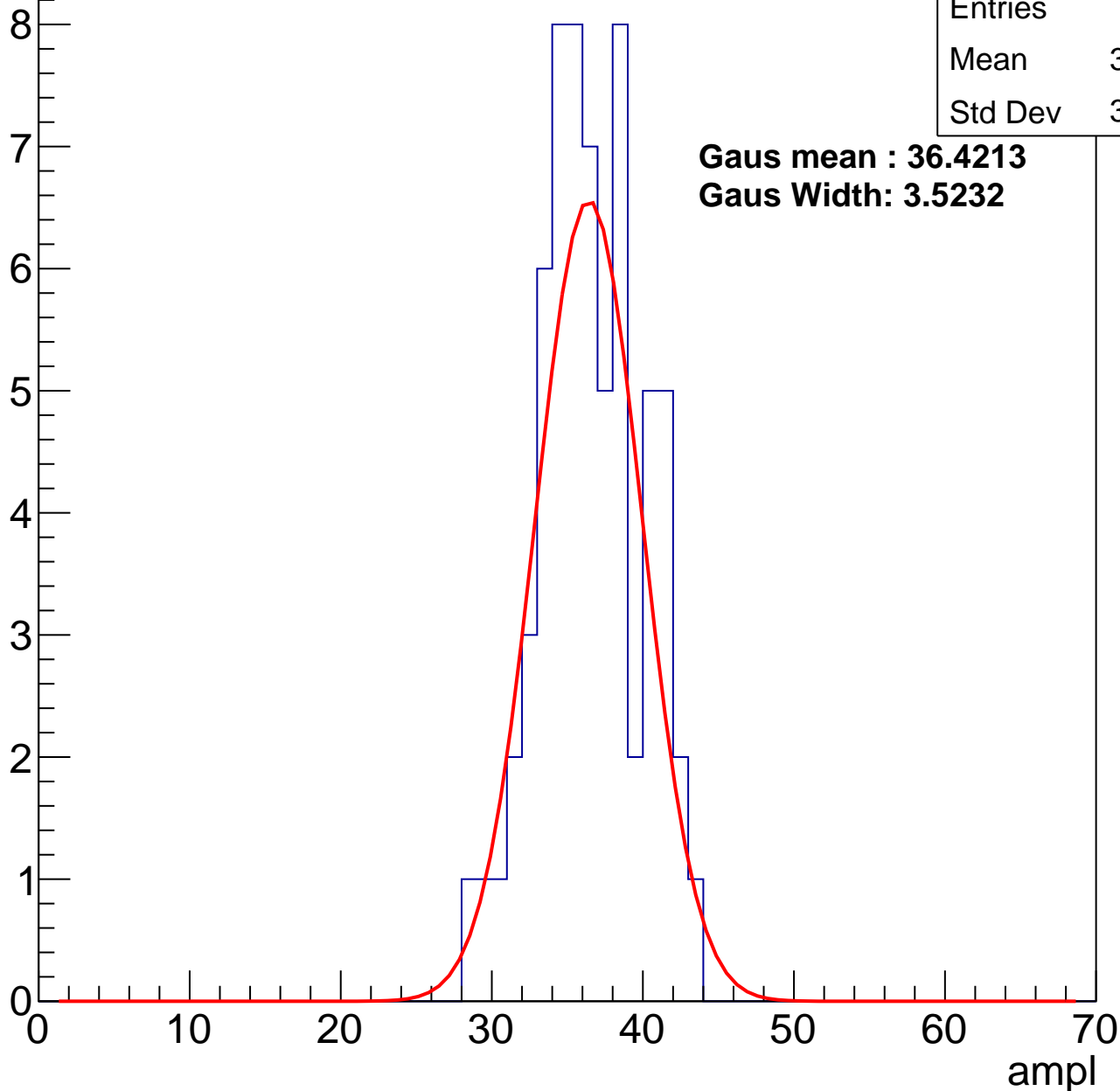
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	36.09
Std Dev	3.332

**Gaus mean : 36.4213**

**Gaus Width: 3.5232**



# B1L000S, U24-ch76, adc2

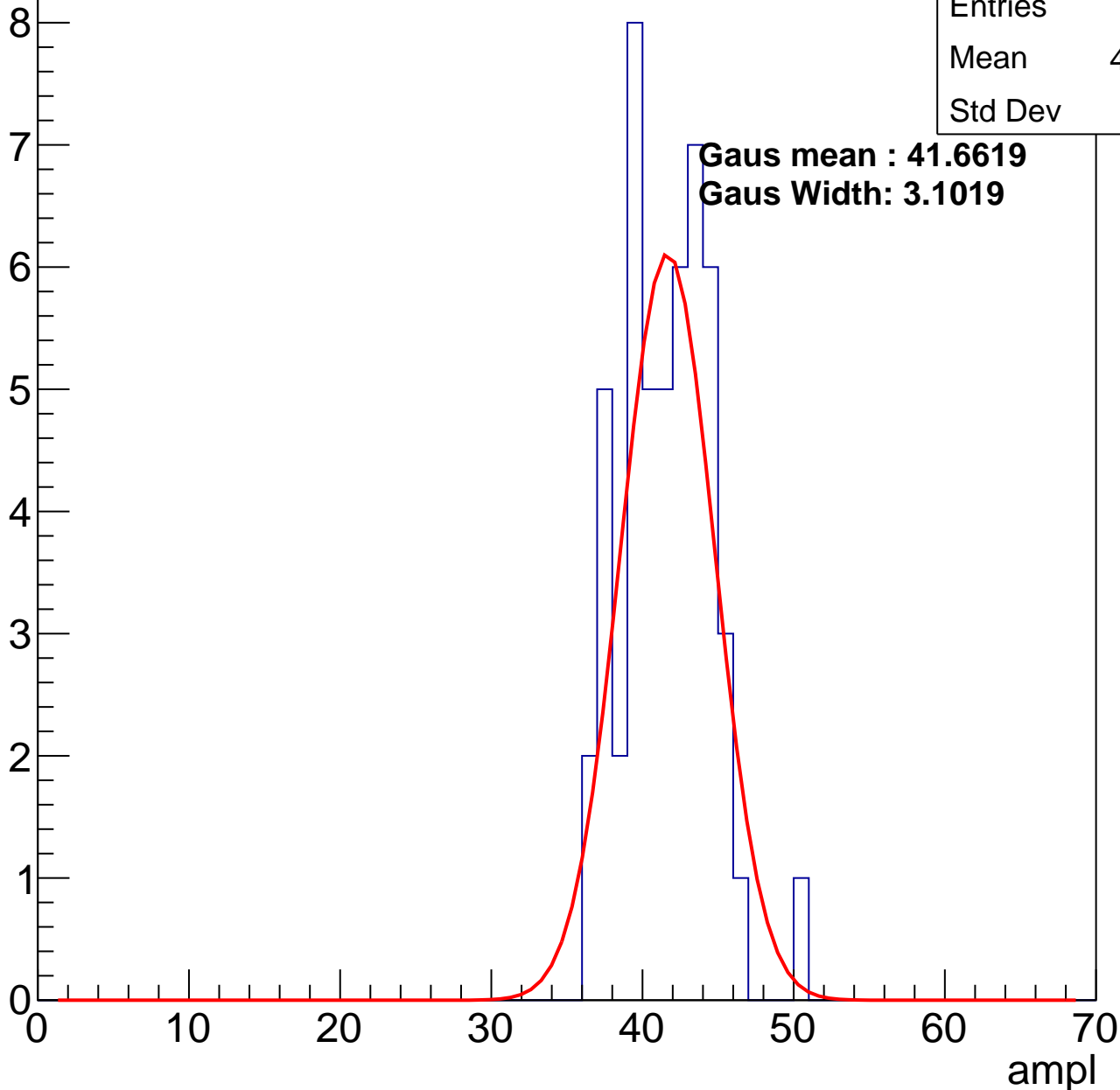
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	41.14
Std Dev	2.89

**Gaus mean : 41.6619**

**Gaus Width: 3.1019**

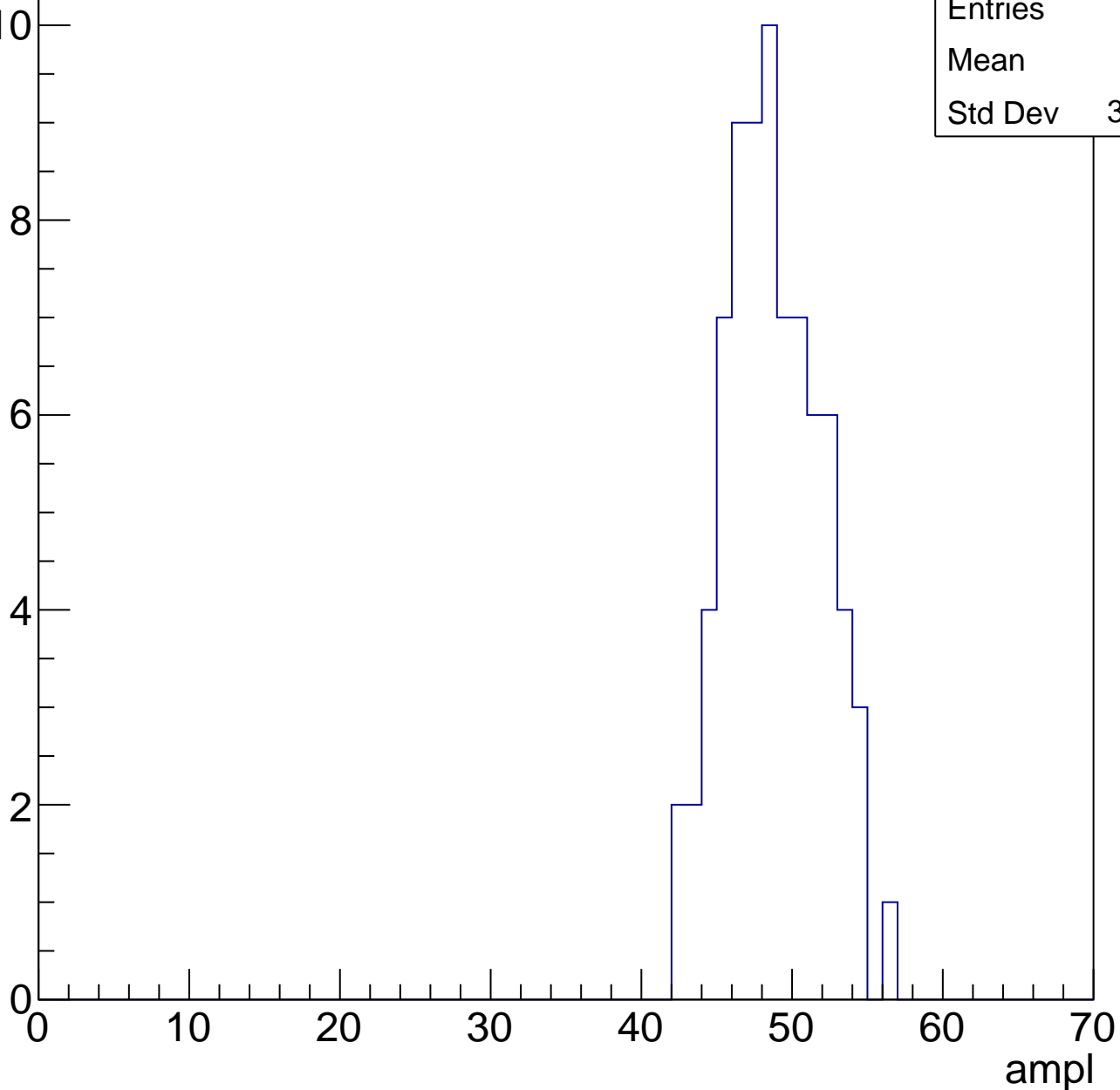


# B1L000S, U24-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	48.3
Std Dev	3.109

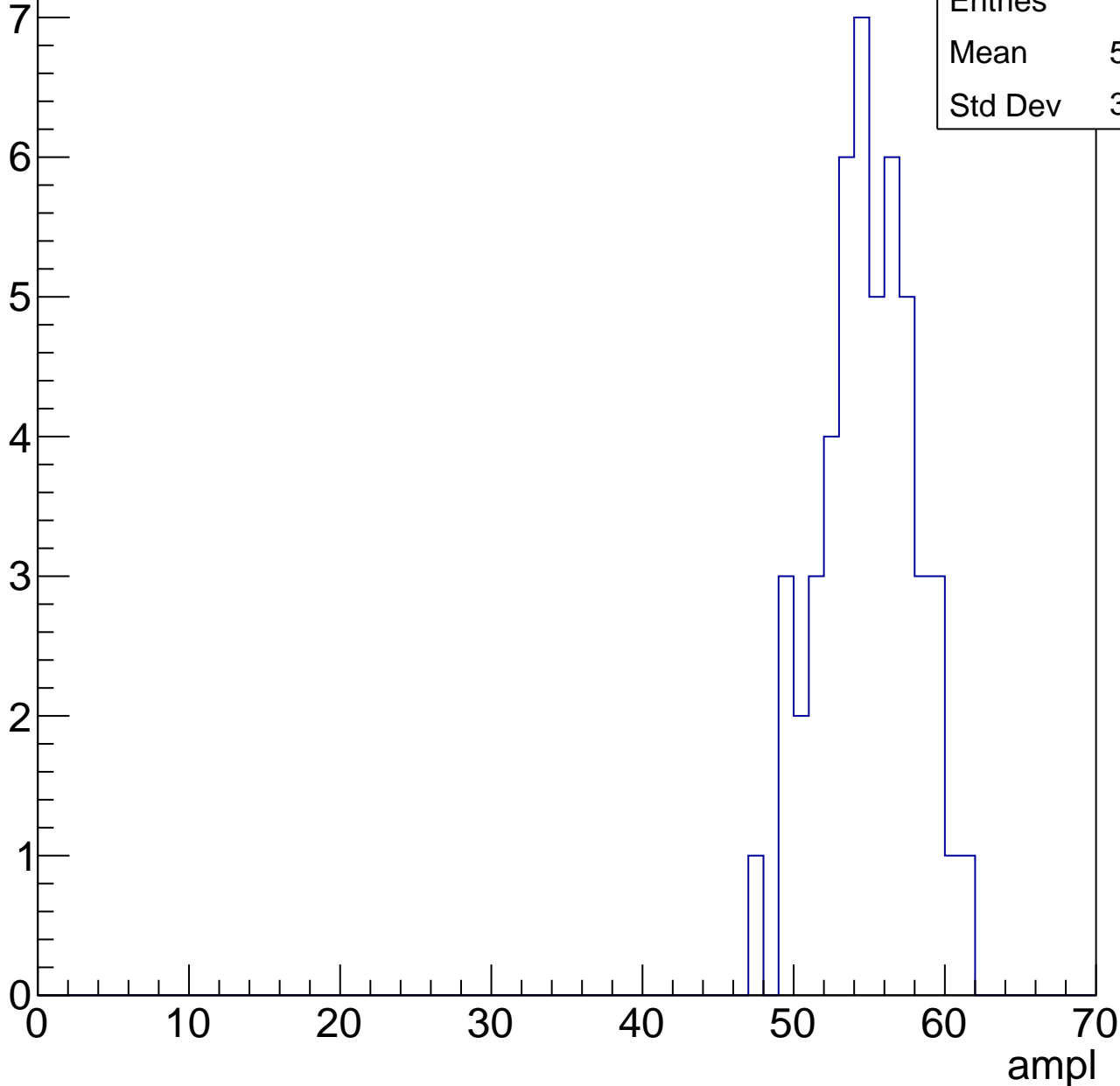


# B1L000S, U24-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

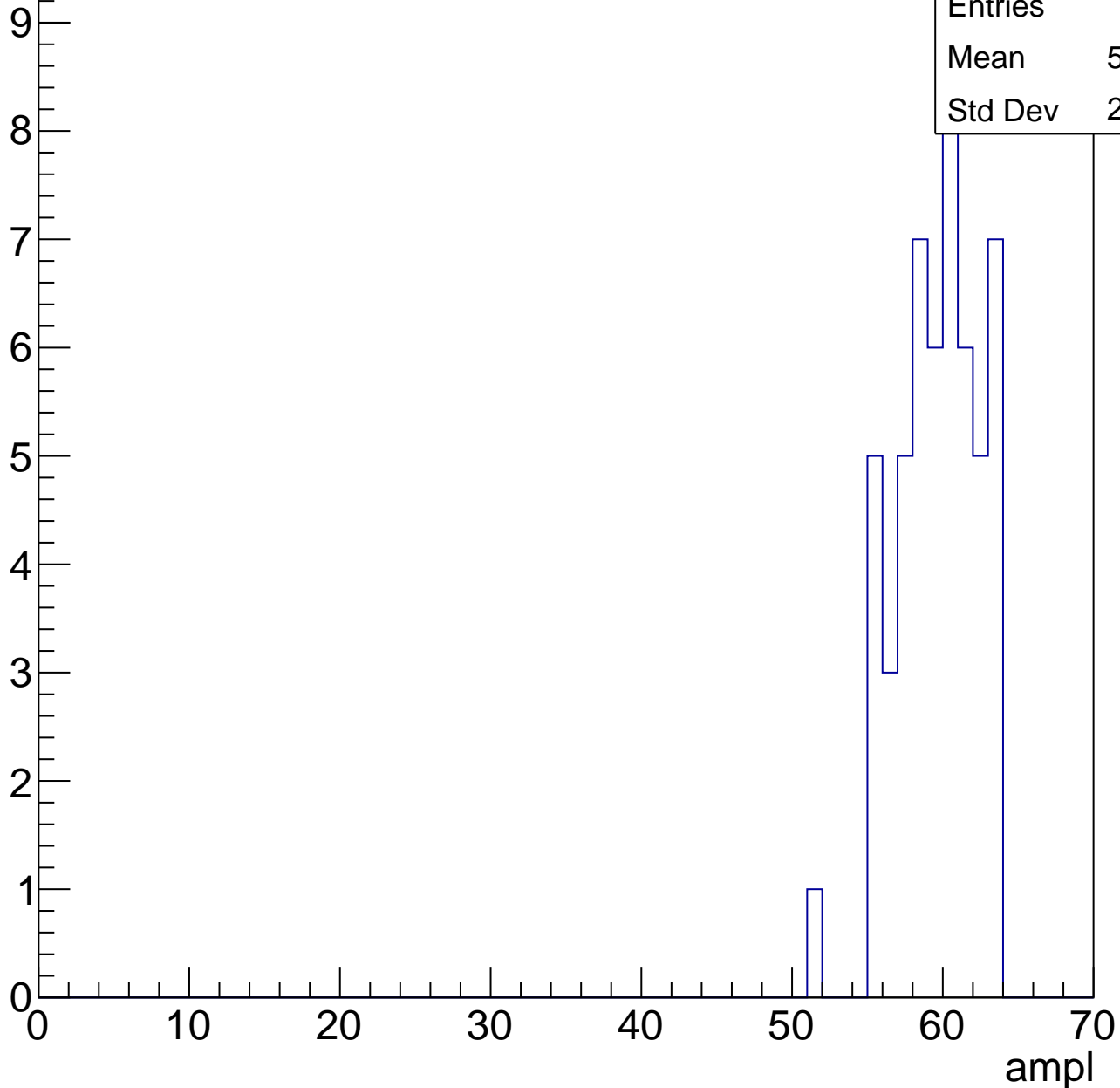
Entries	50
Mean	54.38
Std Dev	3.098



# B1L000S, U24-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

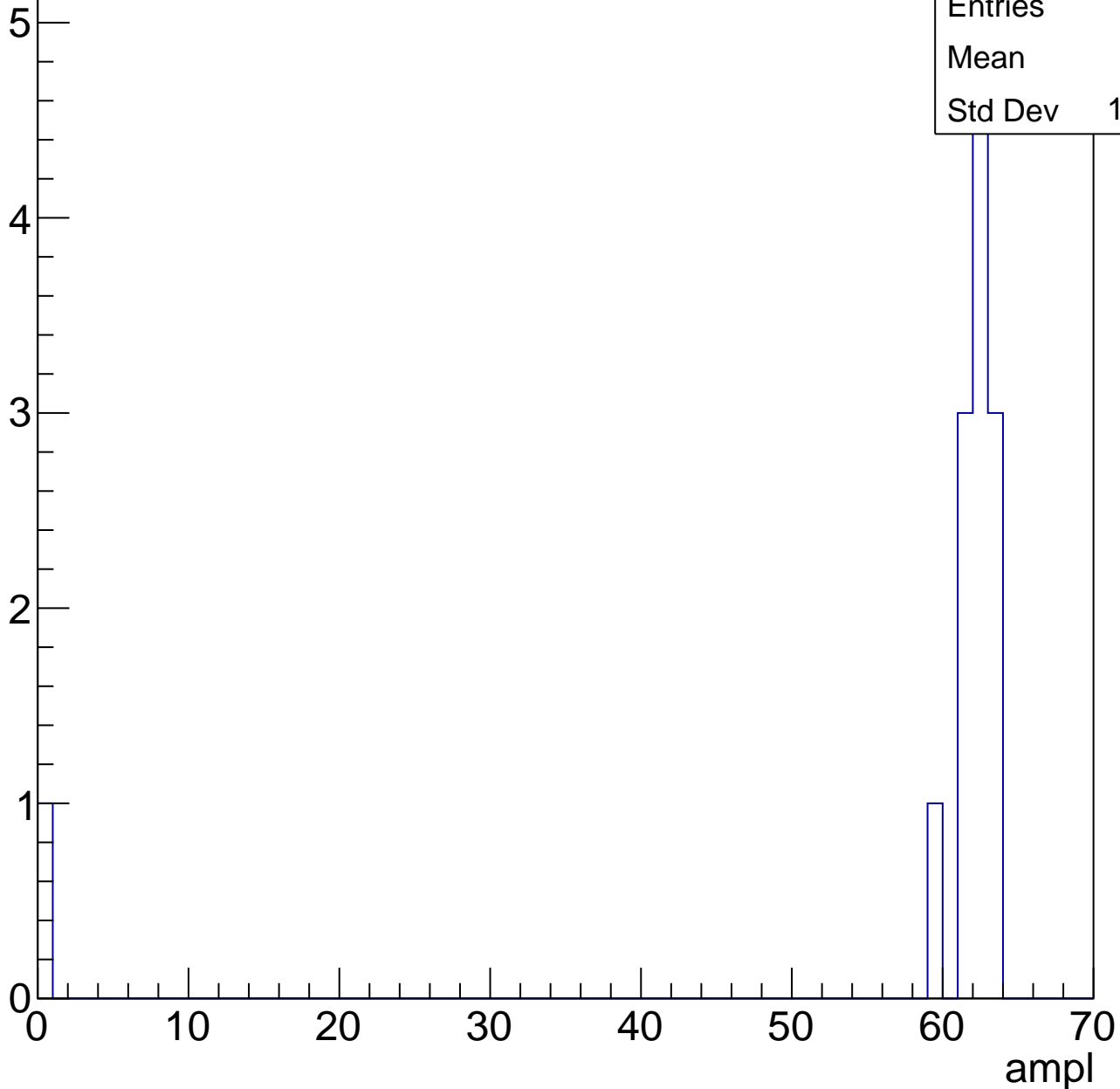


# B1L000S, U24-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57
Std Dev	16.49





# B1L000S, U24-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	27.94
Std Dev	3.208

**Gaus mean : 28.7359**

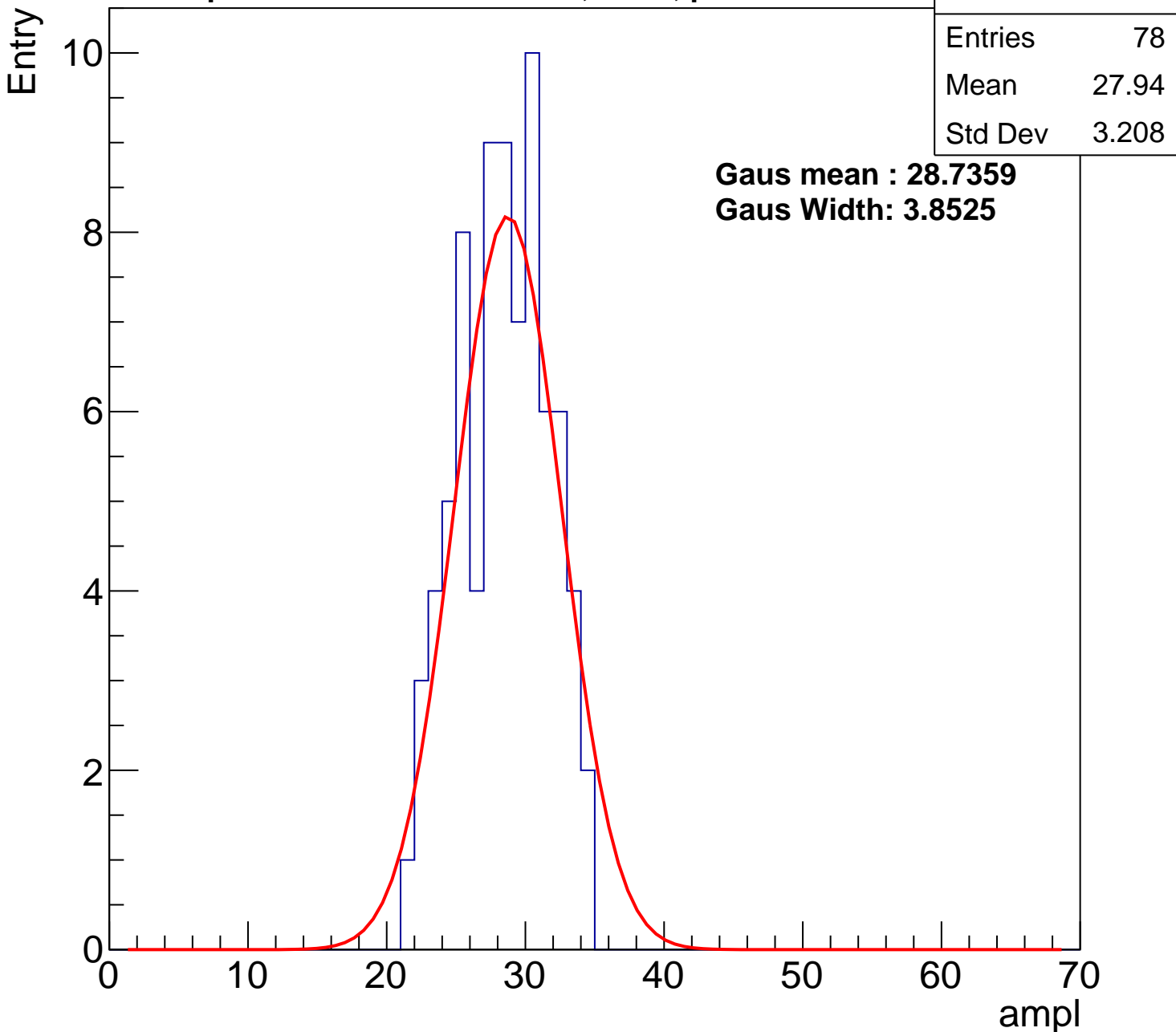
**Gaus Width: 3.8525**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch77, adc1

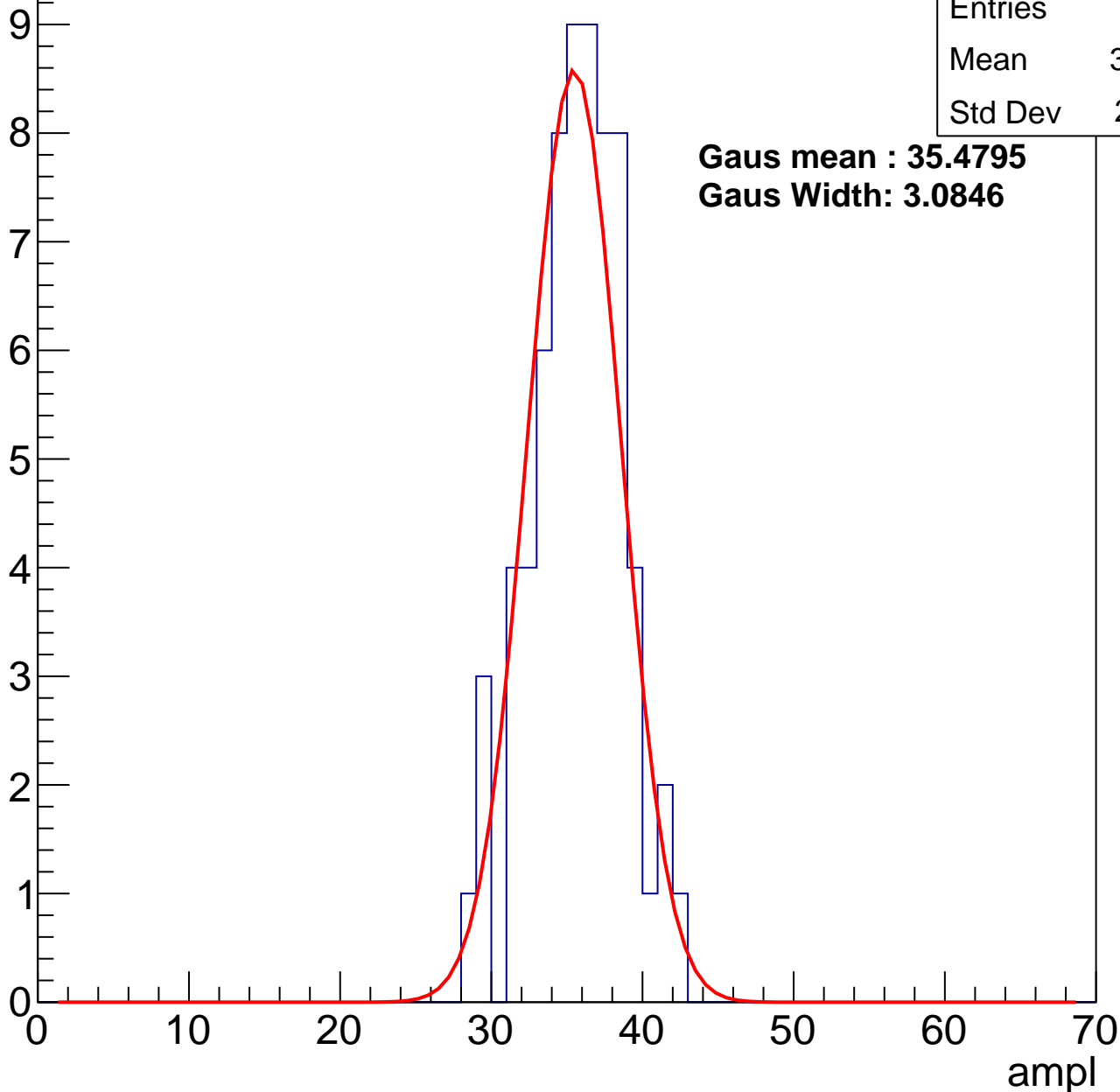
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.24
Std Dev	2.991

**Gaus mean : 35.4795**

**Gaus Width: 3.0846**



# B1L000S, U24-ch77, adc2

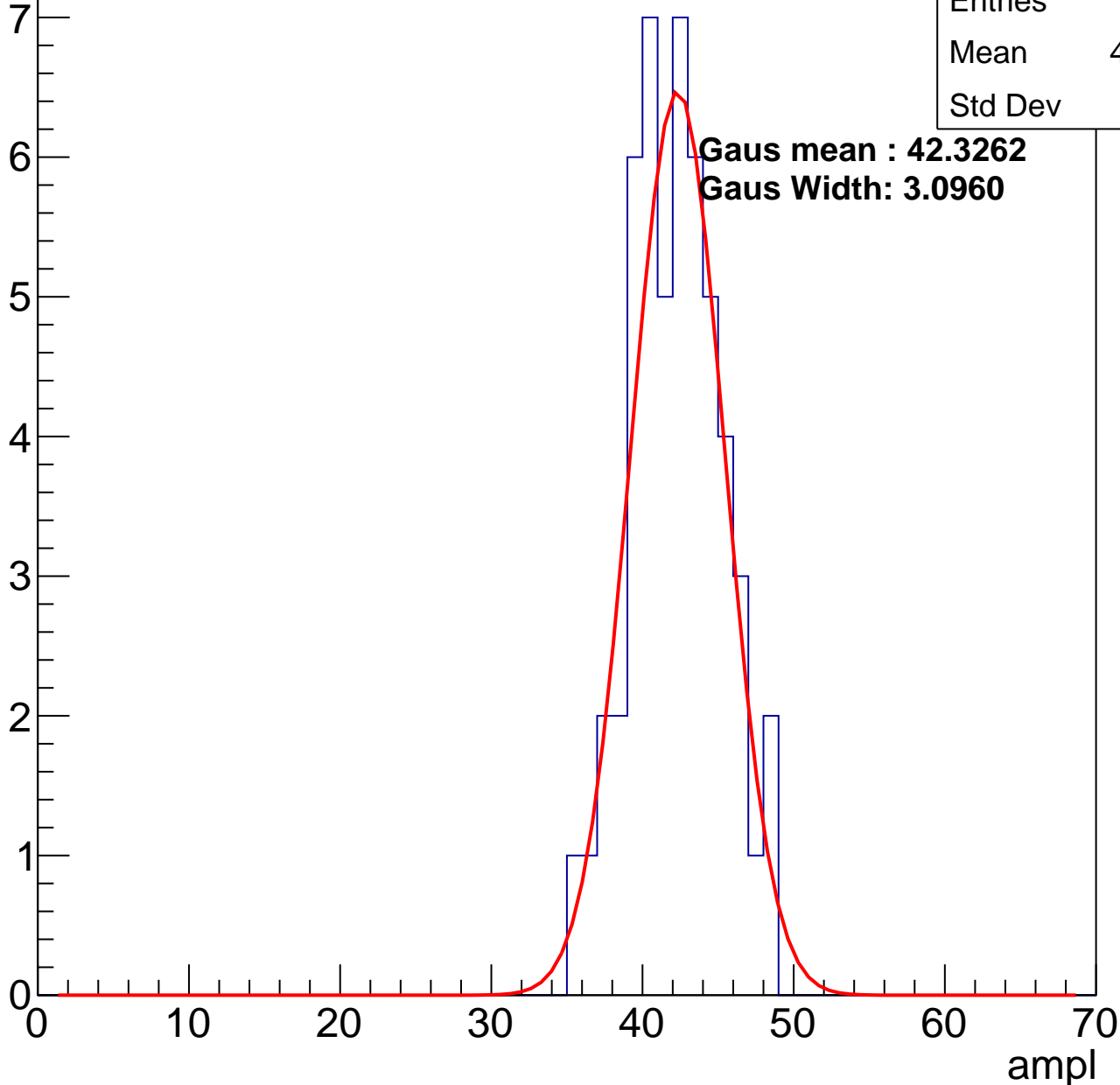
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.79
Std Dev	2.97

**Gaus mean : 42.3262**

**Gaus Width: 3.0960**



# B1L000S, U24-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

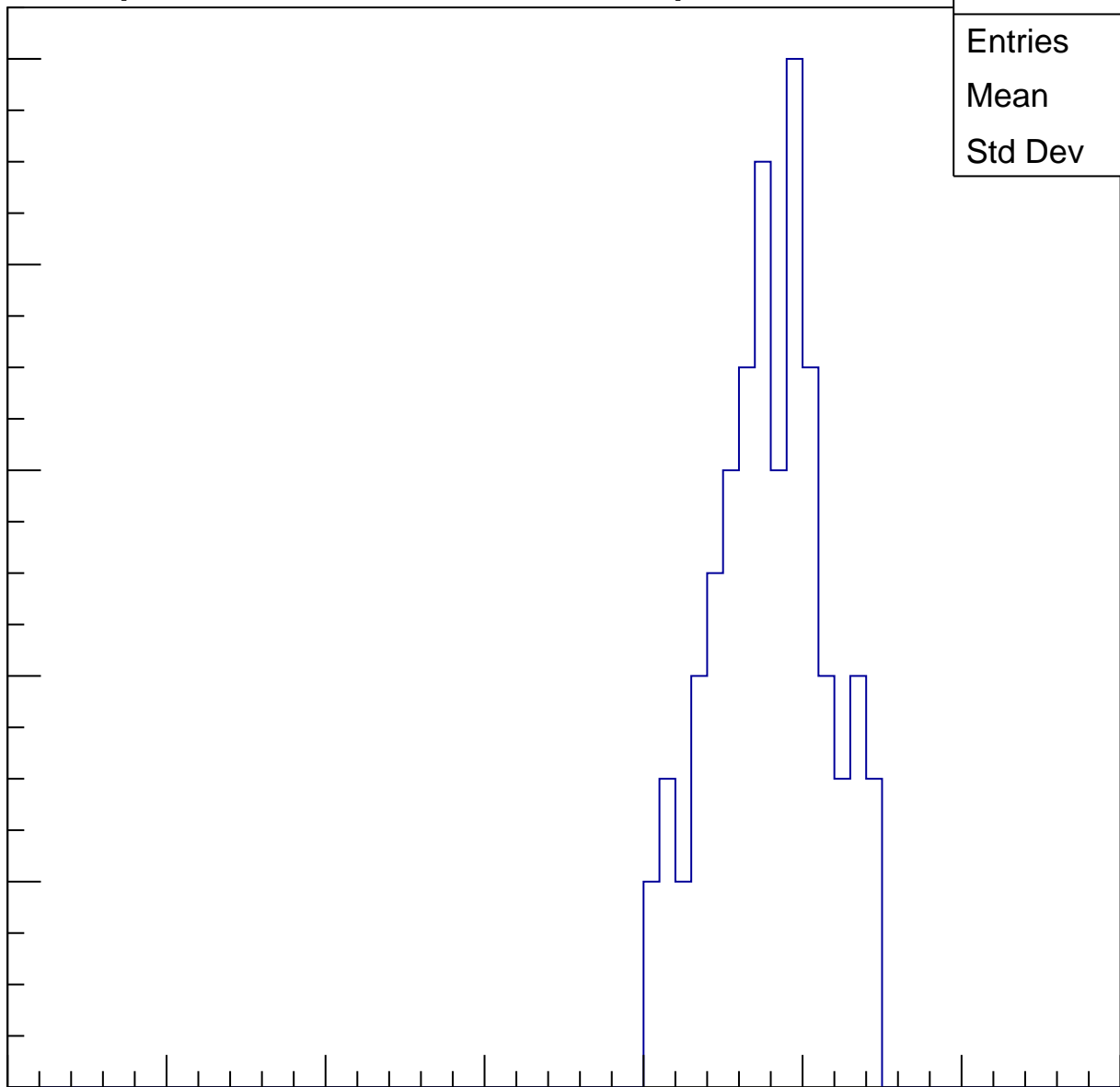
Entries	75
Mean	47.41
Std Dev	3.507

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

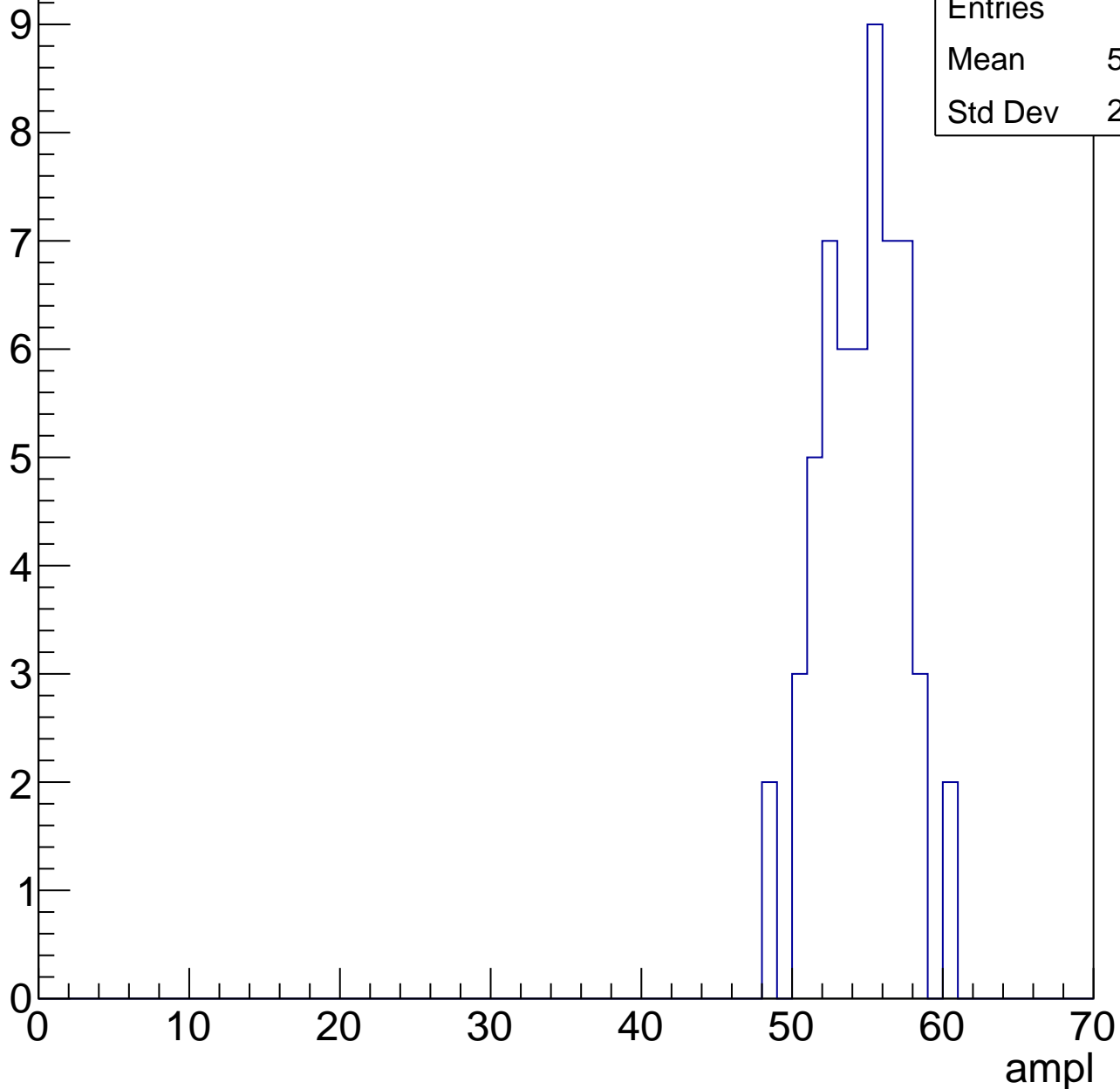
ampl



# B1L000S, U24-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

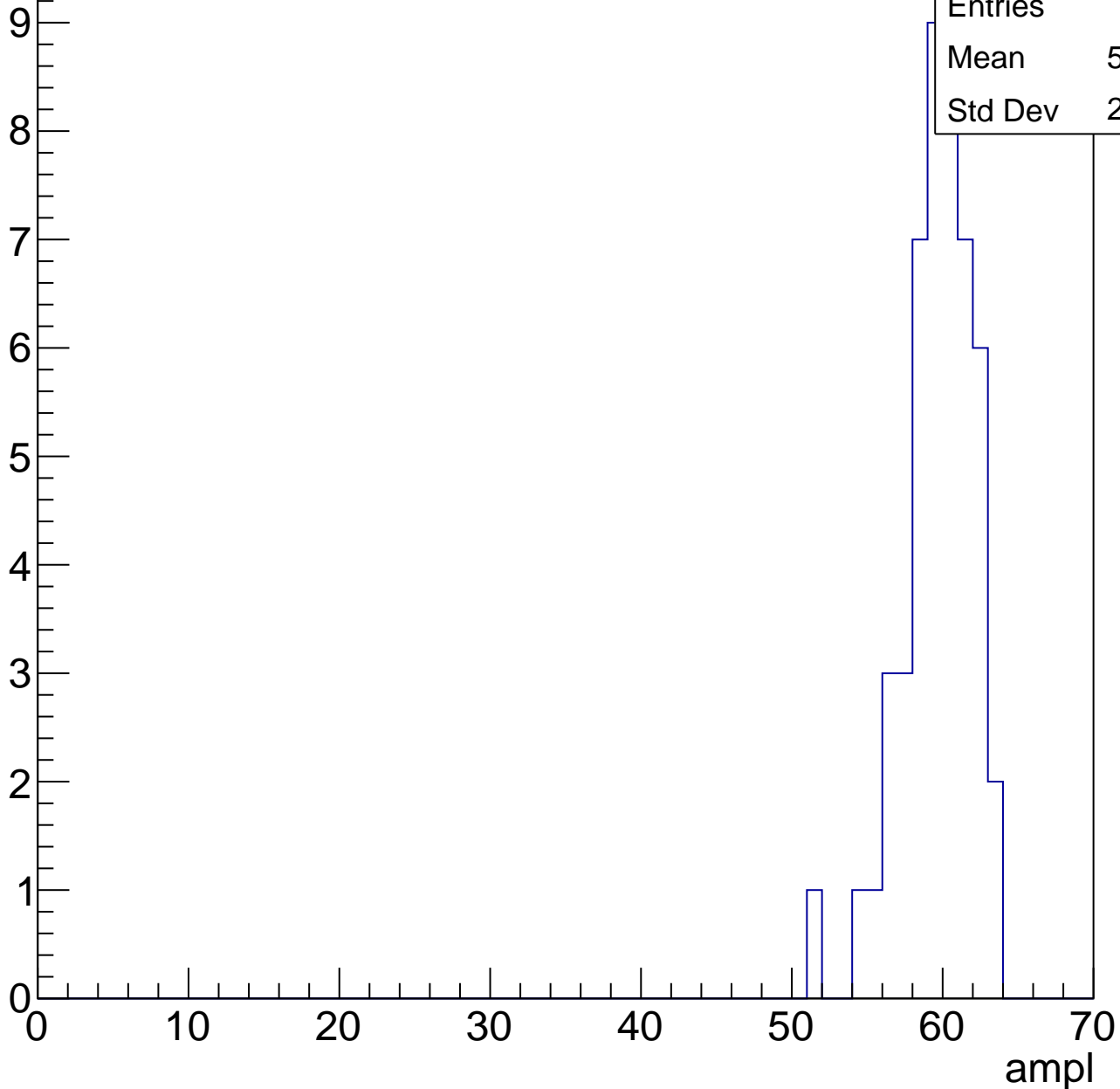


# B1L000S, U24-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	59.19
Std Dev	2.377

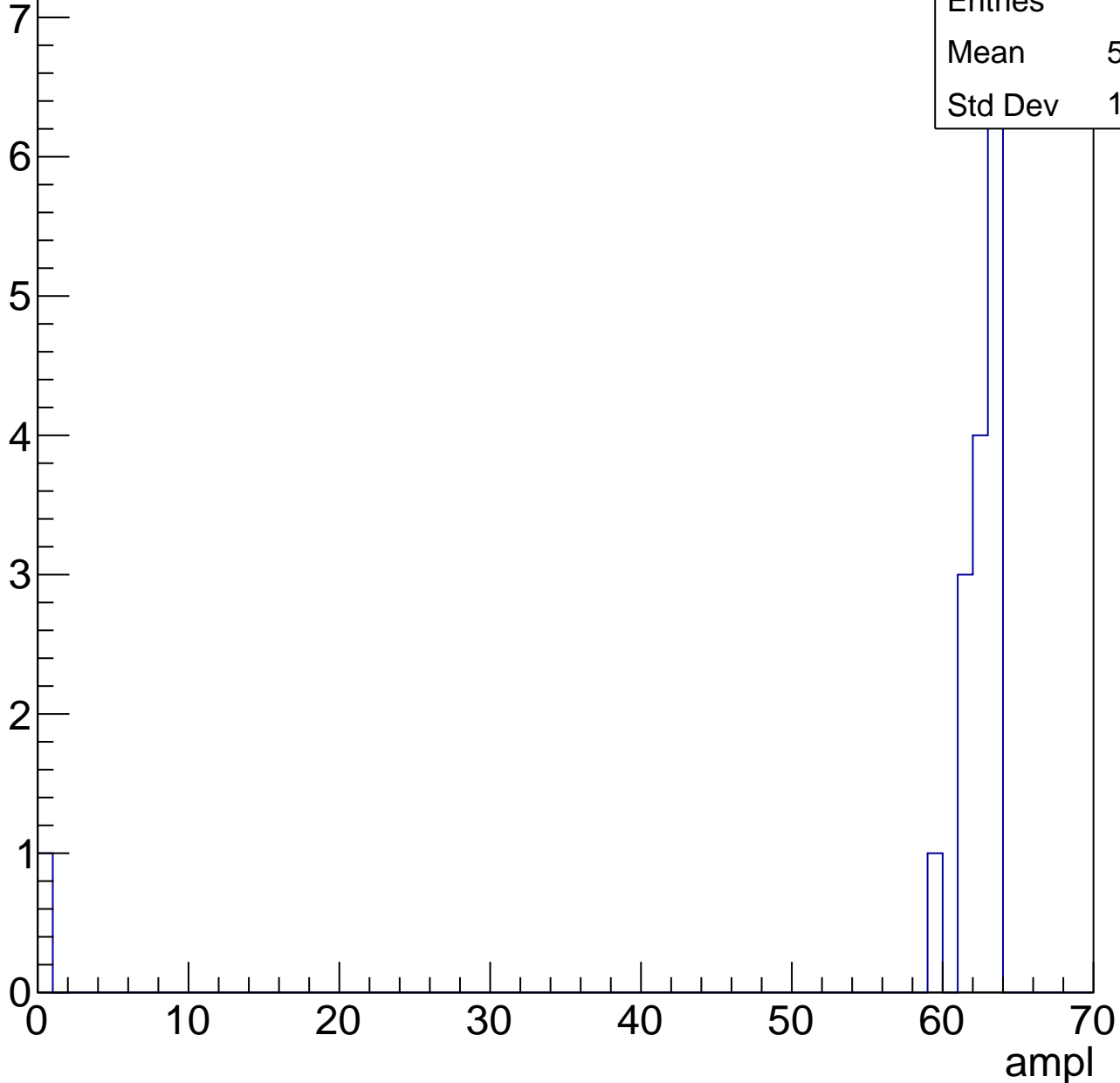


# B1L000S, U24-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	58.19
Std Dev	15.06





# B1L000S, U24-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch78, adc0

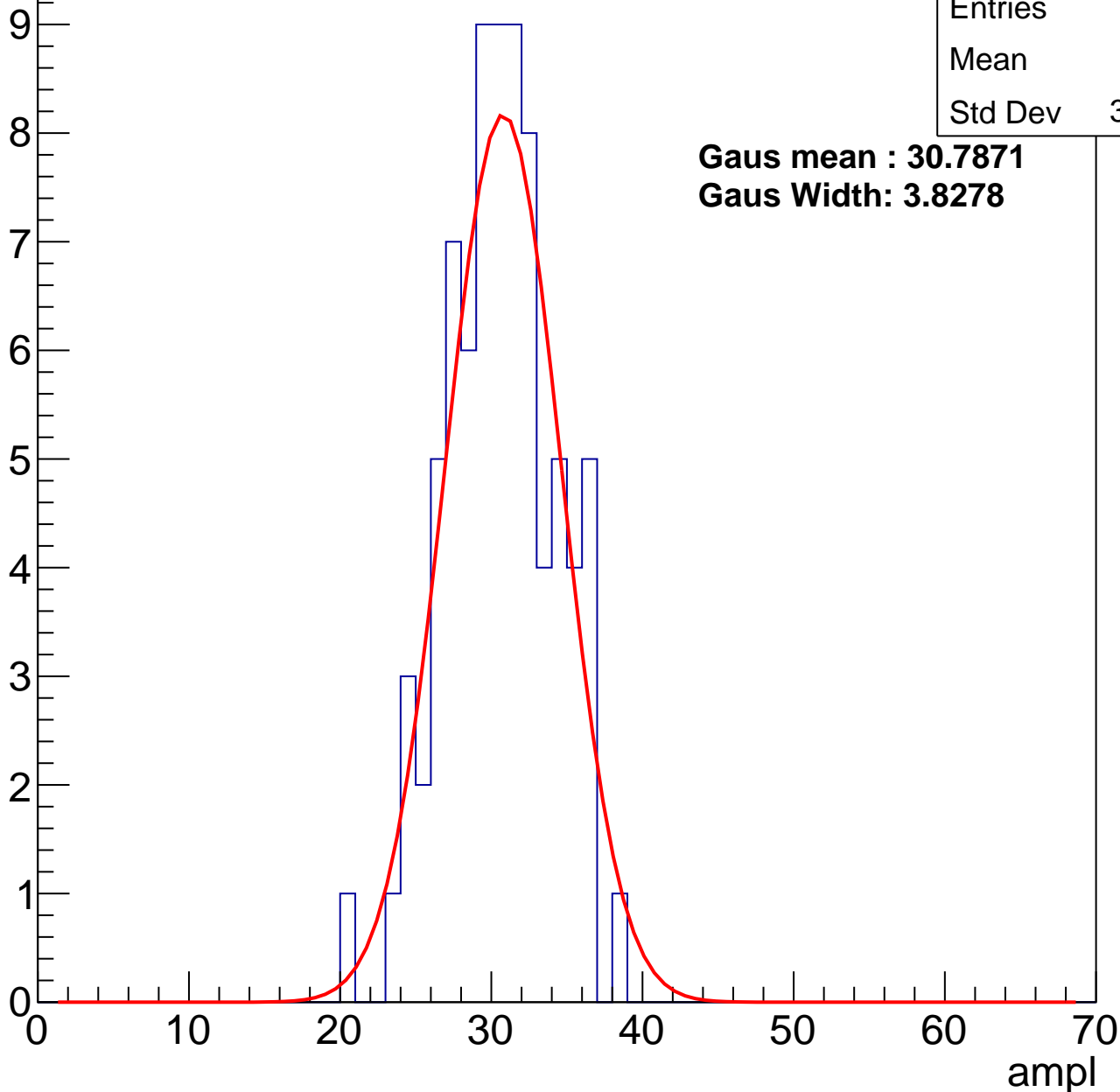
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	30.1
Std Dev	3.528

**Gaus mean : 30.7871**

**Gaus Width: 3.8278**



# B1L000S, U24-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	50
Mean	36.22
Std Dev	2.823

**Gaus mean : 36.4059**

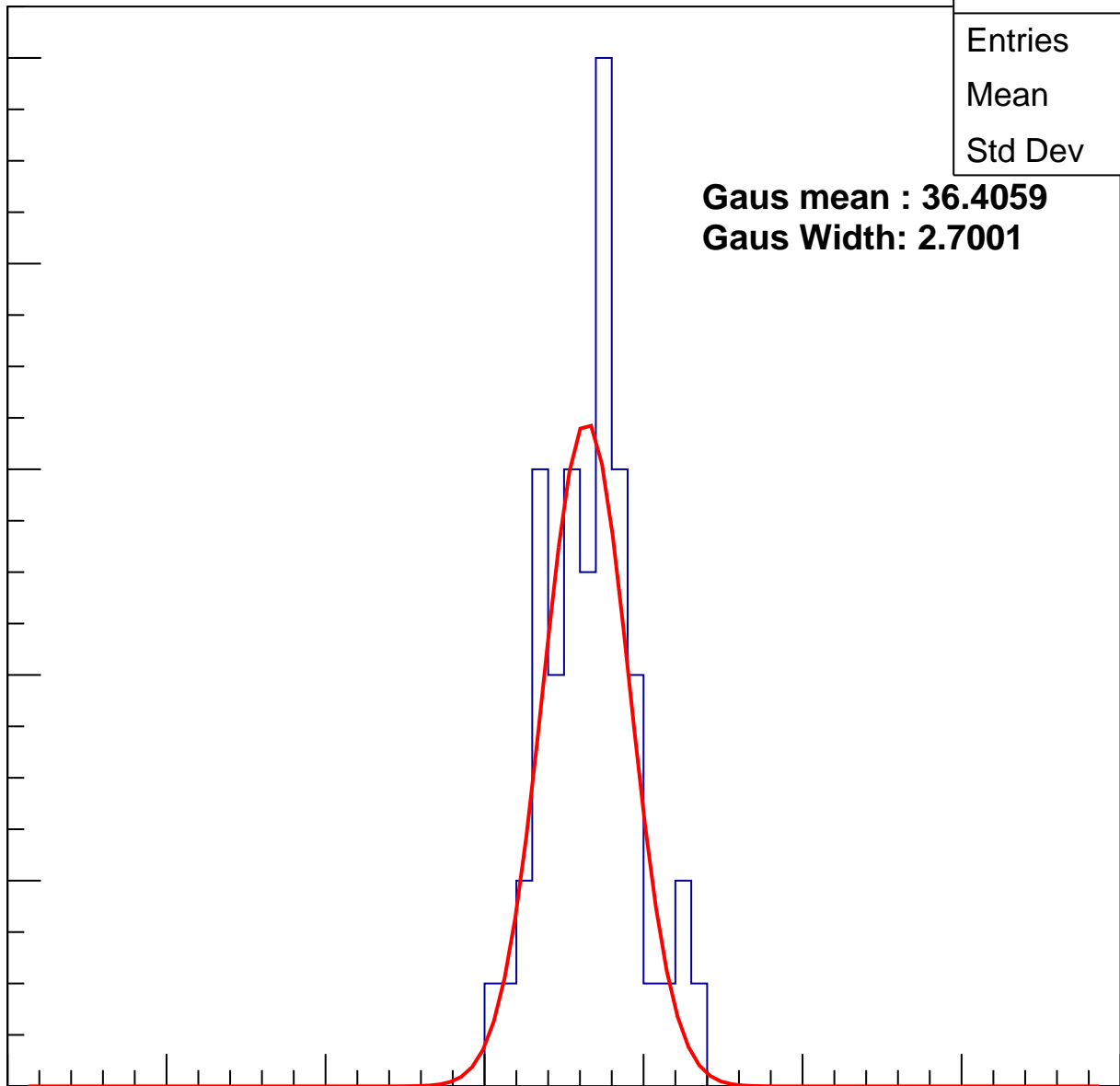
**Gaus Width: 2.7001**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch78, adc2

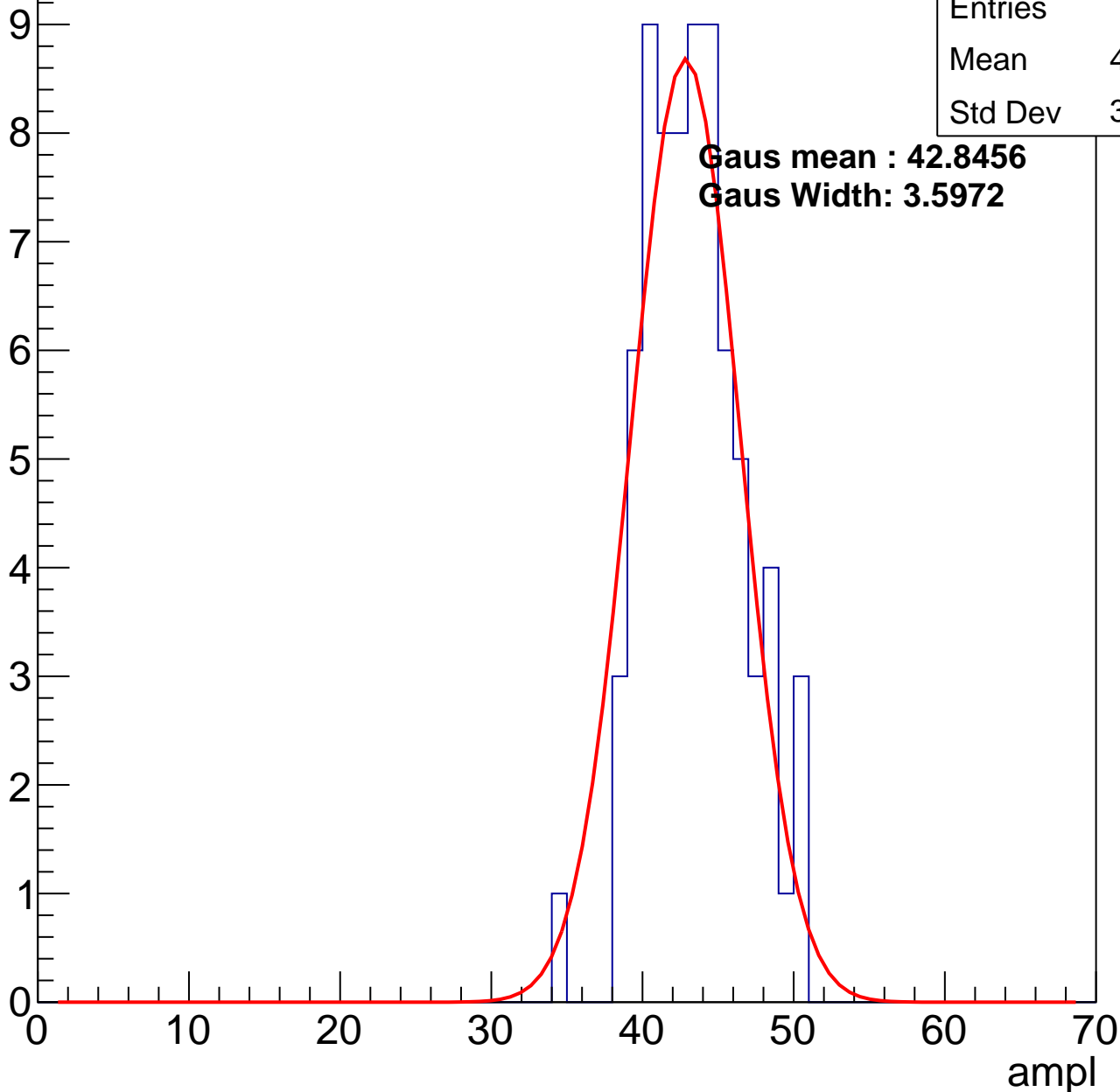
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.95
Std Dev	3.229

**Gaus mean : 42.8456**

**Gaus Width: 3.5972**

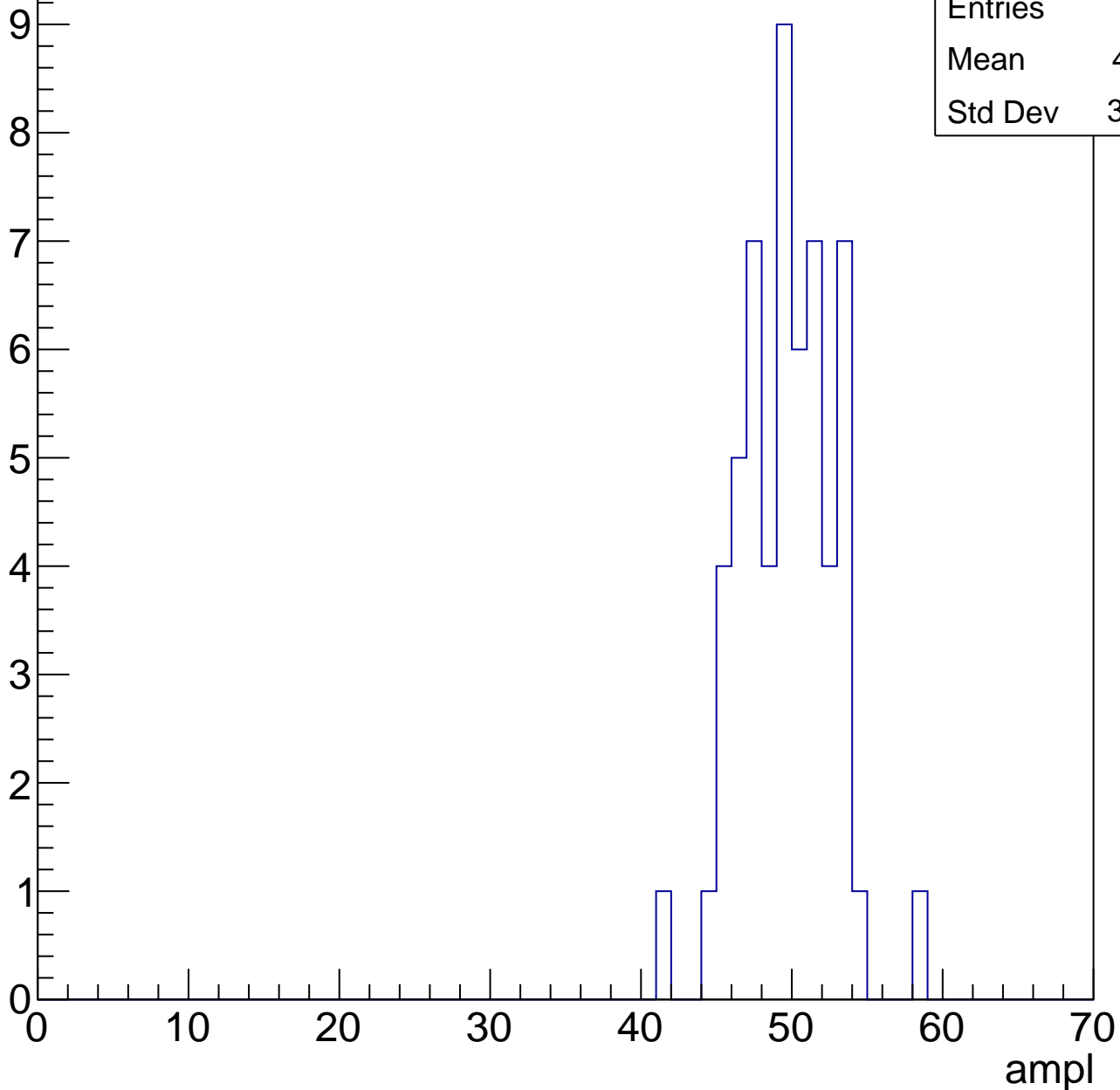


# B1L000S, U24-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

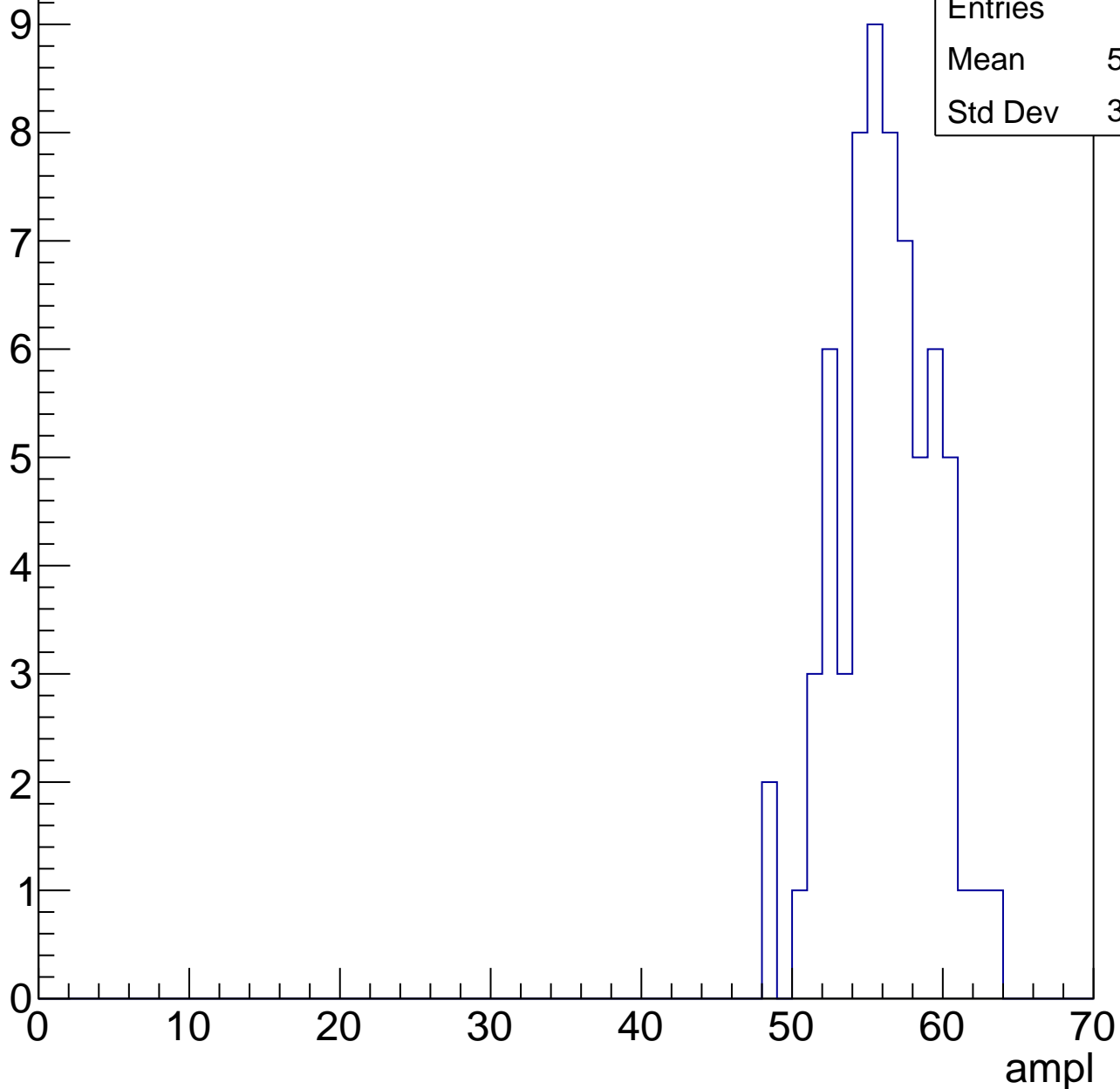
Entries	57
Mean	49.21
Std Dev	3.007



# B1L000S, U24-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



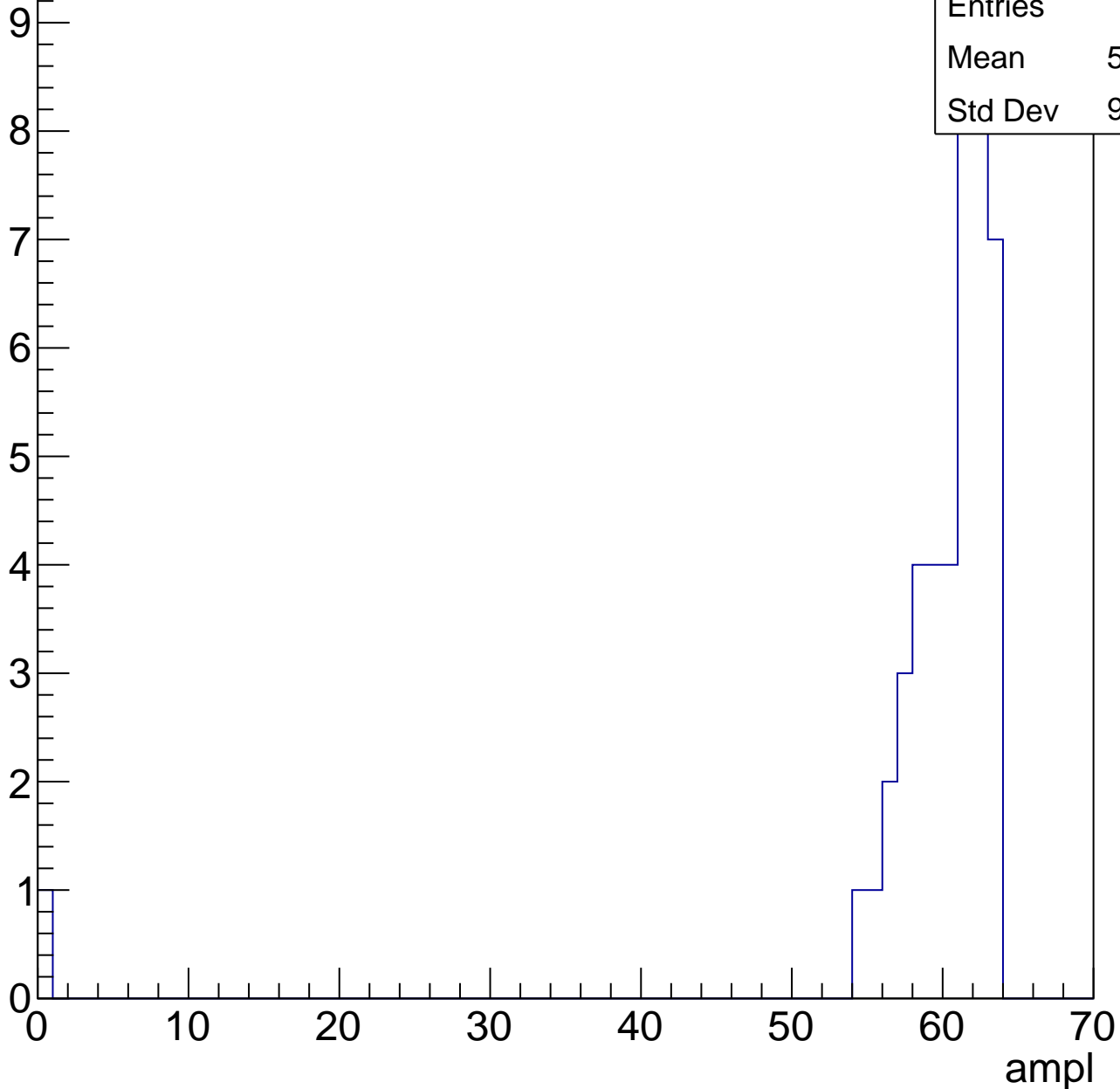
Entries	66
Mean	55.67
Std Dev	3.178

# B1L000S, U24-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	58.84
Std Dev	9.175



# B1L000S, U24-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch79, adc0

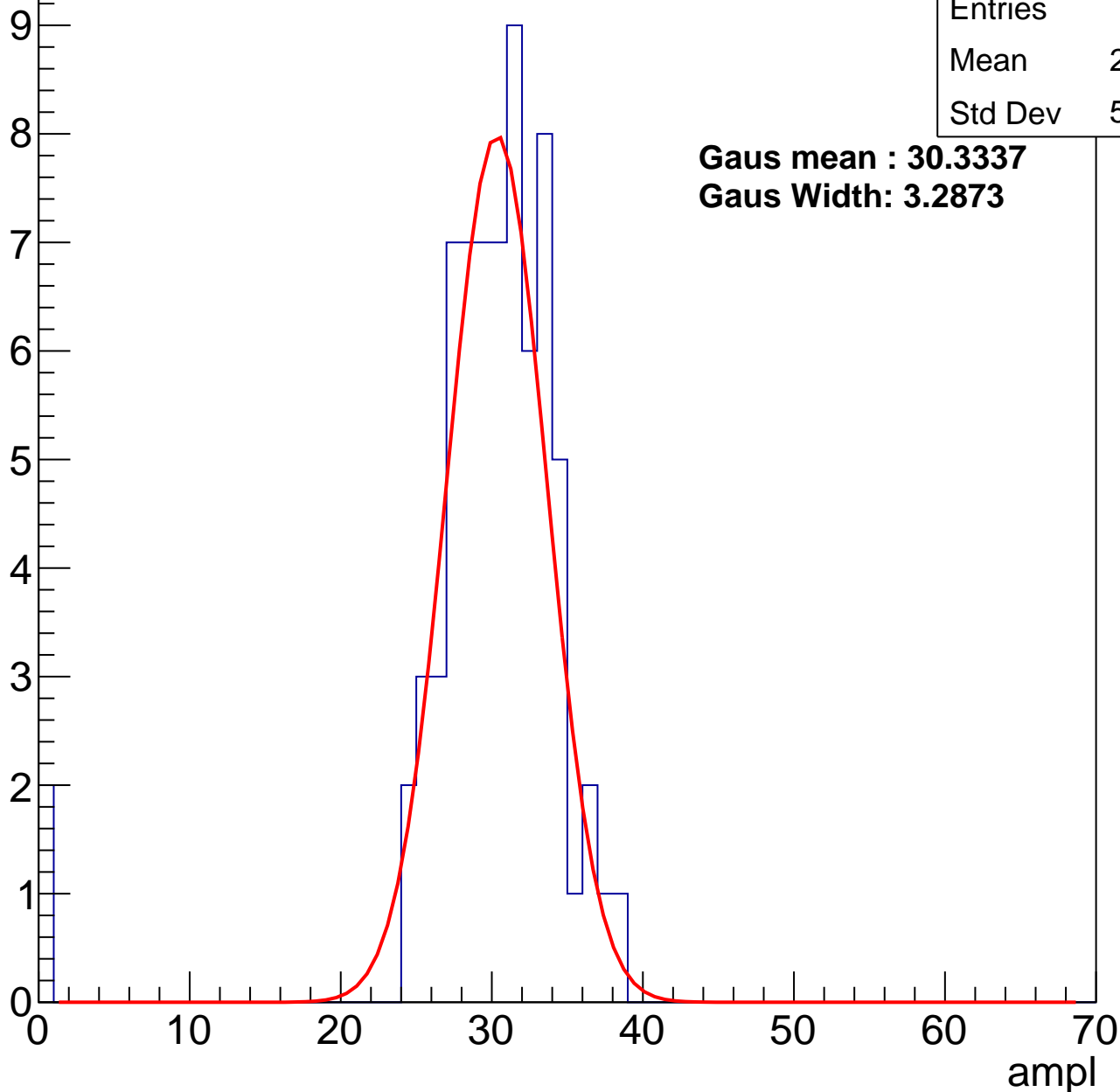
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.38
Std Dev	5.887

**Gaus mean : 30.3337**

**Gaus Width: 3.2873**



# B1L000S, U24-ch79, adc1

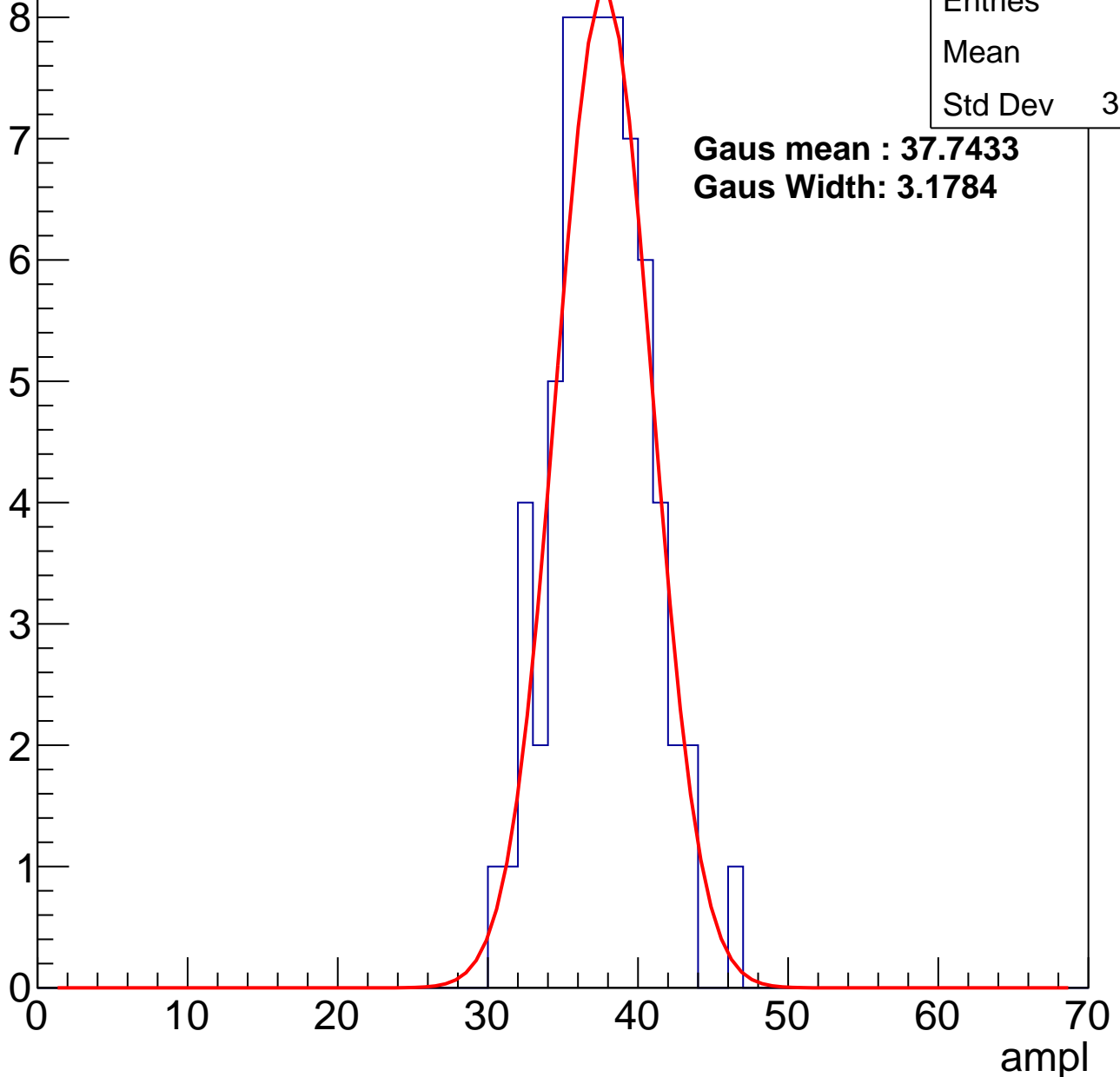
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	37.1
Std Dev	3.134

**Gaus mean : 37.7433**

**Gaus Width: 3.1784**



# B1L000S, U24-ch79, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	43.99
Std Dev	3.424

**Gaus mean : 44.4543**

**Gaus Width: 3.4481**

10

8

6

4

2

0

0

10

20

30

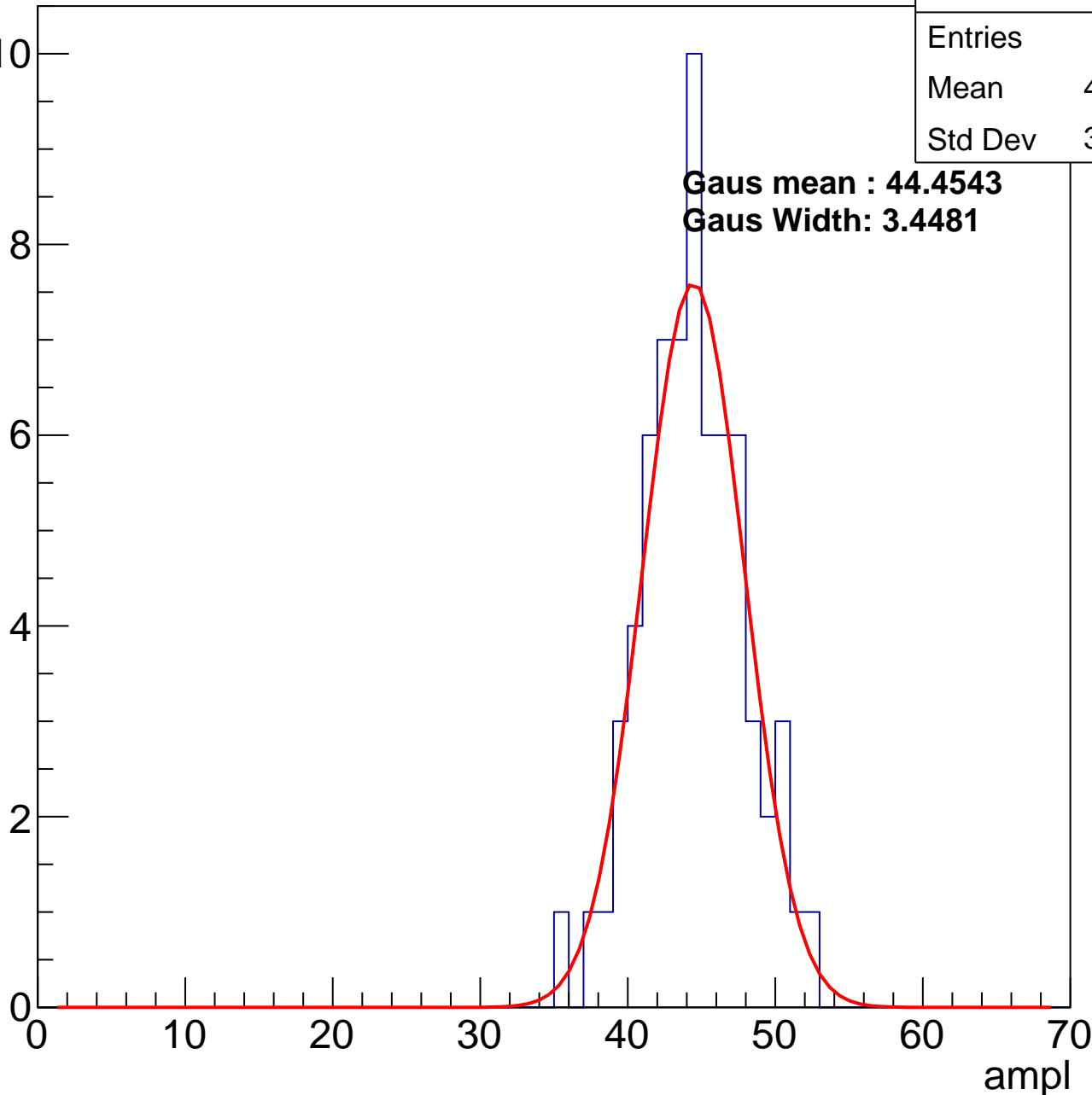
40

50

60

70

ampl

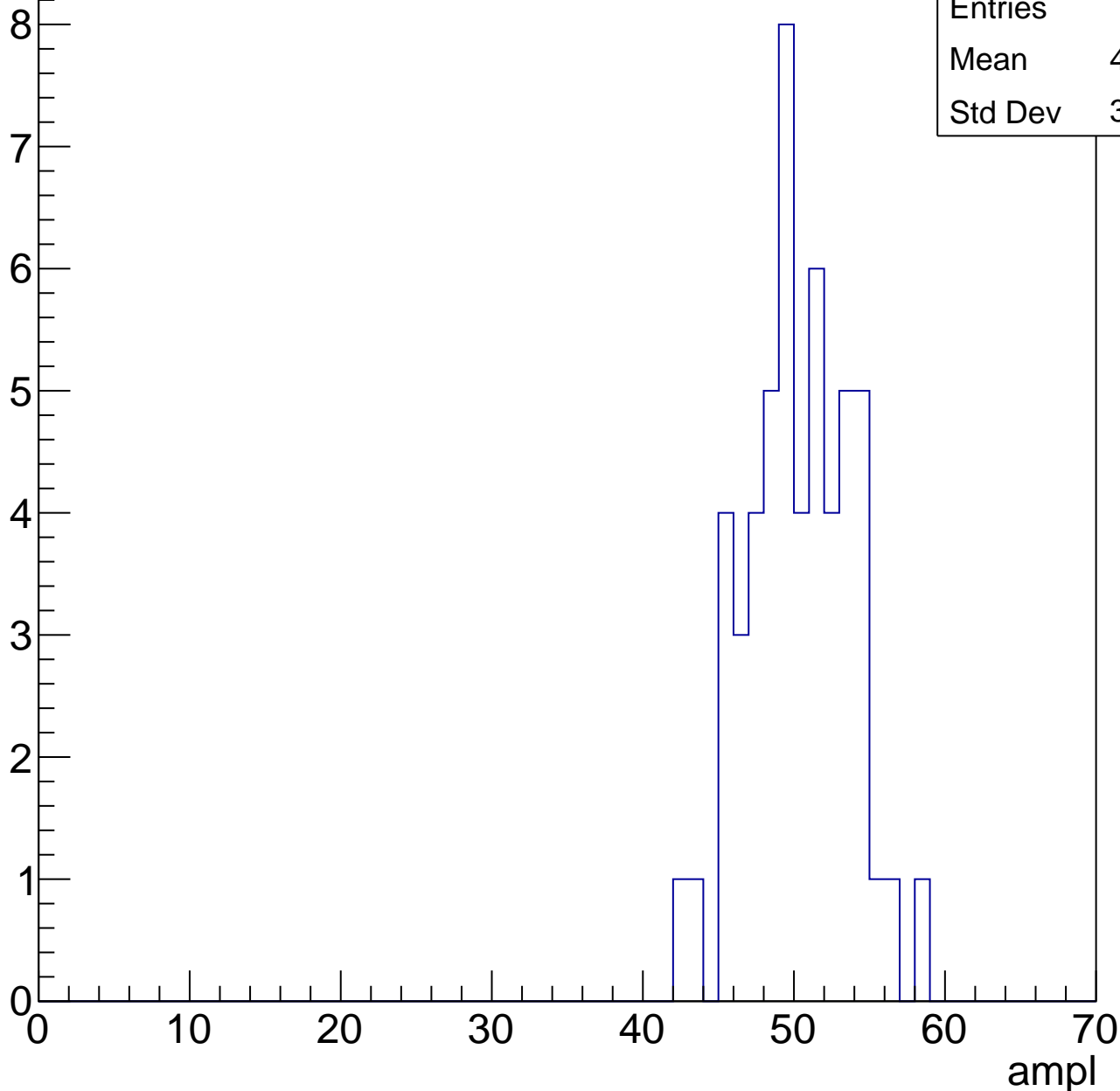


# B1L000S, U24-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

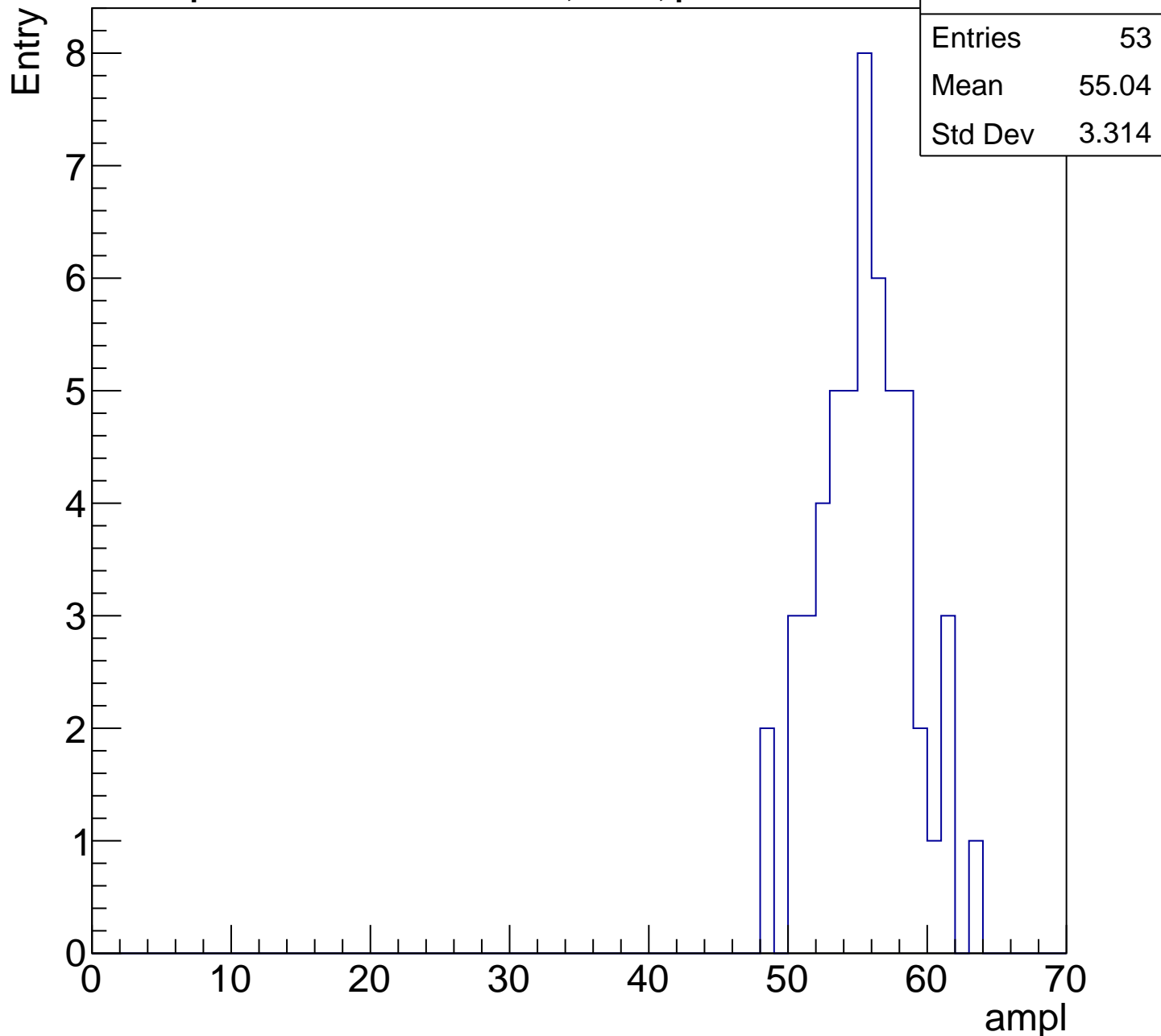
Entry

Entries	53
Mean	49.83
Std Dev	3.352



# B1L000S, U24-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

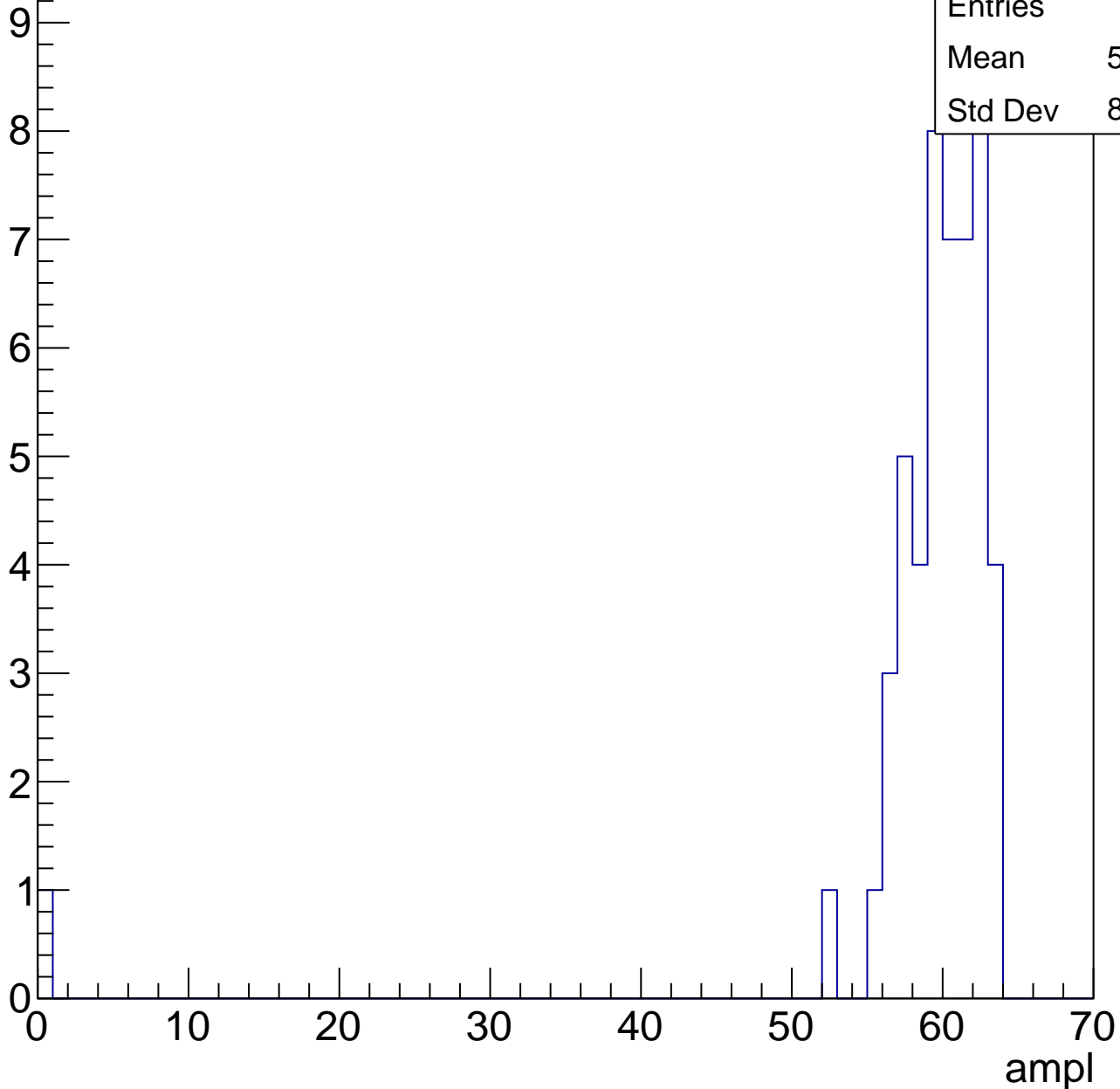


# B1L000S, U24-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.42
Std Dev	8.672

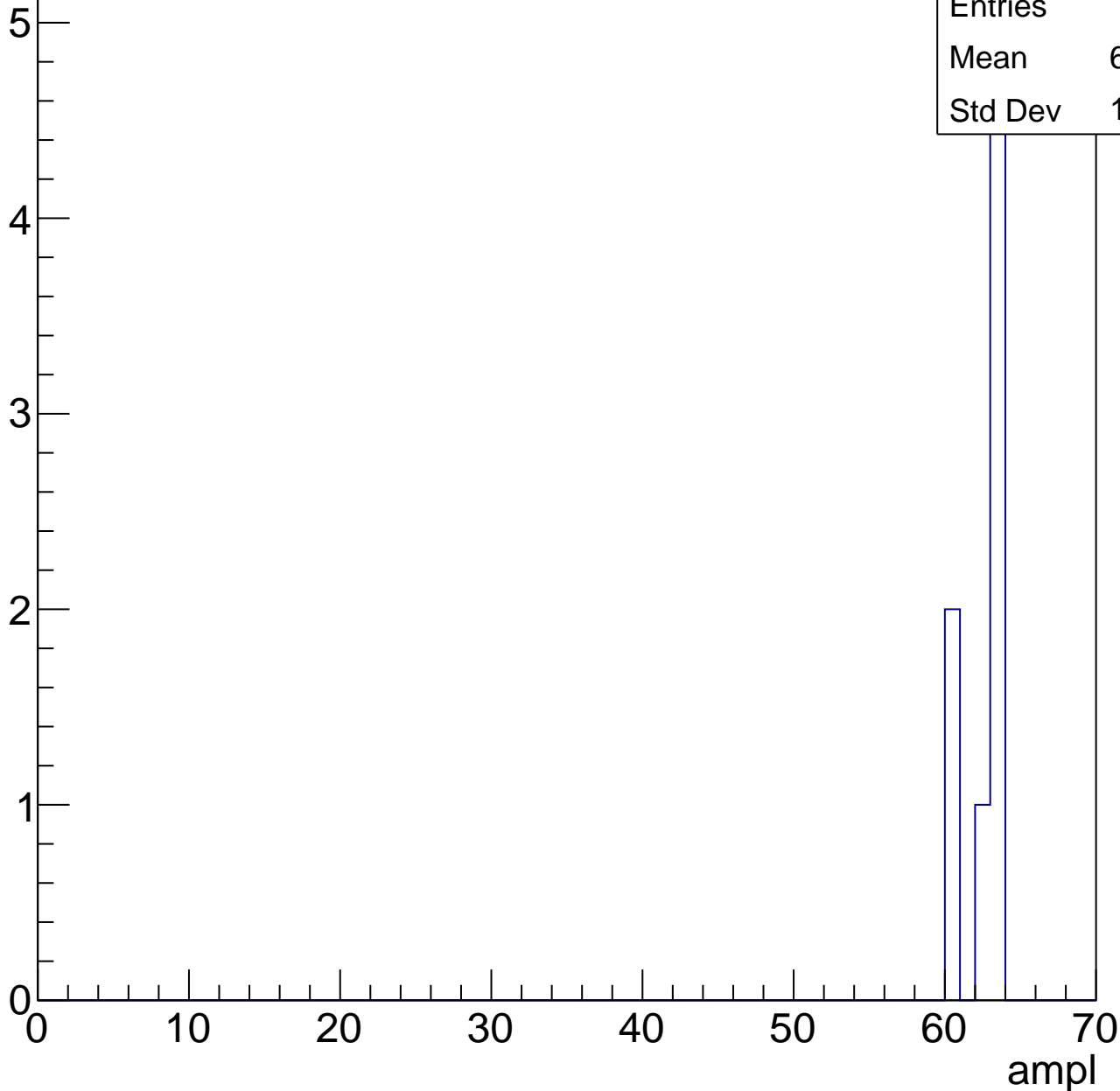


# B1L000S, U24-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.12
Std Dev	1.269

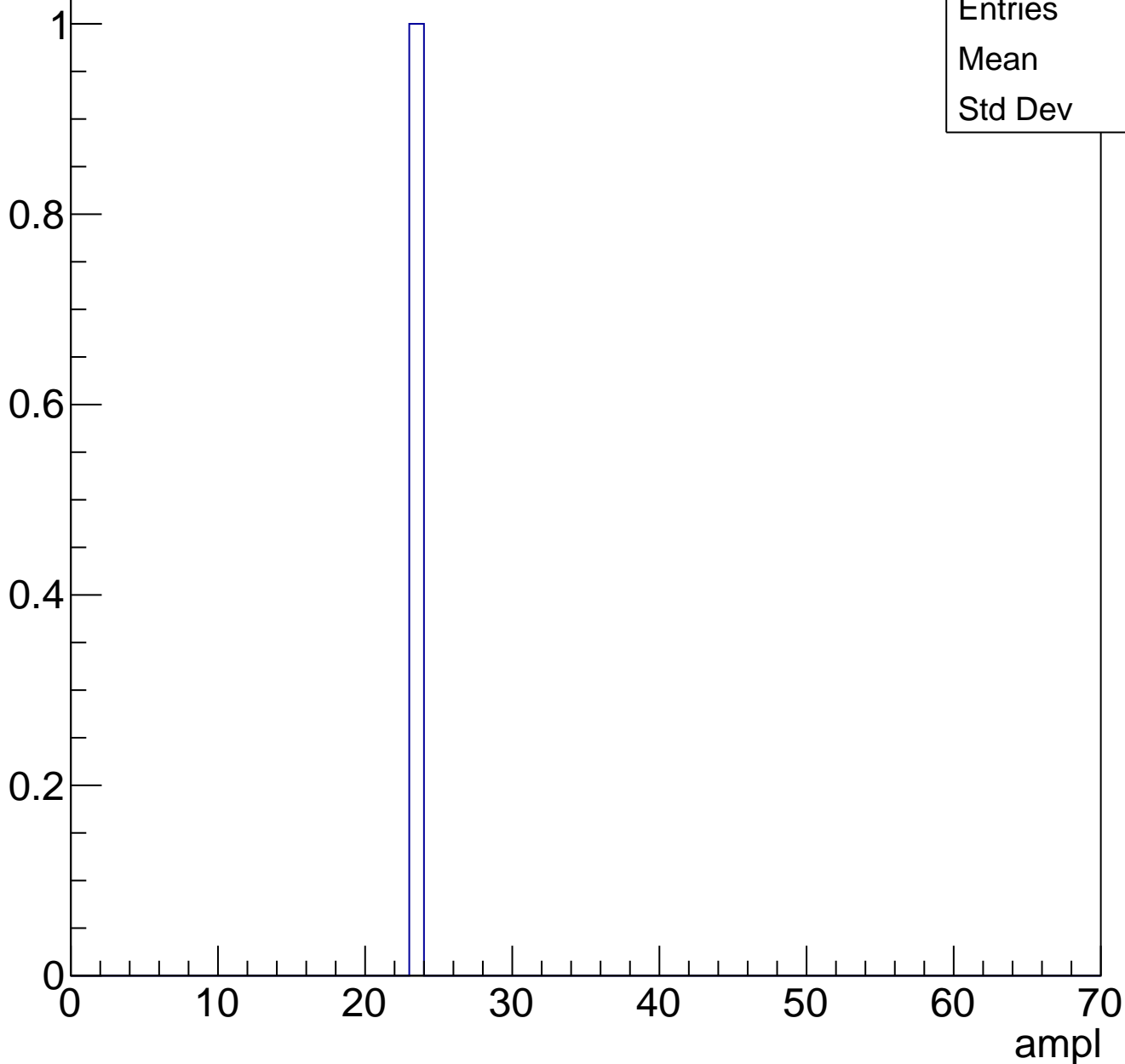




# B1L000S, U24-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch80, adc0

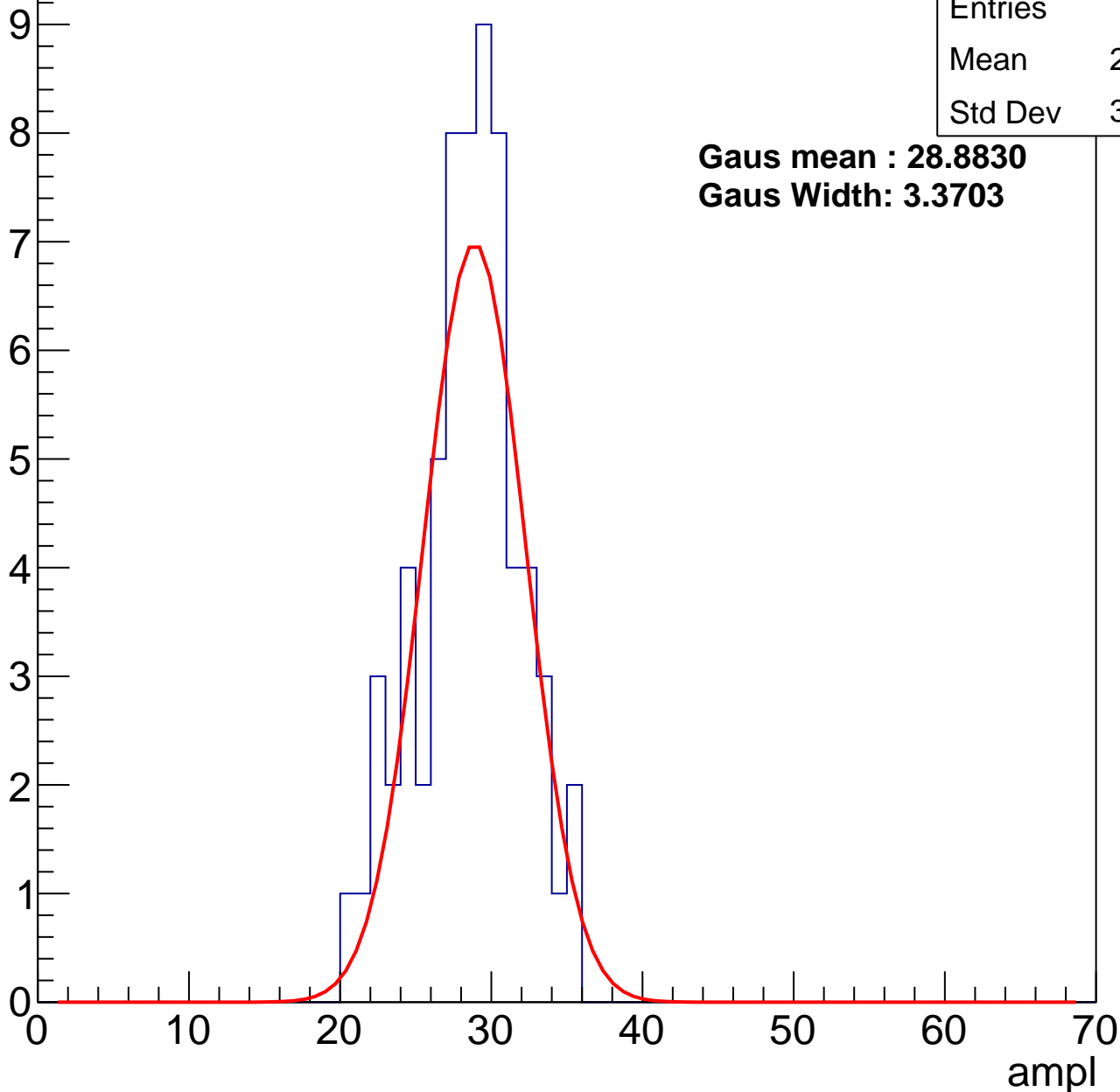
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.08
Std Dev	3.353

**Gaus mean : 28.8830**

**Gaus Width: 3.3703**



# B1L000S, U24-ch80, adc1

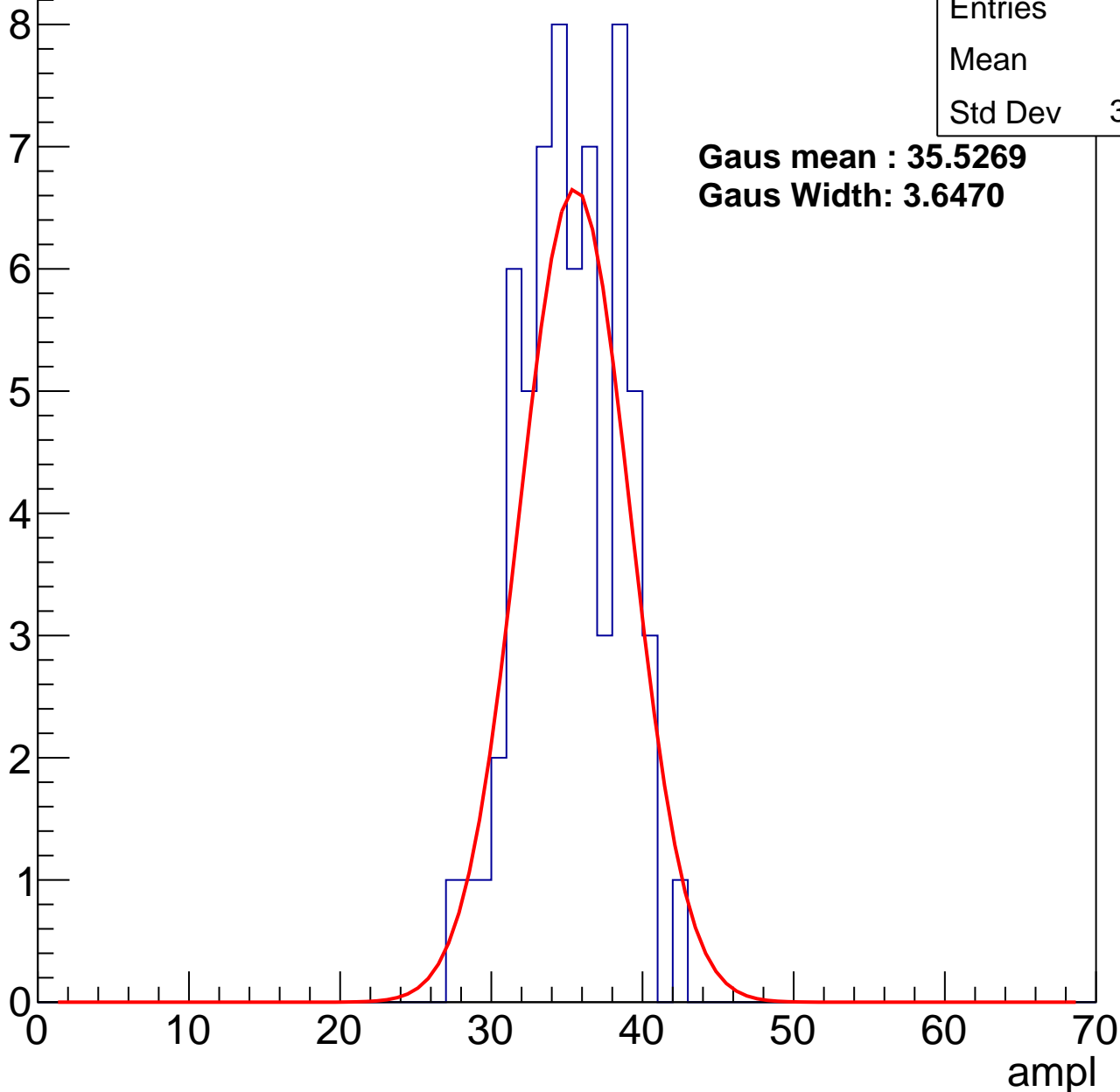
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.8
Std Dev	3.232

**Gaus mean : 35.5269**

**Gaus Width: 3.6470**



# B1L000S, U24-ch80, adc2

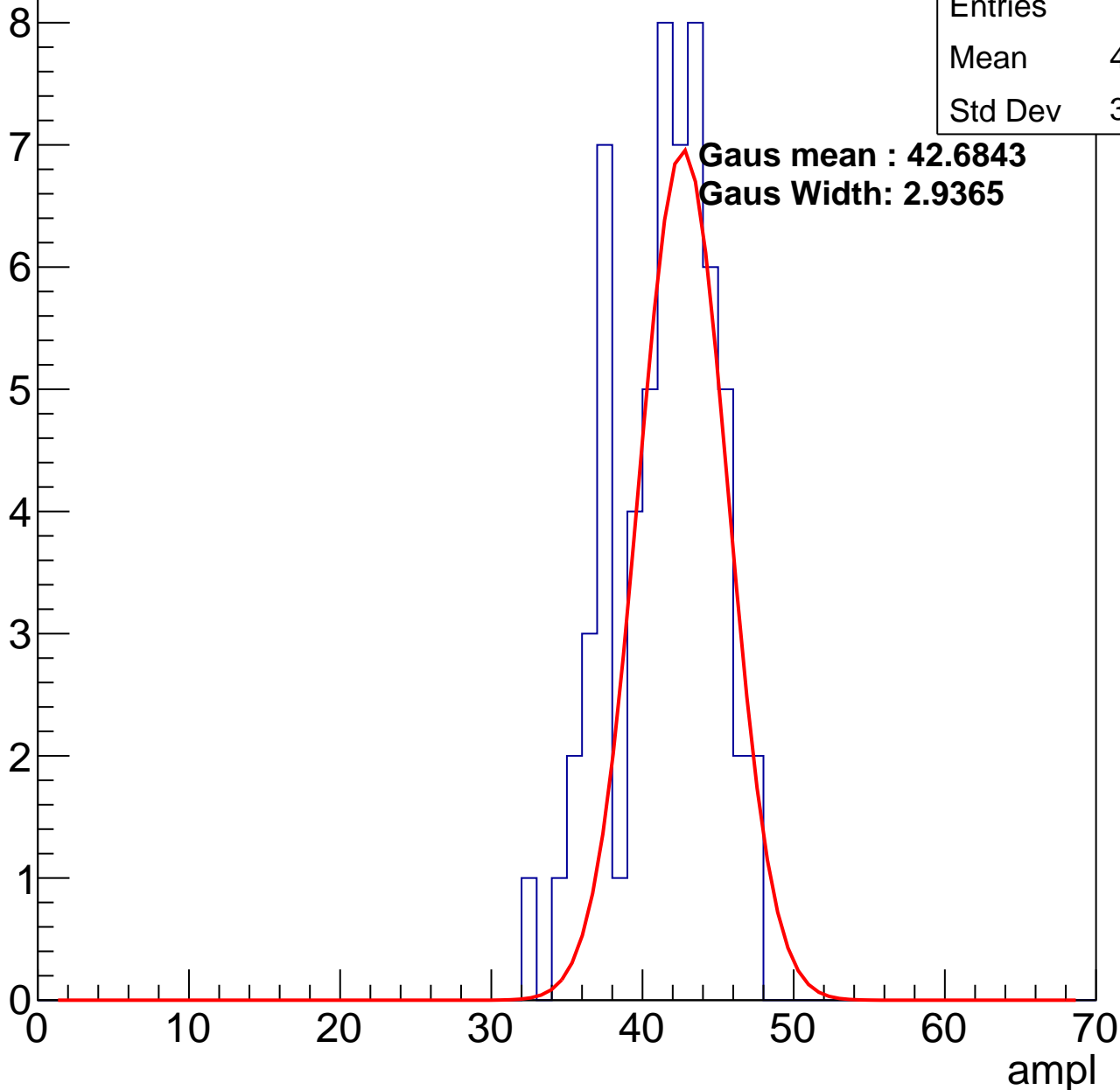
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.94
Std Dev	3.393

**Gaus mean : 42.6843**

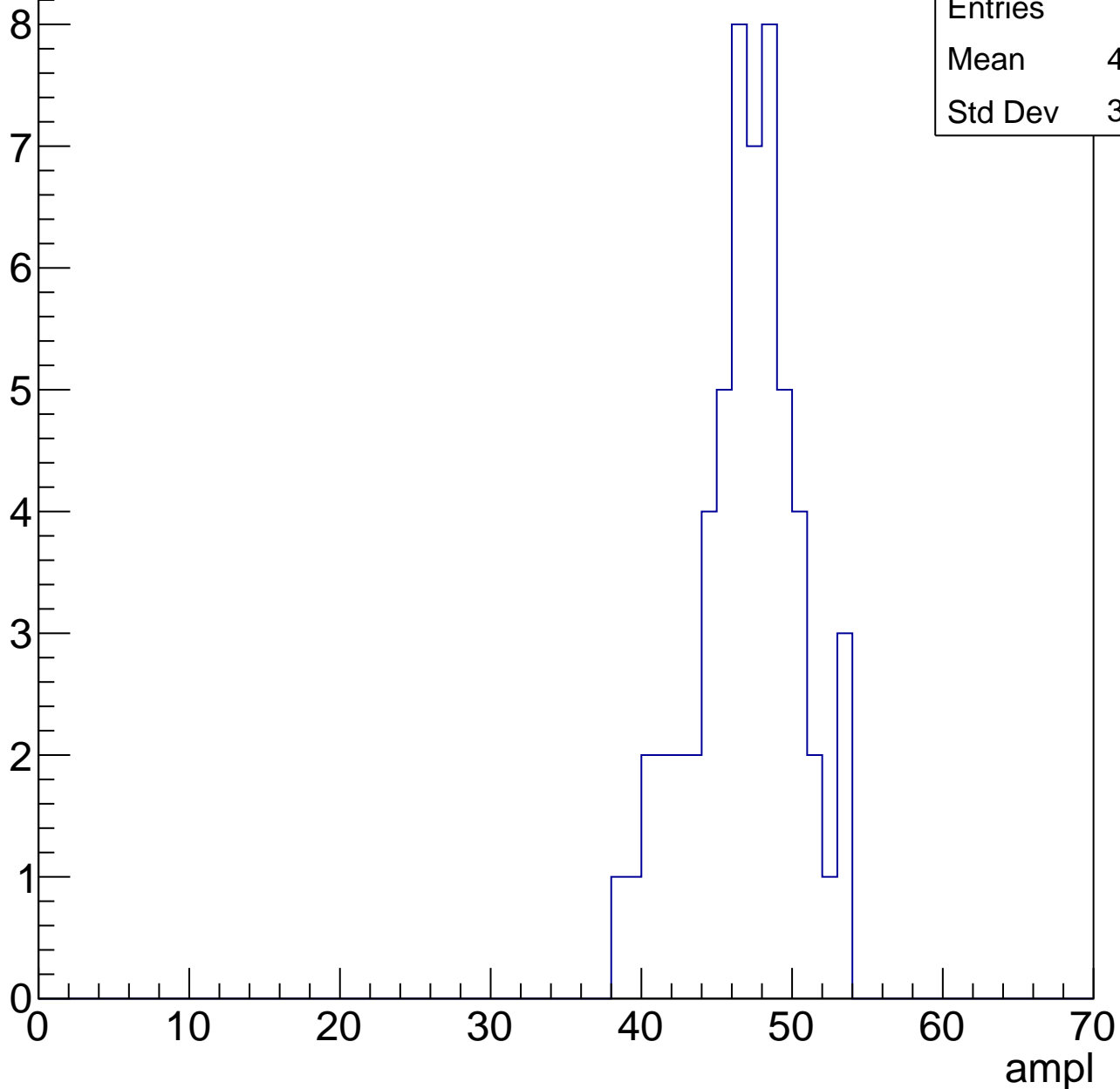
**Gaus Width: 2.9365**



# B1L000S, U24-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

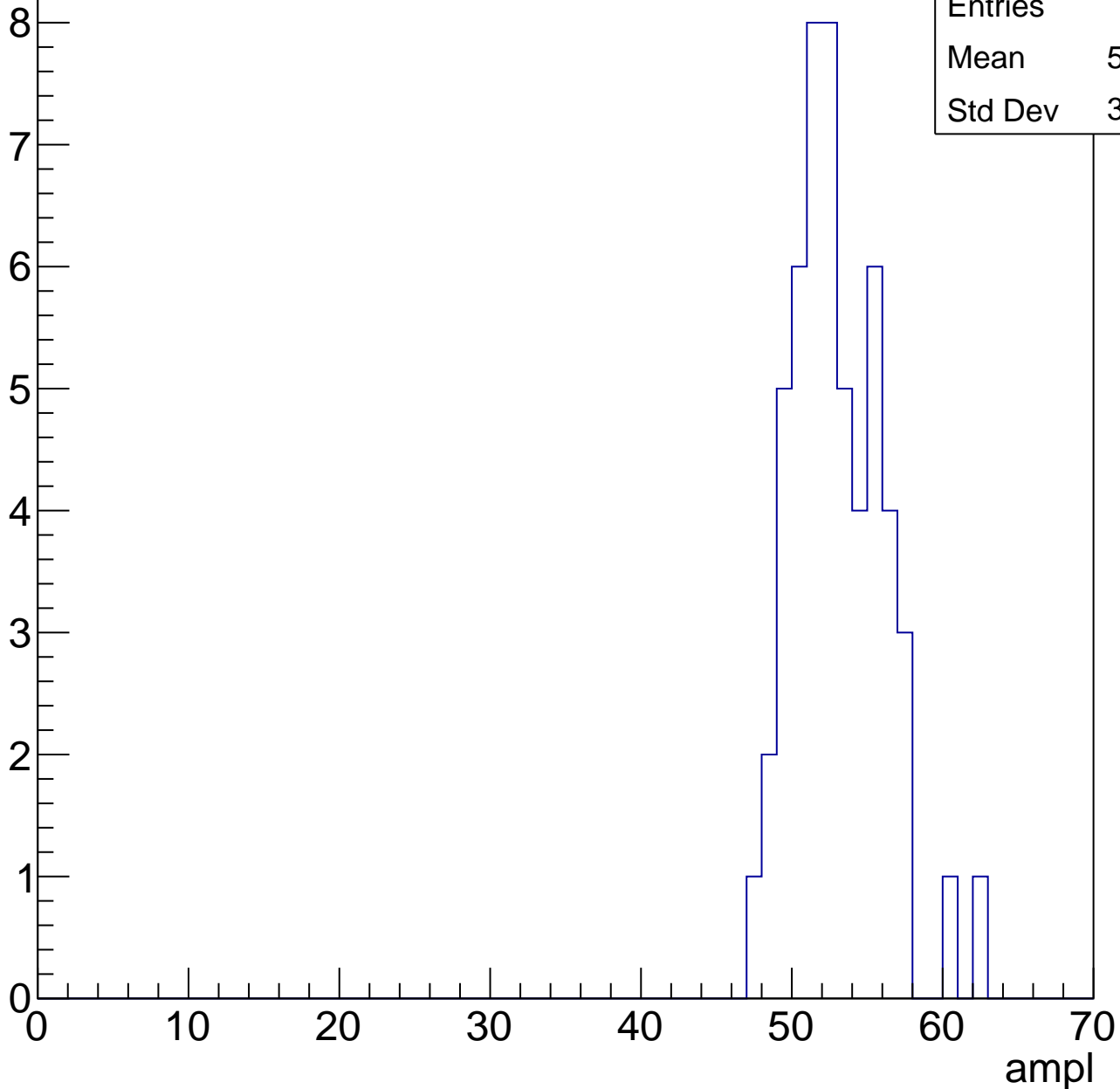


# B1L000S, U24-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	52.59
Std Dev	3.022



# B1L000S, U24-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

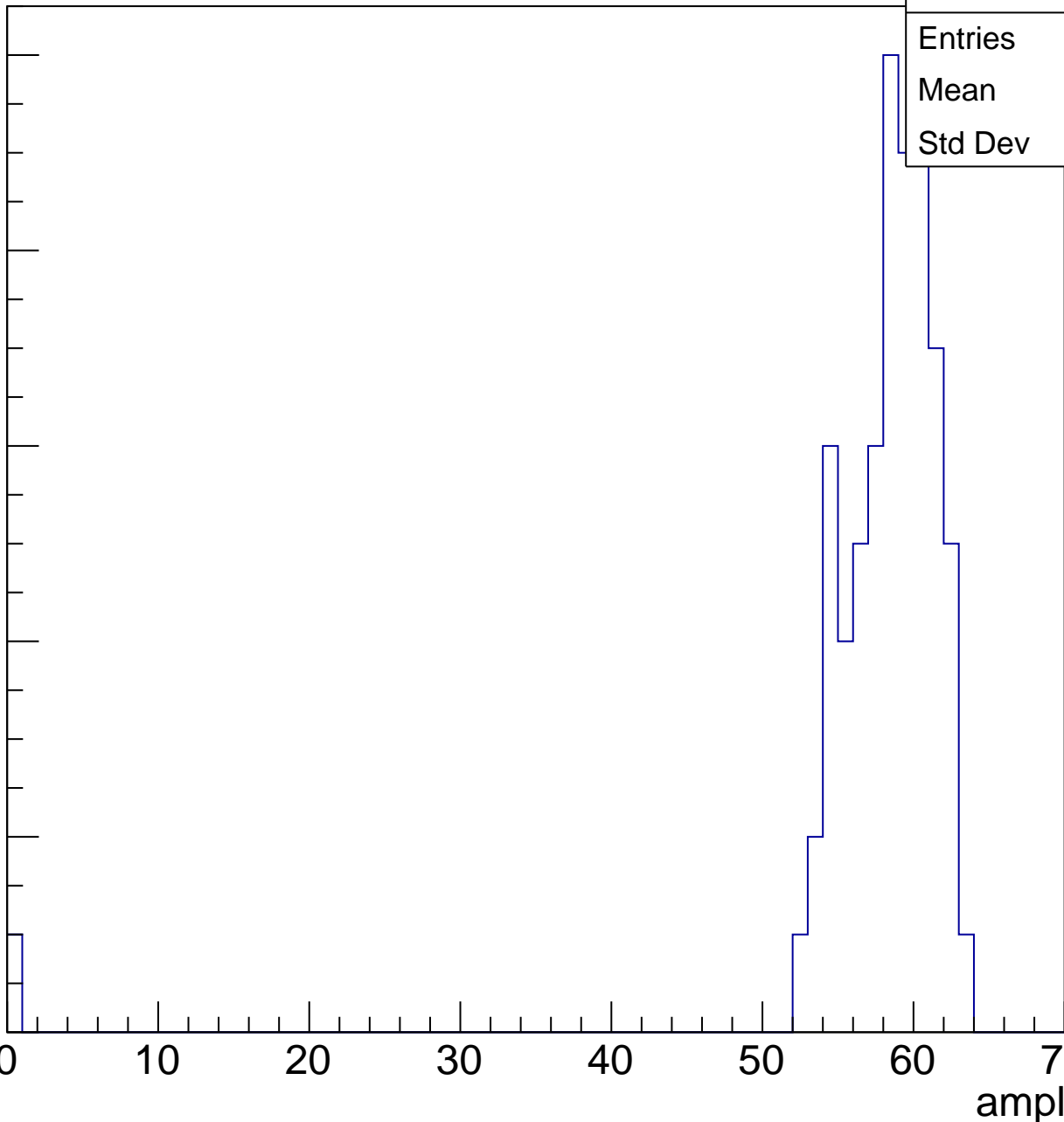
50

60

70

ampl

Entries	66
Mean	57.2
Std Dev	7.566

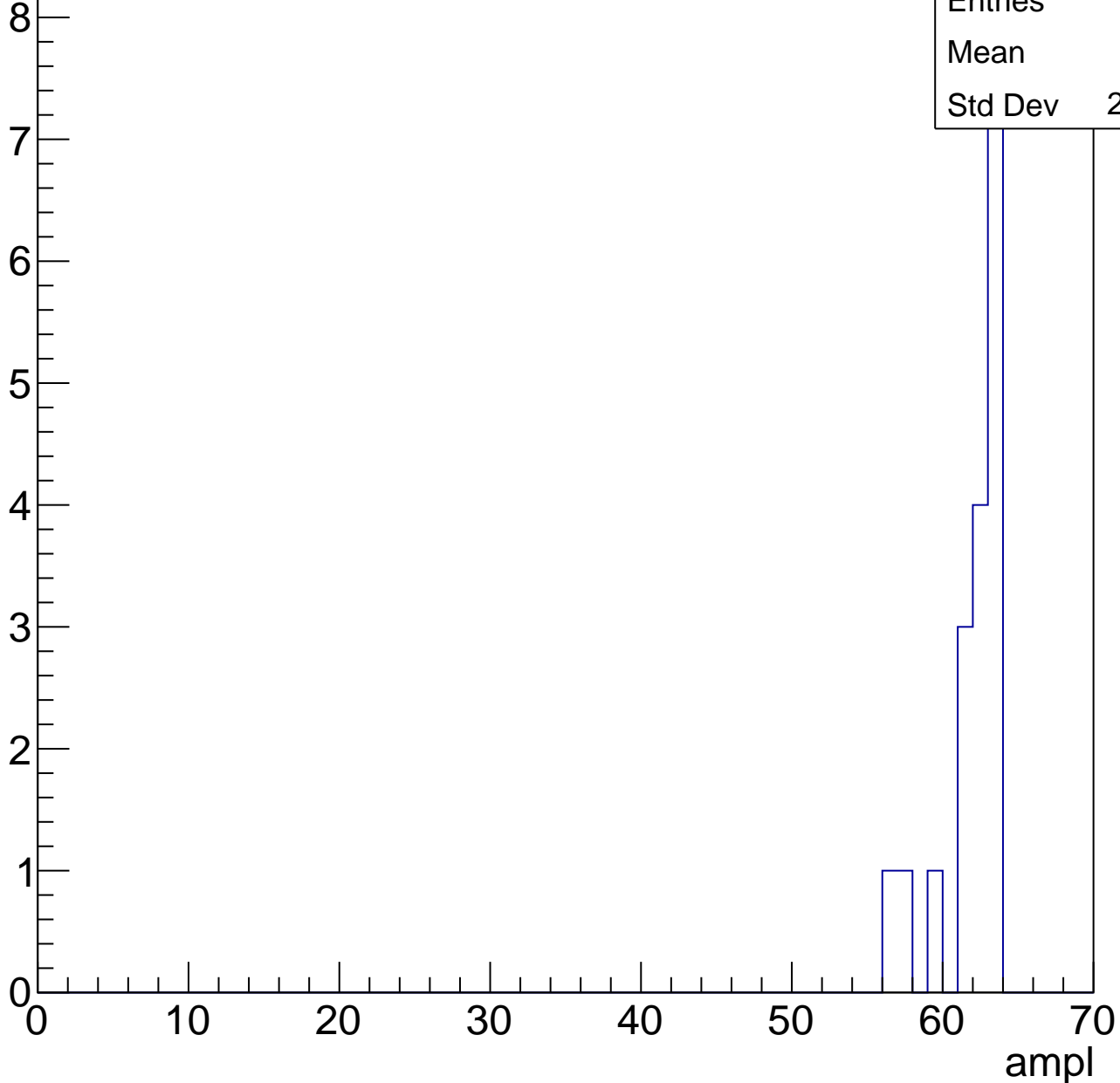


# B1L000S, U24-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.5
Std Dev	2.062





# B1L000S, U24-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch81, adc0

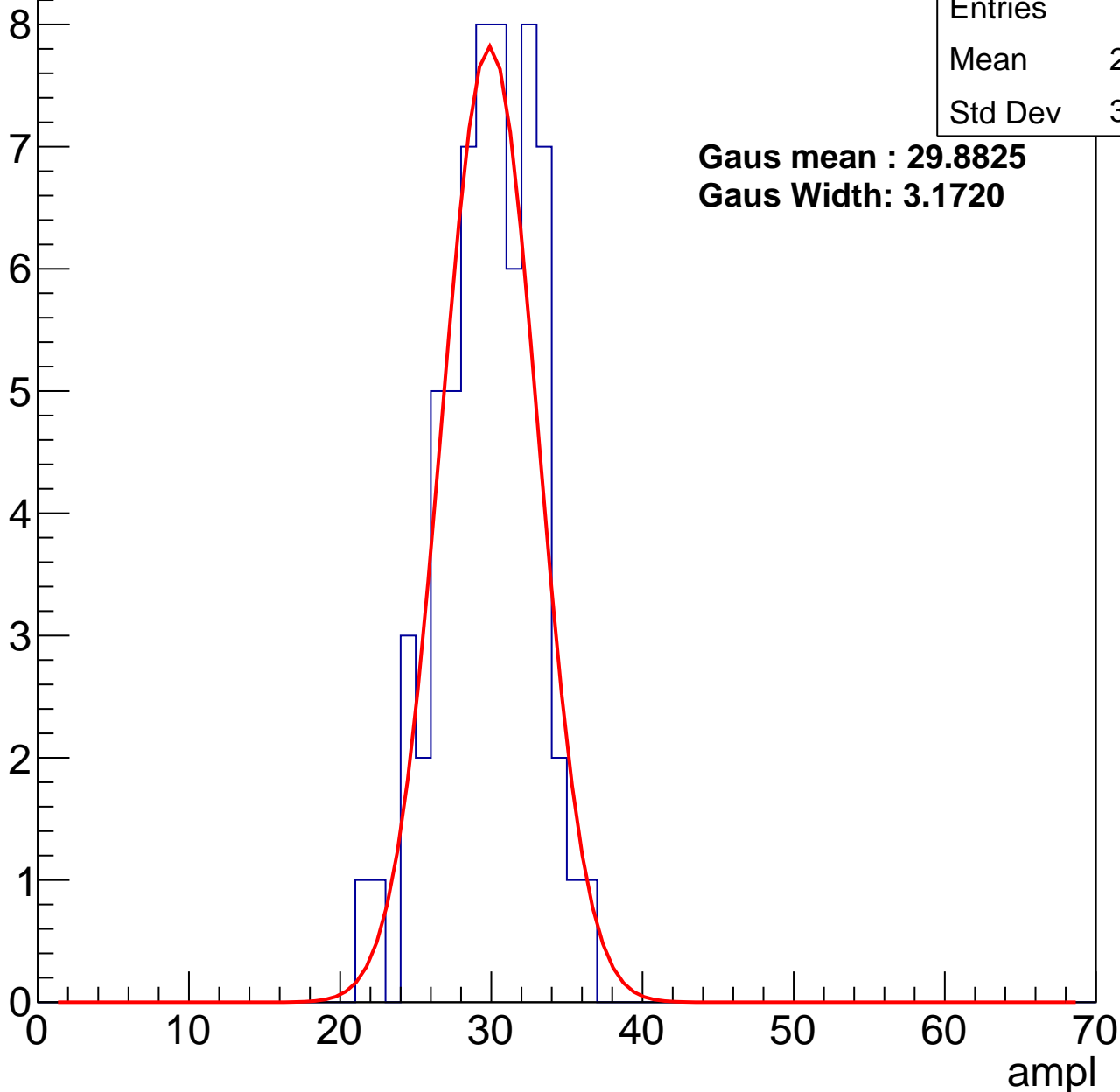
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.38
Std Dev	3.122

**Gaus mean : 29.8825**

**Gaus Width: 3.1720**



# B1L000S, U24-ch81, adc1

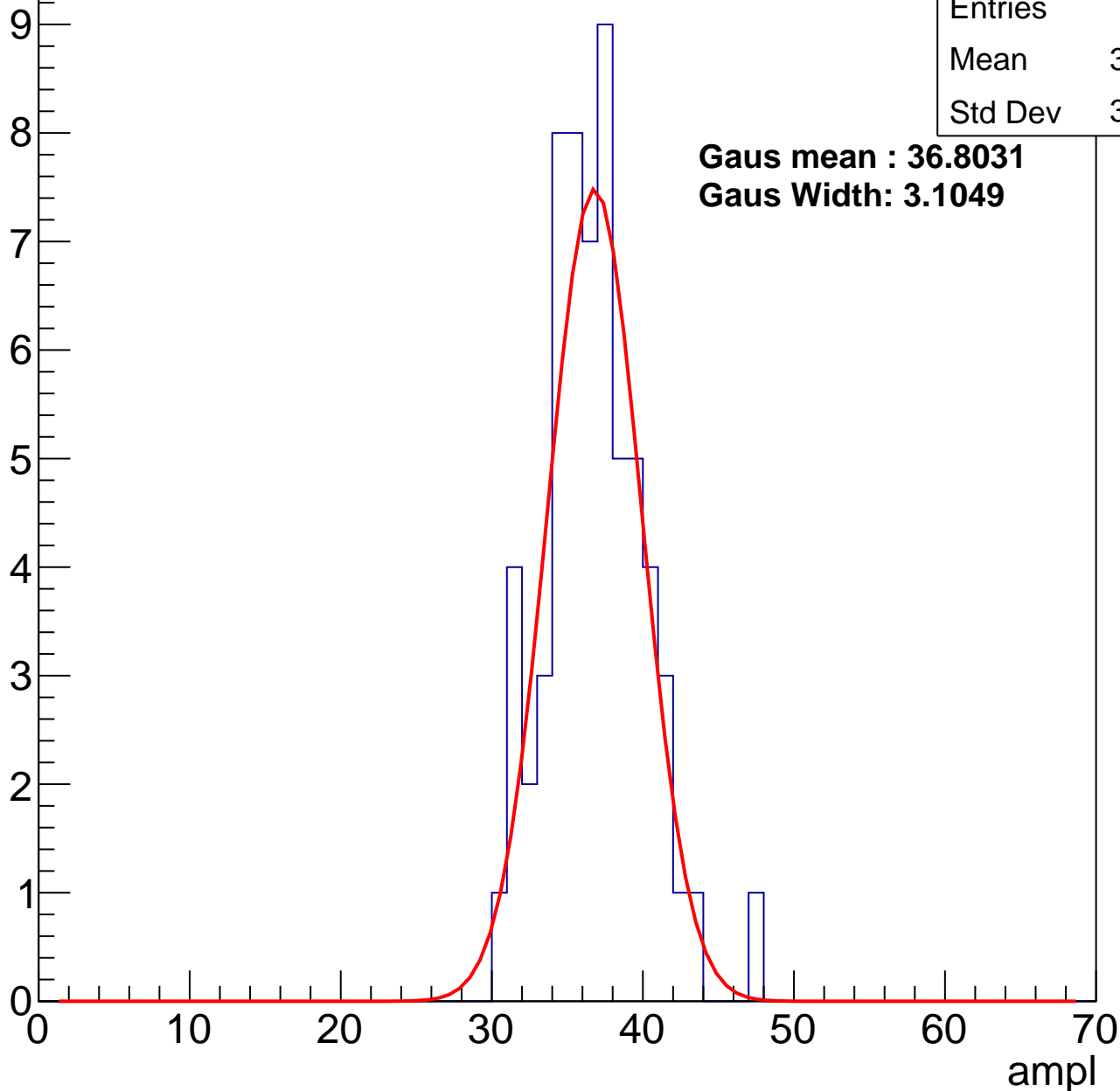
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.35
Std Dev	3.228

**Gaus mean : 36.8031**

**Gaus Width: 3.1049**



# B1L000S, U24-ch81, adc2

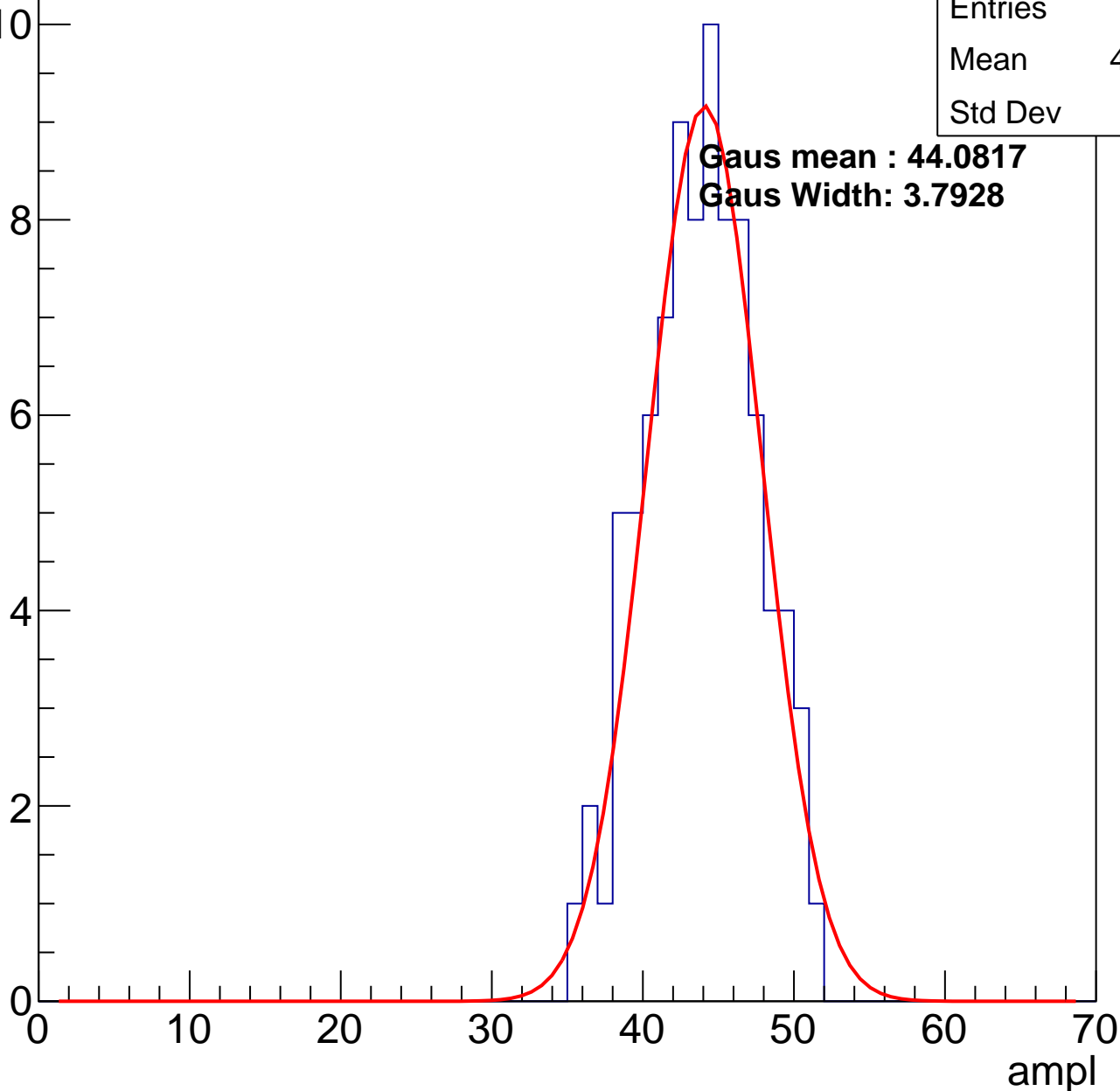
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	88
Mean	43.38
Std Dev	3.61

**Gaus mean : 44.0817**

**Gaus Width: 3.7928**

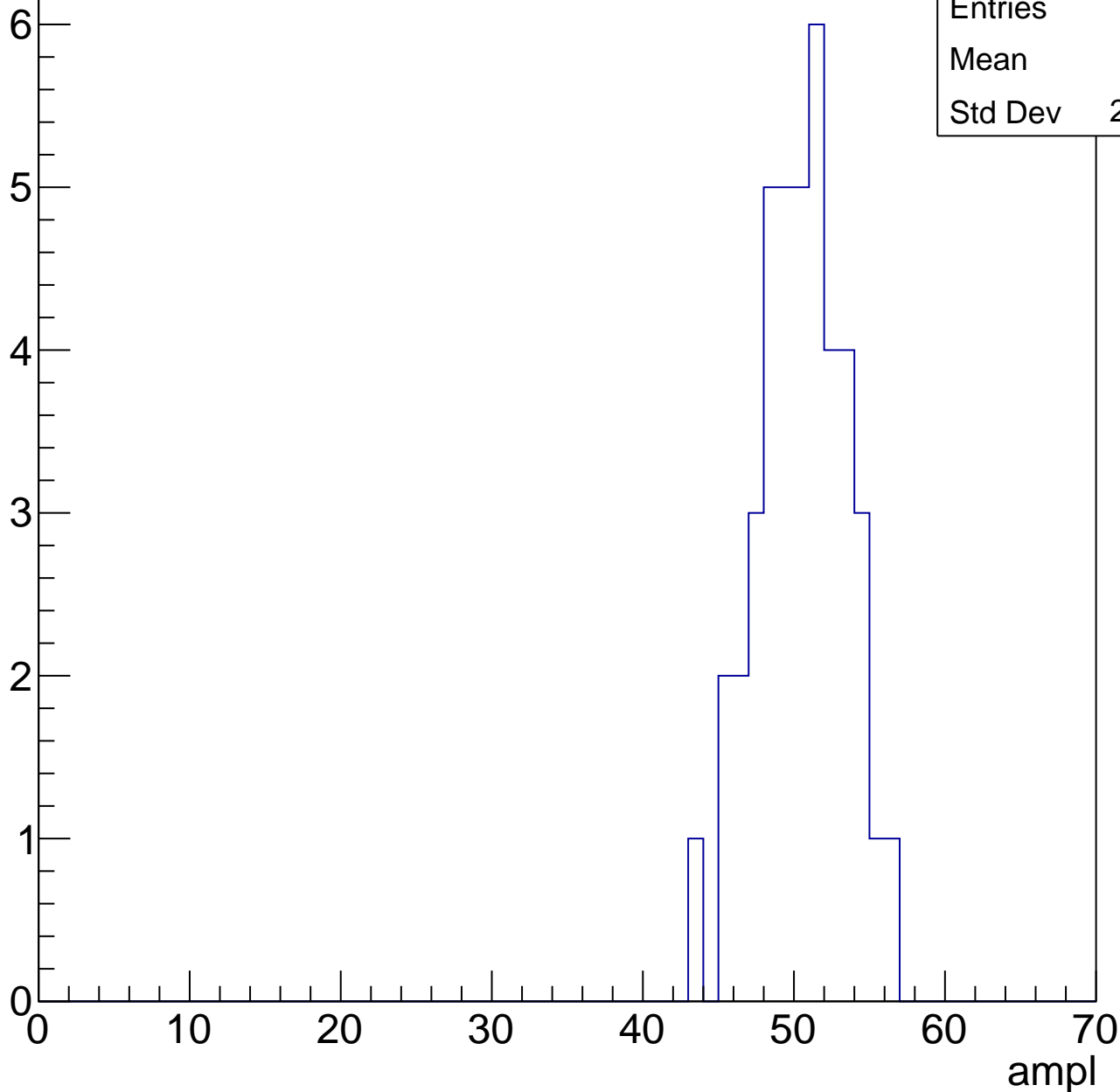


# B1L000S, U24-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	50
Std Dev	2.887

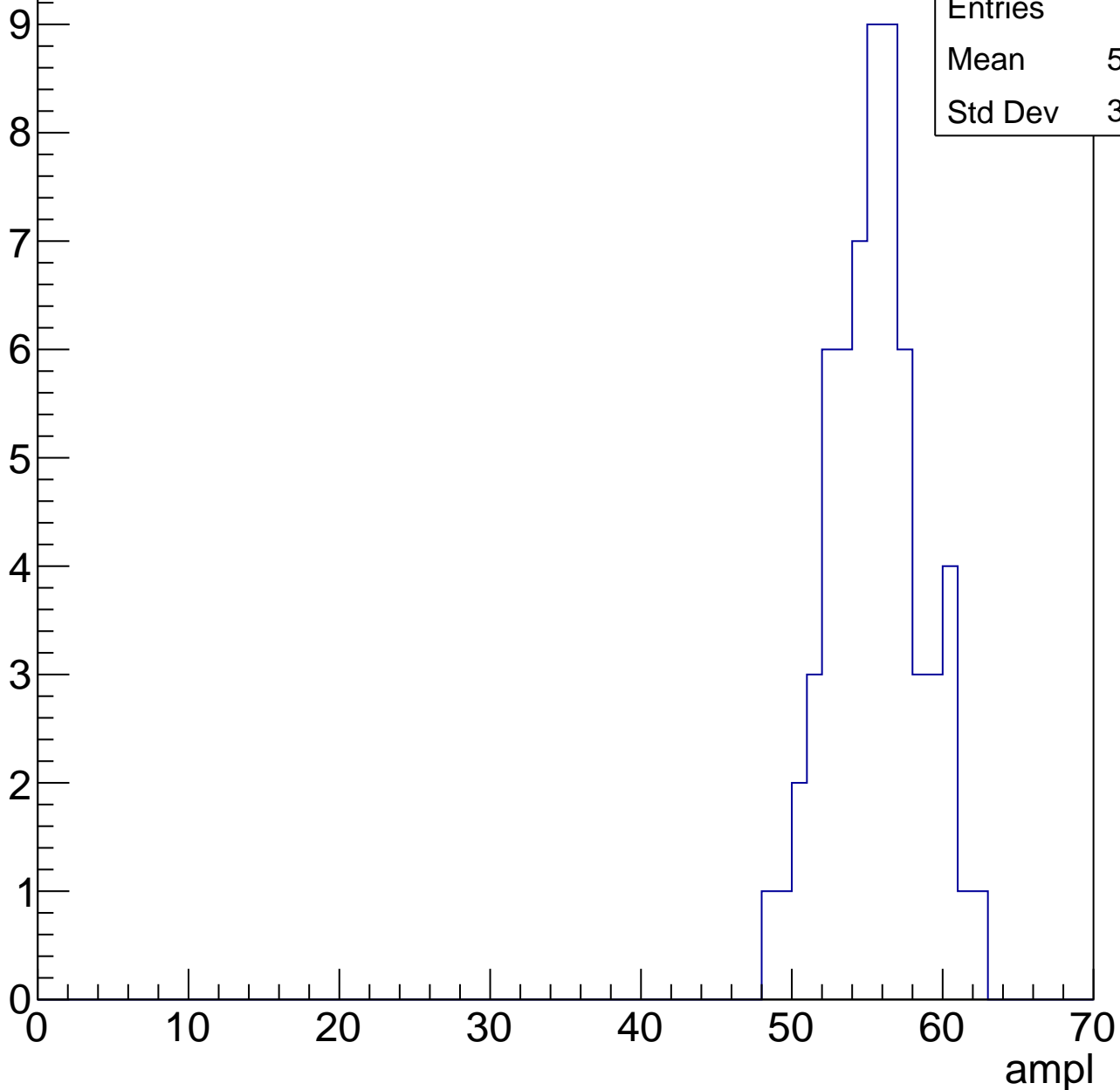


# B1L000S, U24-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	55.05
Std Dev	3.008

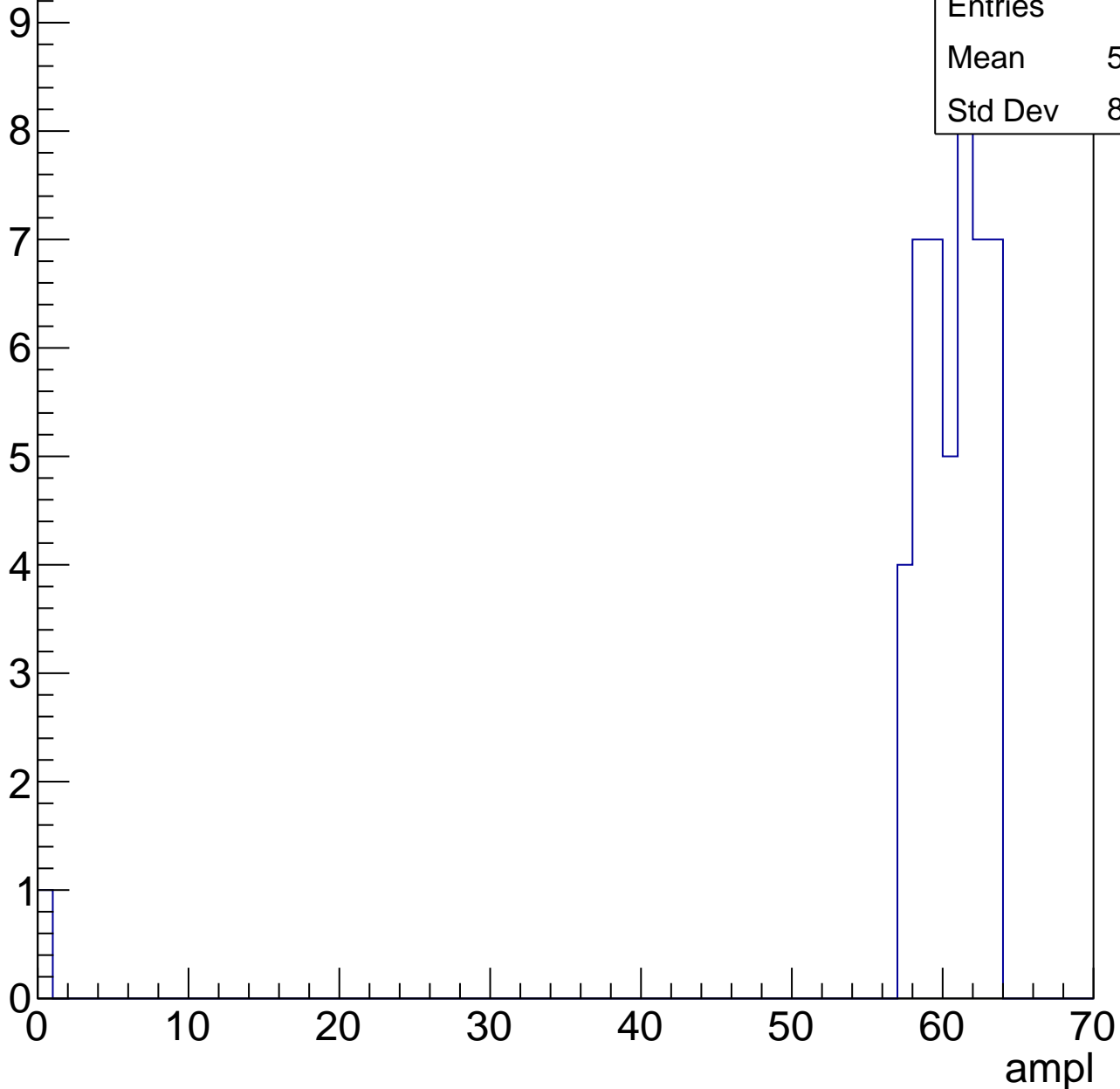


# B1L000S, U24-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	58.96
Std Dev	8.896



# B1L000S, U24-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch82, adc0

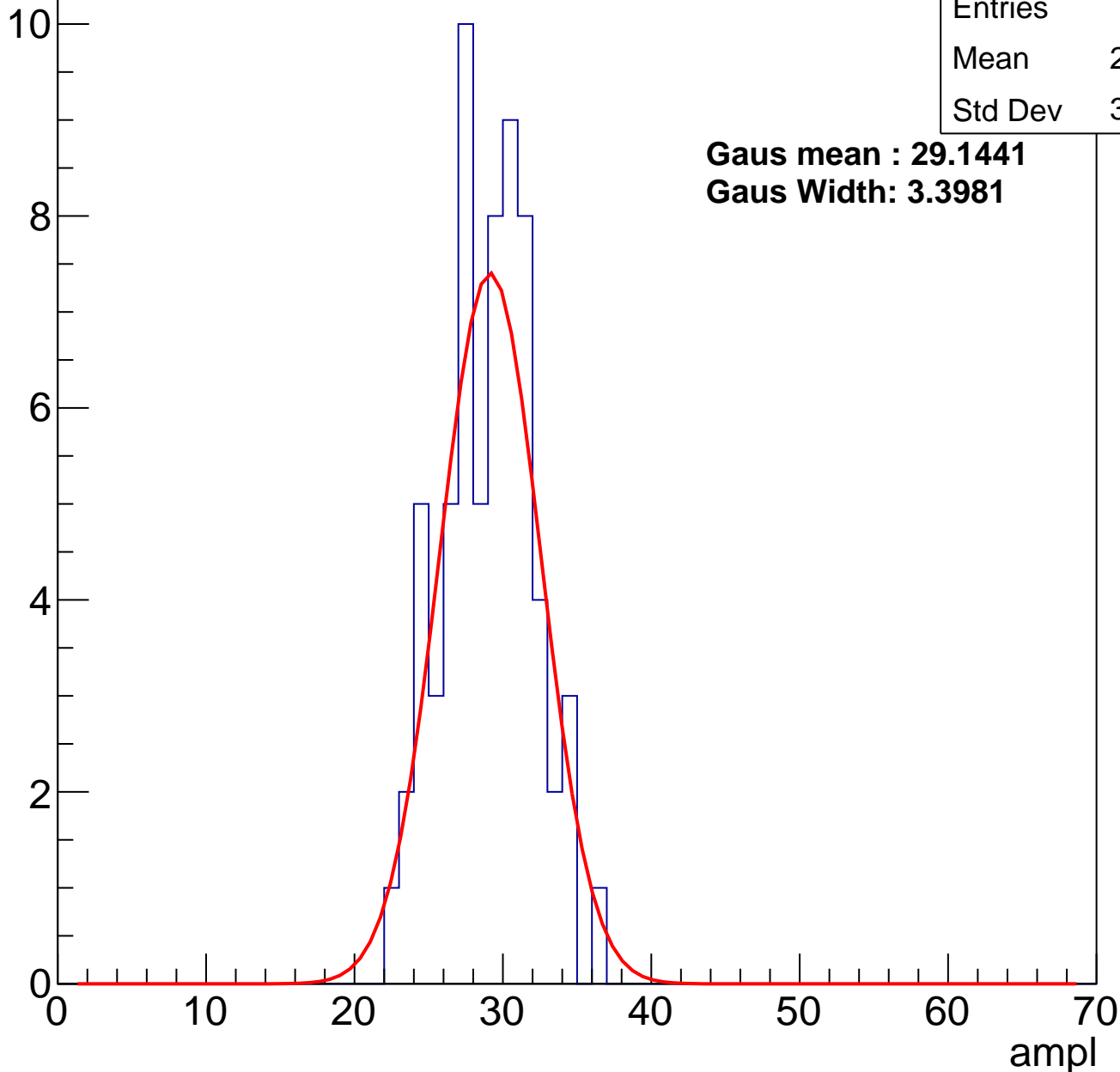
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	28.56
Std Dev	3.026

**Gaus mean : 29.1441**

**Gaus Width: 3.3981**

Entry



# B1L000S, U24-ch82, adc1

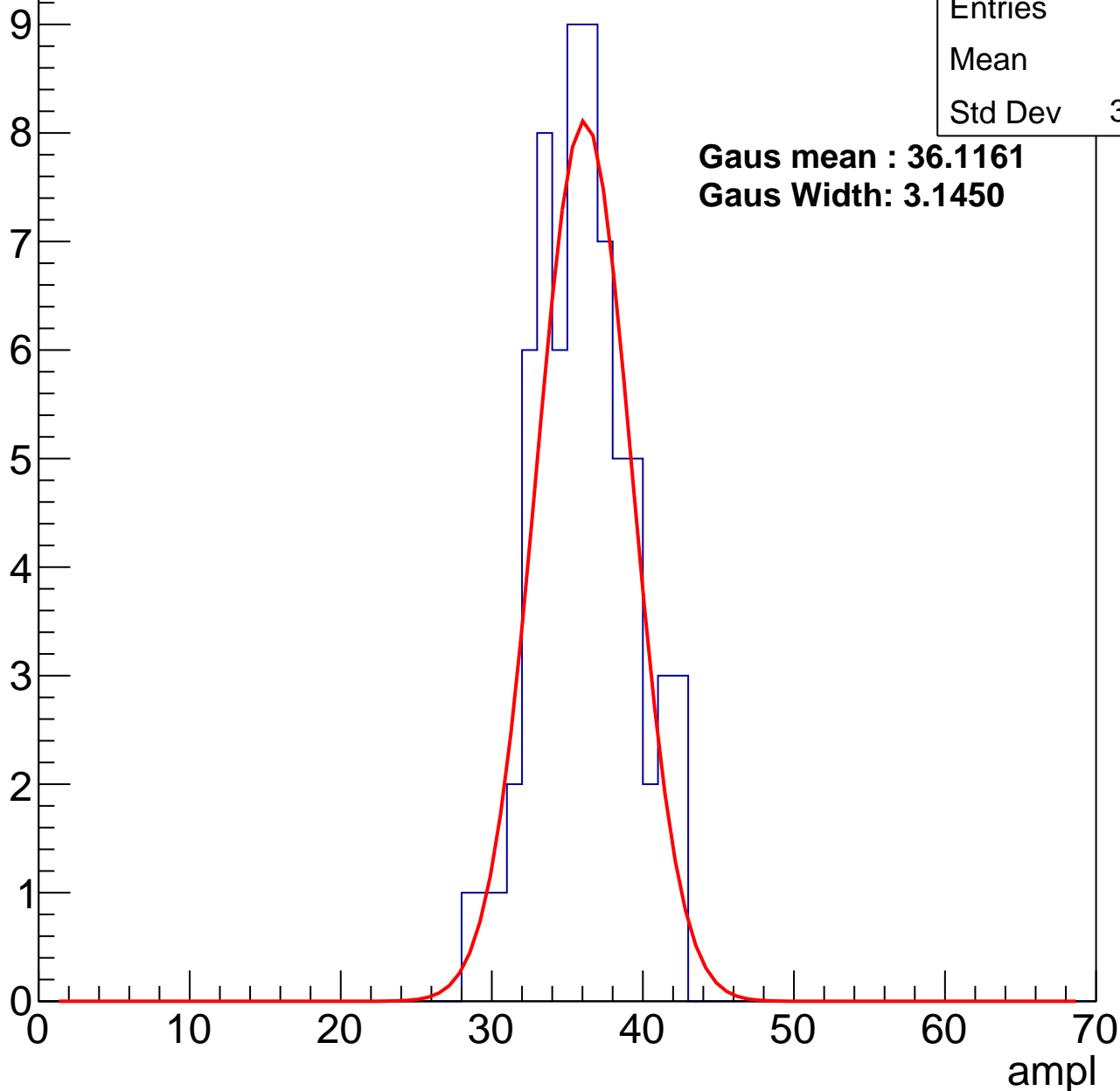
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.6
Std Dev	3.154

**Gaus mean : 36.1161**

**Gaus Width: 3.1450**



# B1L000S, U24-ch82, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	42.3
Std Dev	3.15

**Gaus mean : 42.6258**

**Gaus Width: 3.8278**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

10

8

6

4

2

0

0

10

20

30

40

50

60

70

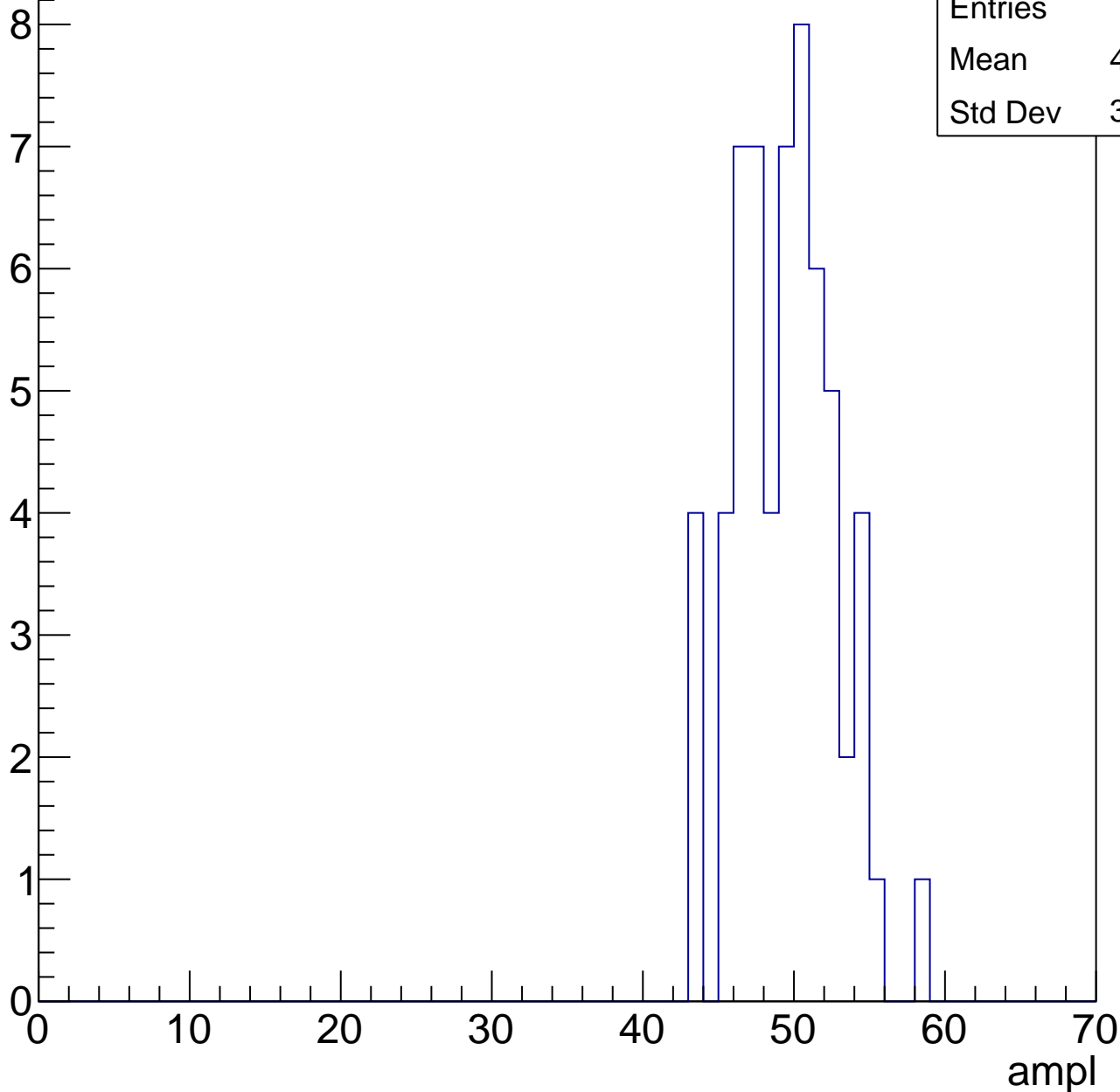
ampl

# B1L000S, U24-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

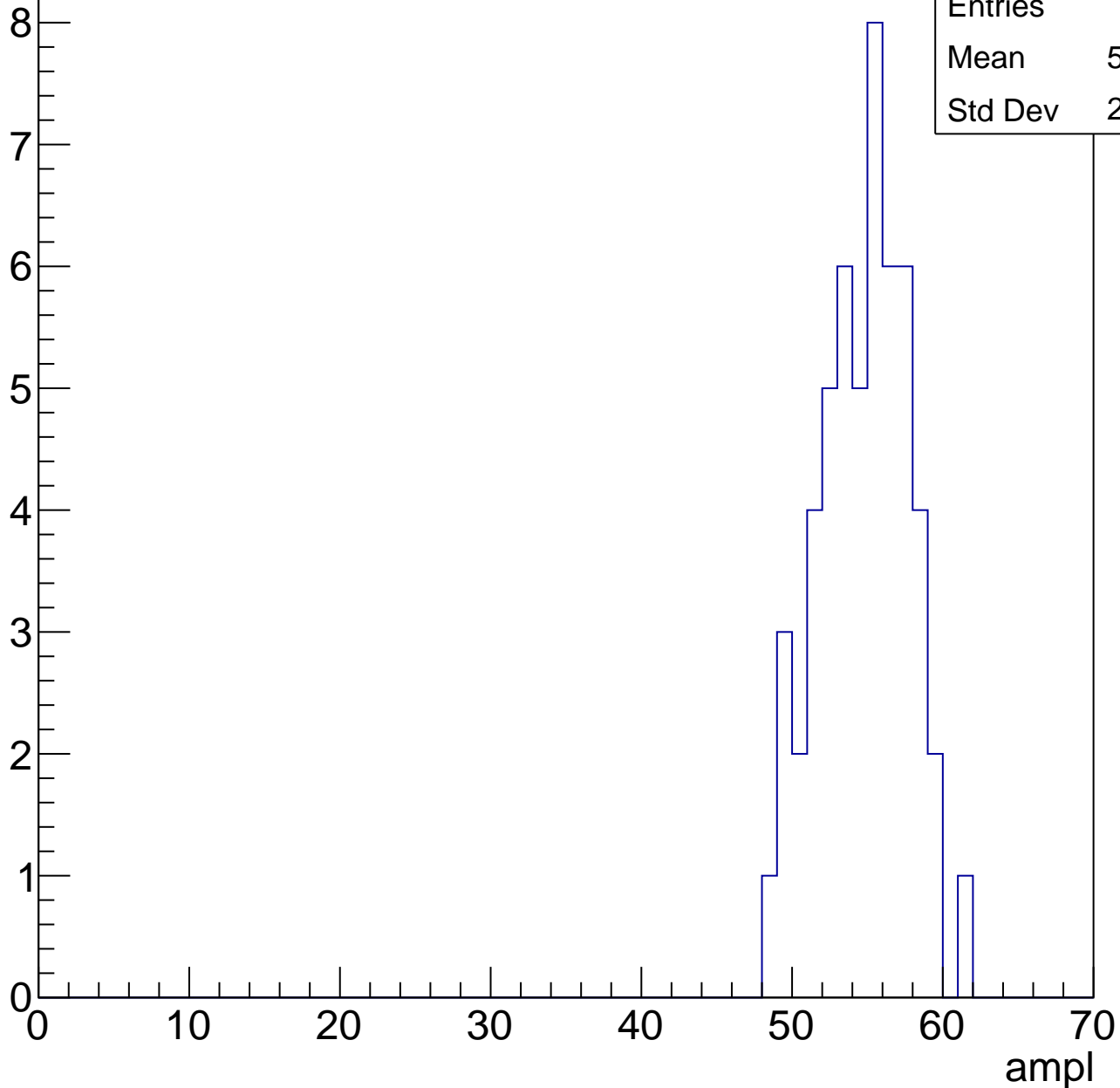
Entries	60
Mean	48.98
Std Dev	3.238



# B1L000S, U24-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

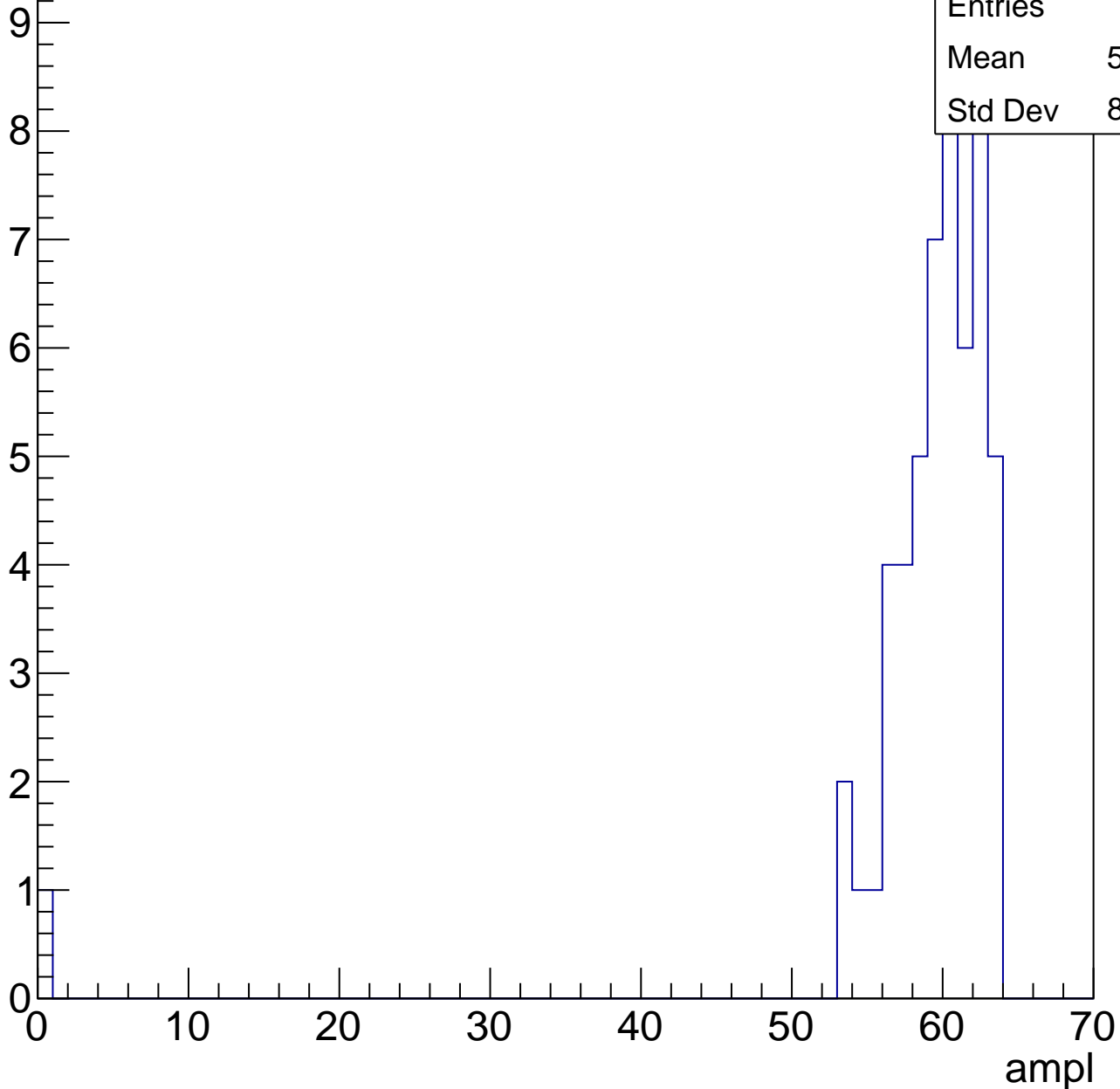


# B1L000S, U24-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.25
Std Dev	8.474

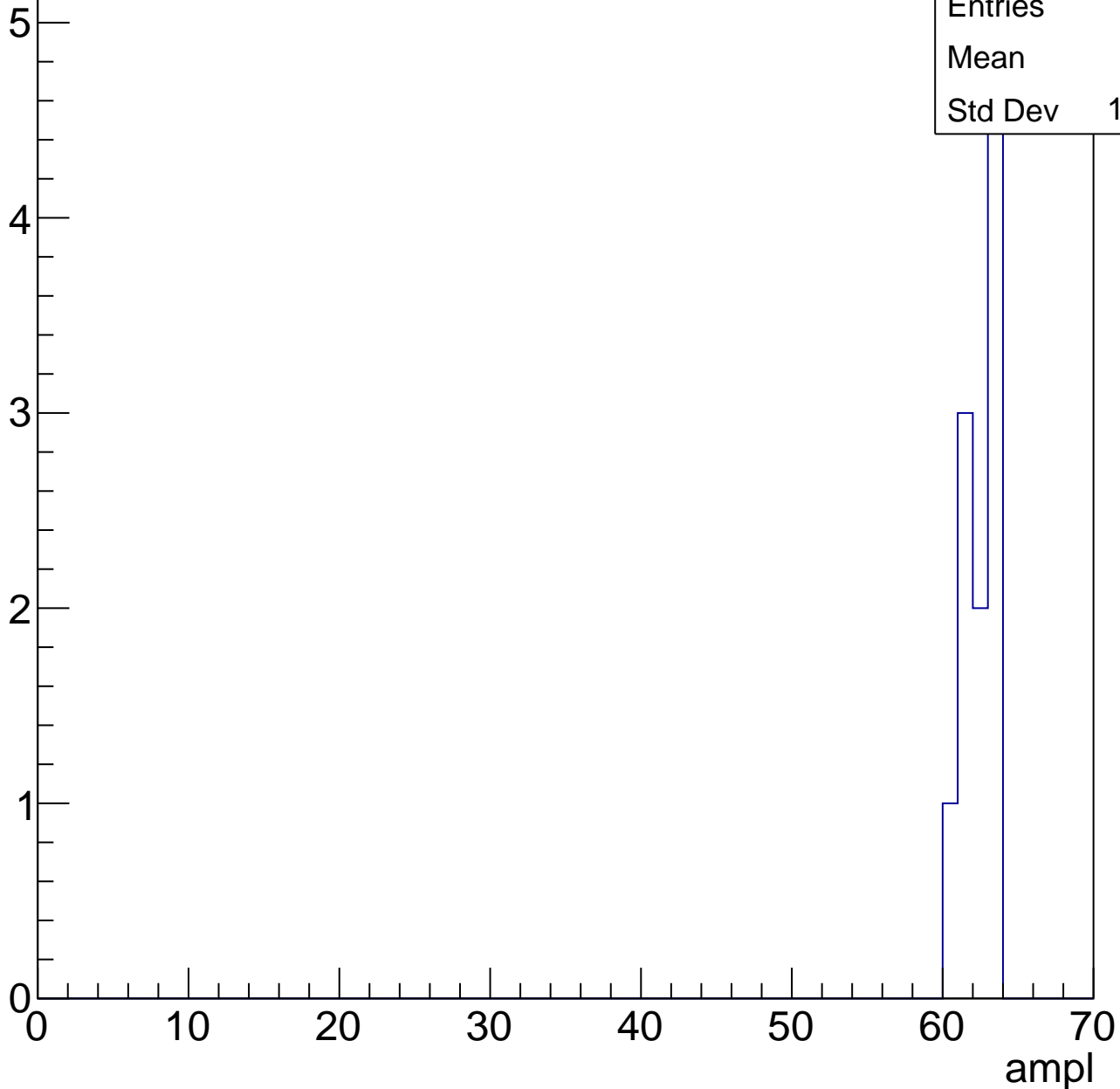


# B1L000S, U24-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	62
Std Dev	1.044

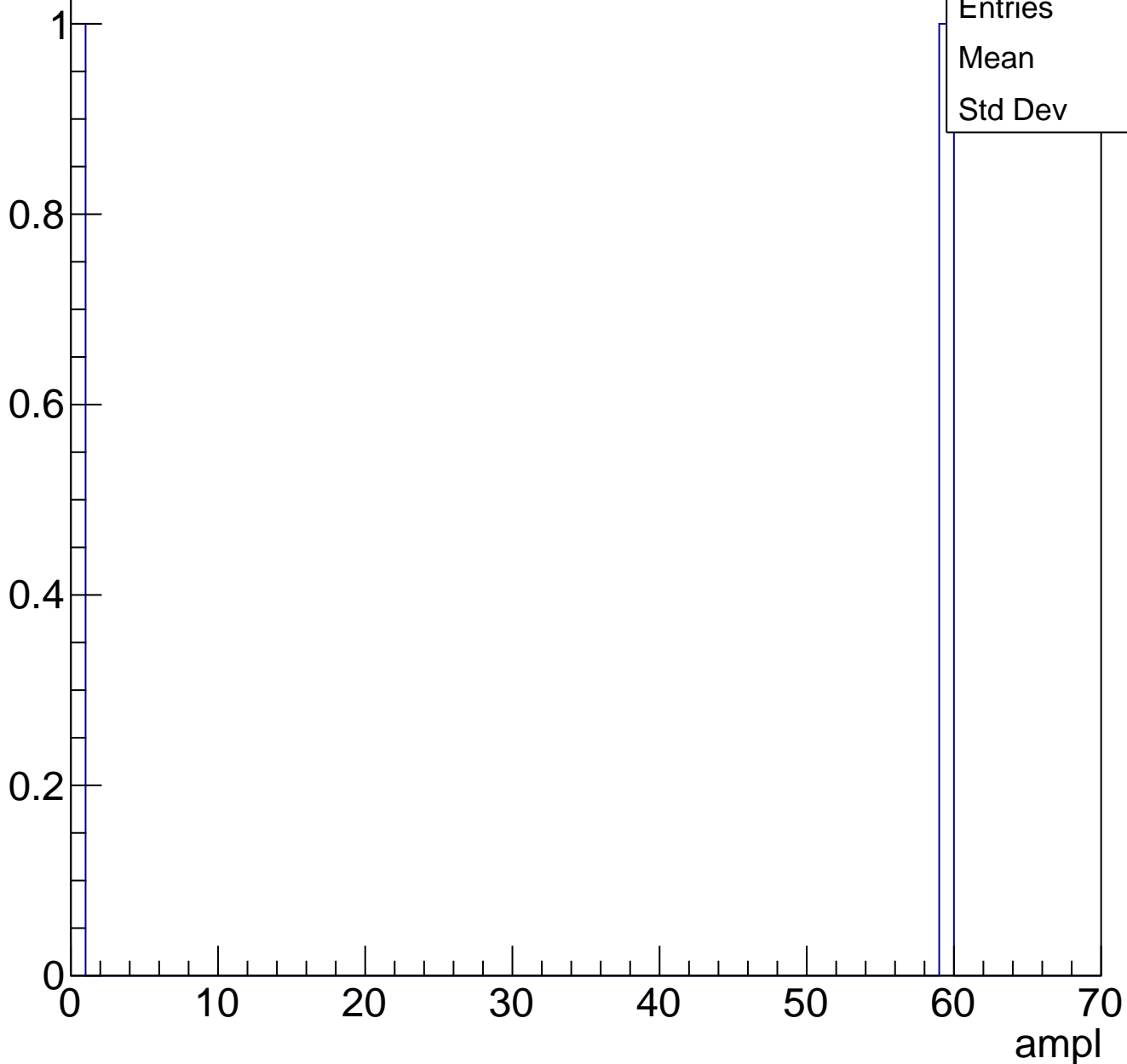




# B1L000S, U24-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch83, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	29.06
Std Dev	3.437

**Gaus mean : 29.6874**

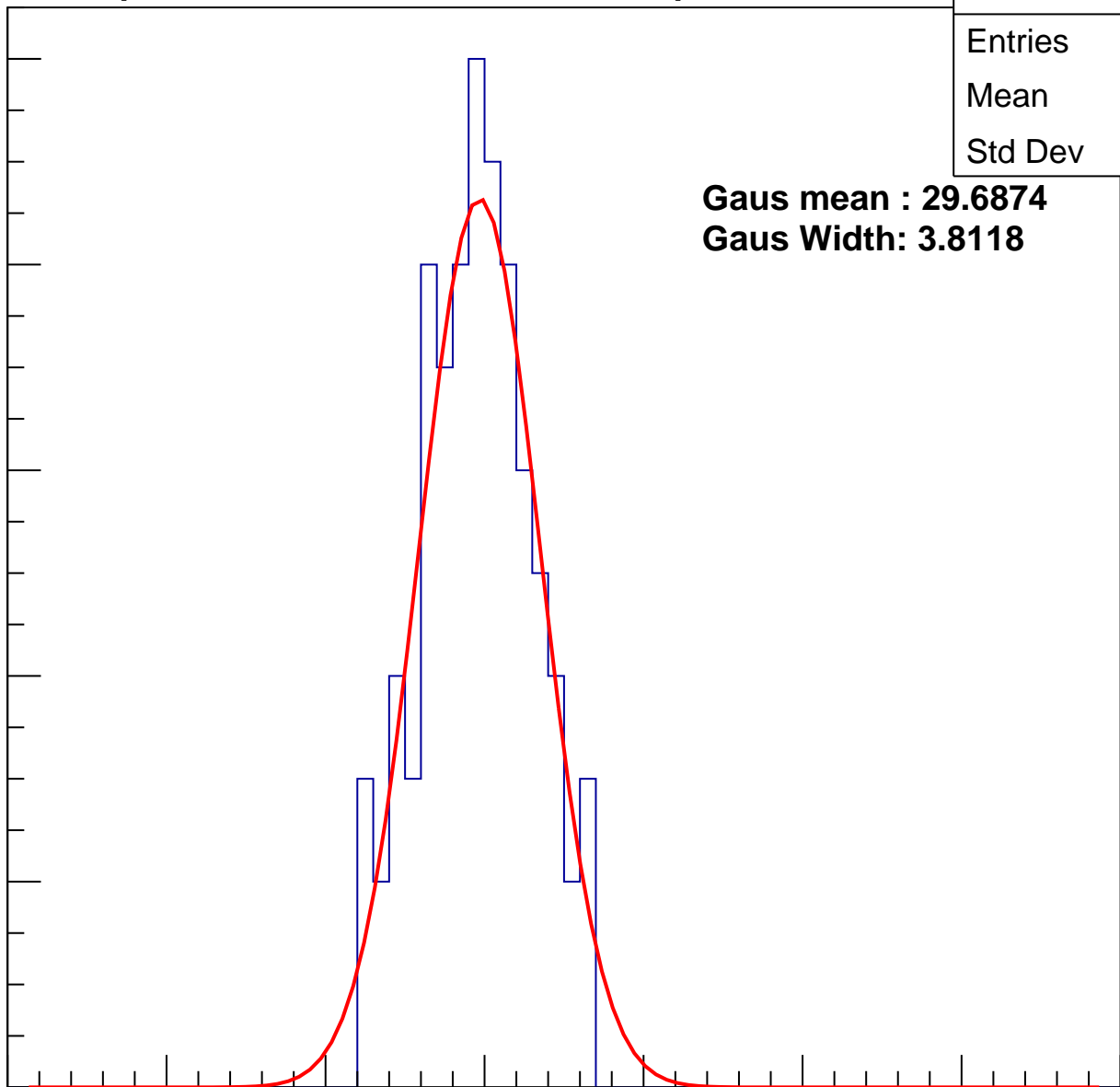
**Gaus Width: 3.8118**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch83, adc1

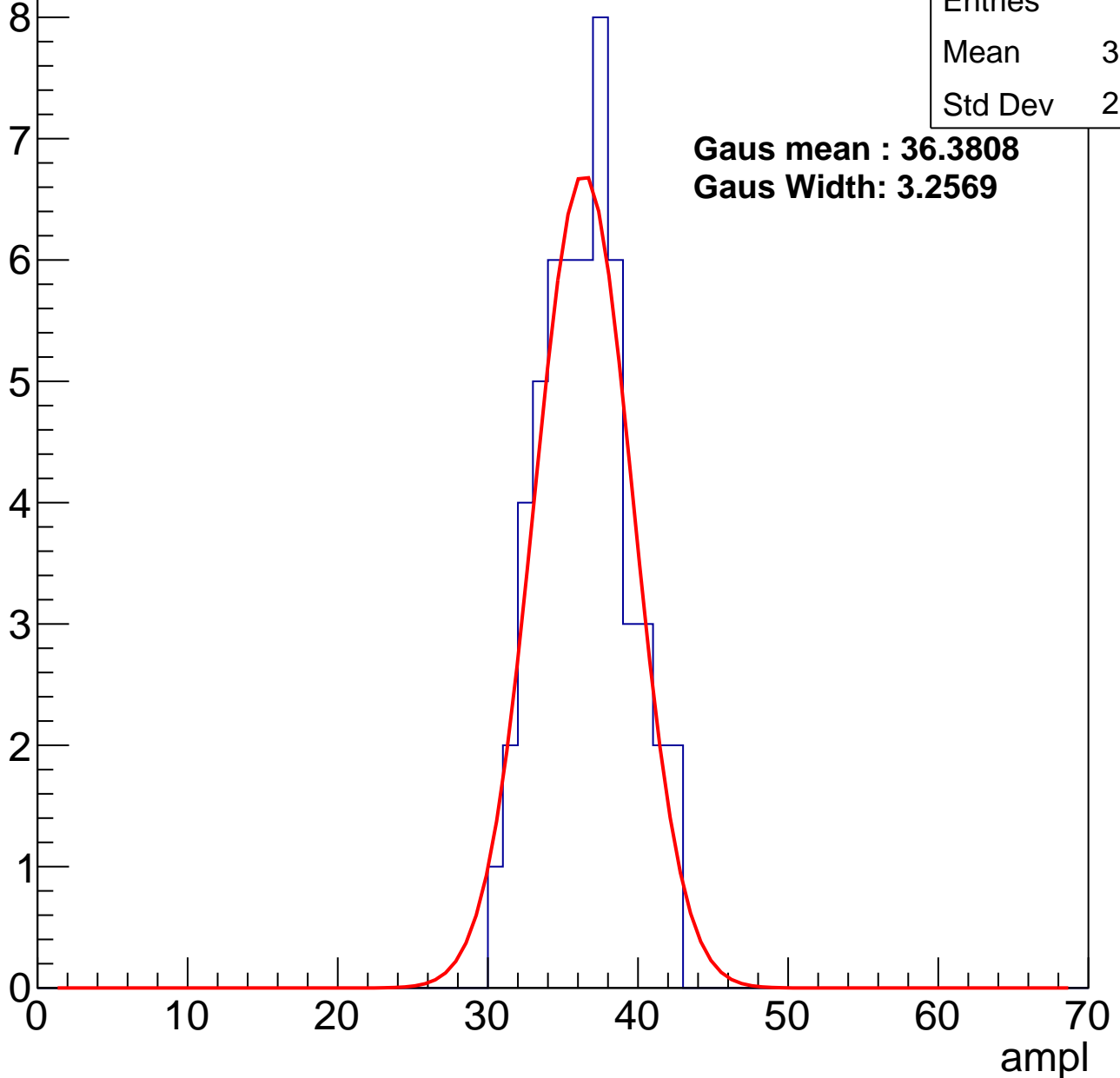
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35.96
Std Dev	2.899

**Gaus mean : 36.3808**

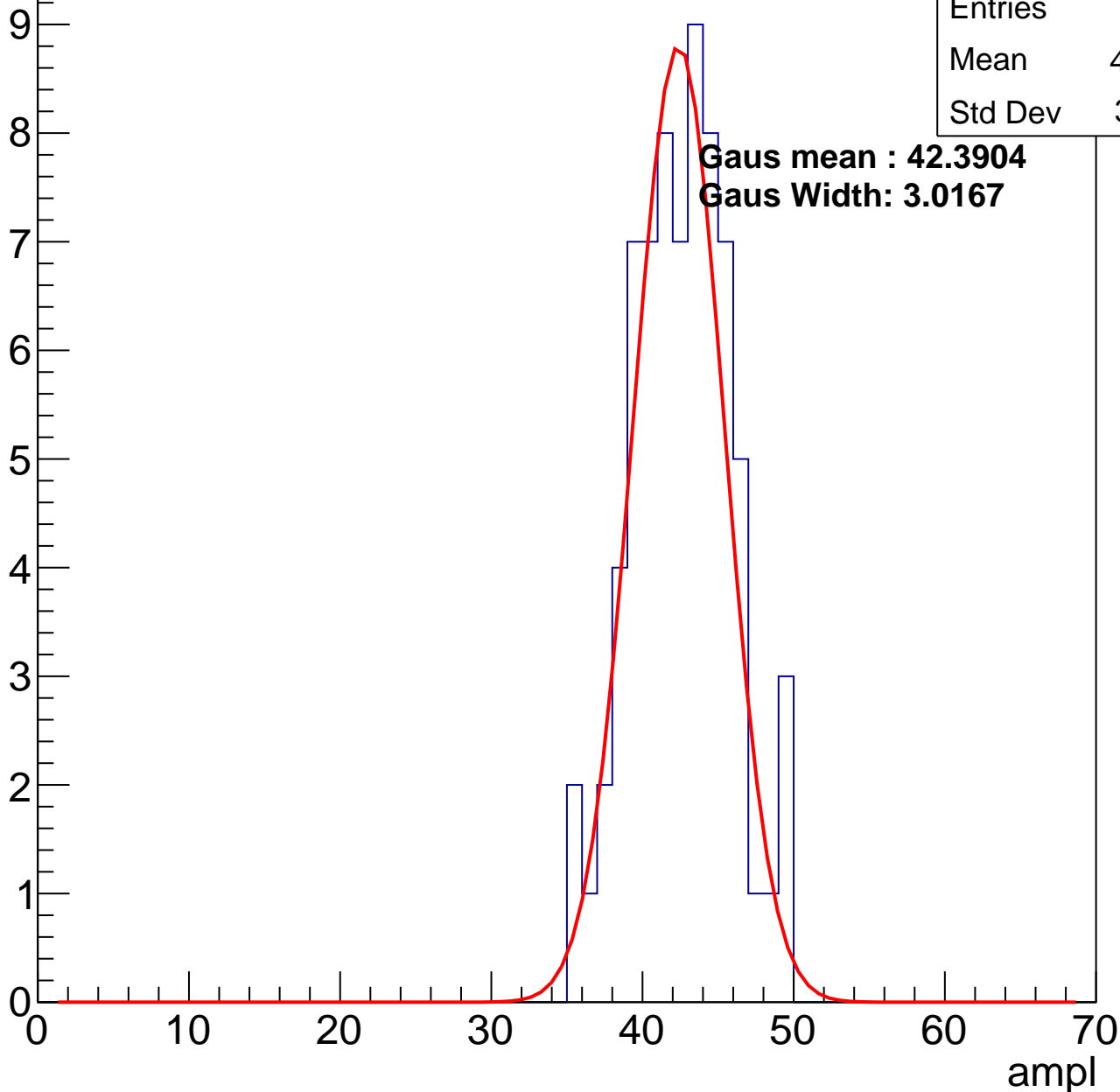
**Gaus Width: 3.2569**



# B1L000S, U24-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

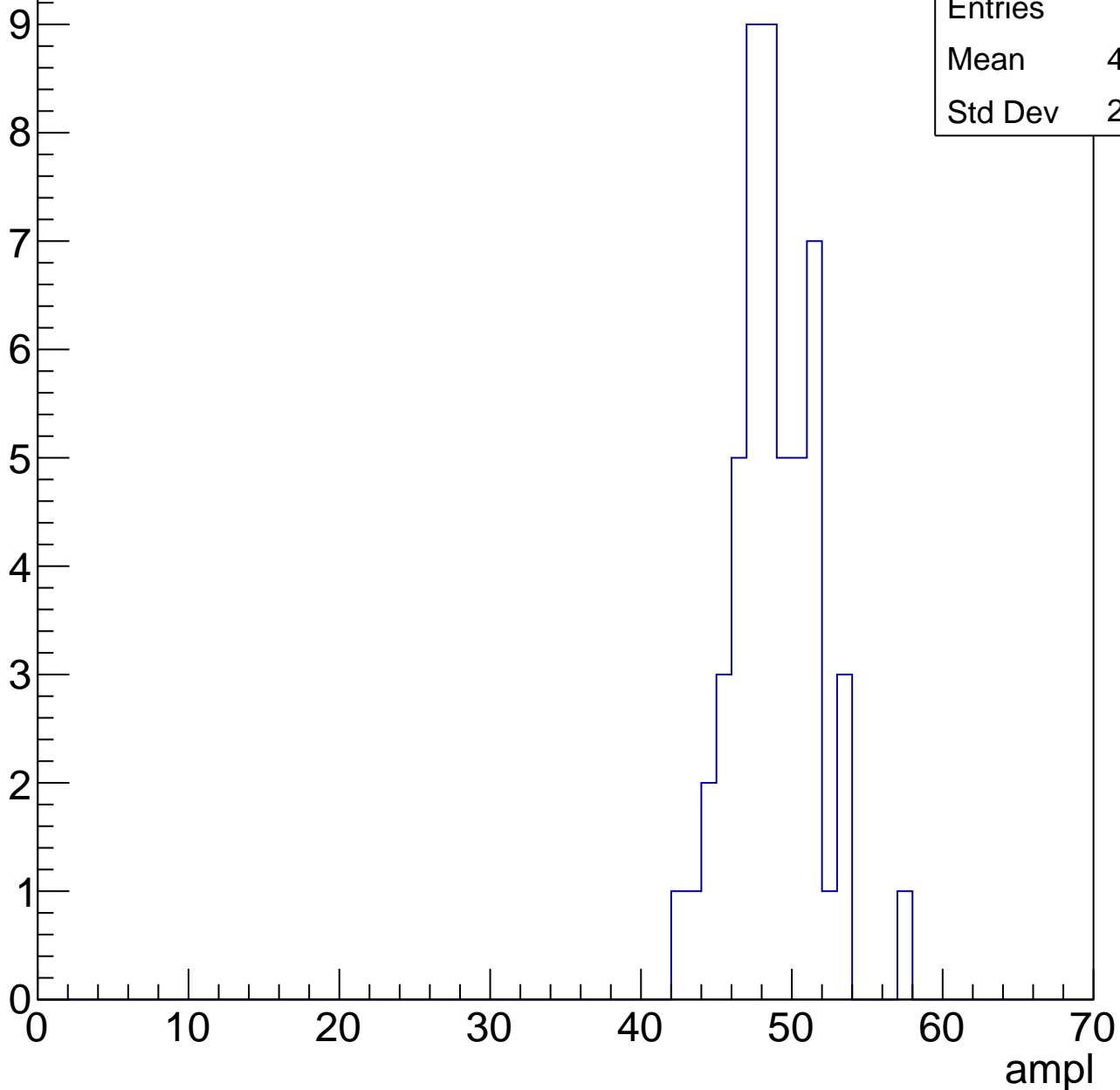


# B1L000S, U24-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	48.33
Std Dev	2.785



# B1L000S, U24-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	54.58
Std Dev	2.818

Entry

10

8

6

4

2

0

0

10

20

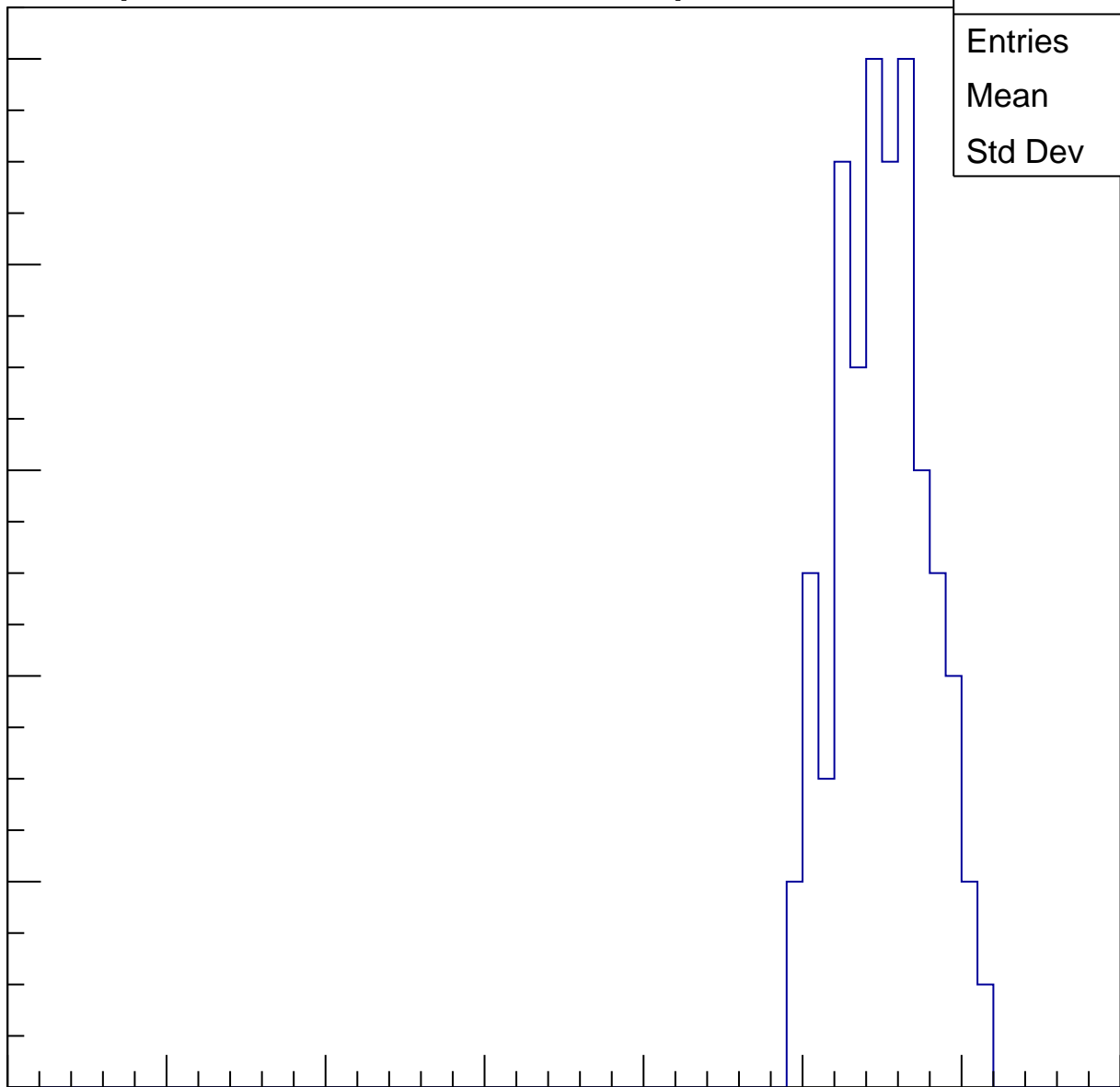
30

40

50

60

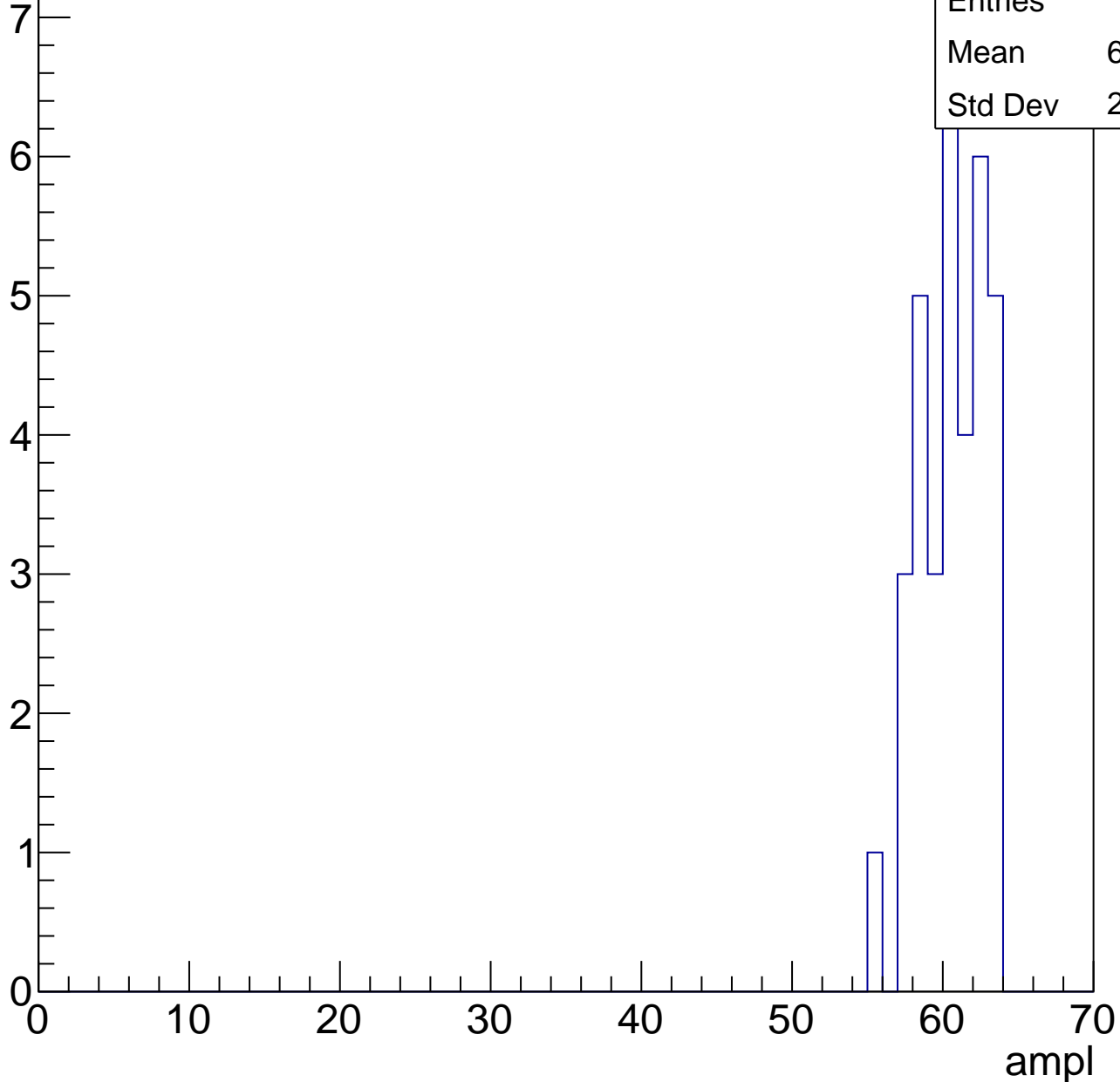
ampl



# B1L000S, U24-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

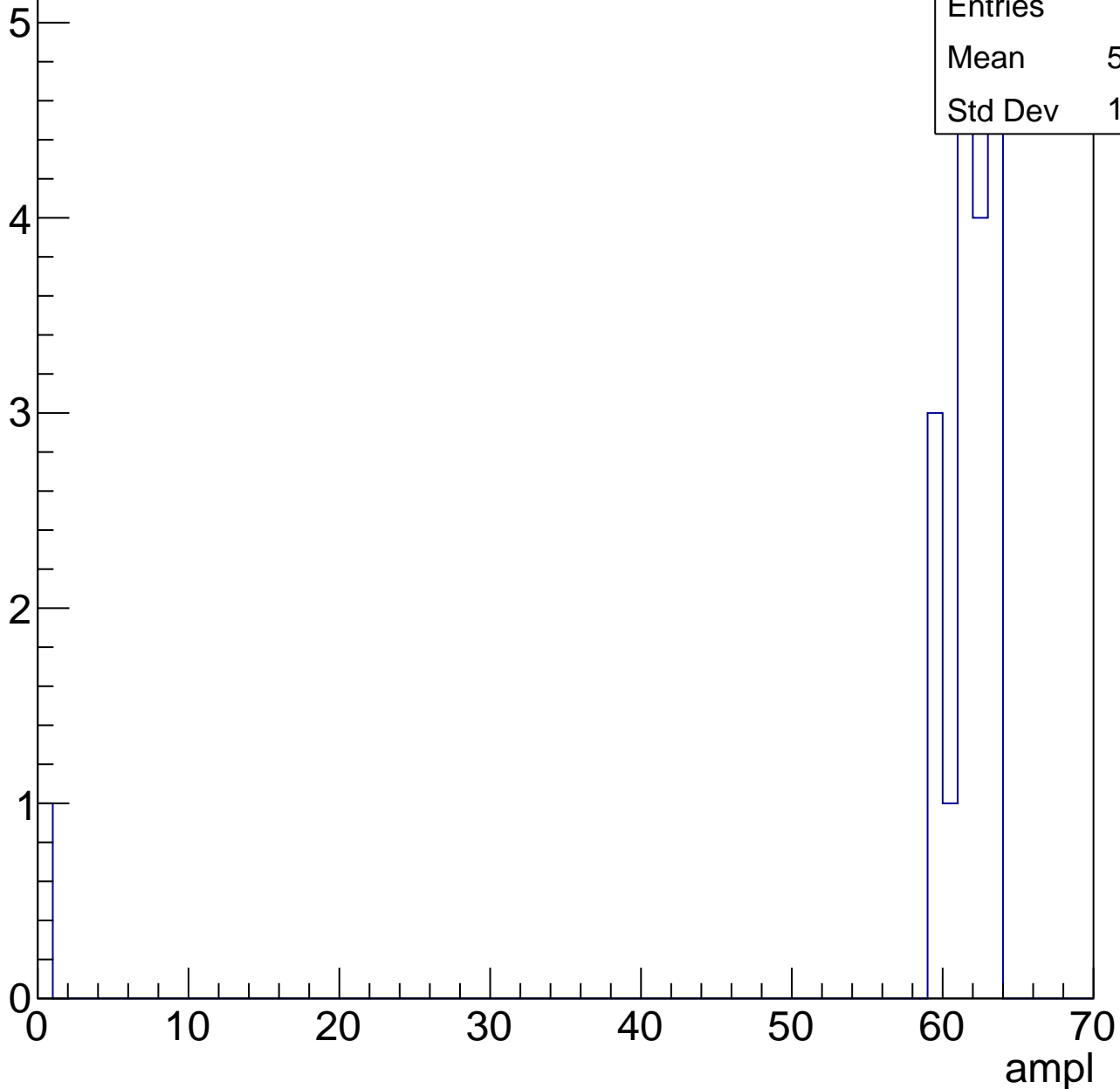


# B1L000S, U24-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.16
Std Dev	13.77

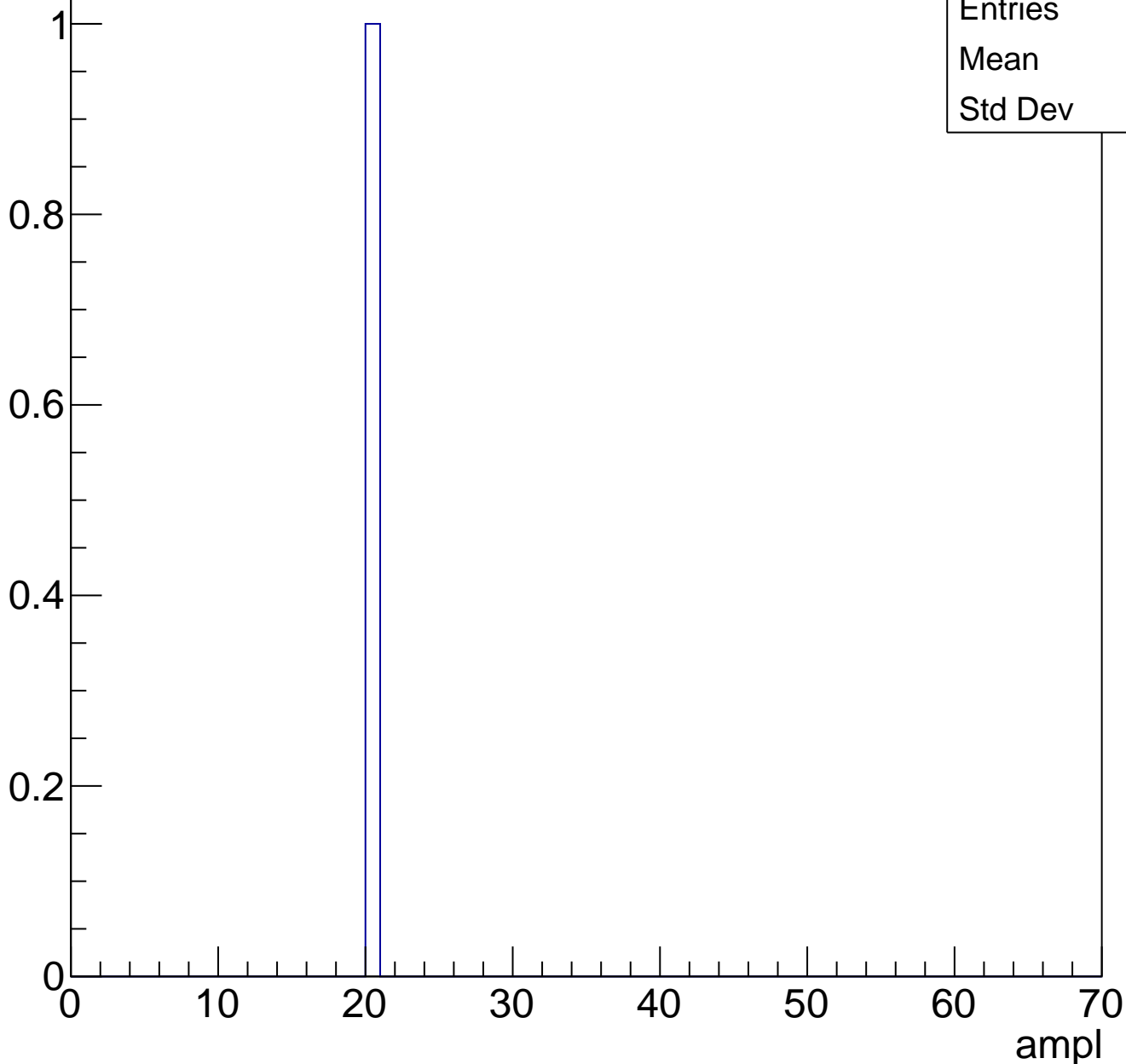




# B1L000S, U24-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch84, adc0

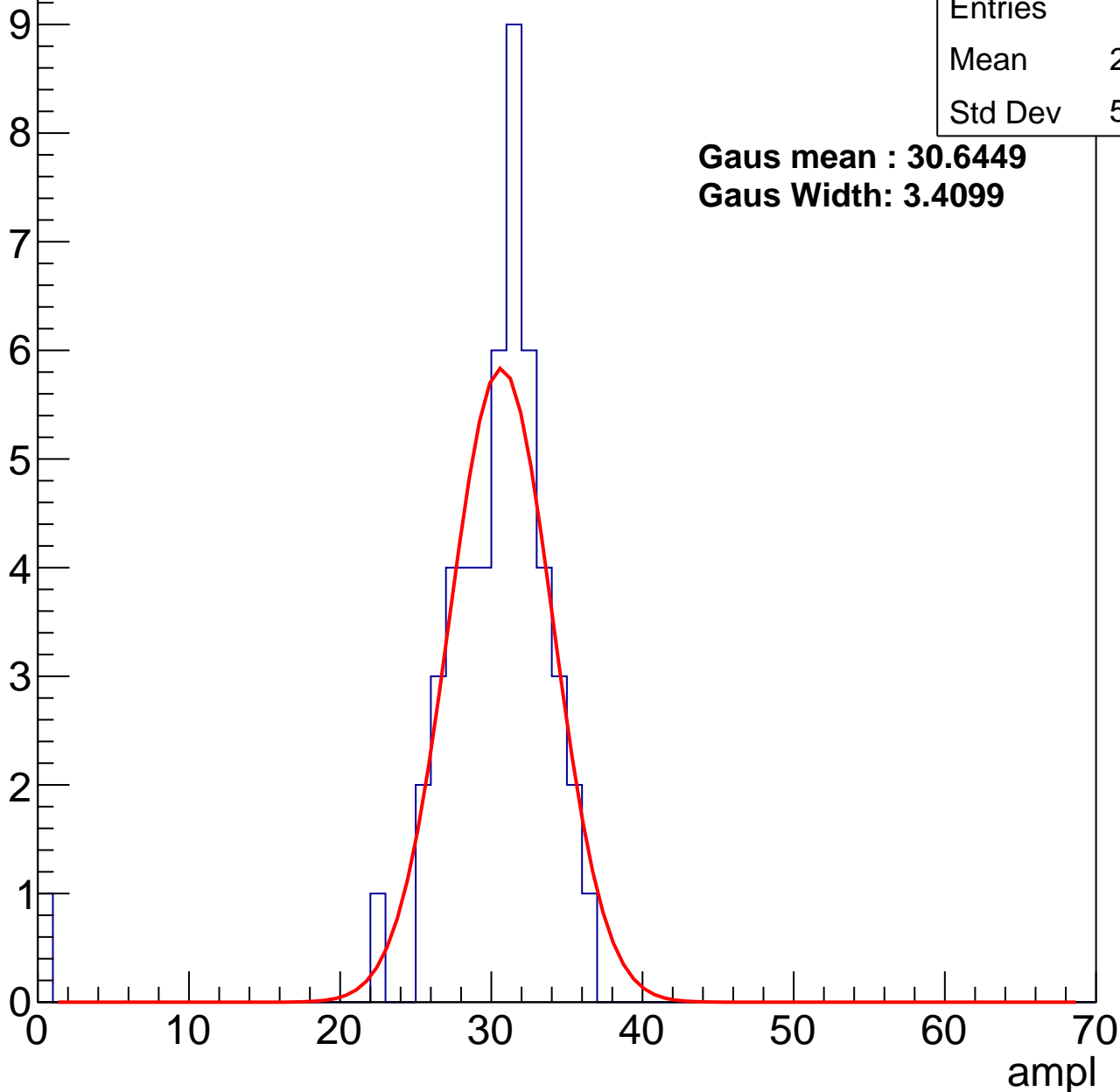
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	29.54
Std Dev	5.119

**Gaus mean : 30.6449**

**Gaus Width: 3.4099**



# B1L000S, U24-ch84, adc1

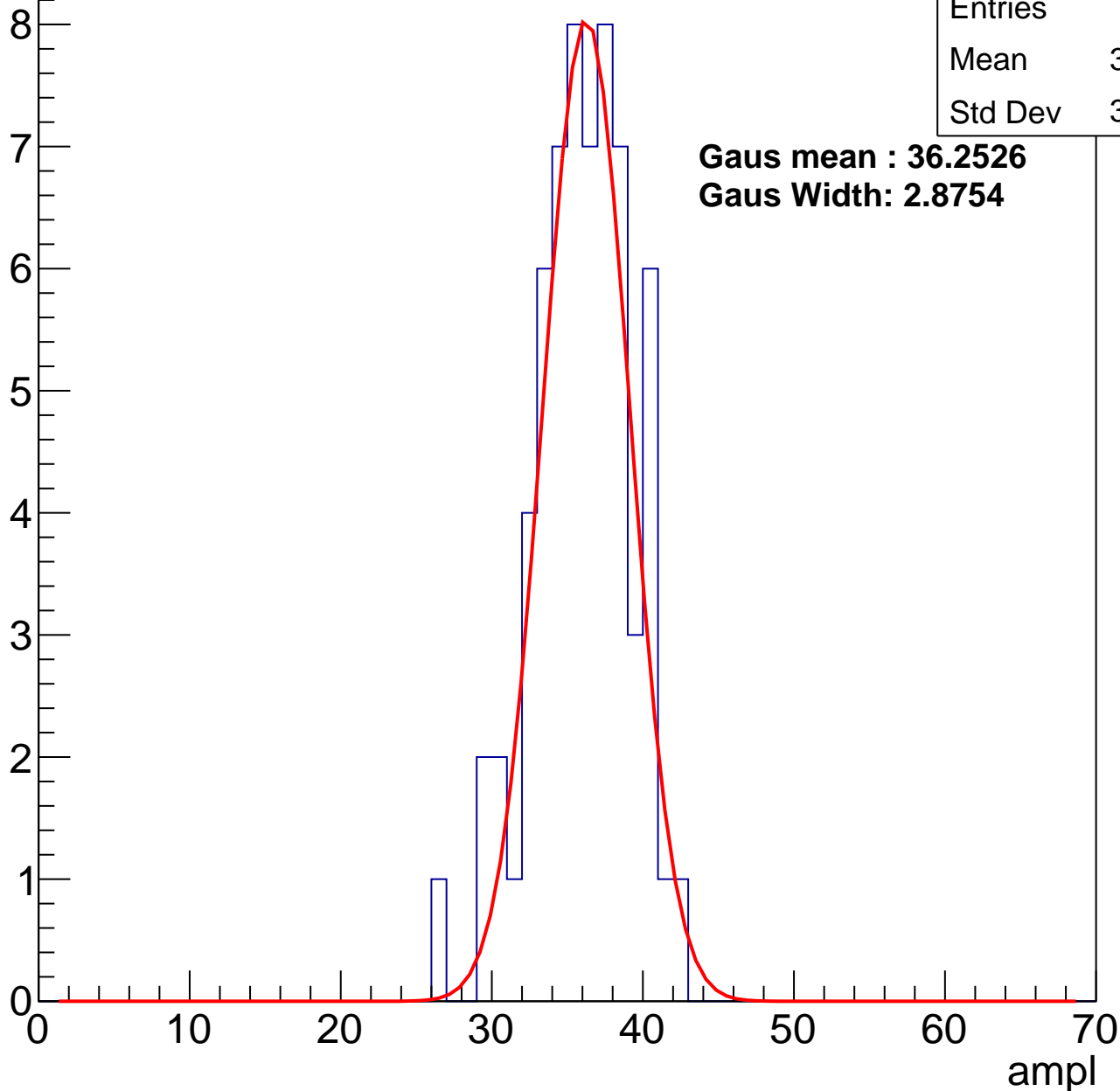
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.52
Std Dev	3.197

**Gaus mean : 36.2526**

**Gaus Width: 2.8754**



# B1L000S, U24-ch84, adc2

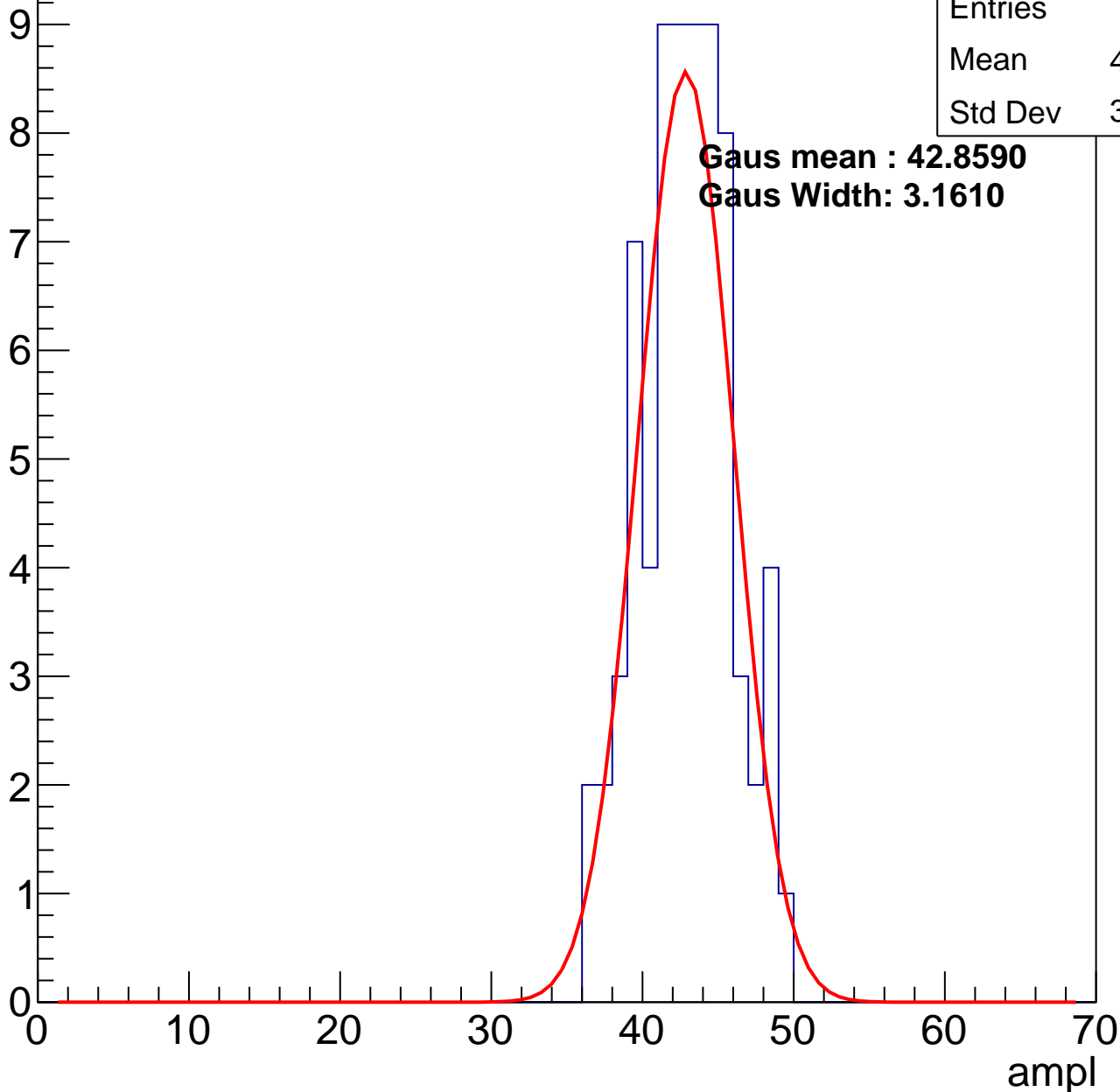
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	42.44
Std Dev	3.009

**Gaus mean : 42.8590**

**Gaus Width: 3.1610**

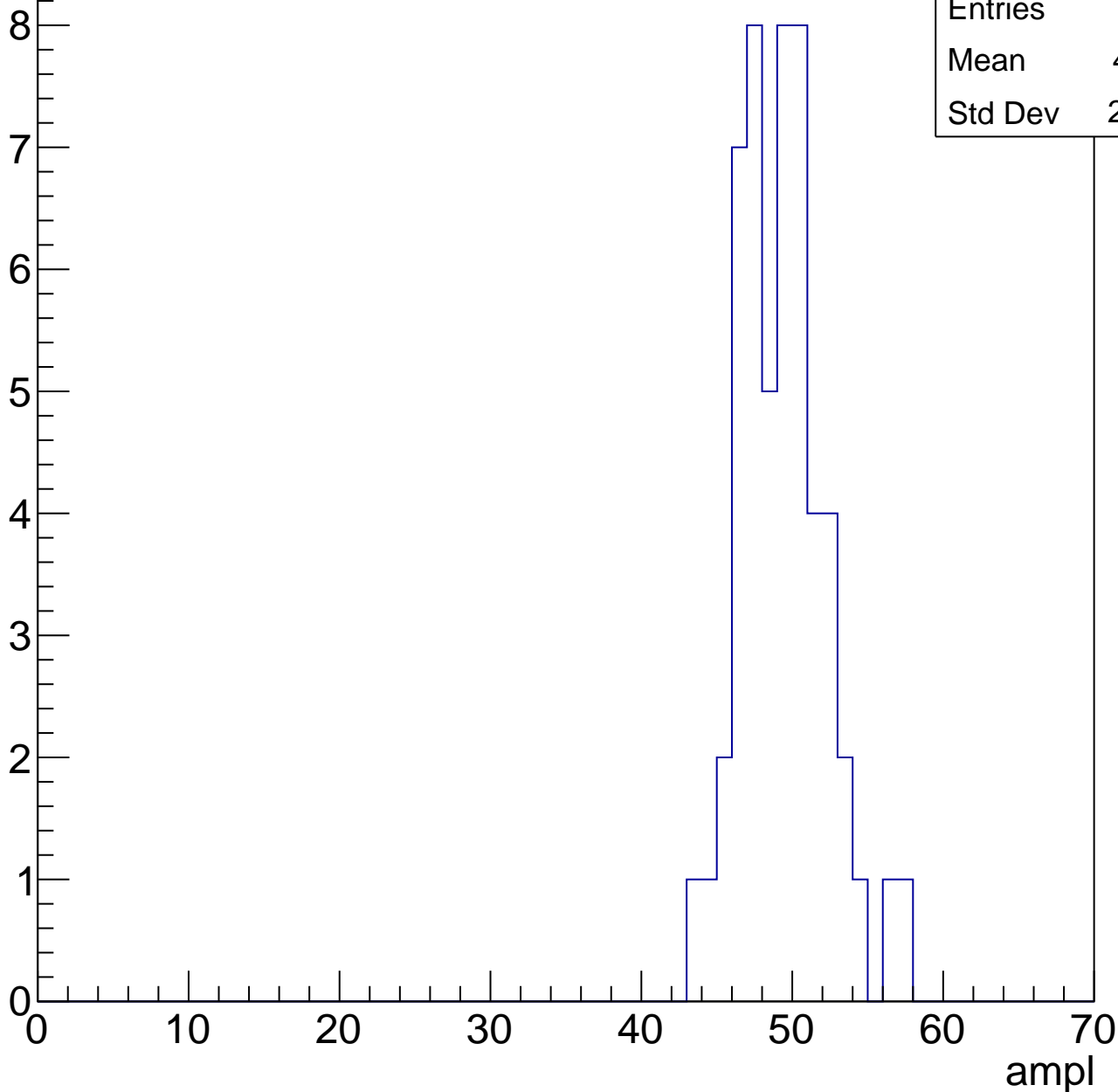


# B1L000S, U24-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

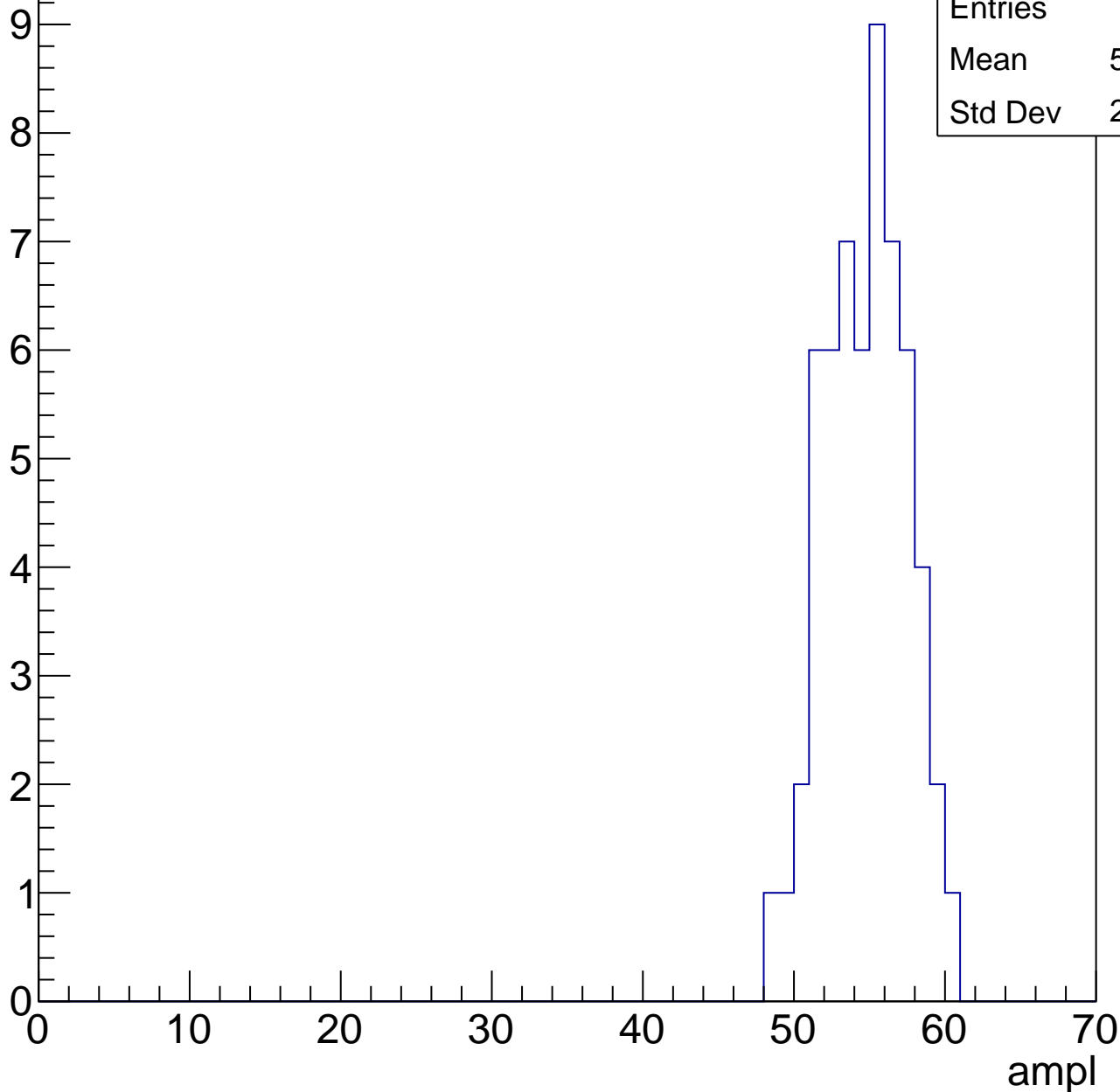
Entries	53
Mean	48.91
Std Dev	2.824



# B1L000S, U24-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



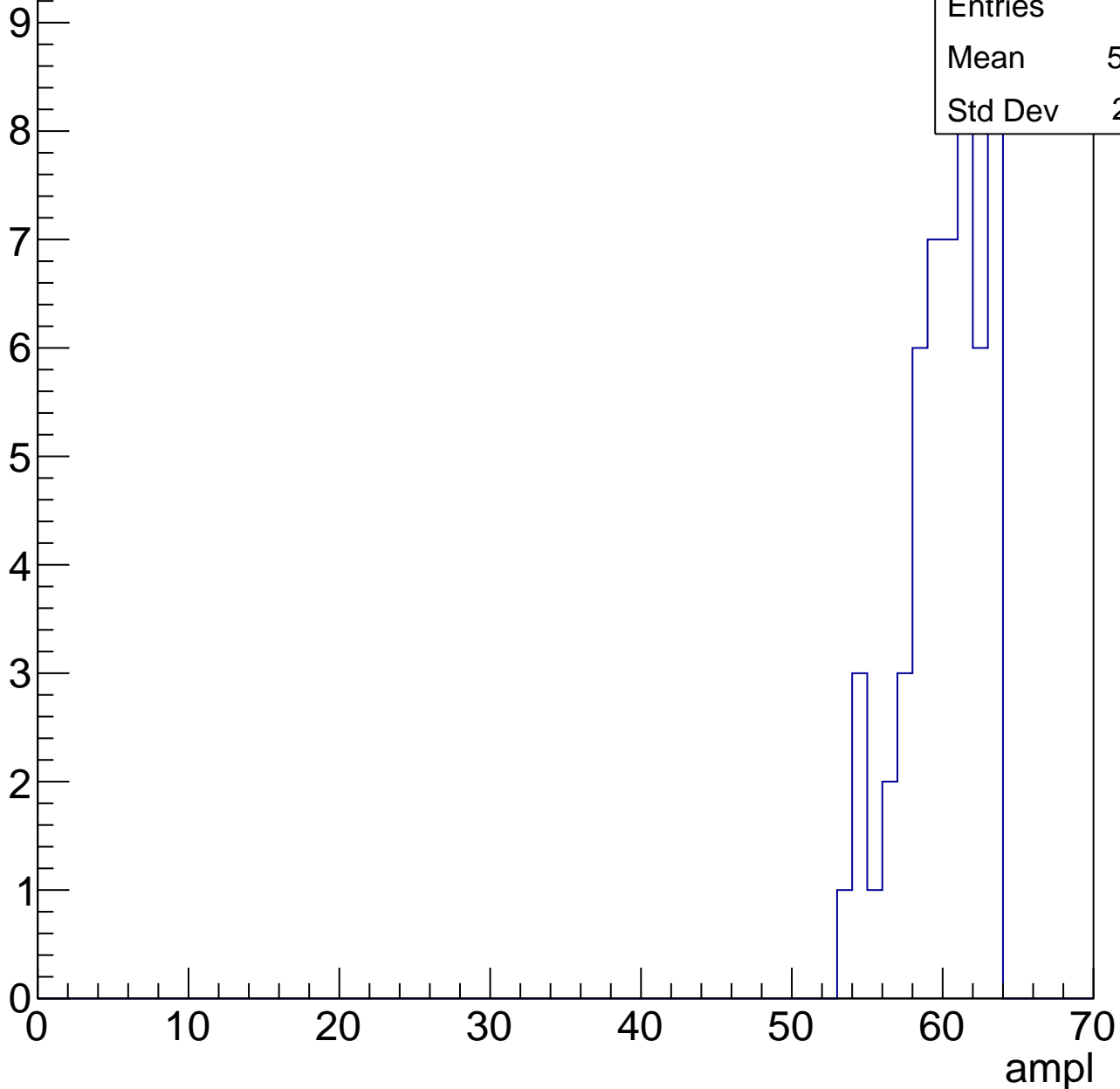
Entries	58
Mean	54.29
Std Dev	2.672

# B1L000S, U24-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

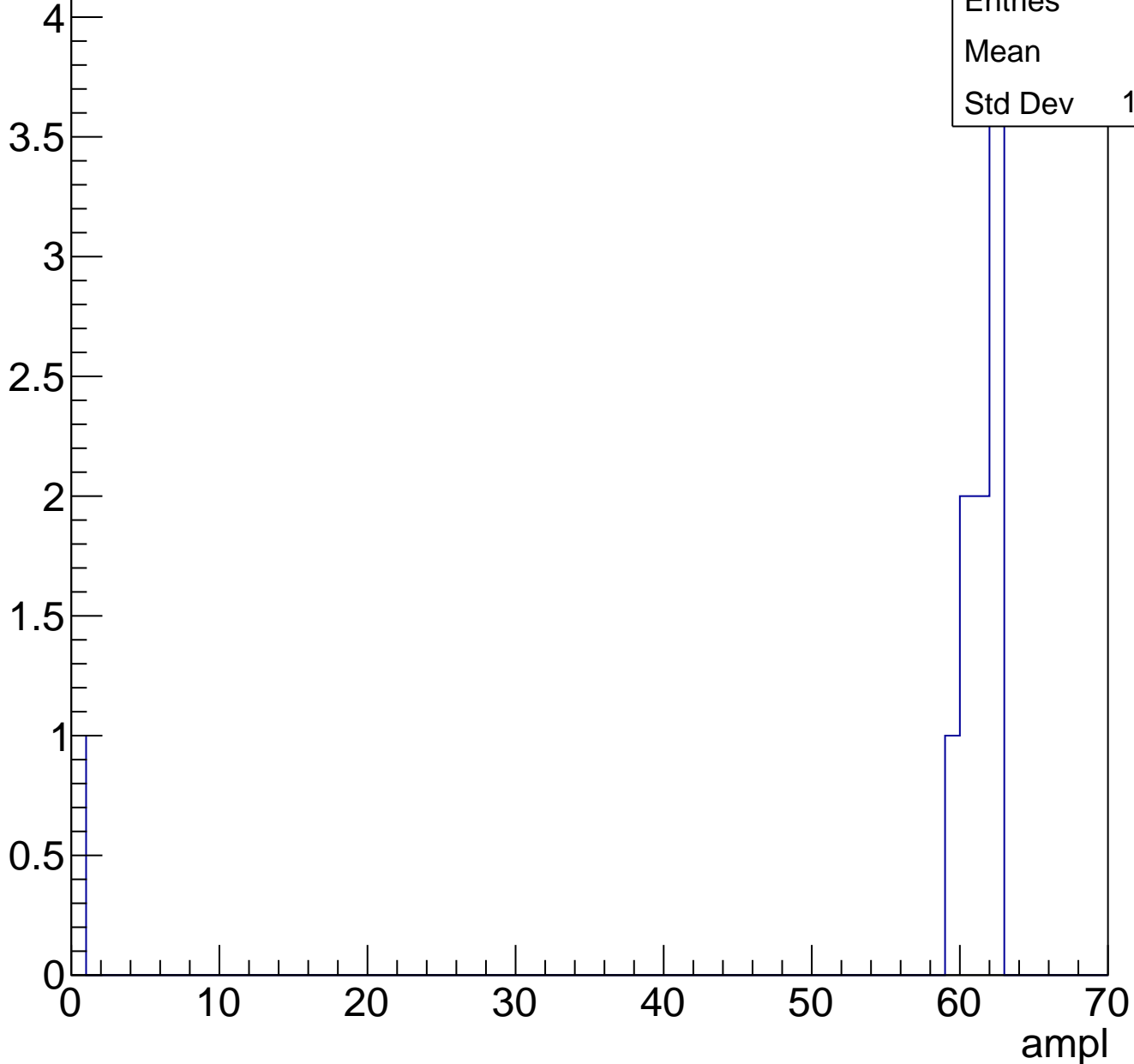
Entries	53
Mean	59.64
Std Dev	2.671



# B1L000S, U24-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch85, adc0

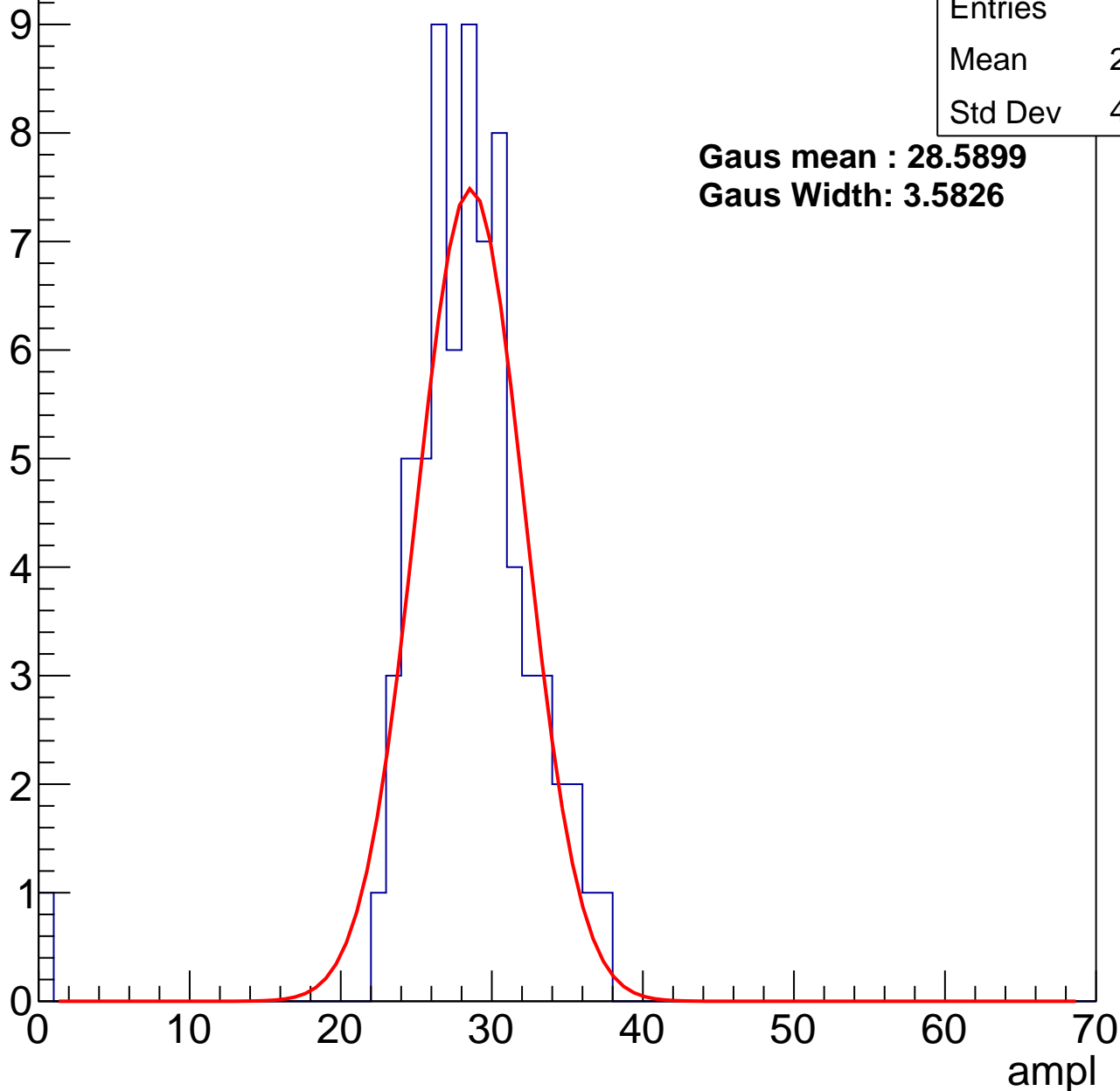
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	27.96
Std Dev	4.743

**Gaus mean : 28.5899**

**Gaus Width: 3.5826**



# B1L000S, U24-ch85, adc1

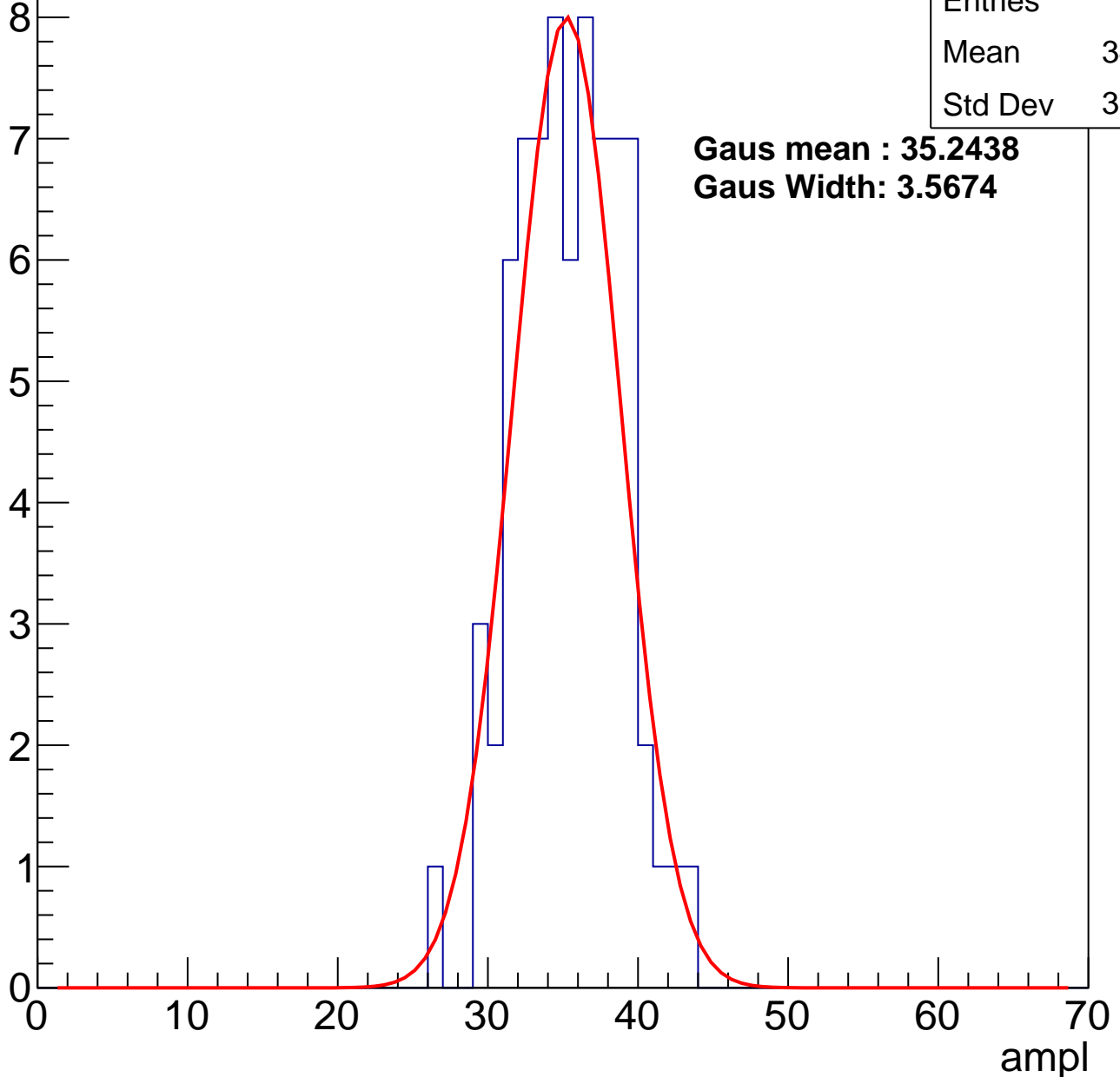
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	34.97
Std Dev	3.377

**Gaus mean : 35.2438**

**Gaus Width: 3.5674**



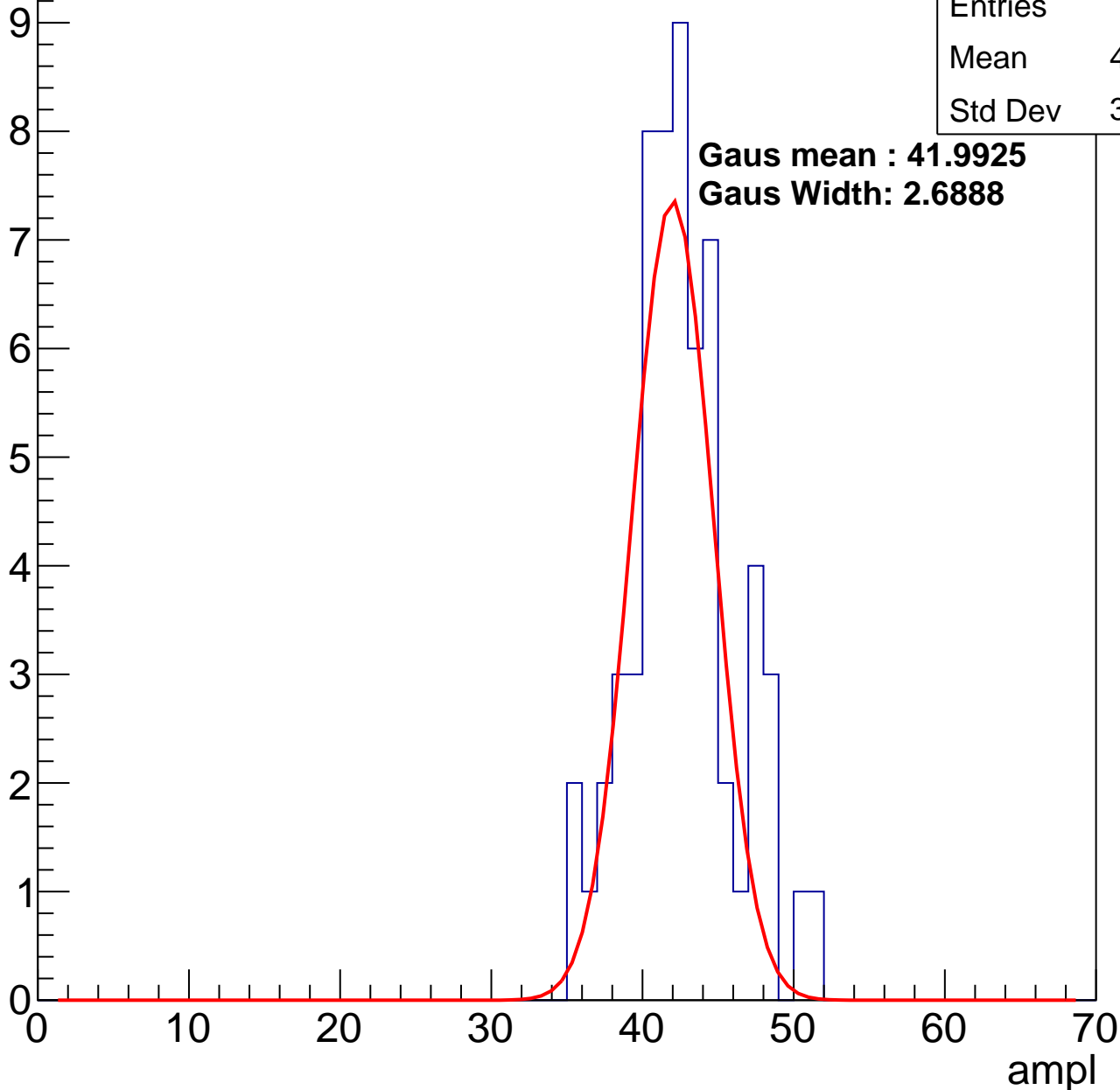
# B1L000S, U24-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.16
Std Dev	3.432

**Gaus mean : 41.9925**  
**Gaus Width: 2.6888**

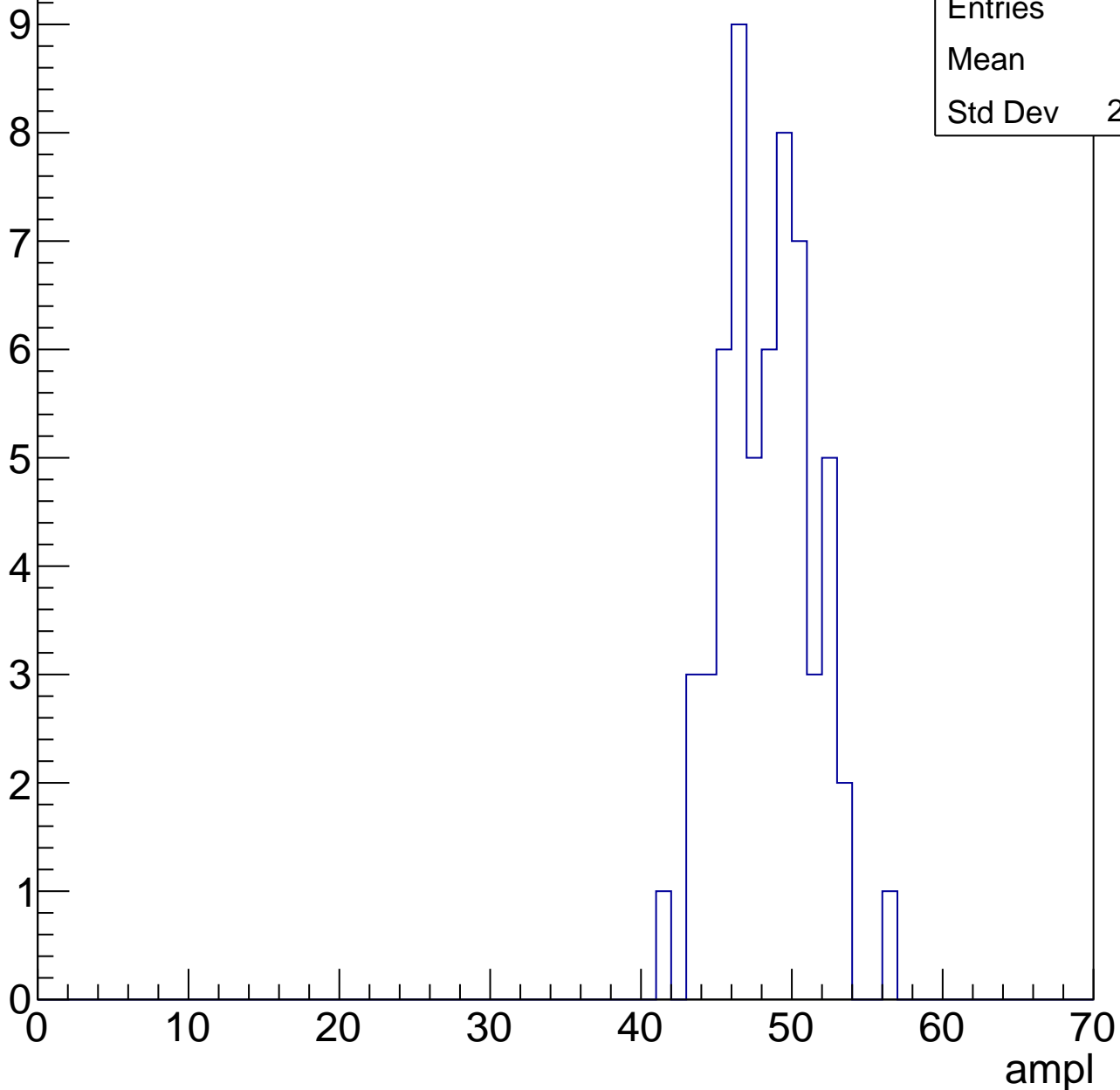


# B1L000S, U24-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.9
Std Dev	2.978



# B1L000S, U24-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

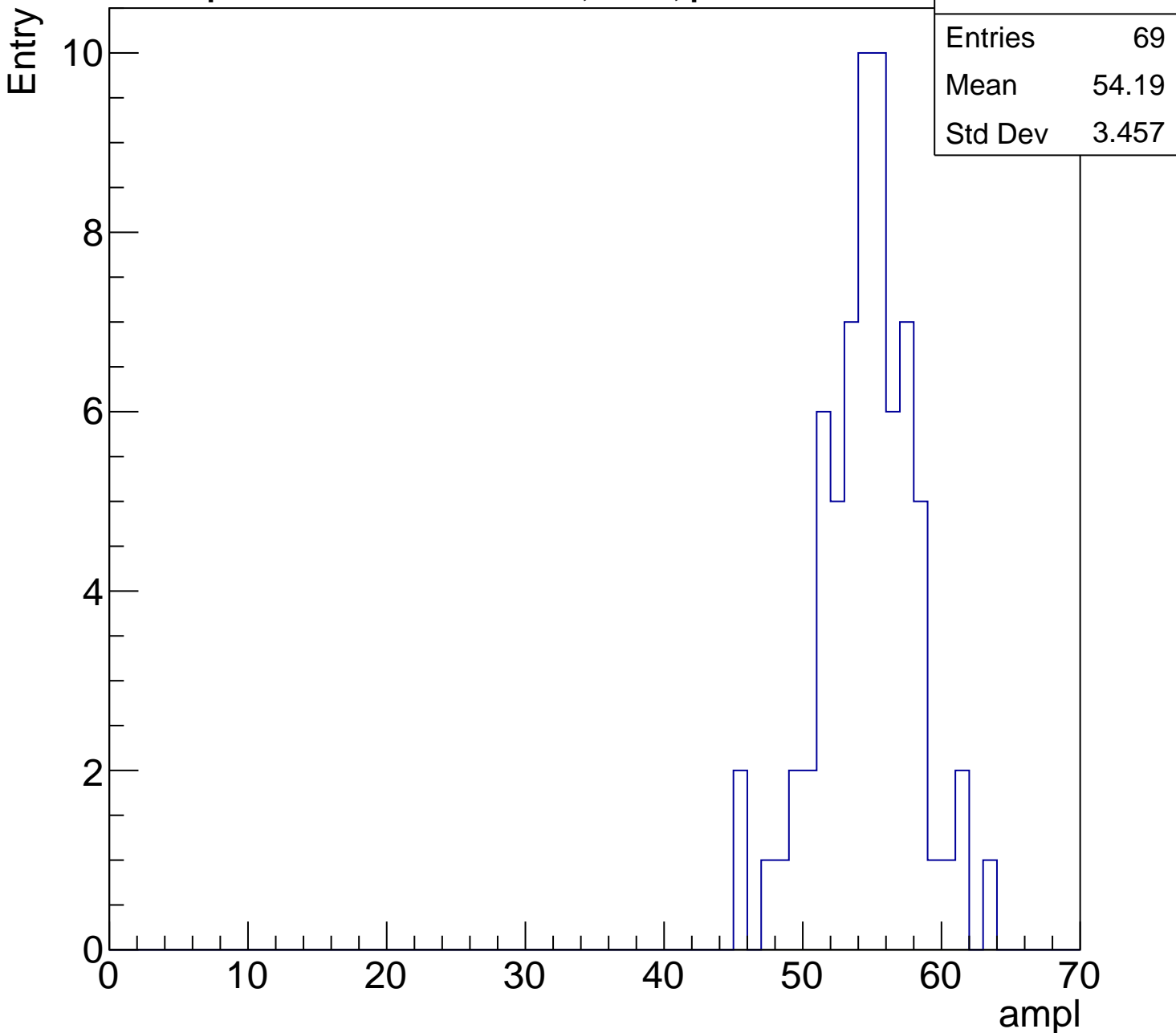
Entries	69
Mean	54.19
Std Dev	3.457

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

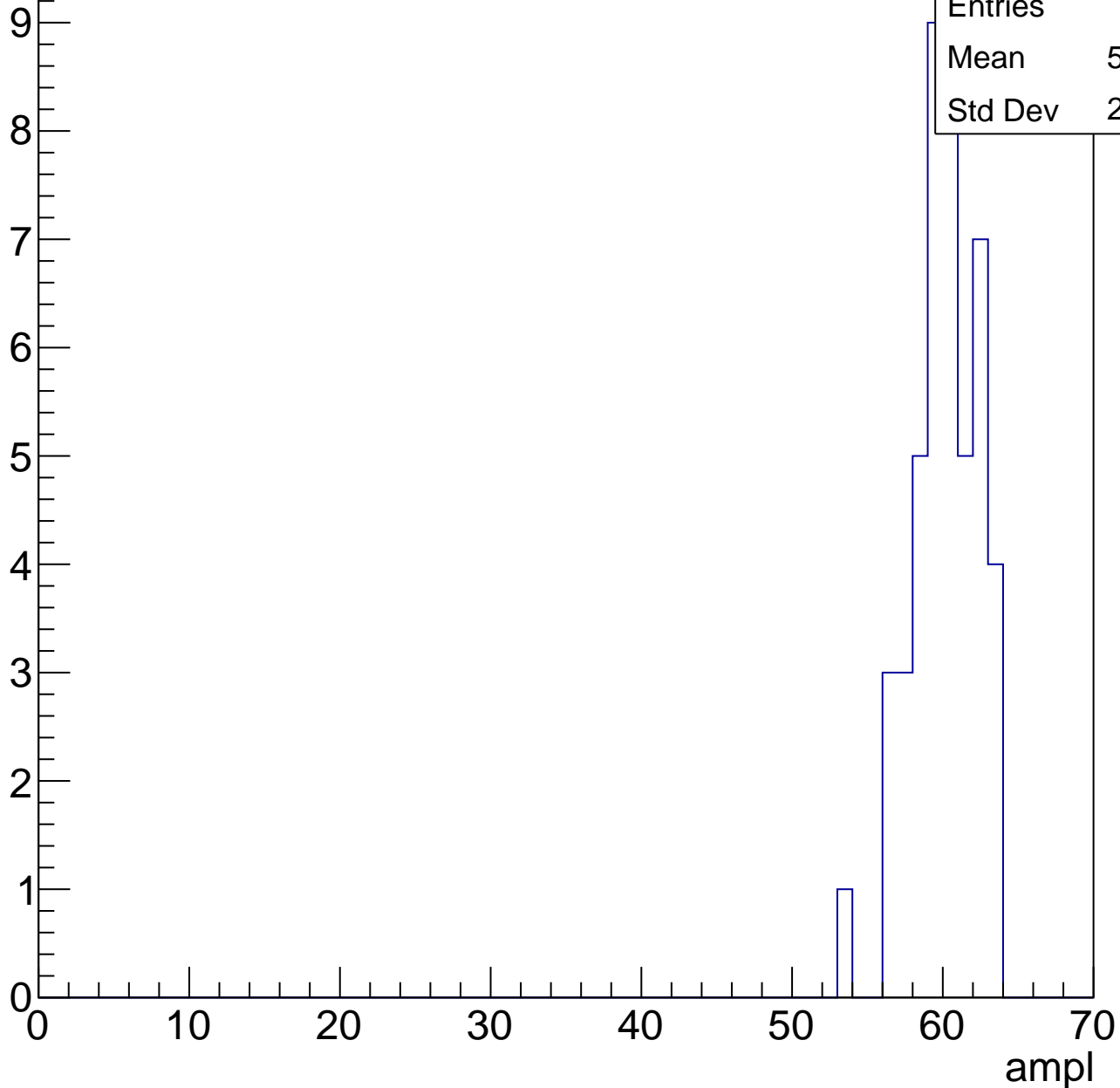
ampl



# B1L000S, U24-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

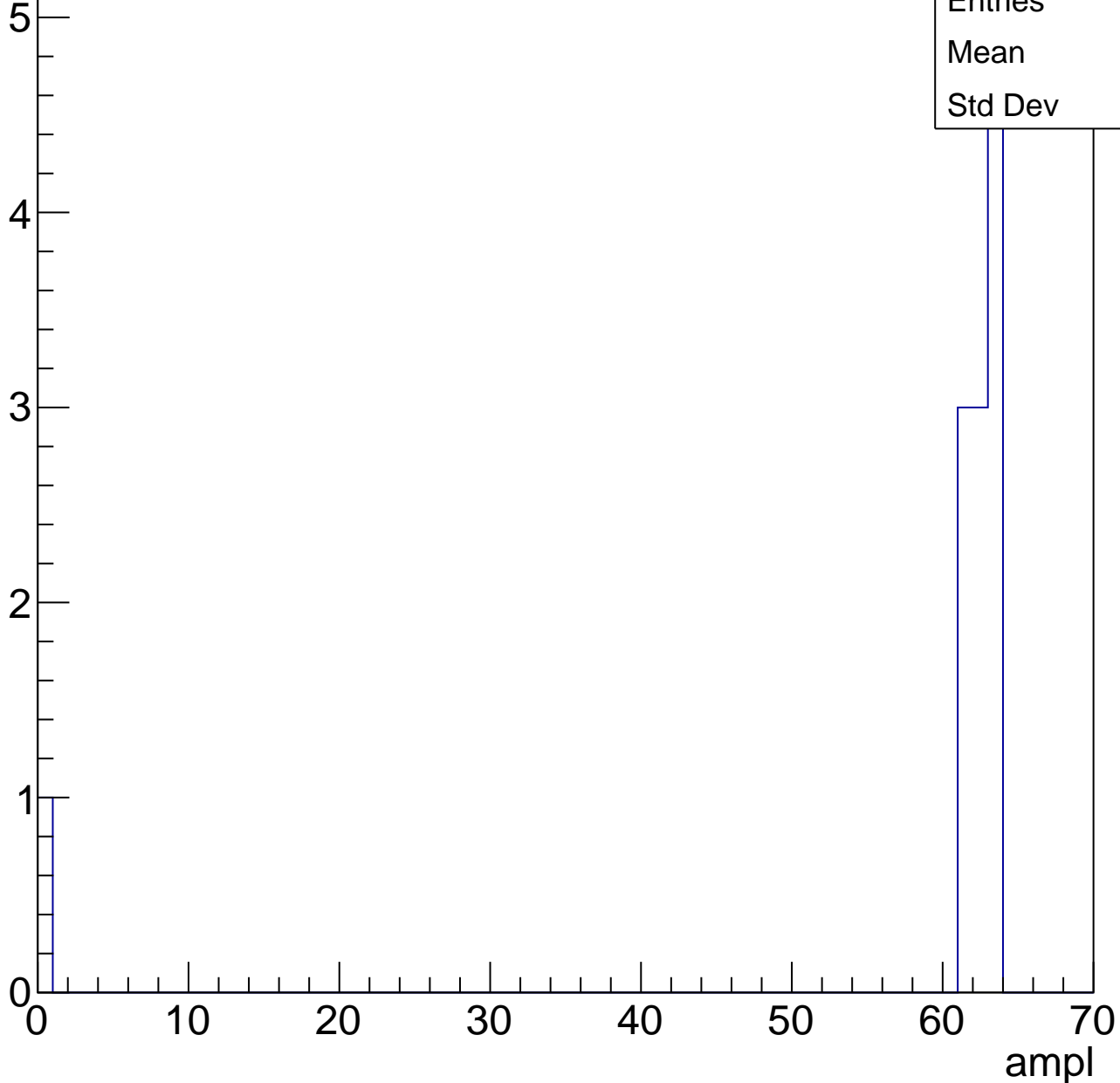


# B1L000S, U24-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	57
Std Dev	17.2

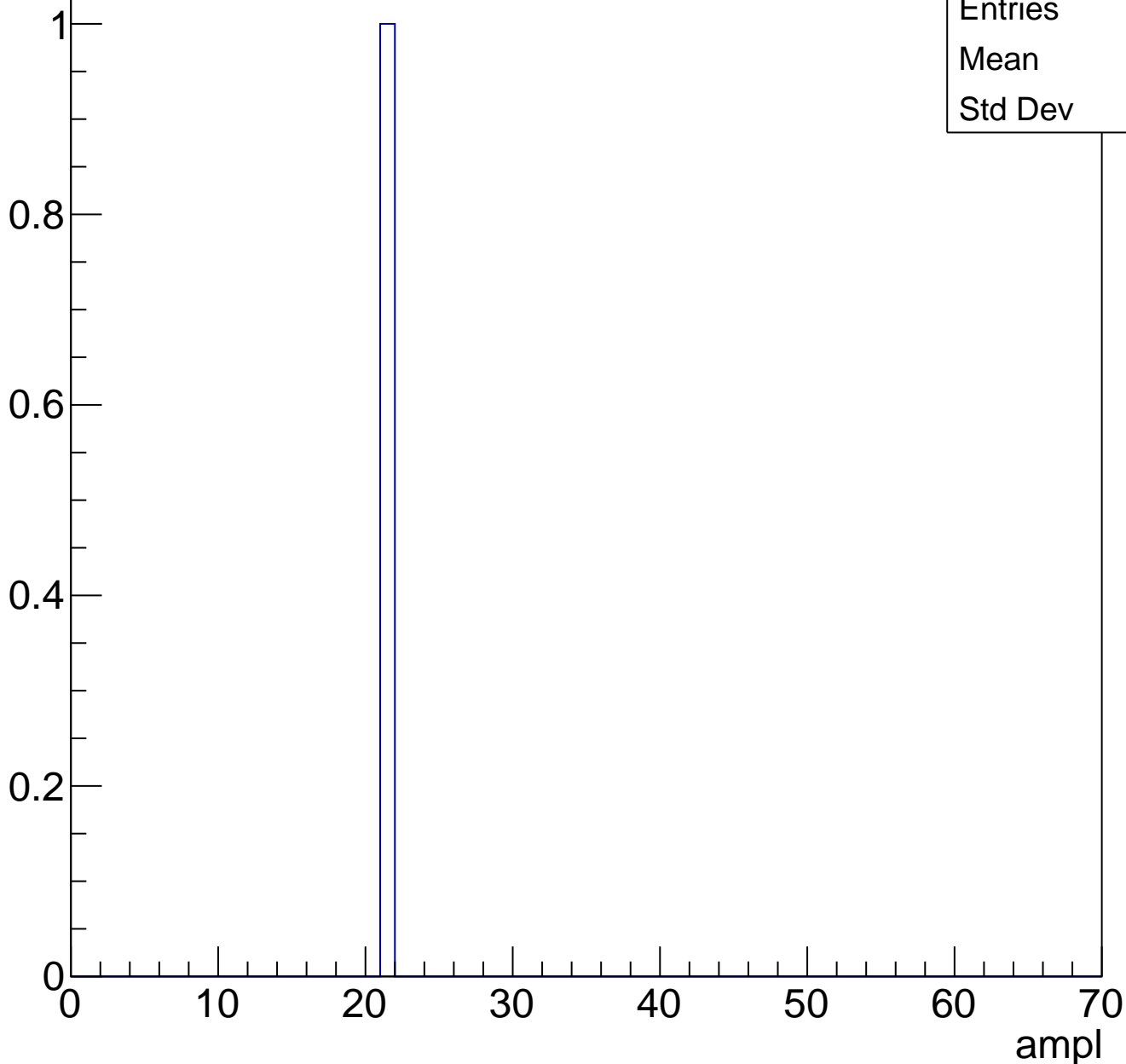




# B1L000S, U24-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch86, adc0

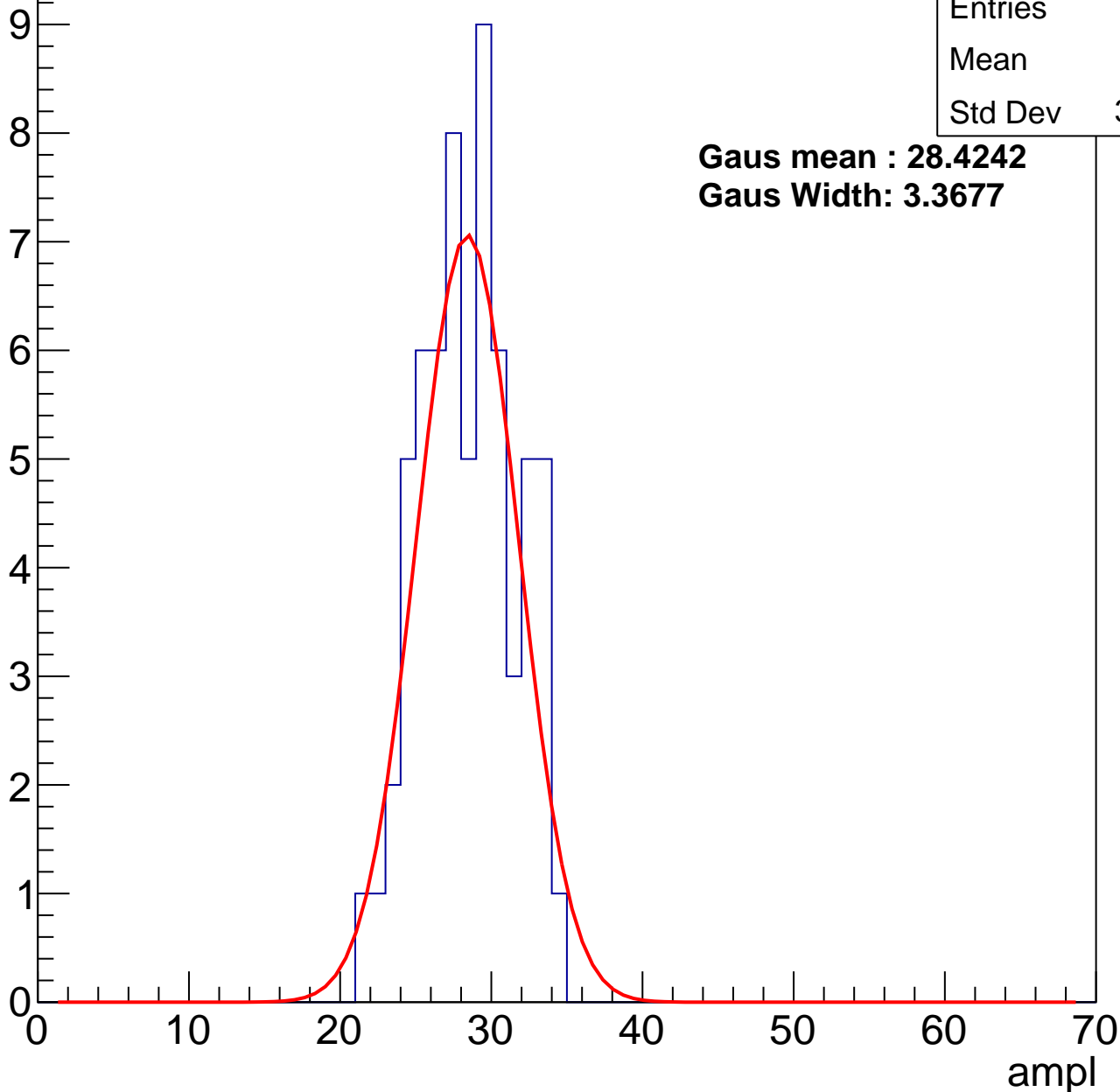
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28
Std Dev	3.091

**Gaus mean : 28.4242**

**Gaus Width: 3.3677**



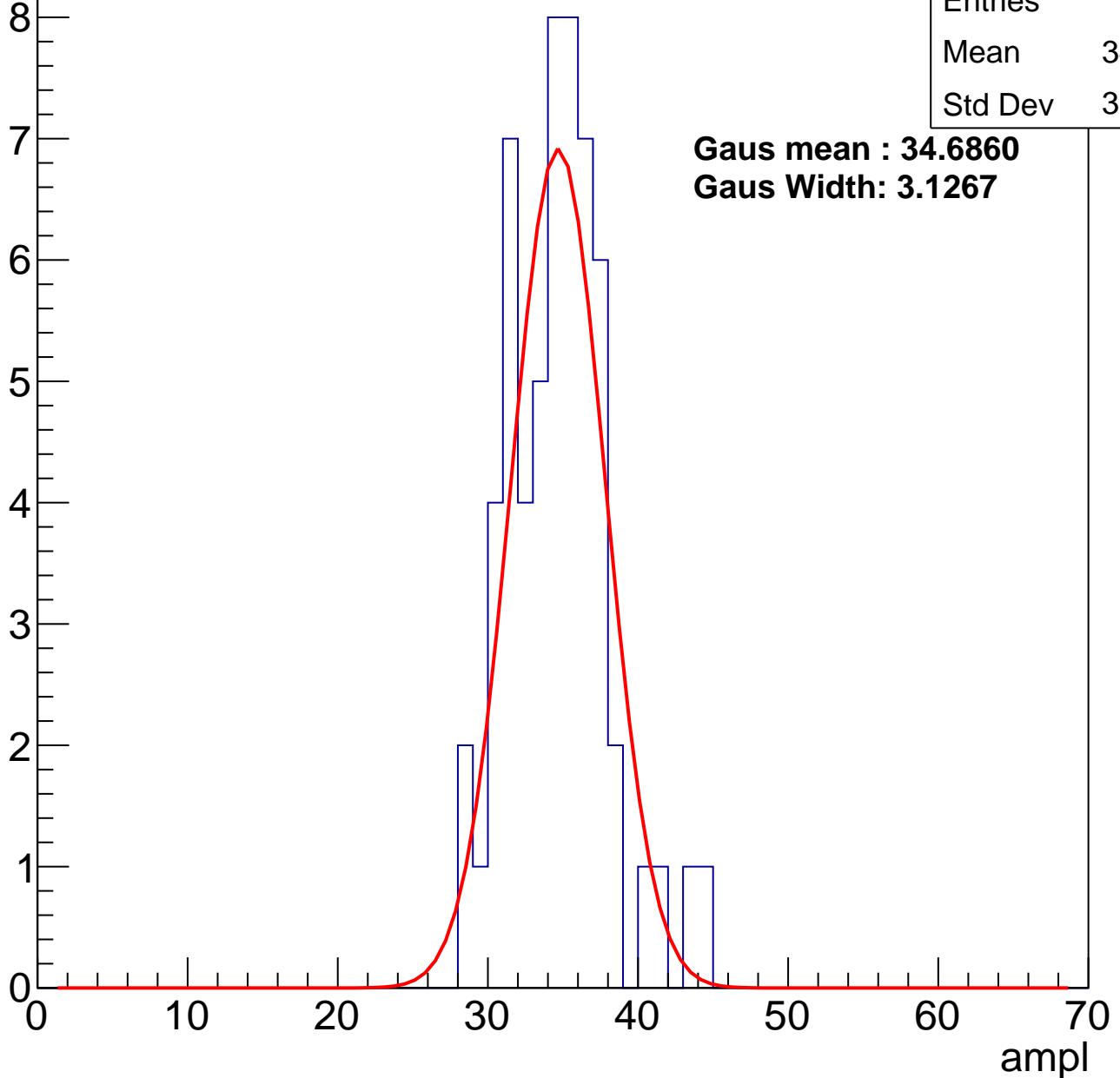
# B1L000S, U24-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.22
Std Dev	3.296

**Gaus mean : 34.6860**  
**Gaus Width: 3.1267**



# B1L000S, U24-ch86, adc2

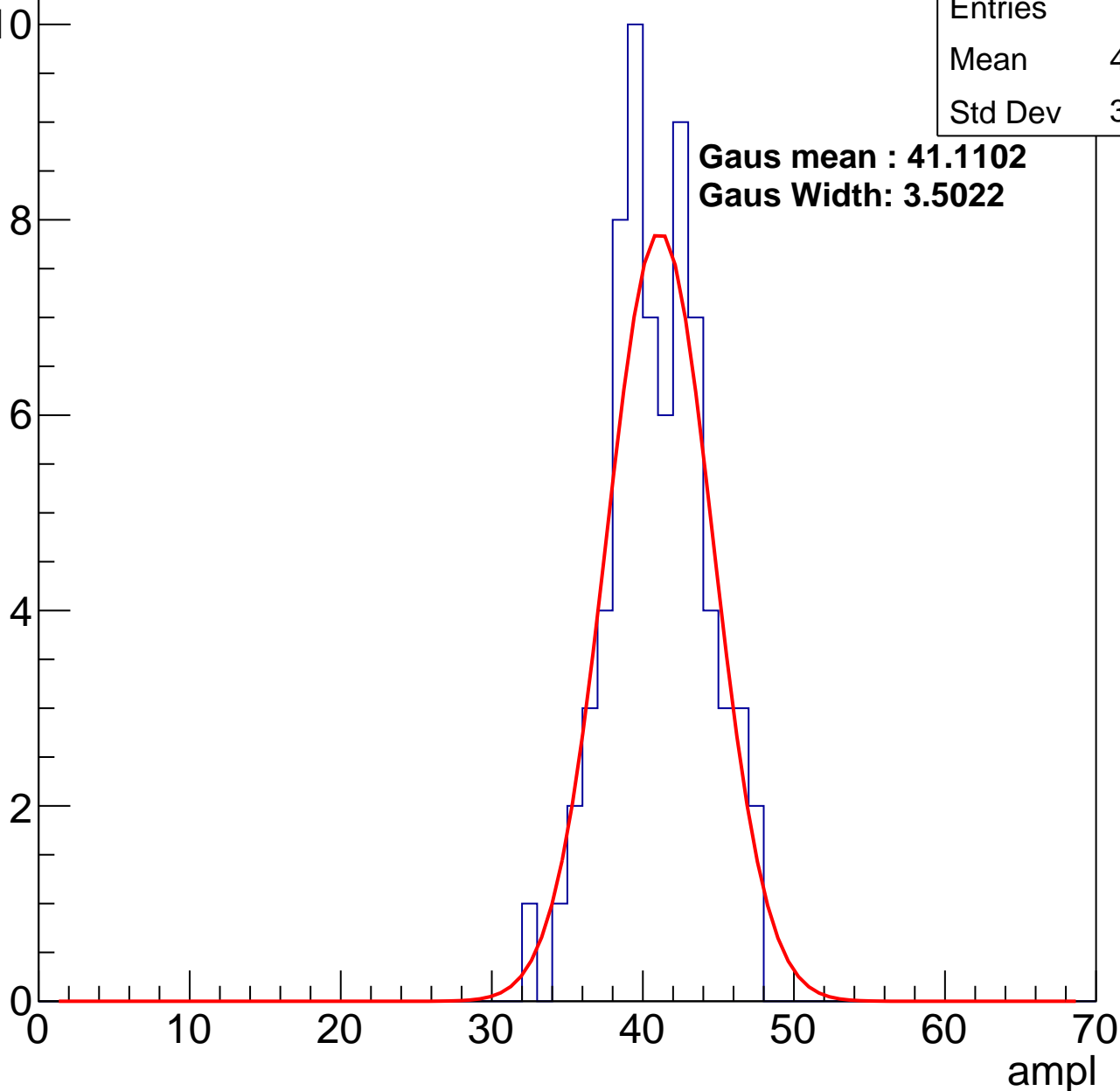
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	40.49
Std Dev	3.184

**Gaus mean : 41.1102**

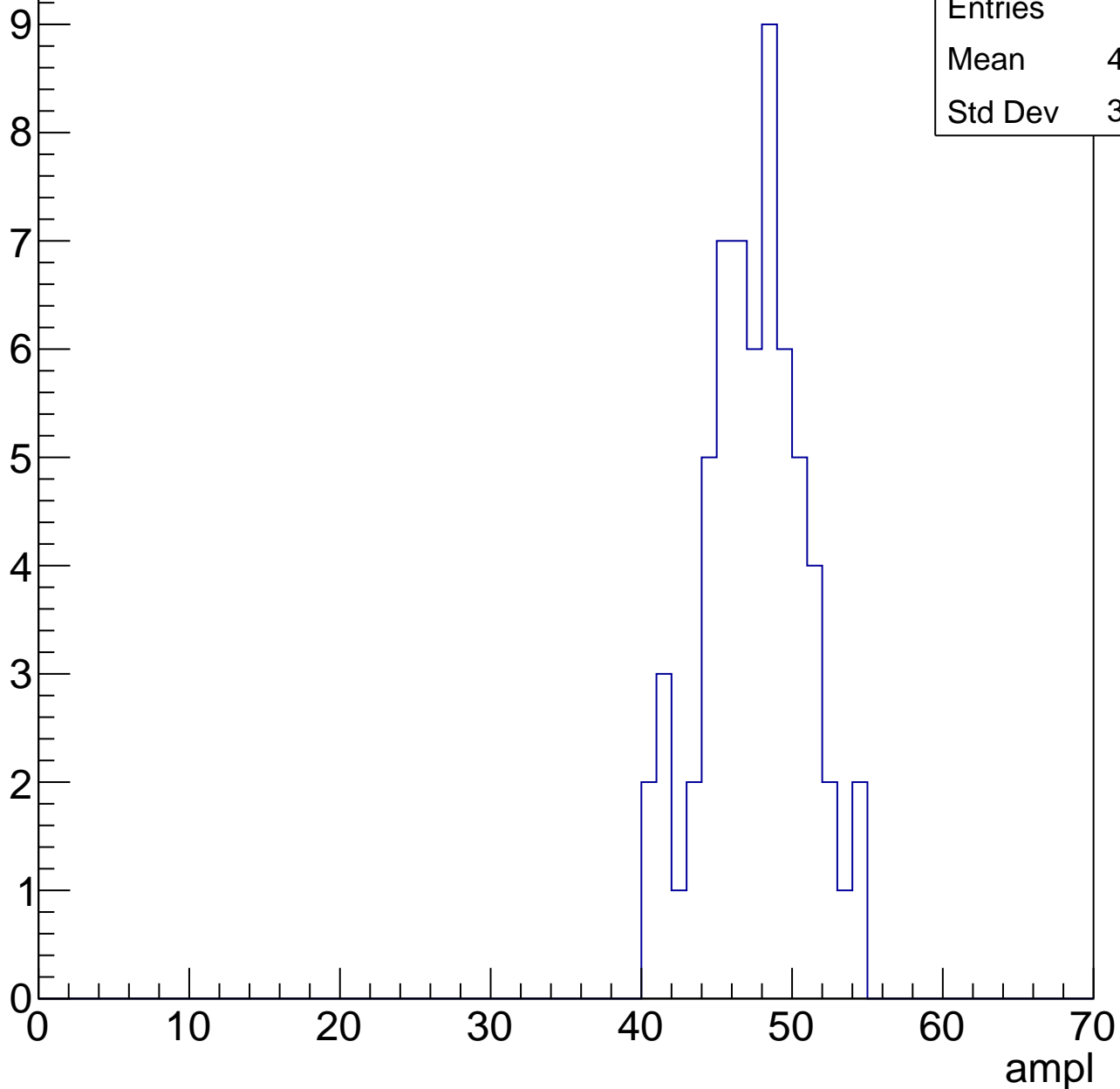
**Gaus Width: 3.5022**



# B1L000S, U24-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

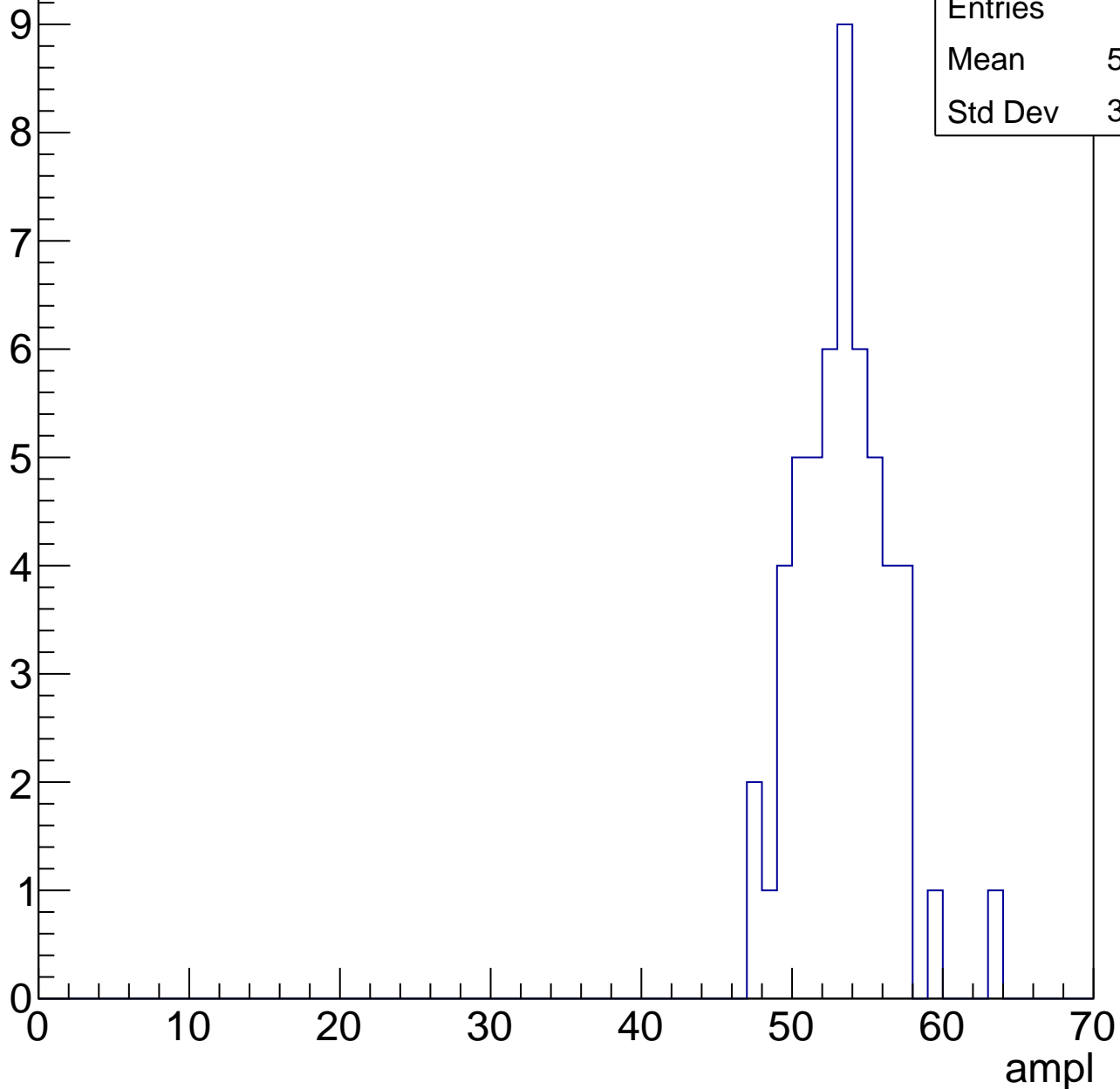


Entries	62
Mean	47.02
Std Dev	3.285

# B1L000S, U24-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	58.4
Std Dev	2.767

Entry

10

8

6

4

2

0

0

10

20

30

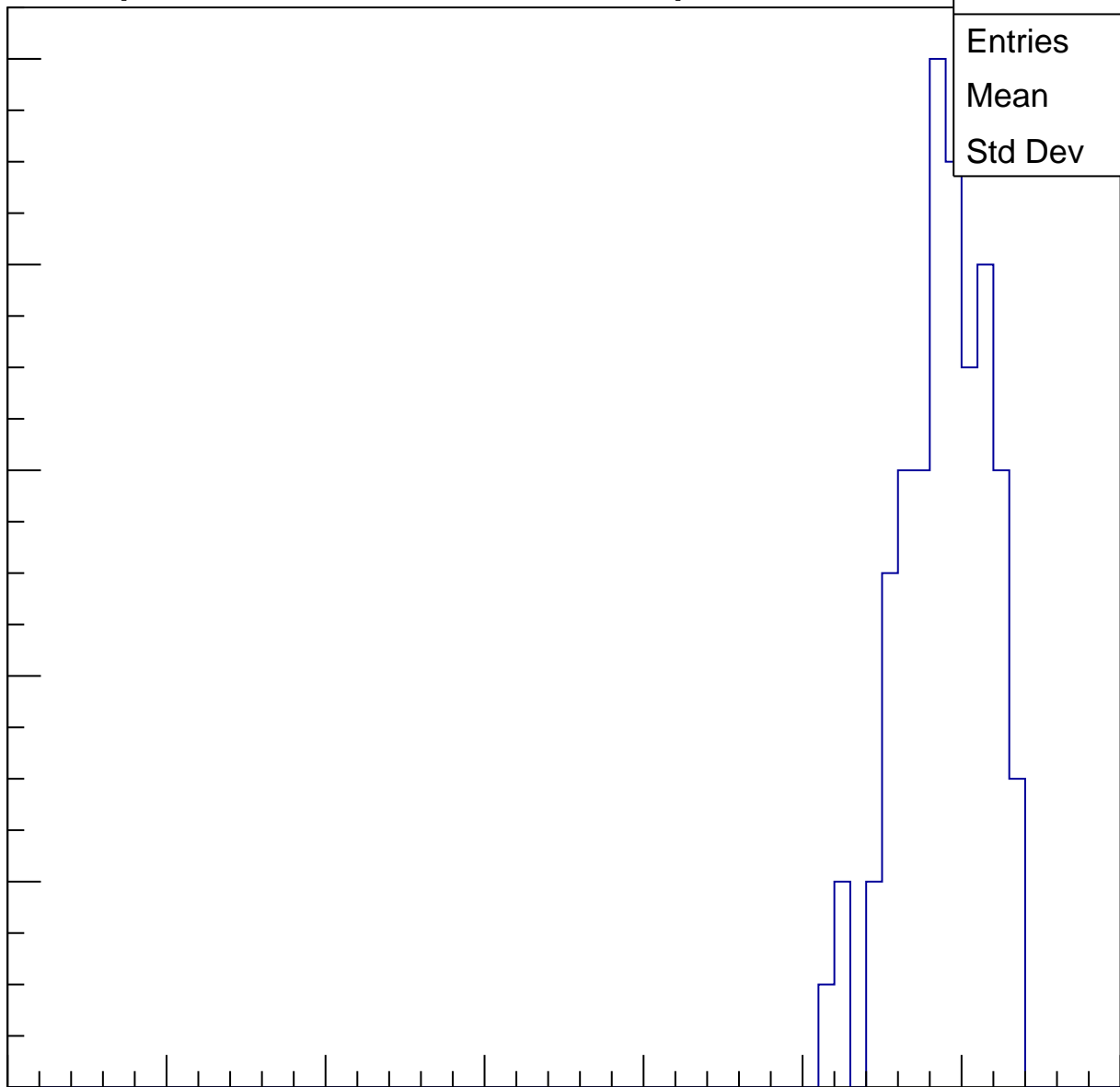
40

50

60

70

ampl

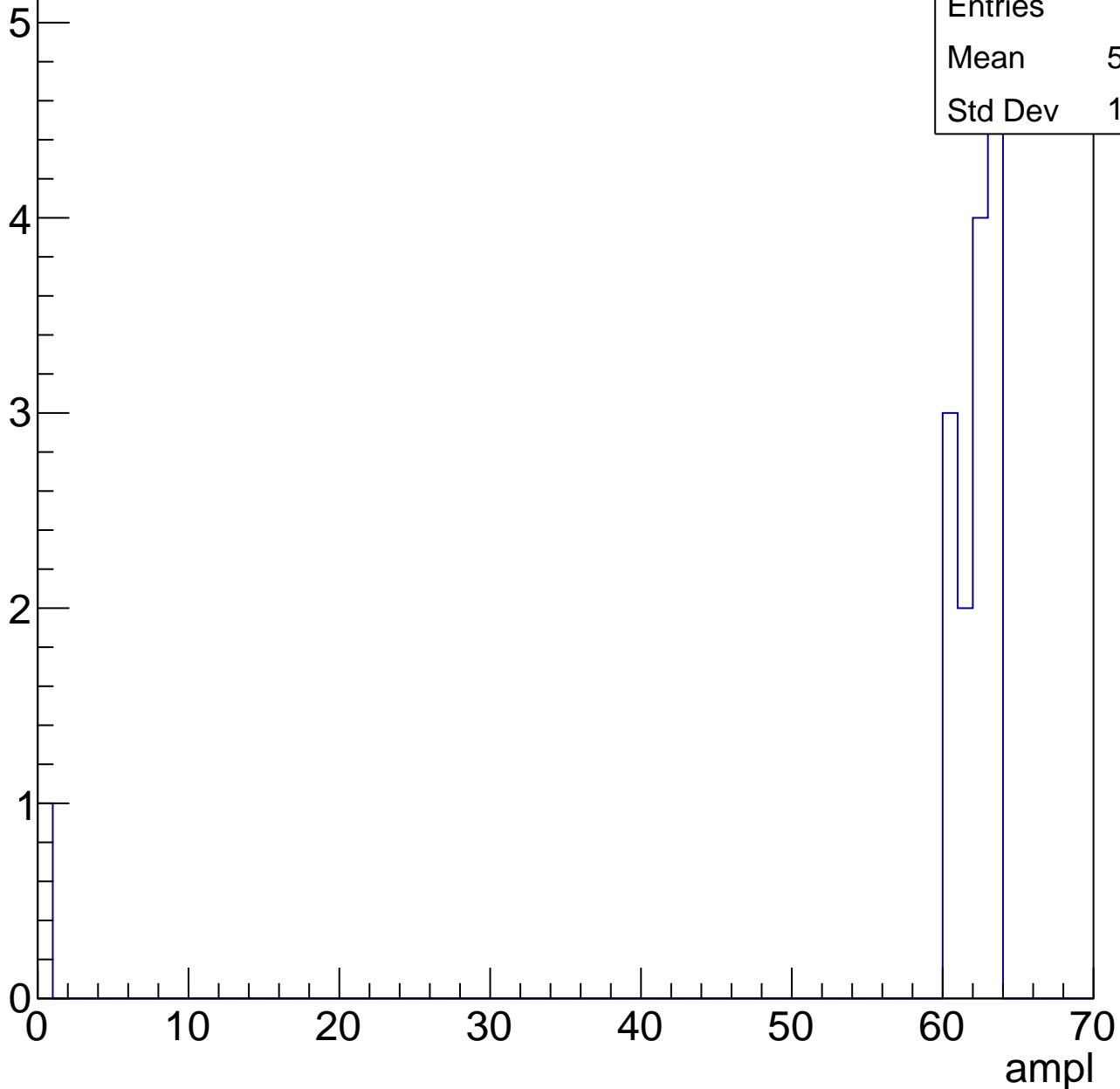


# B1L000S, U24-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.67
Std Dev	15.45





# B1L000S, U24-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch87, adc0

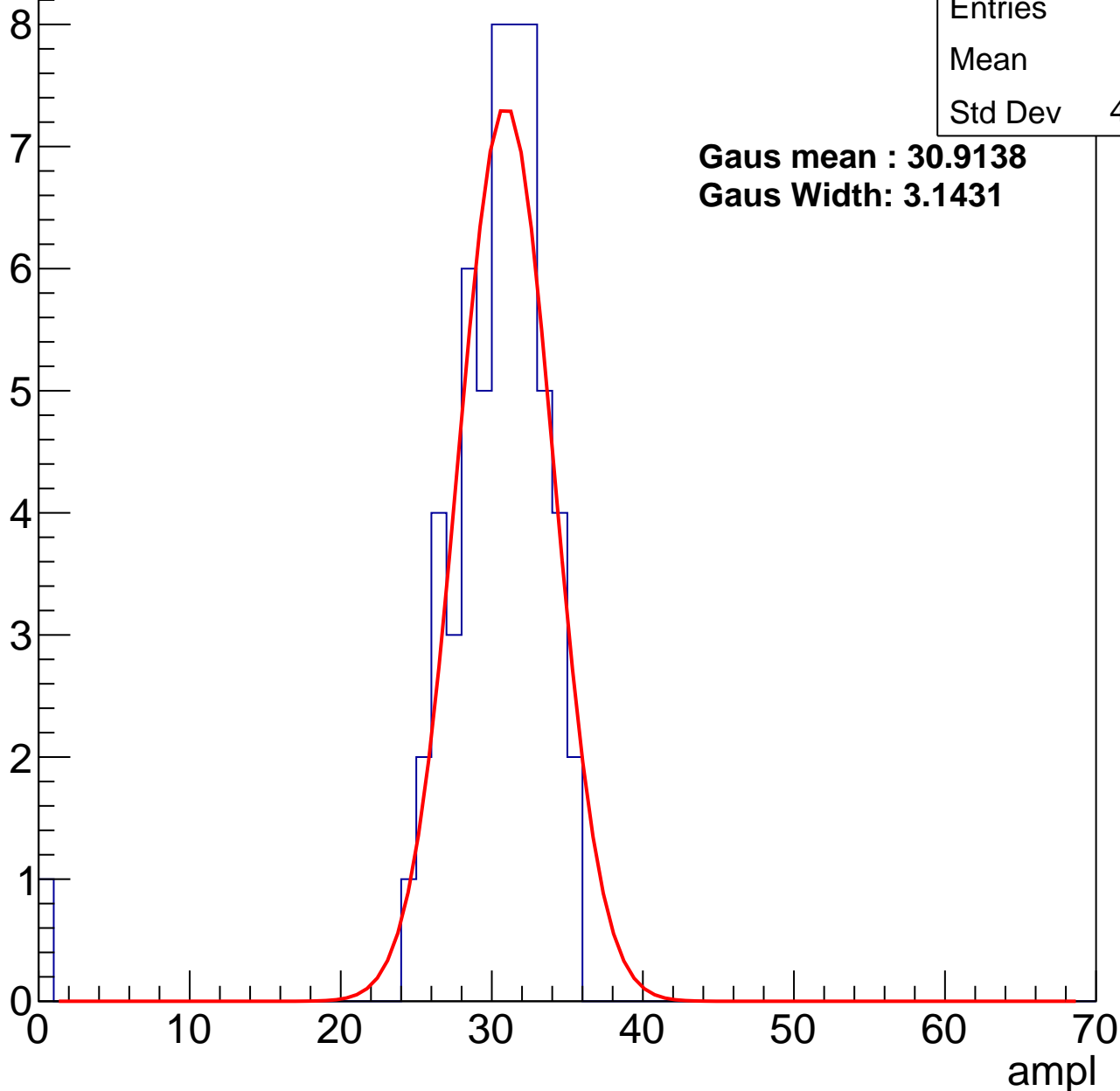
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.6
Std Dev	4.768

**Gaus mean : 30.9138**

**Gaus Width: 3.1431**



# B1L000S, U24-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	37.05
Std Dev	3.731

**Gaus mean : 37.5103**

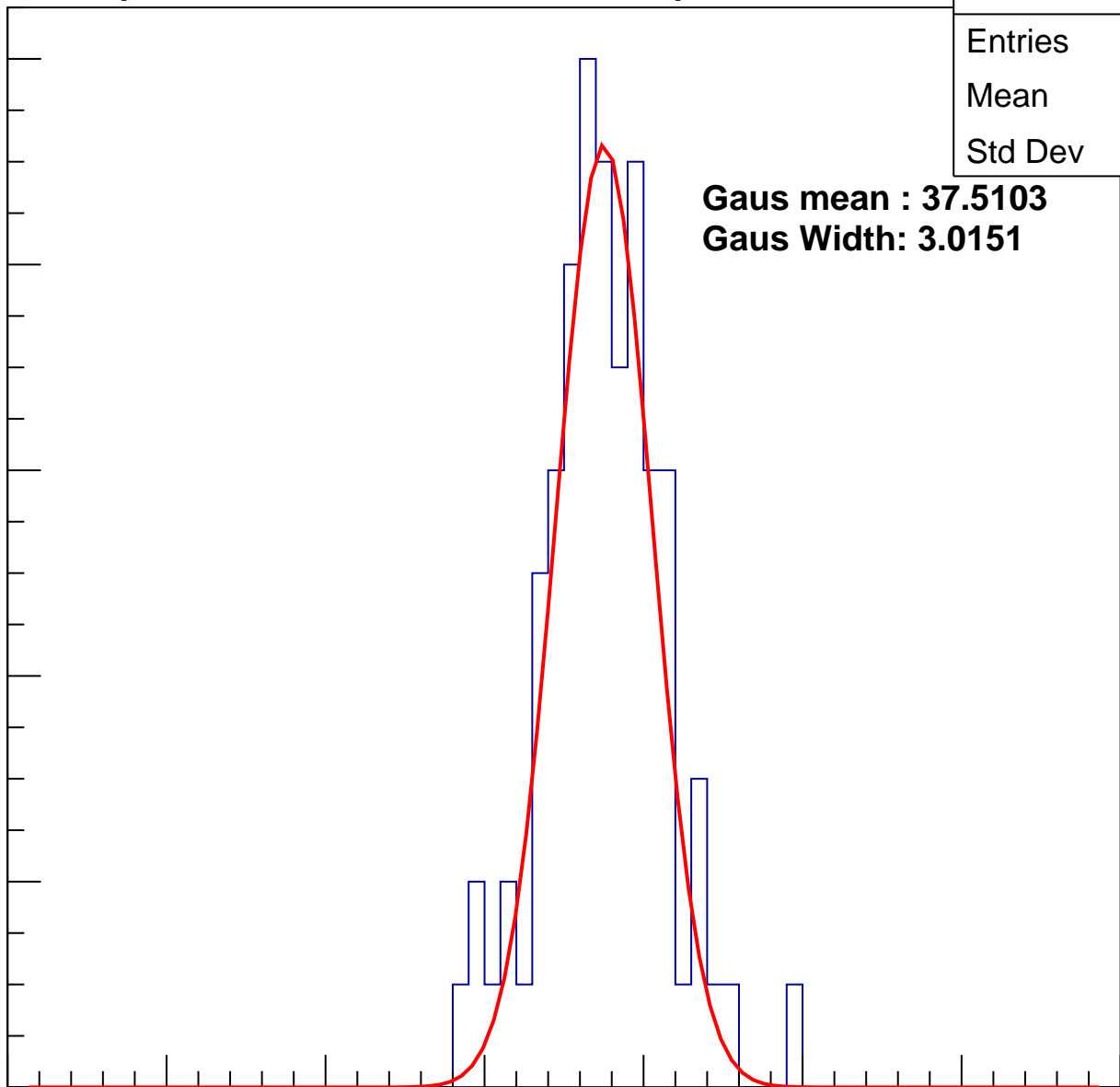
**Gaus Width: 3.0151**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch87, adc2

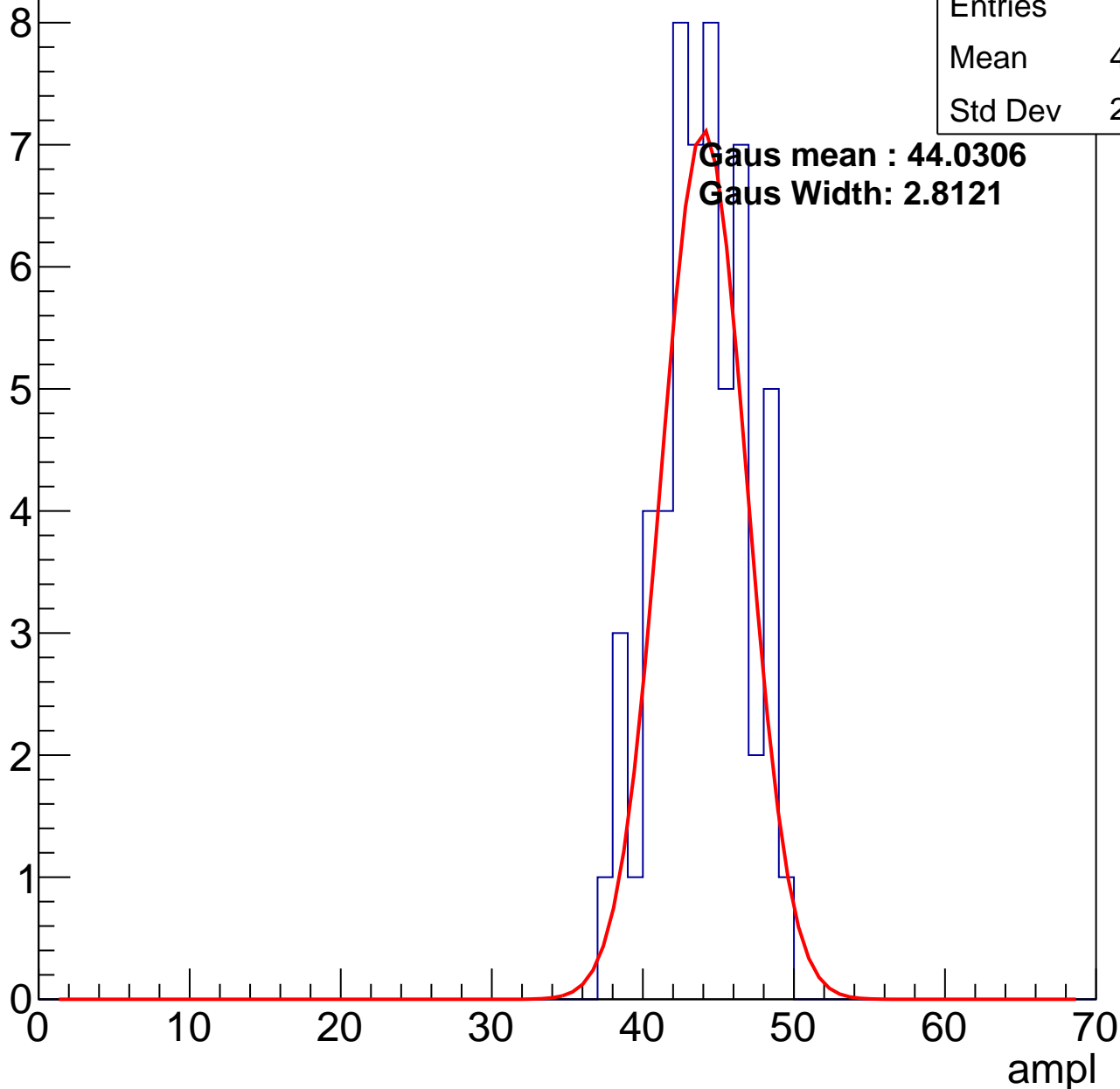
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	43.45
Std Dev	2.866

**Gaus mean : 44.0306**

**Gaus Width: 2.8121**

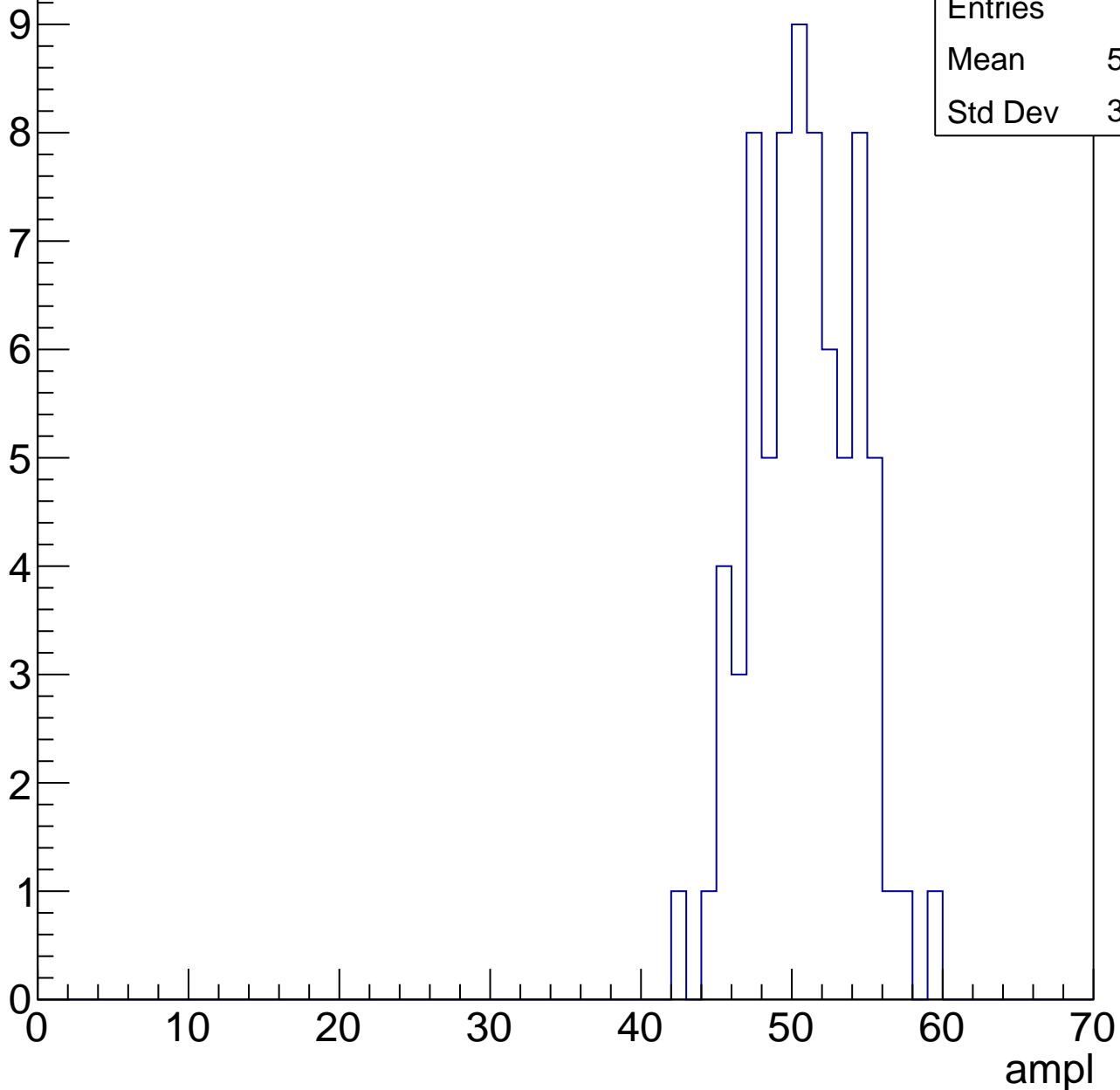


# B1L000S, U24-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	50.35
Std Dev	3.359

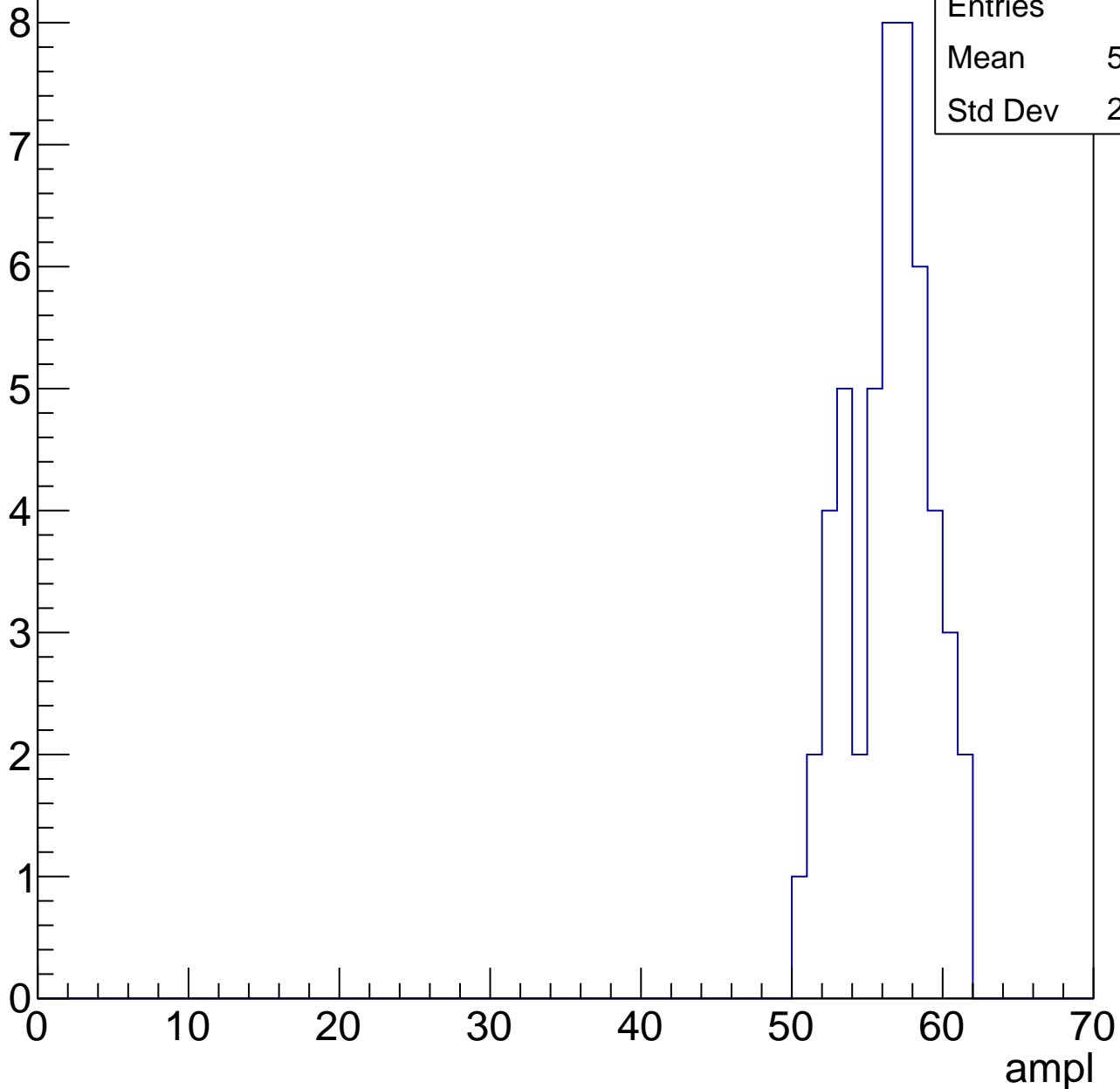


# B1L000S, U24-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

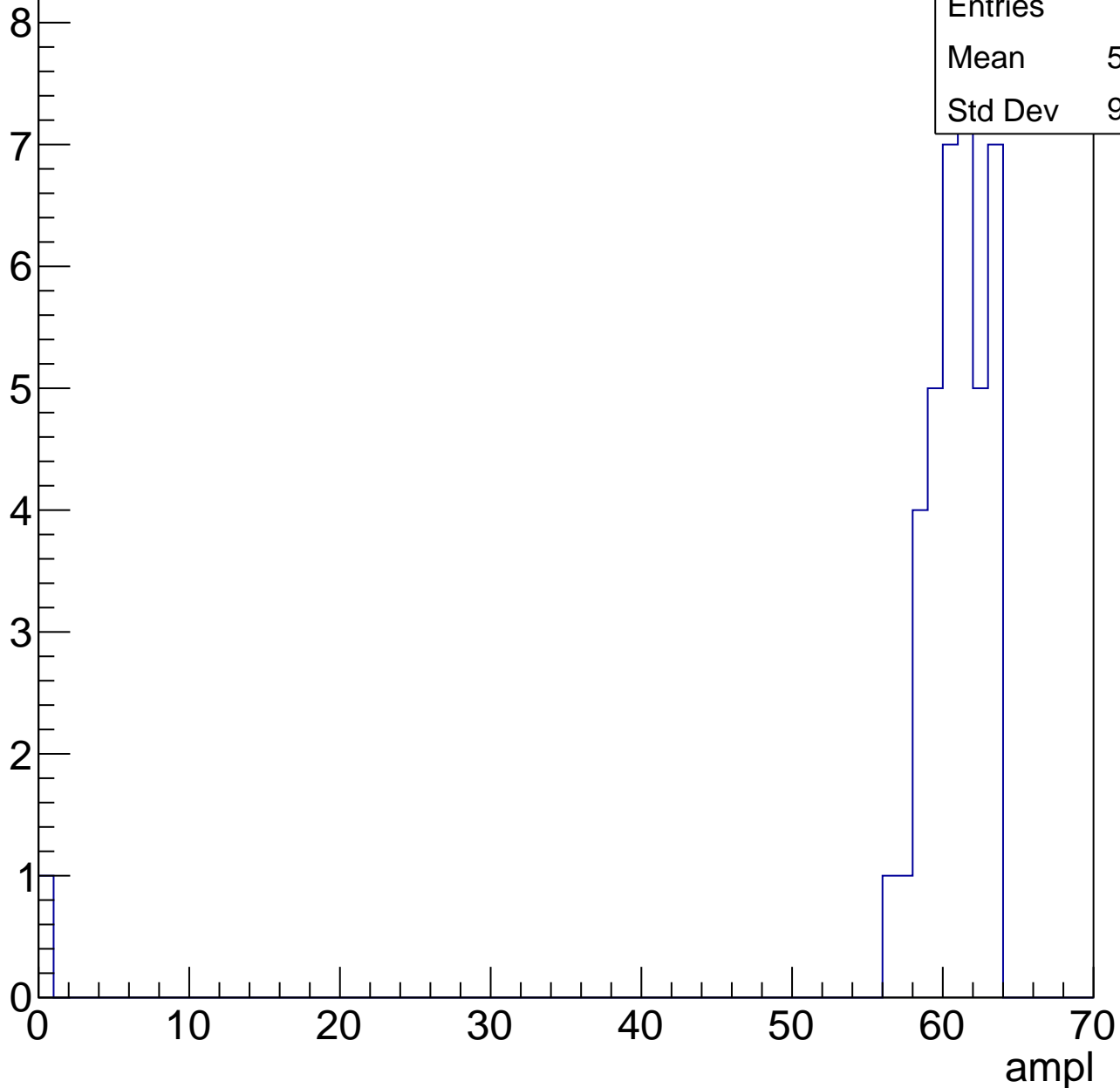
Entries	50
Mean	55.96
Std Dev	2.735



# B1L000S, U24-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

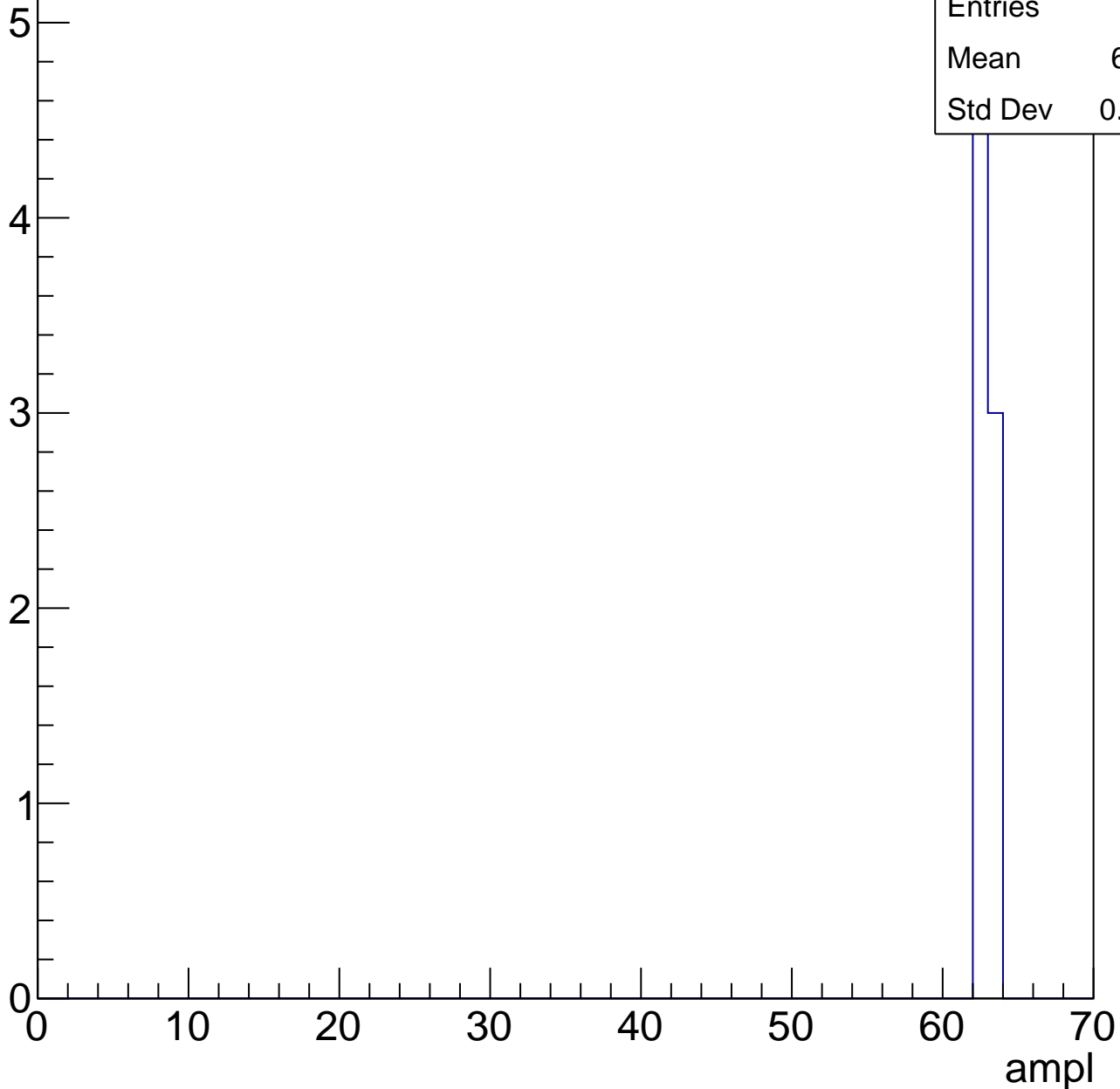


# B1L000S, U24-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.38
Std Dev	0.4841





# B1L000S, U24-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B1L000S, U24-ch88, adc0

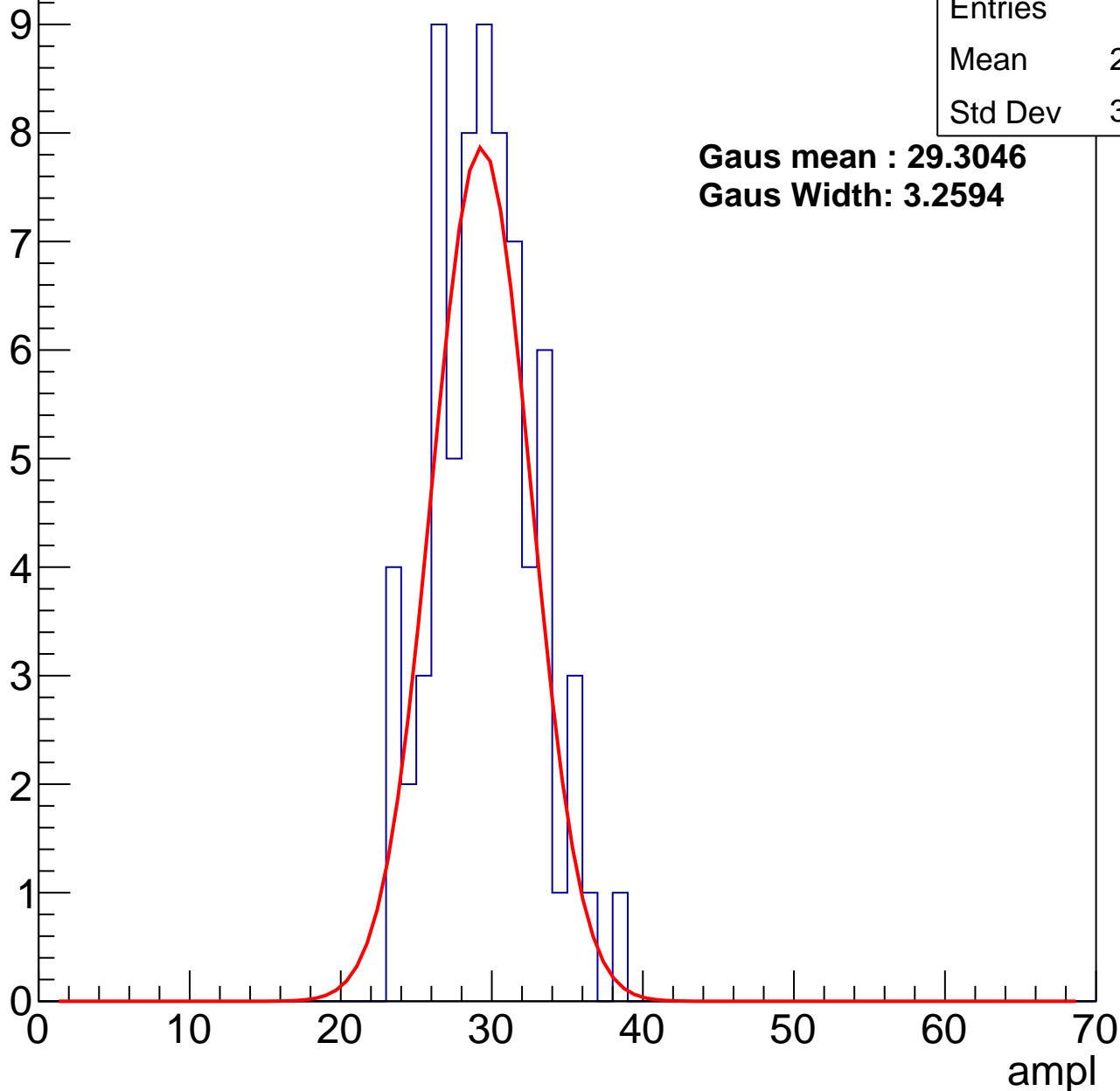
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.08
Std Dev	3.318

**Gaus mean : 29.3046**

**Gaus Width: 3.2594**



# B1L000S, U24-ch88, adc1

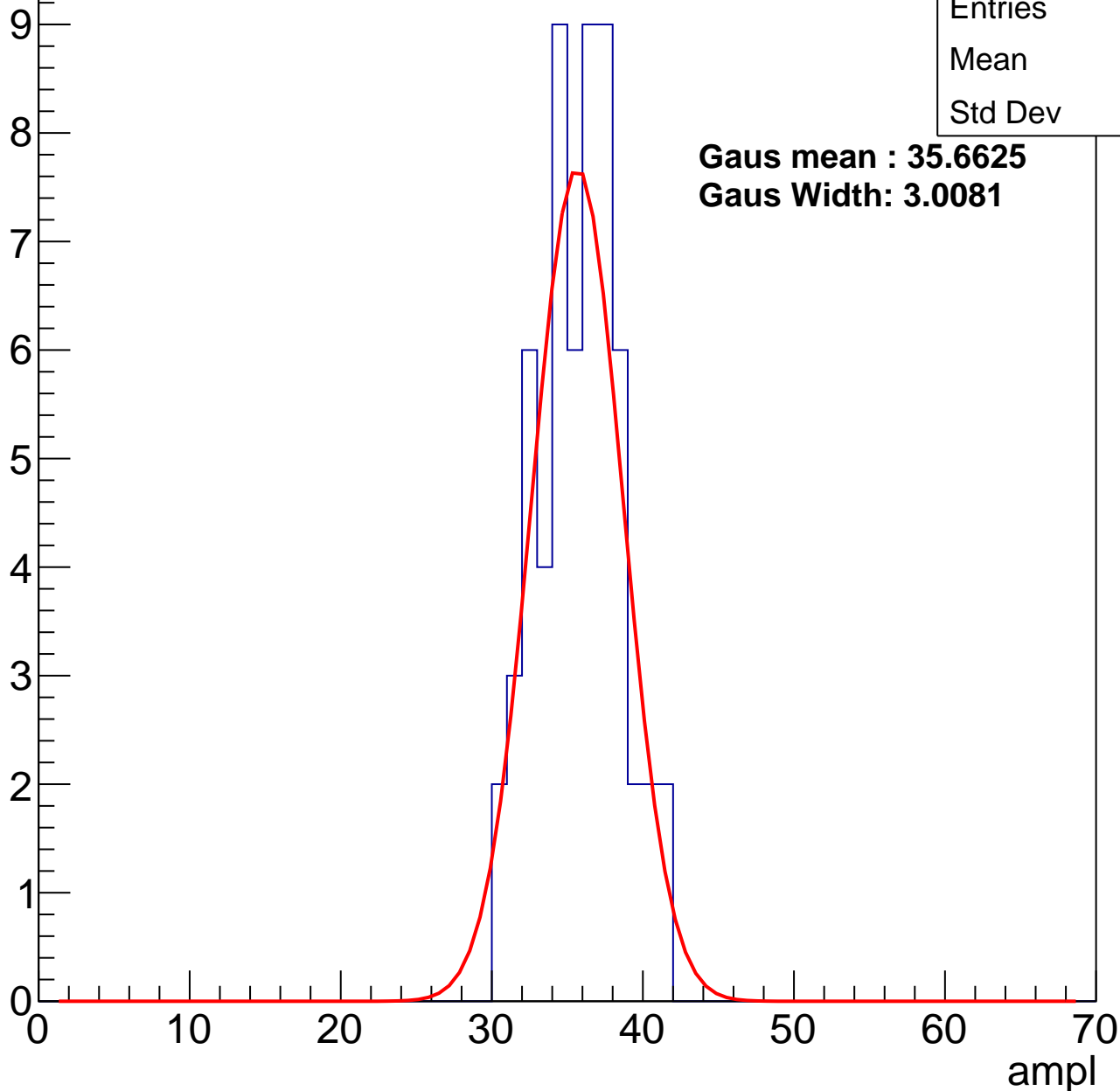
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	35.3
Std Dev	2.66

**Gaus mean : 35.6625**

**Gaus Width: 3.0081**



# B1L000S, U24-ch88, adc2

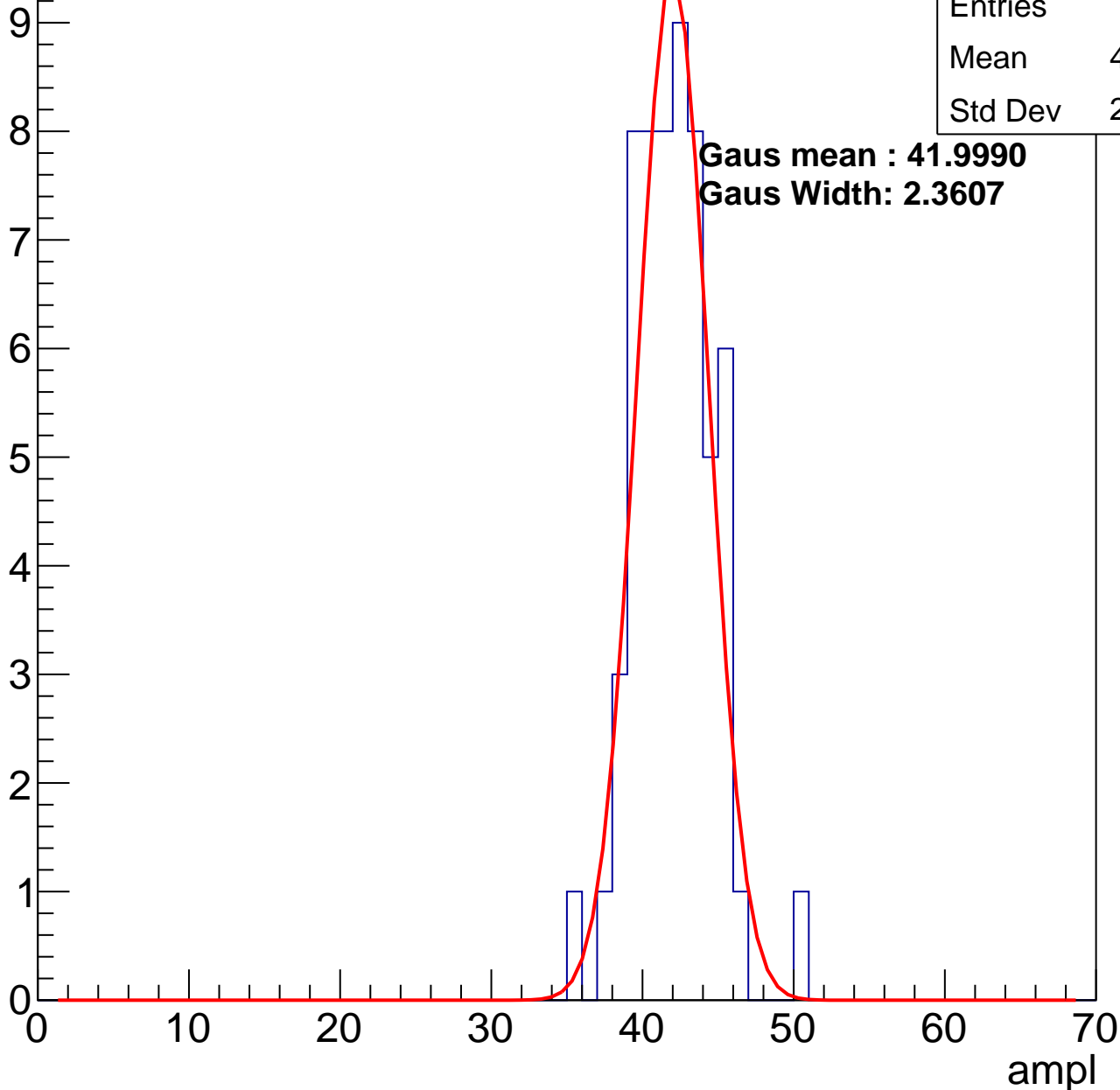
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.59
Std Dev	2.559

**Gaus mean : 41.9990**

**Gaus Width: 2.3607**



# B1L000S, U24-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

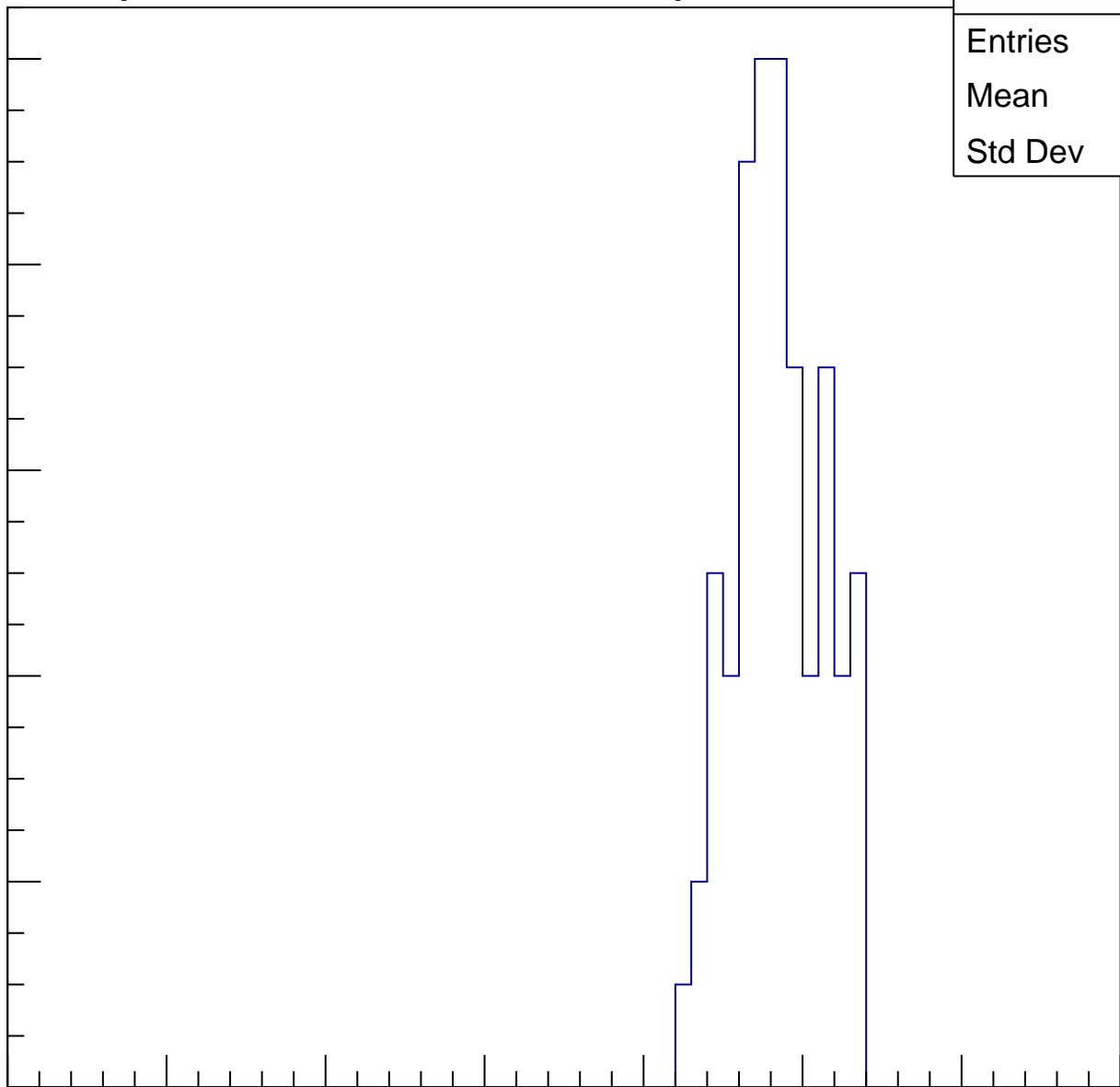
Entries	68
Mean	48.01
Std Dev	2.773

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

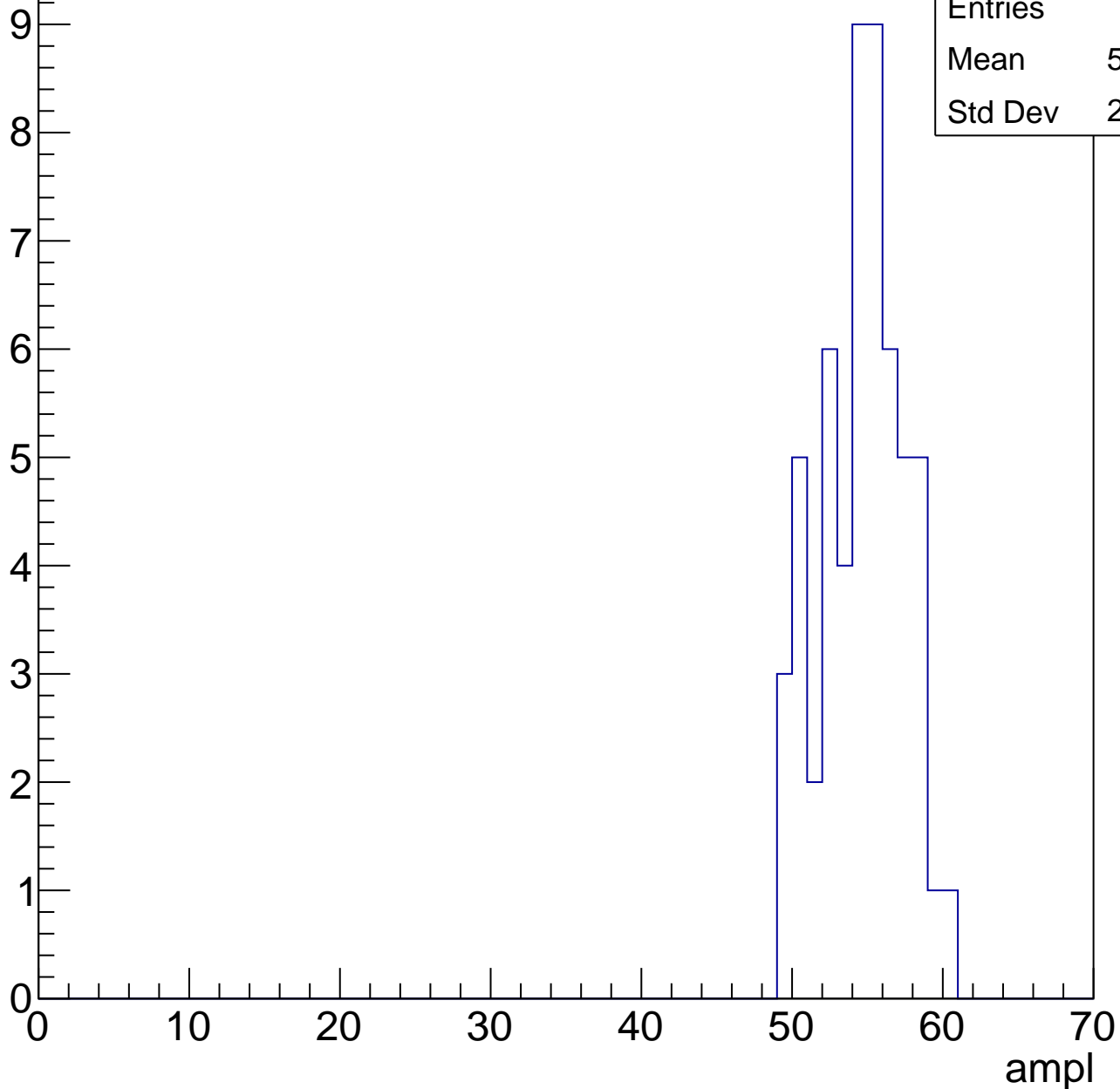
ampl



# B1L000S, U24-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

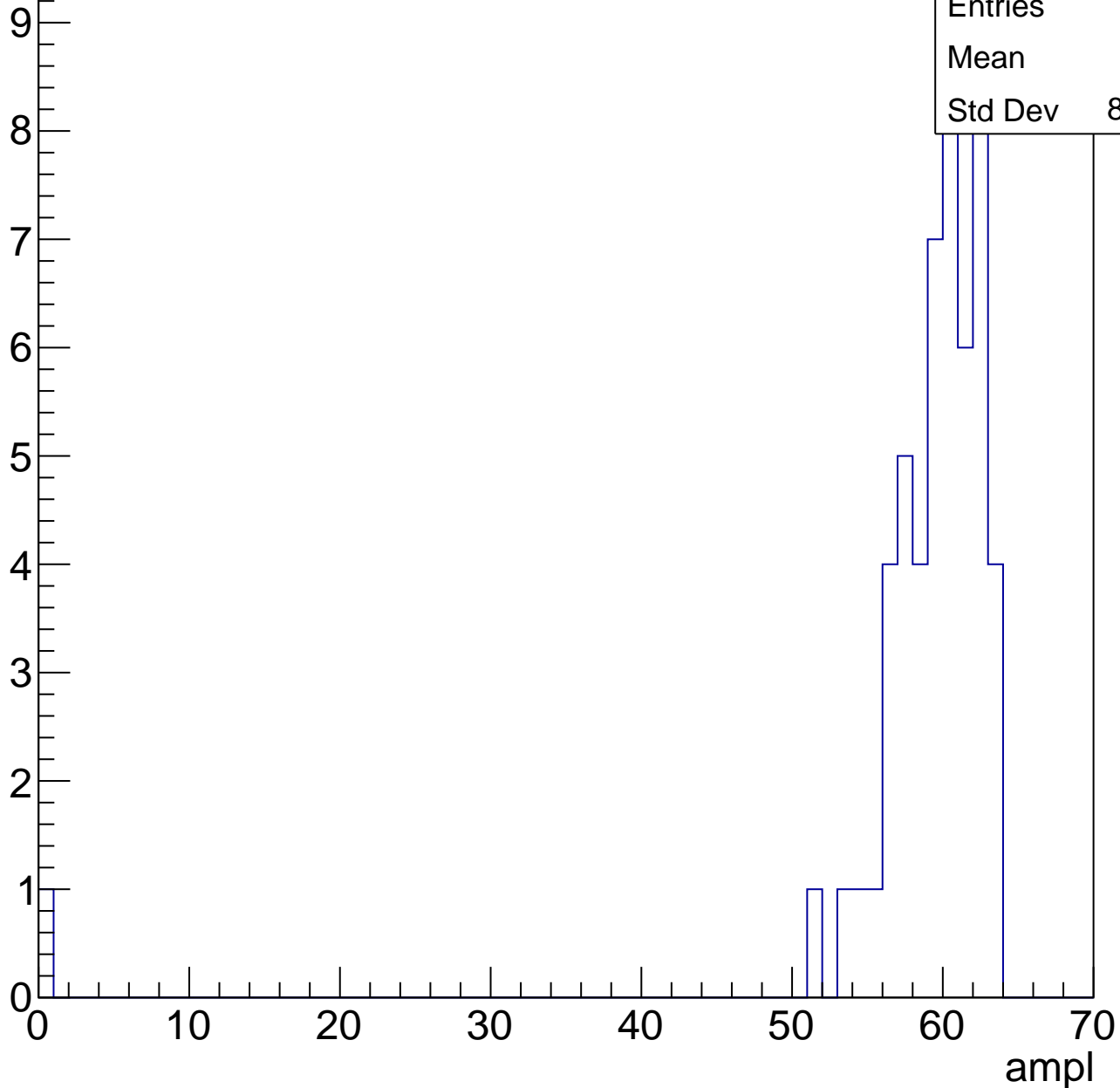


Entries	56
Mean	54.18
Std Dev	2.733

# B1L000S, U24-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

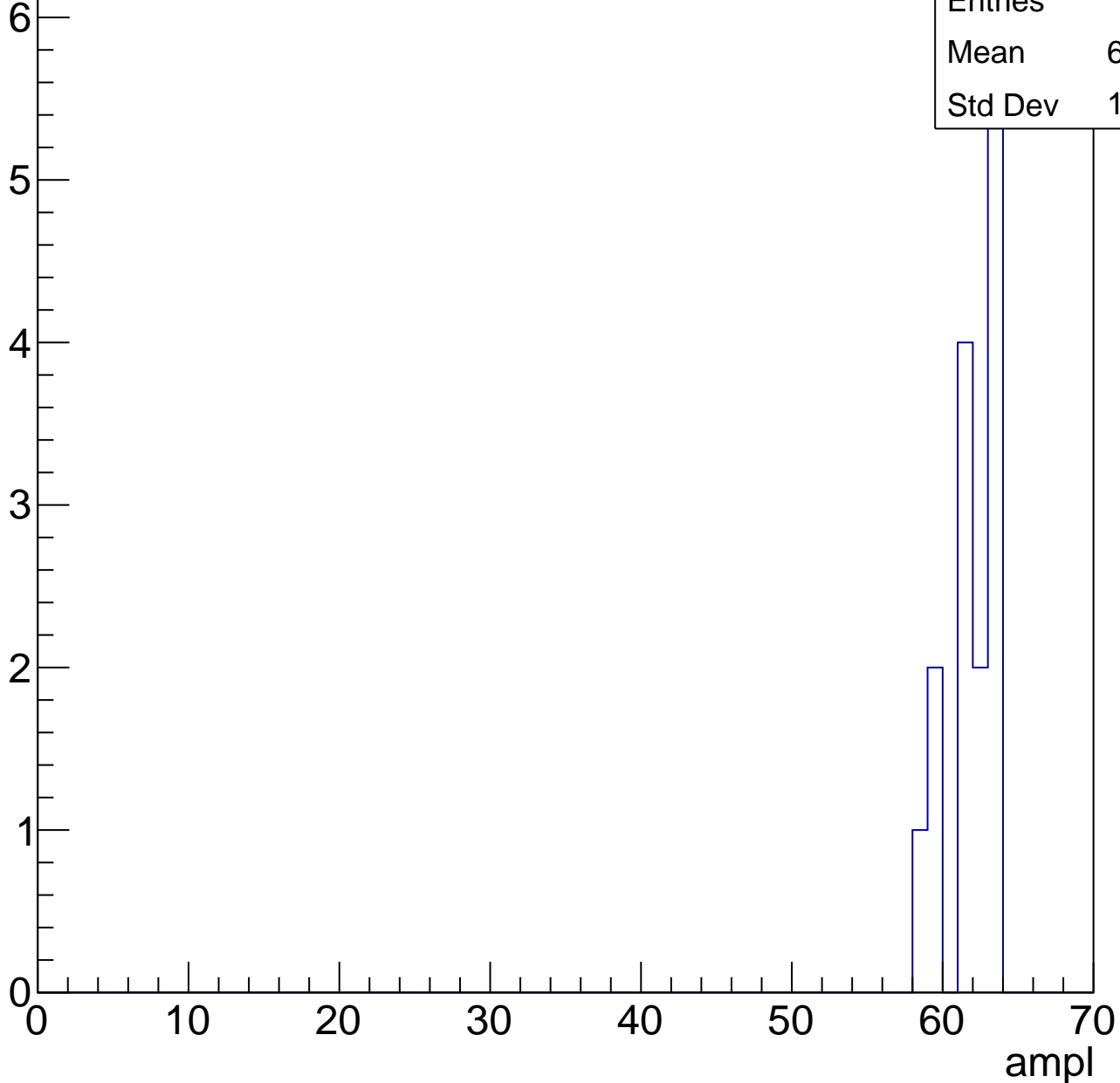


# B1L000S, U24-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.47
Std Dev	1.628





# B1L000S, U24-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch89, adc0

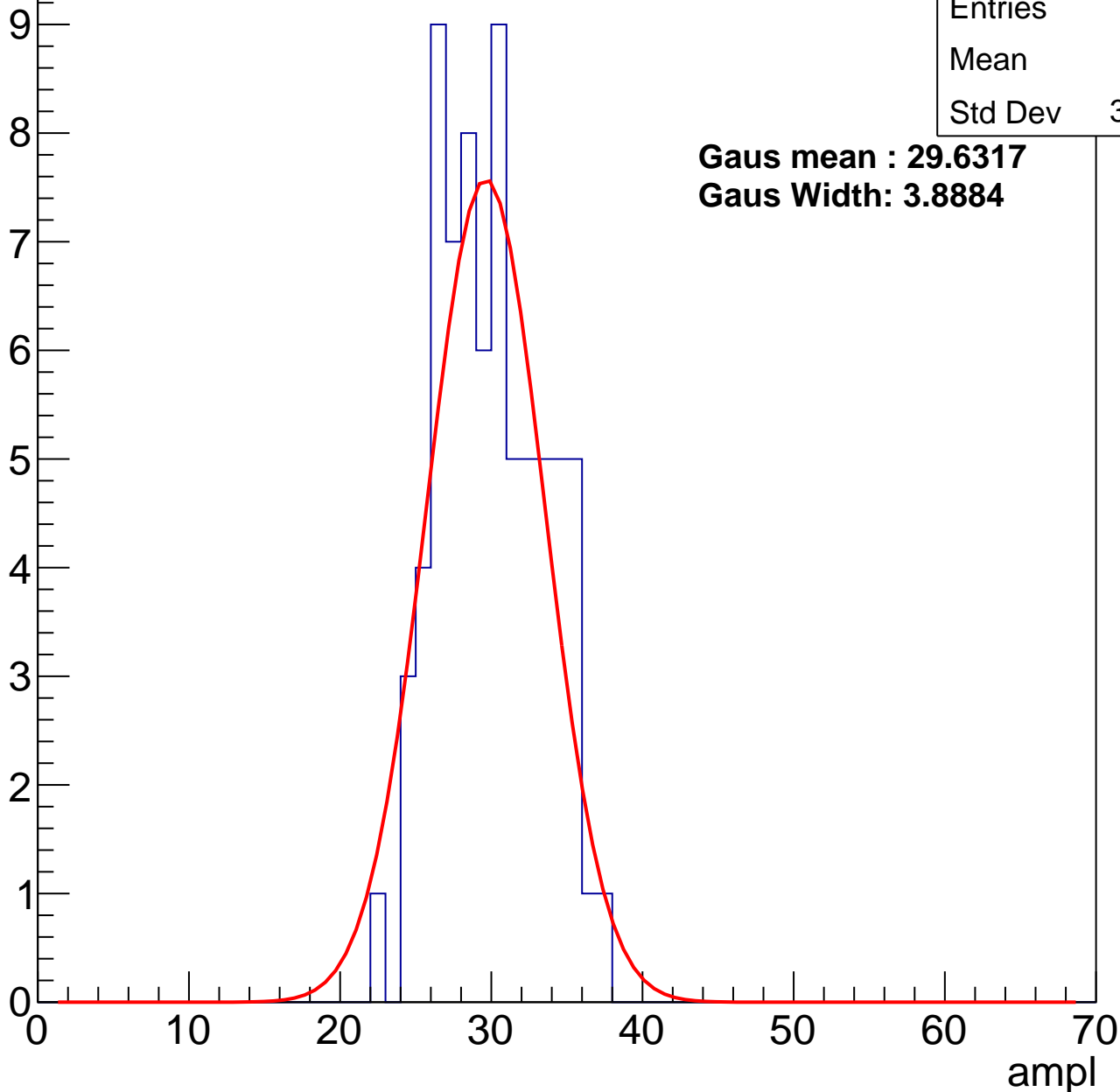
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29.5
Std Dev	3.406

**Gaus mean : 29.6317**

**Gaus Width: 3.8884**



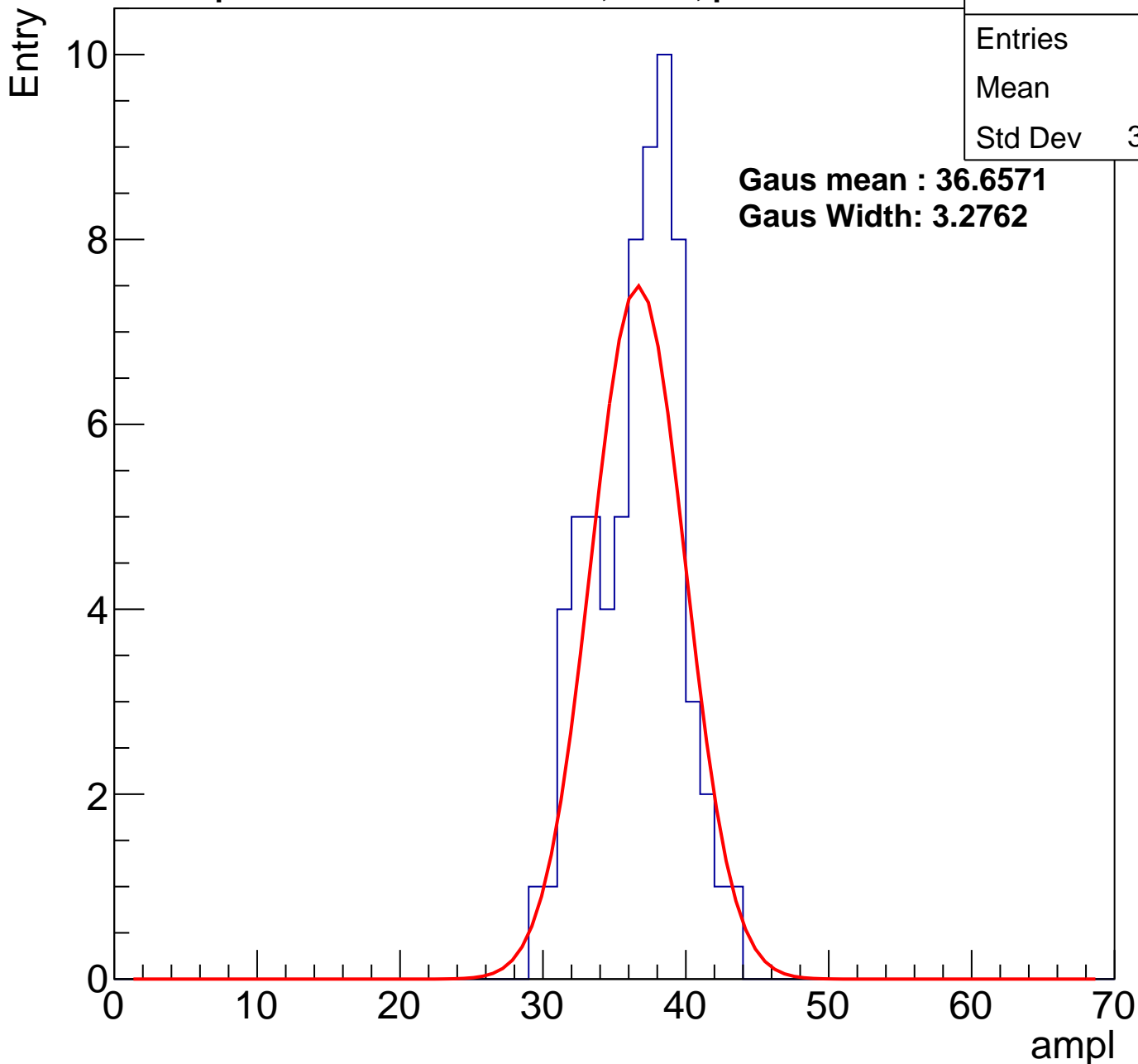
# B1L000S, U24-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	36.1
Std Dev	3.077

**Gaus mean : 36.6571**

**Gaus Width: 3.2762**



# B1L000S, U24-ch89, adc2

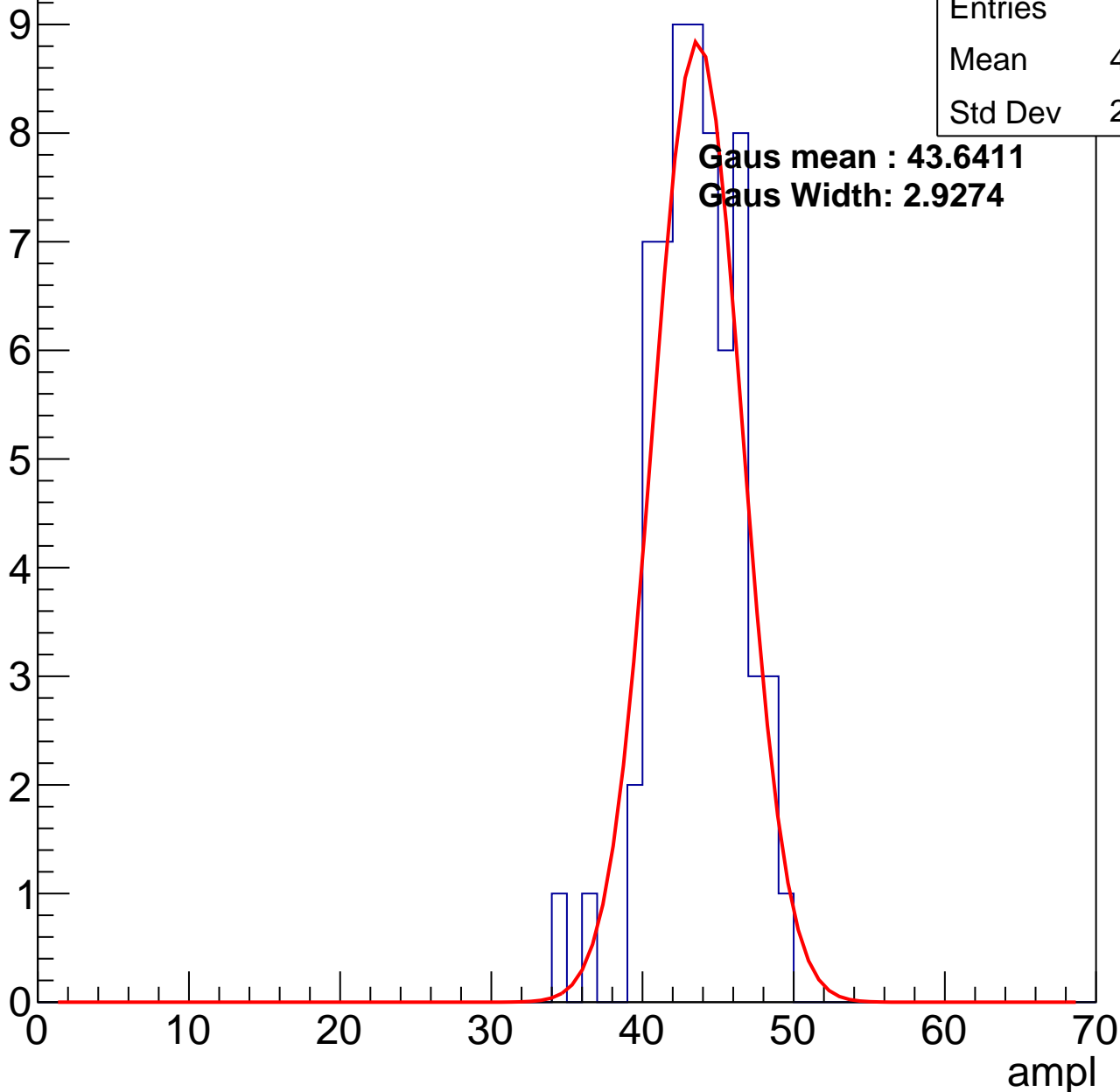
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	43.14
Std Dev	2.839

**Gaus mean : 43.6411**

**Gaus Width: 2.9274**

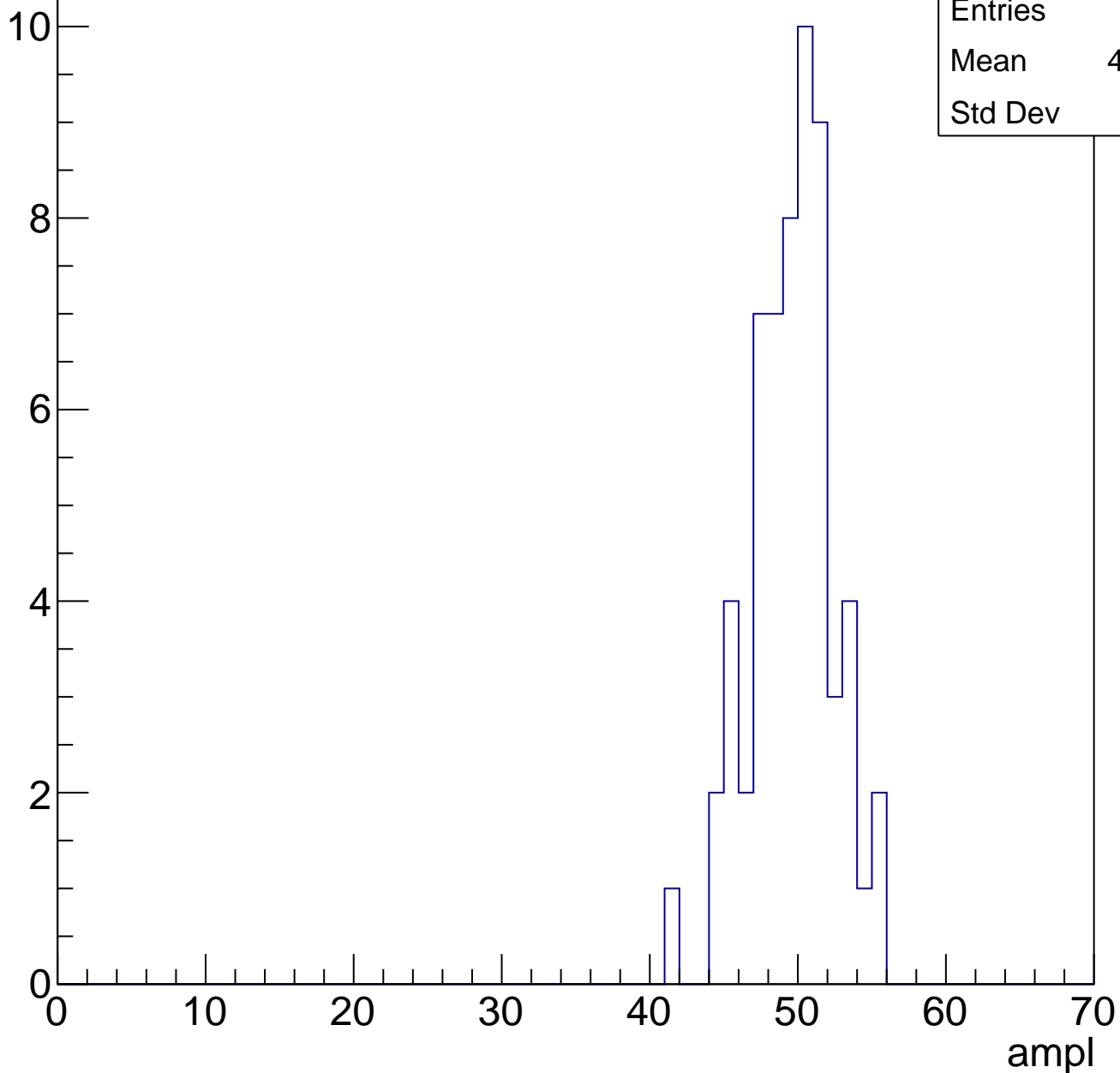


# B1L000S, U24-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	49.15
Std Dev	2.78

Entry

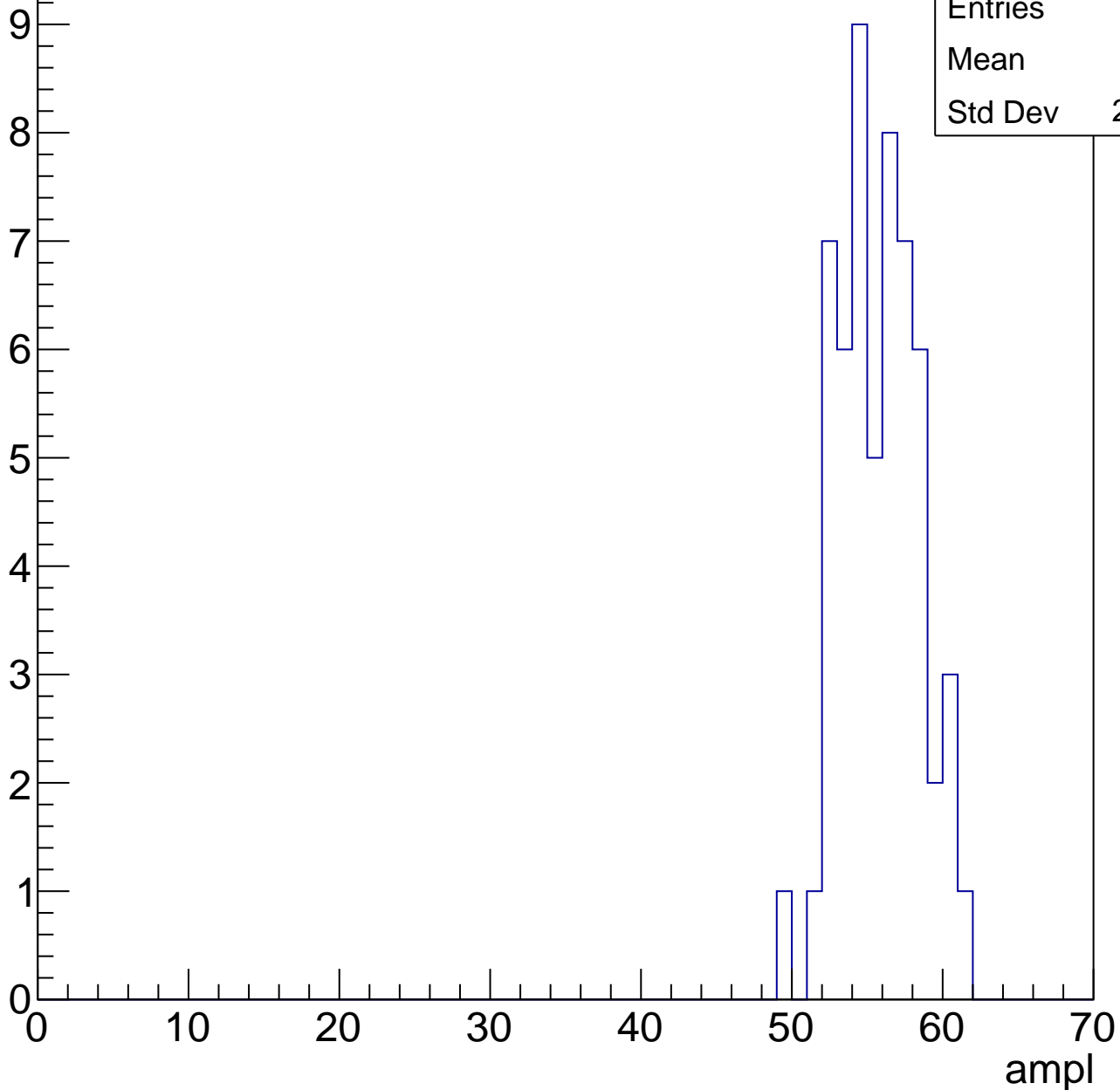


# B1L000S, U24-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

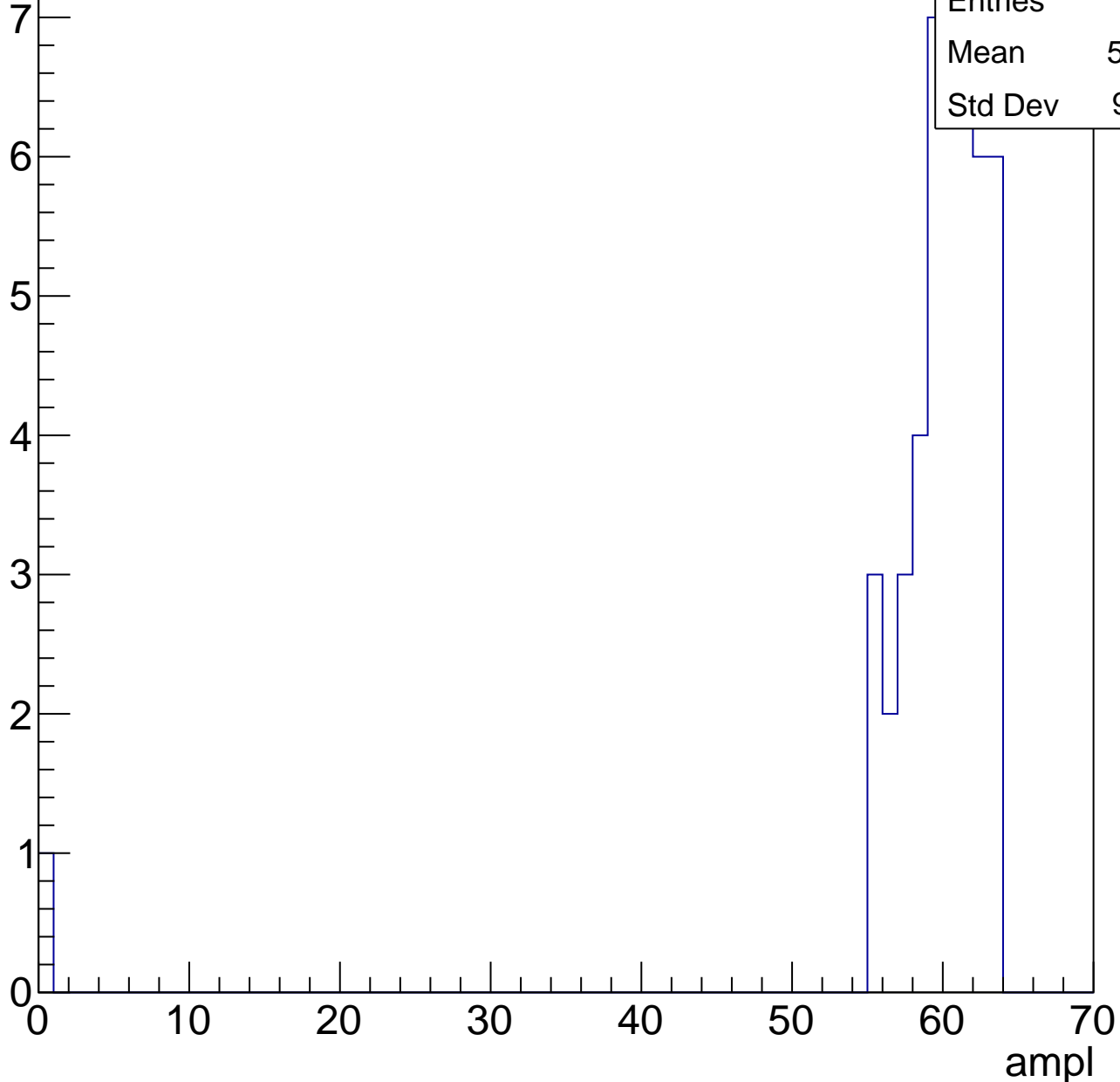
Entries	56
Mean	55.3
Std Dev	2.591



# B1L000S, U24-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

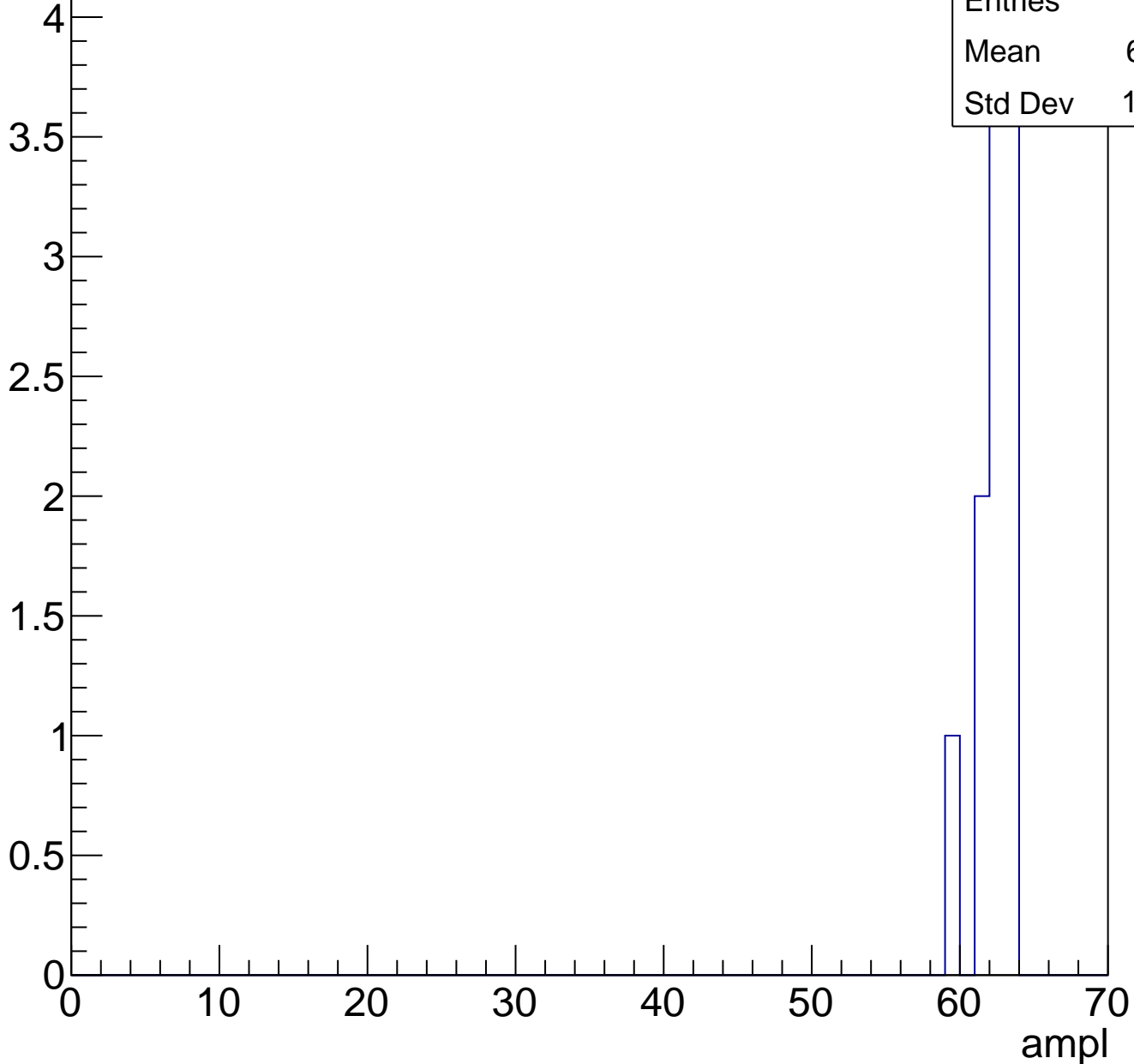
Entry



# B1L000S, U24-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

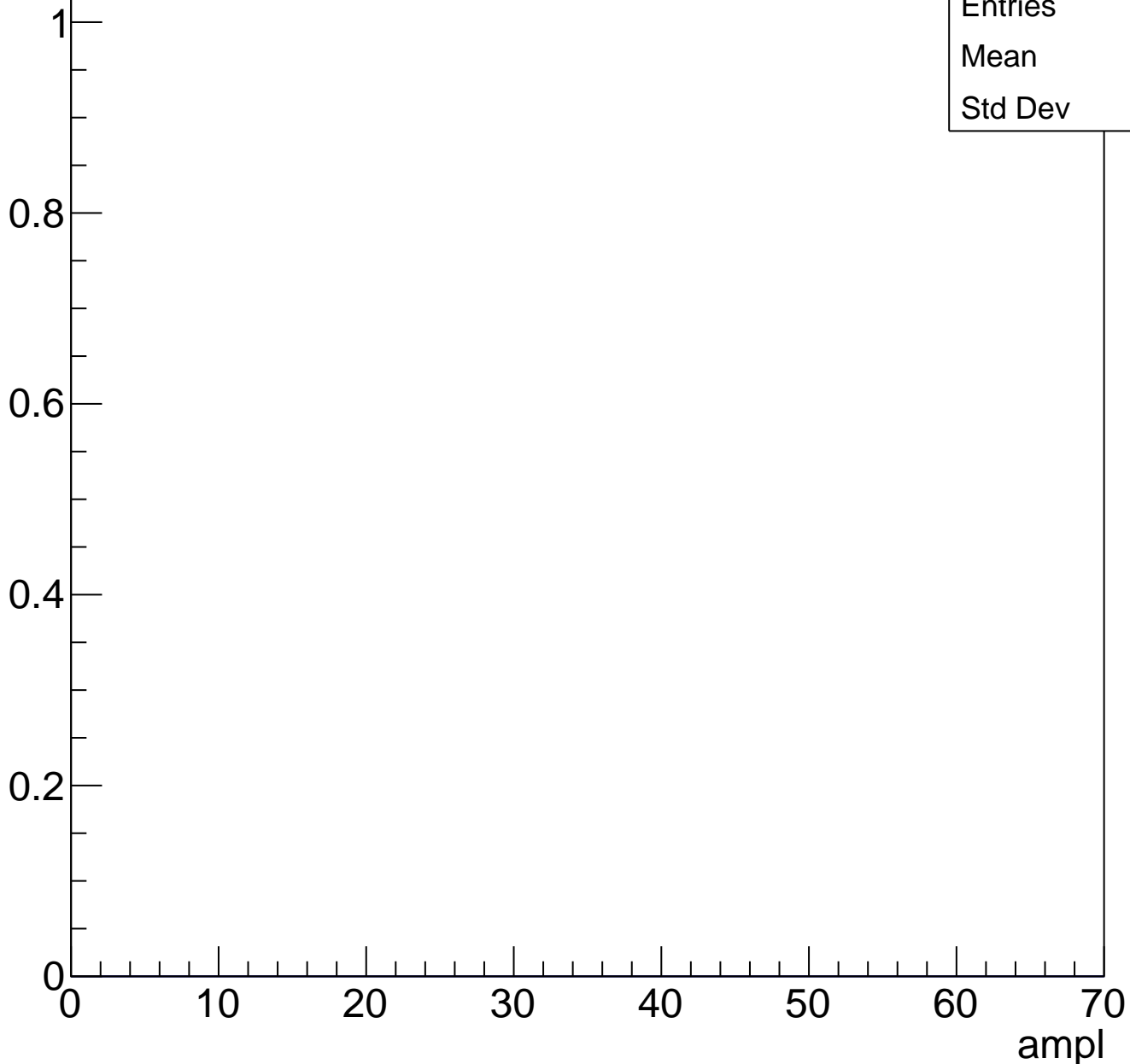




# B1L000S, U24-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	28.63
Std Dev	2.366

**Gaus mean : 29.4588**

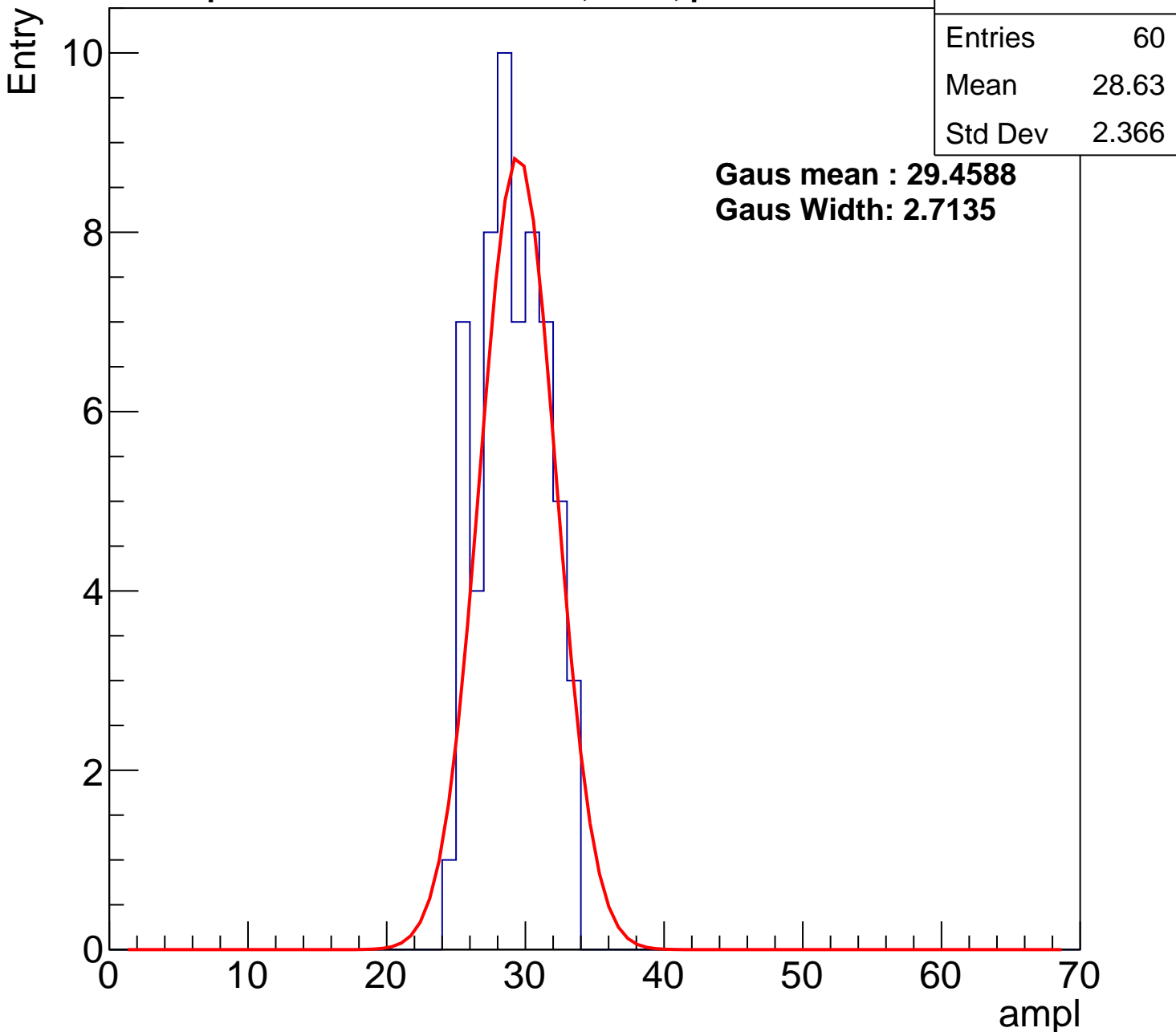
**Gaus Width: 2.7135**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch90, adc1

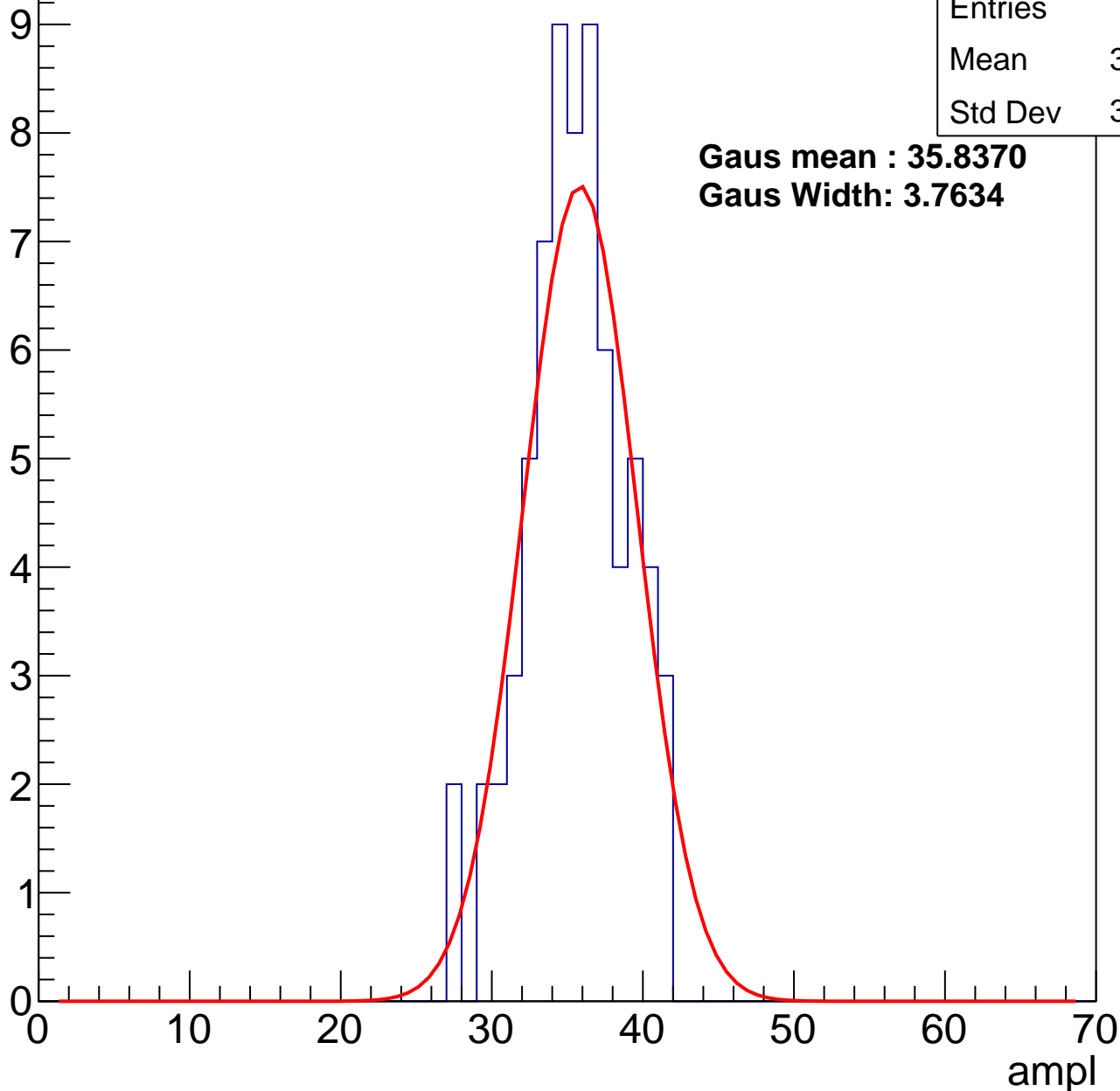
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.04
Std Dev	3.268

**Gaus mean : 35.8370**

**Gaus Width: 3.7634**



# B1L000S, U24-ch90, adc2

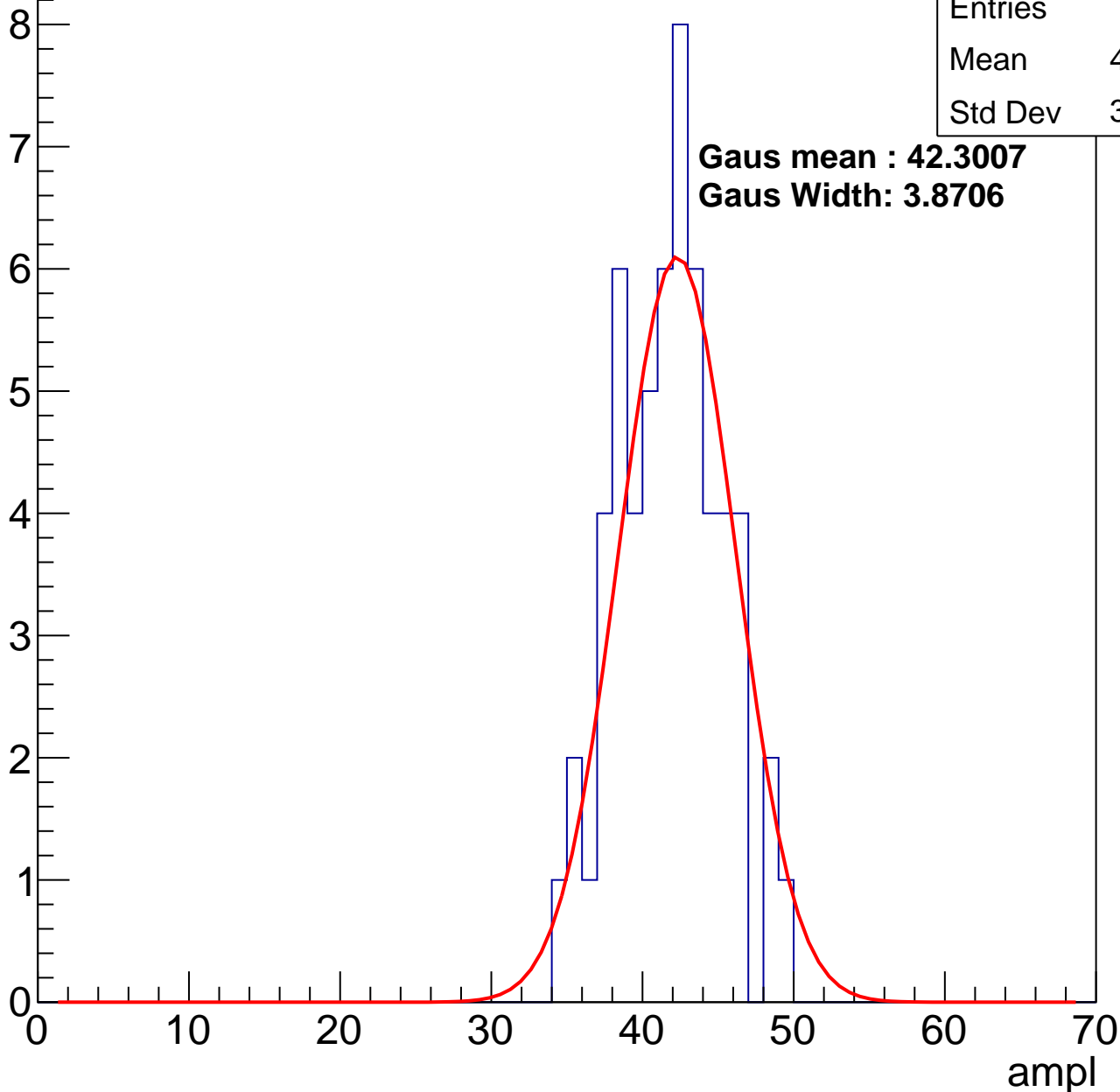
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.33
Std Dev	3.406

**Gaus mean : 42.3007**

**Gaus Width: 3.8706**

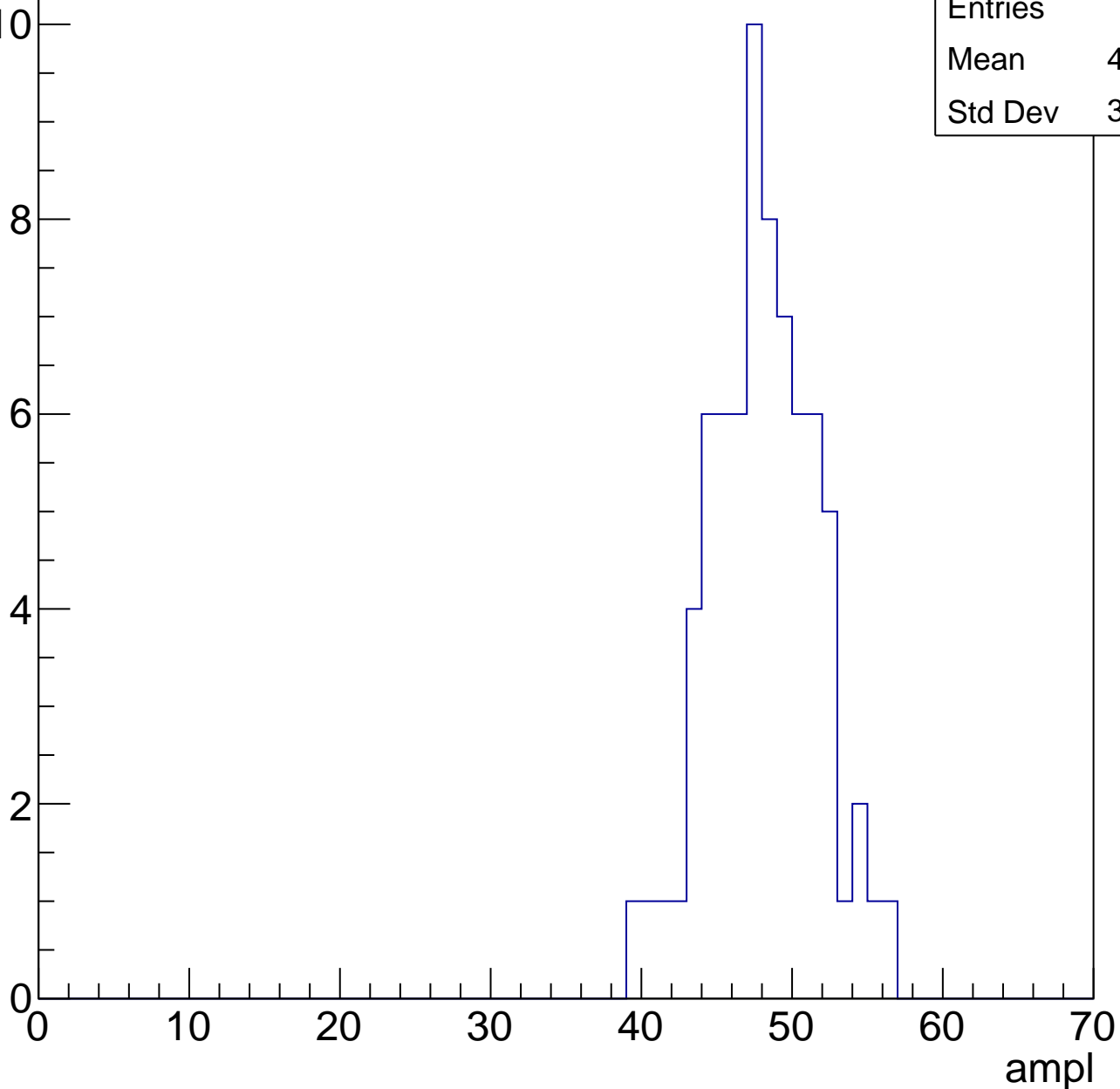


# B1L000S, U24-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	47.66
Std Dev	3.473

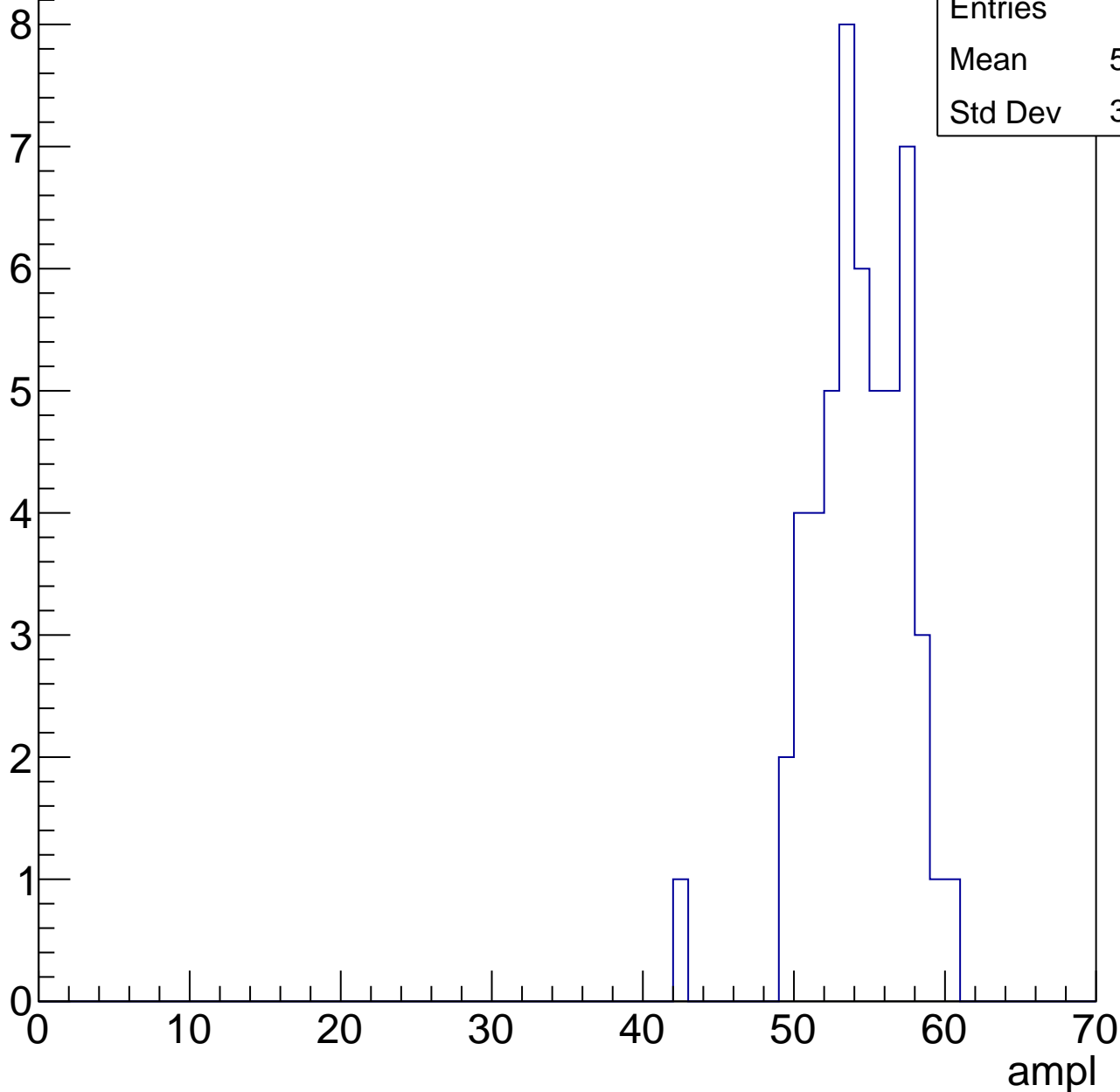


# B1L000S, U24-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	53.83
Std Dev	3.154



# B1L000S, U24-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	57.46
Std Dev	8.772

ampl

0

10

20

30

40

50

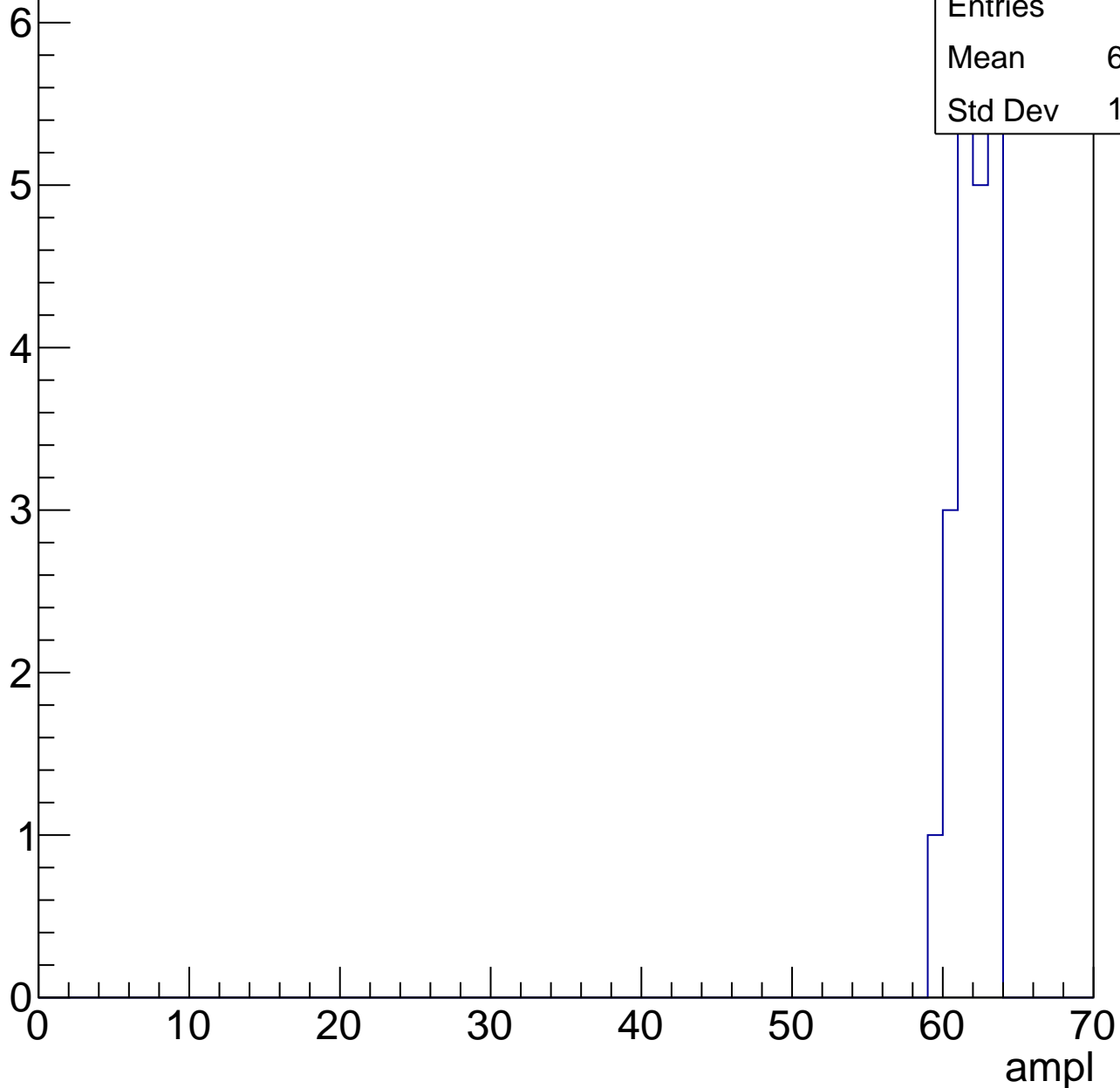
60

70

# B1L000S, U24-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



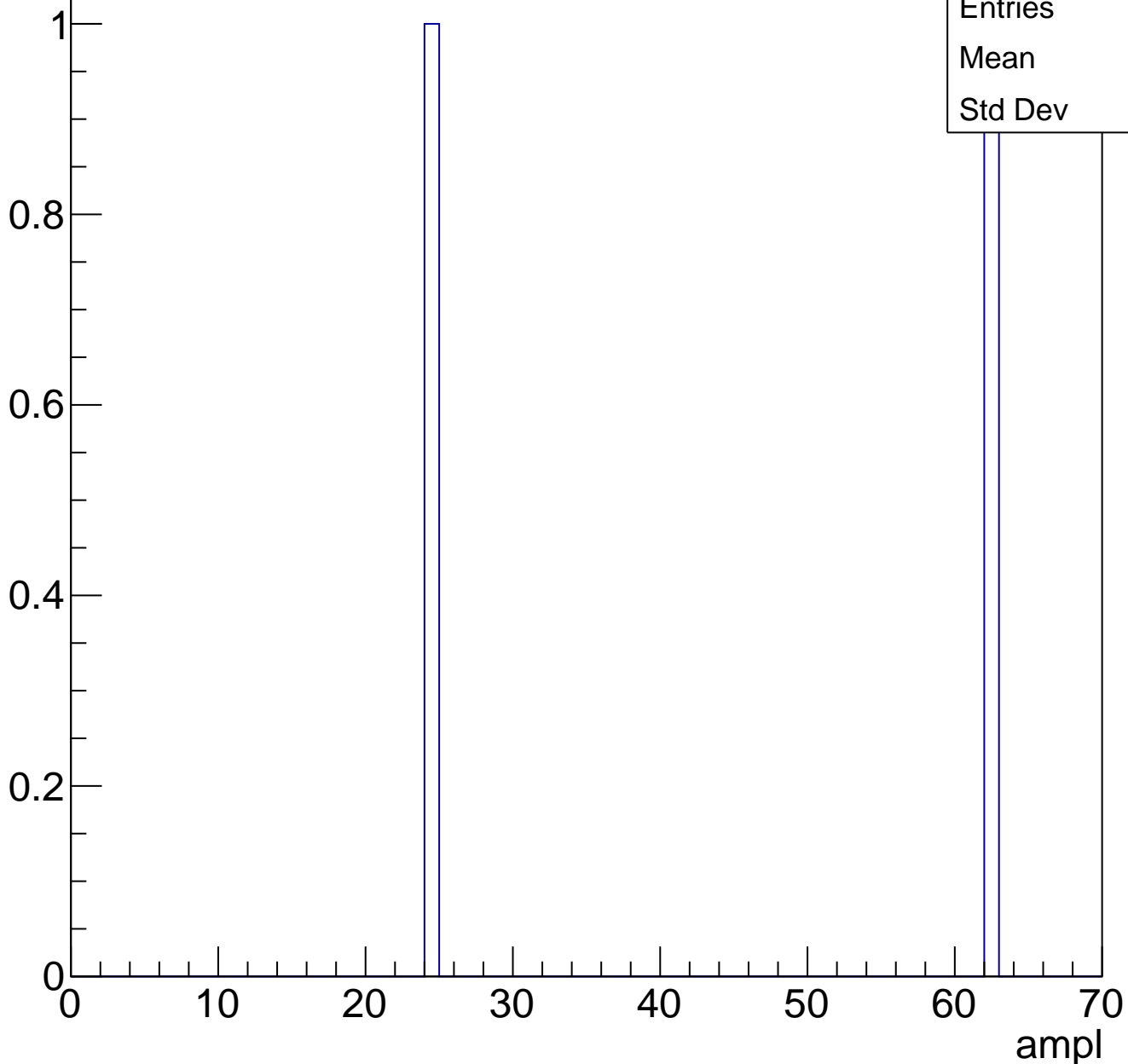
Entries	21
Mean	61.57
Std Dev	1.178



# B1L000S, U24-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch91, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	27.89
Std Dev	4.733

**Gaus mean : 28.8629**

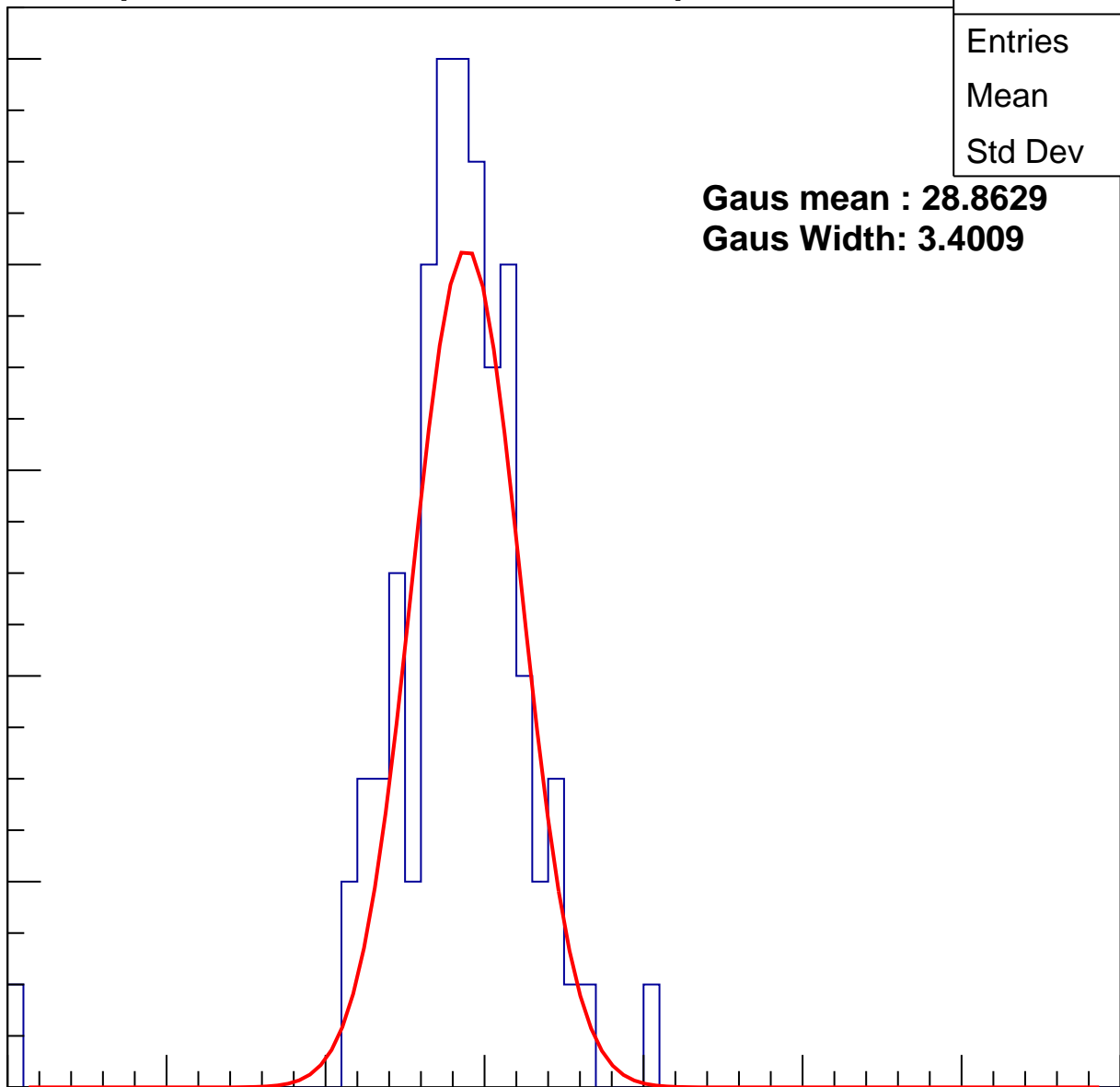
**Gaus Width: 3.4009**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch91, adc1

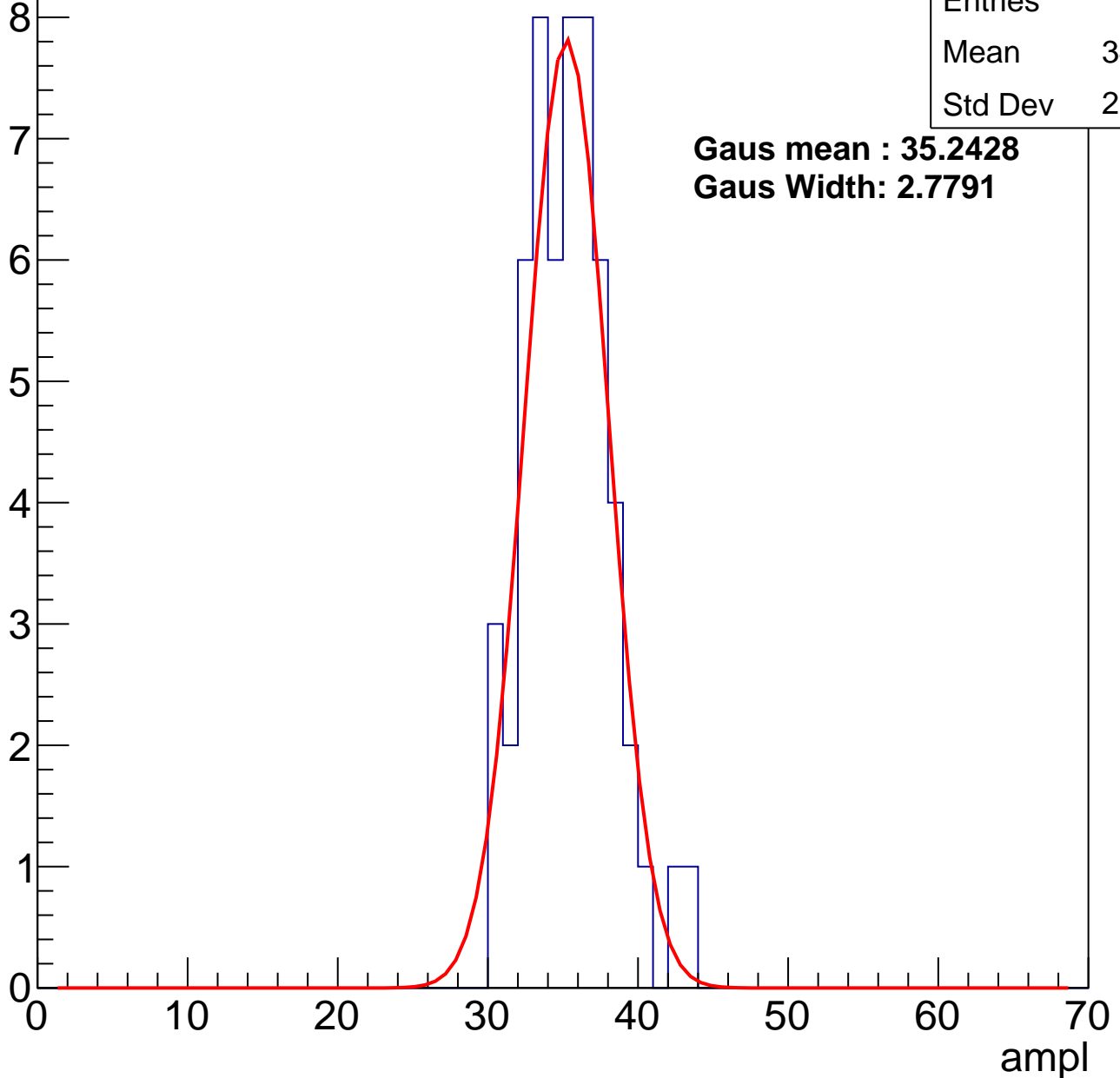
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	34.95
Std Dev	2.793

**Gaus mean : 35.2428**

**Gaus Width: 2.7791**



# B1L000S, U24-ch91, adc2

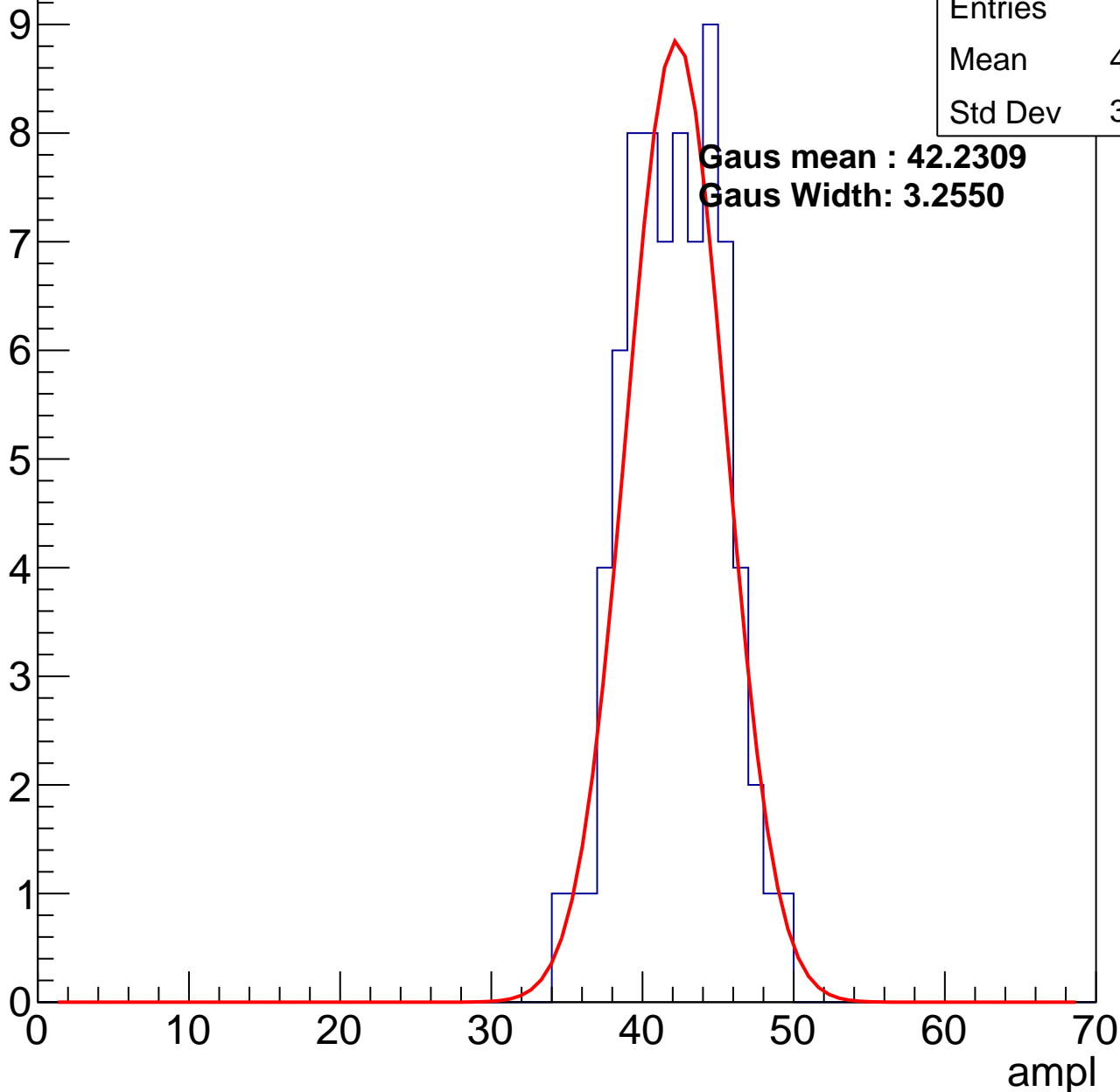
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	41.64
Std Dev	3.165

**Gaus mean : 42.2309**

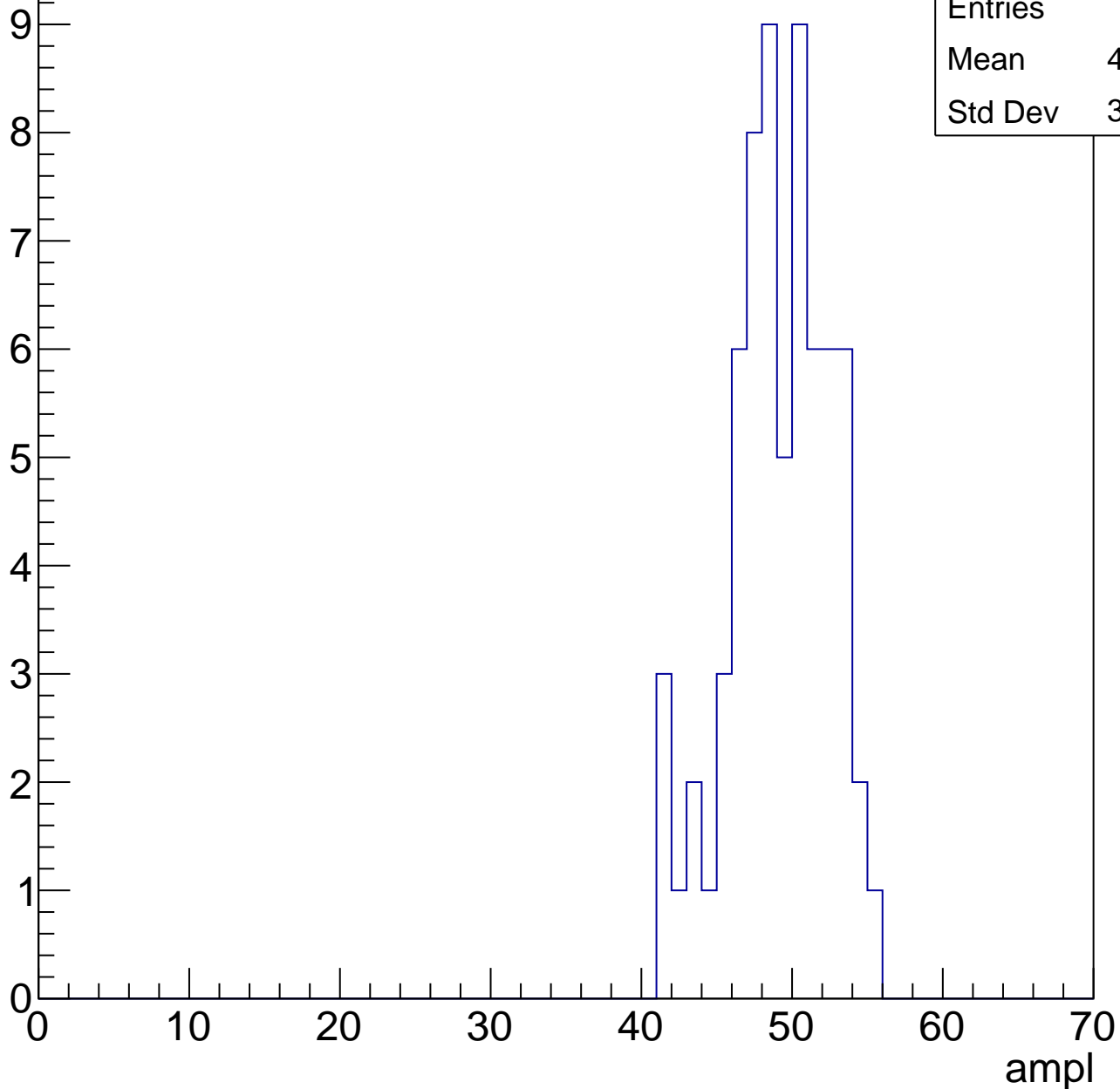
**Gaus Width: 3.2550**



# B1L000S, U24-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

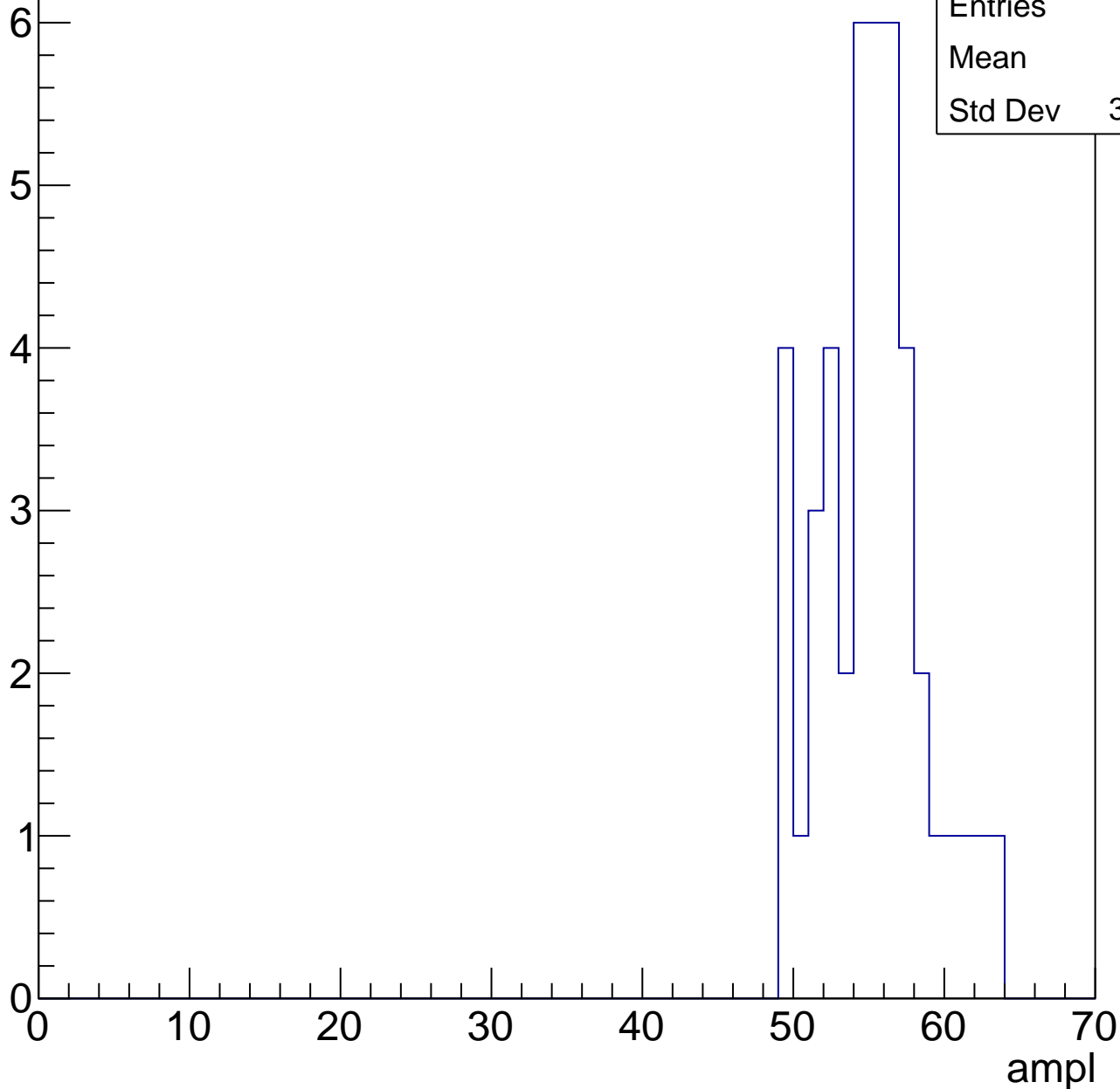


# B1L000S, U24-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

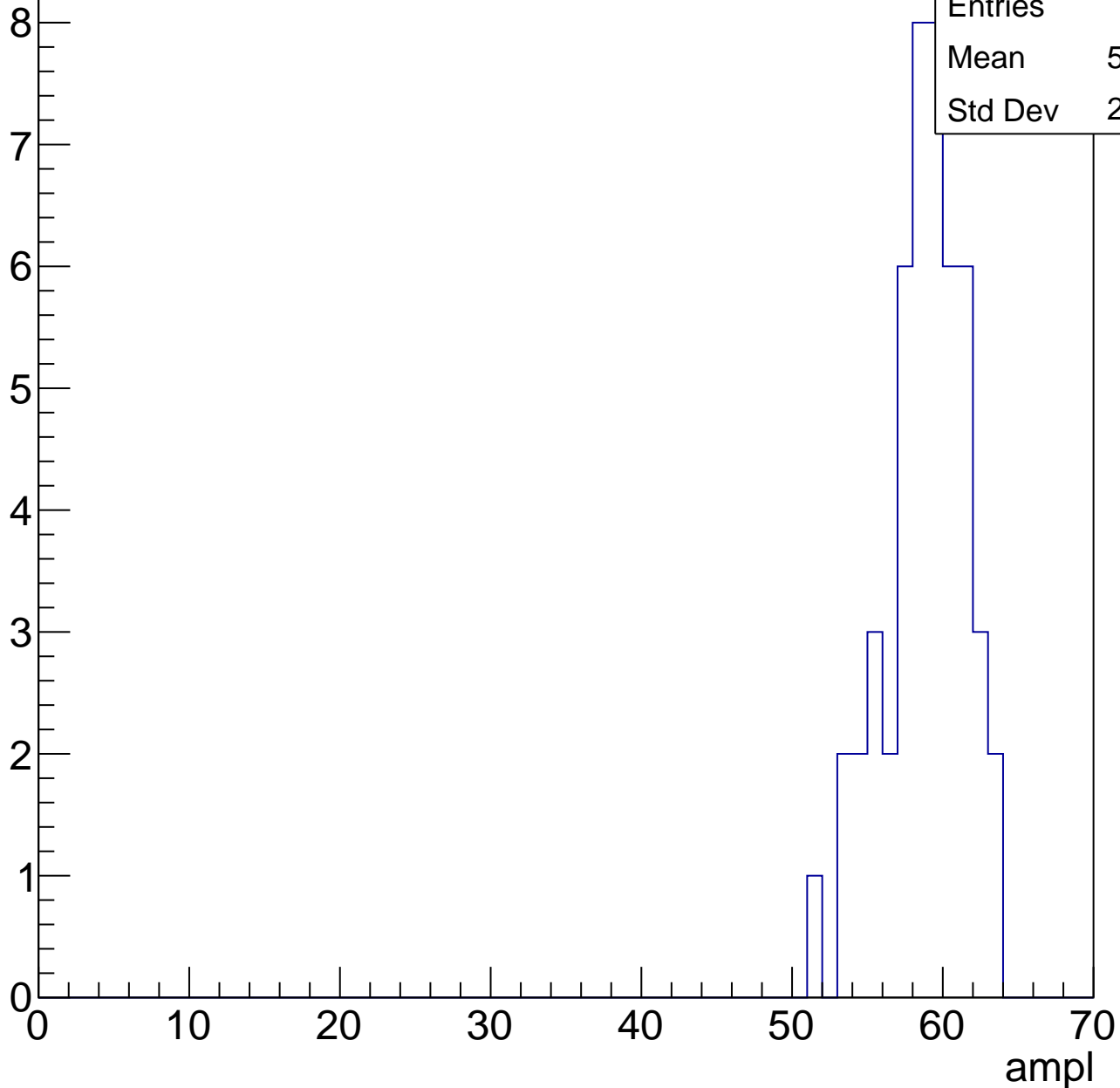
Entries	43
Mean	54.7
Std Dev	3.386



# B1L000S, U24-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

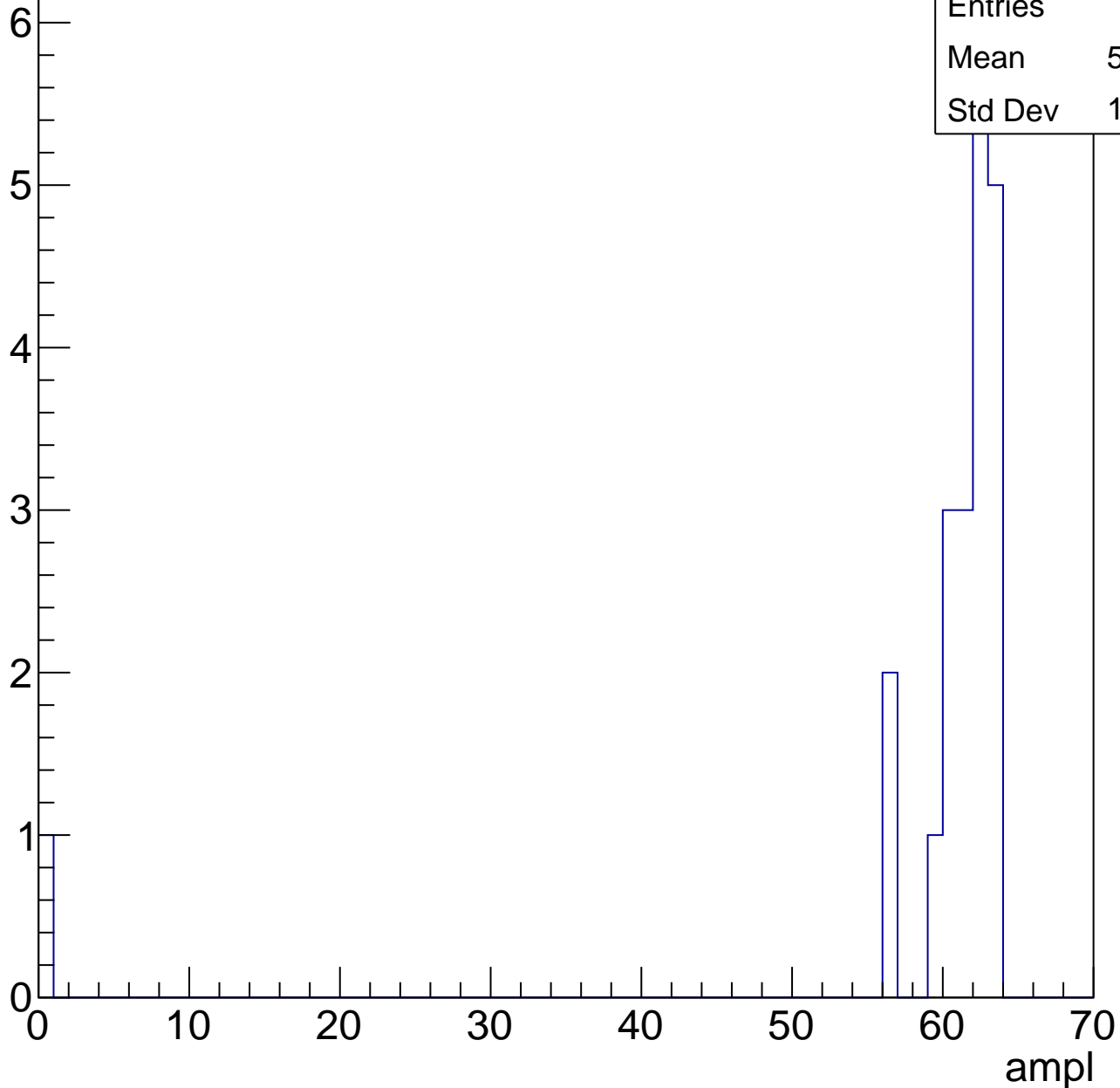


# B1L000S, U24-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.14
Std Dev	13.15





# B1L000S, U24-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch92, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	27.87
Std Dev	3.605

**Gaus mean : 28.1210**

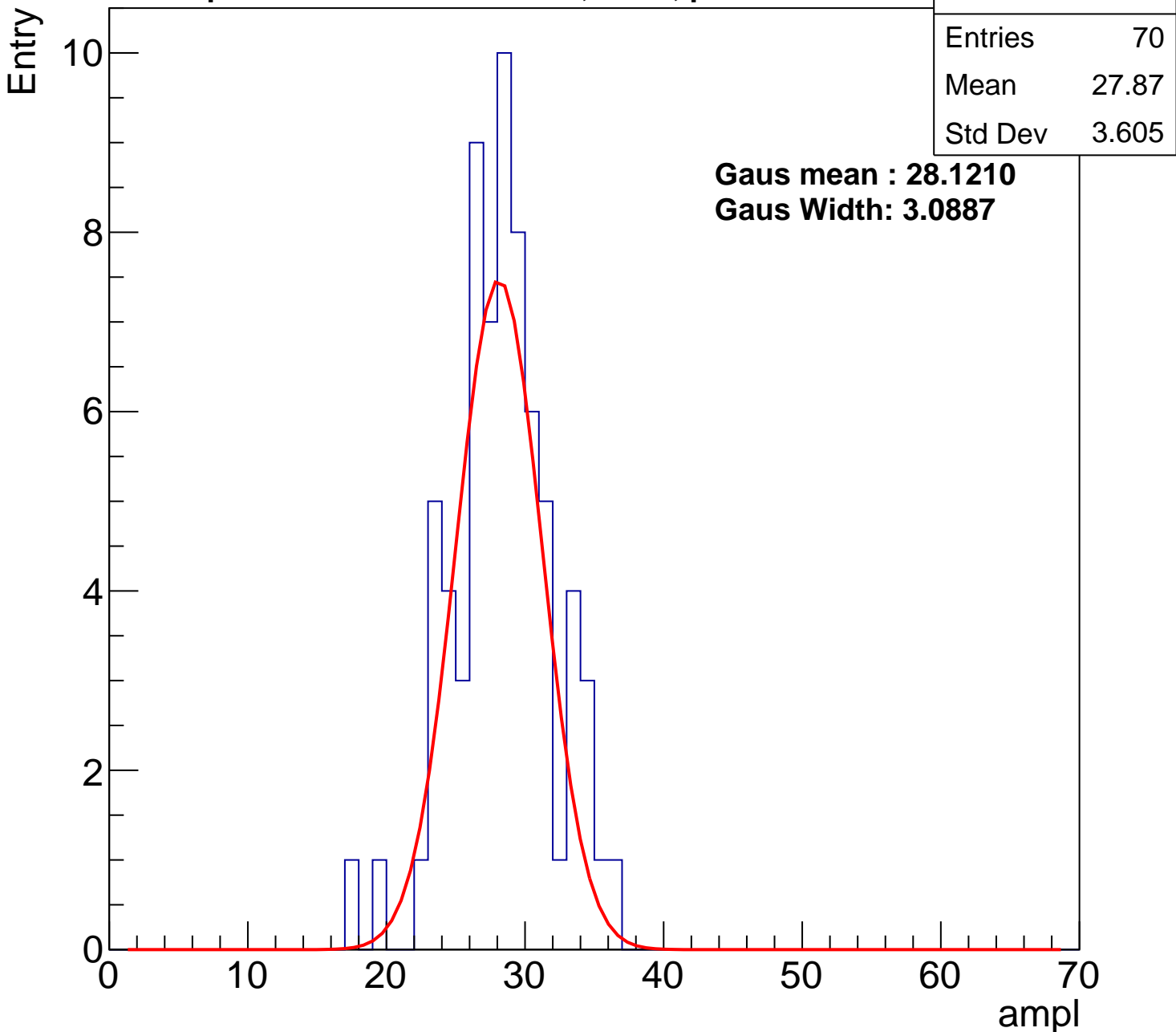
**Gaus Width: 3.0887**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch92, adc1

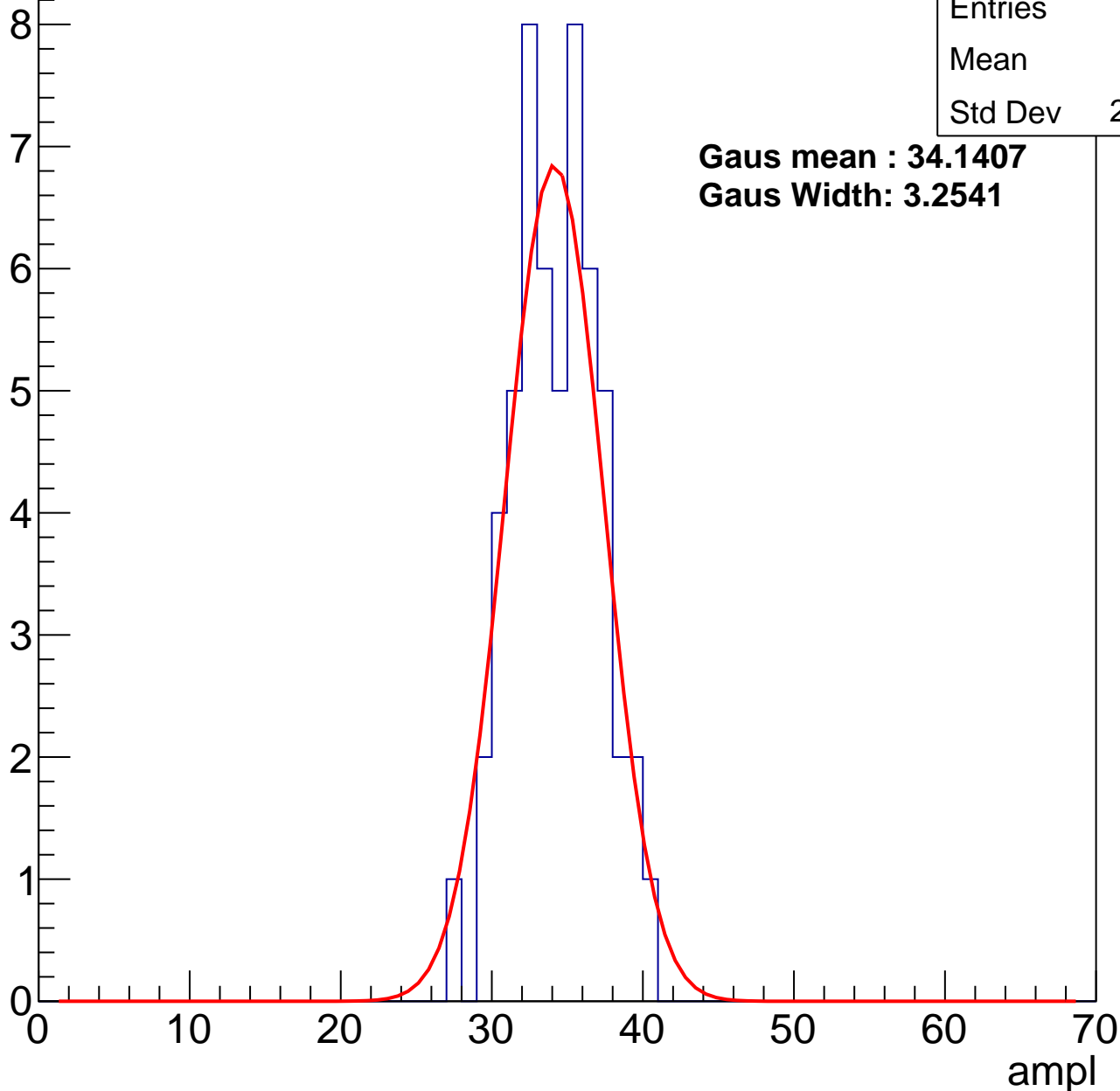
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	33.8
Std Dev	2.825

**Gaus mean : 34.1407**

**Gaus Width: 3.2541**



# B1L000S, U24-ch92, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	40.49
Std Dev	3.112

**Gaus mean : 40.9586**

**Gaus Width: 2.9808**

10

8

6

4

2

0

0

10

20

30

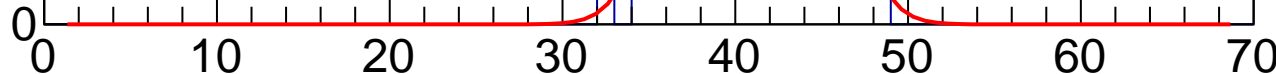
40

50

60

70

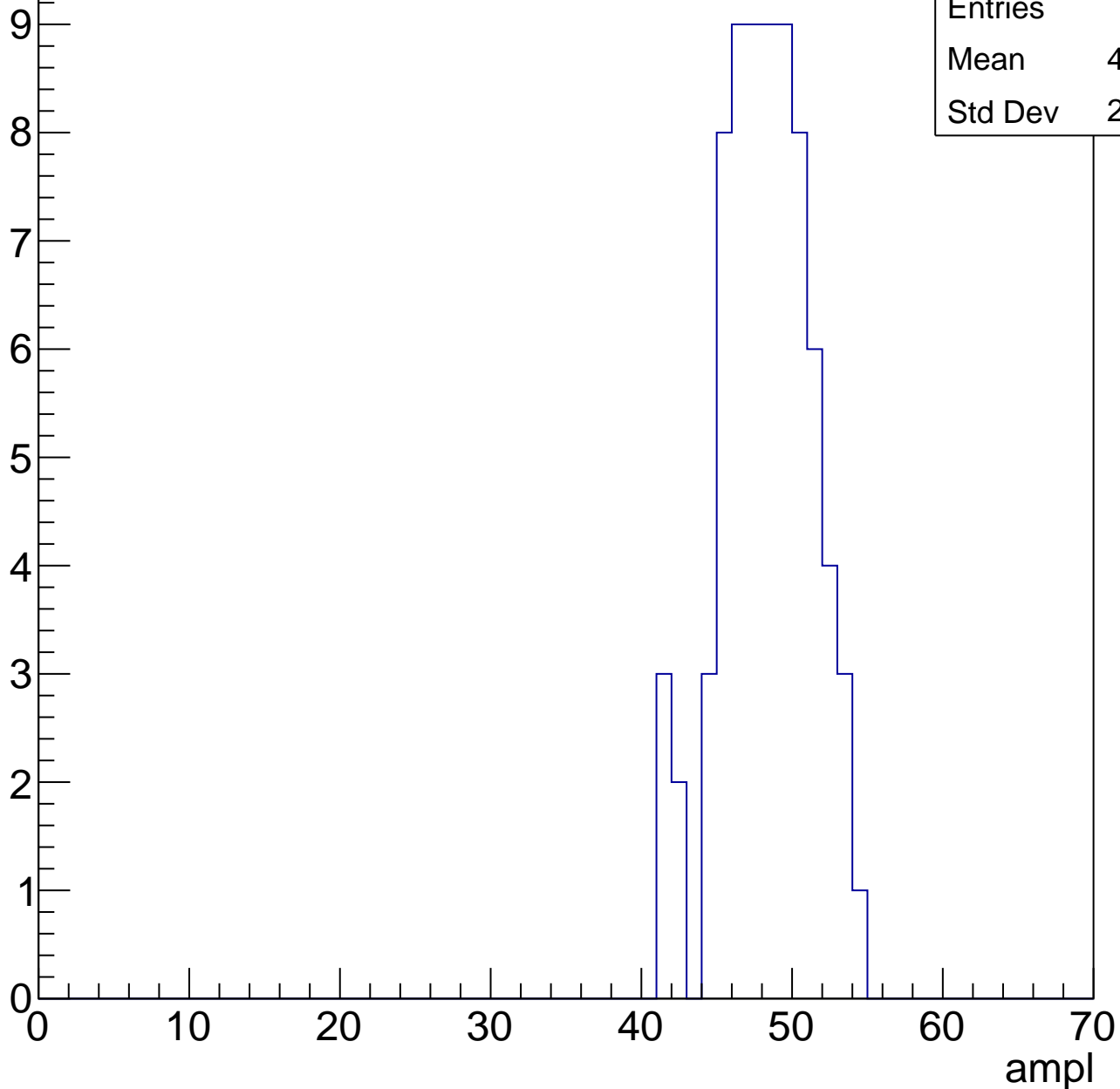
ampl



# B1L000S, U24-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

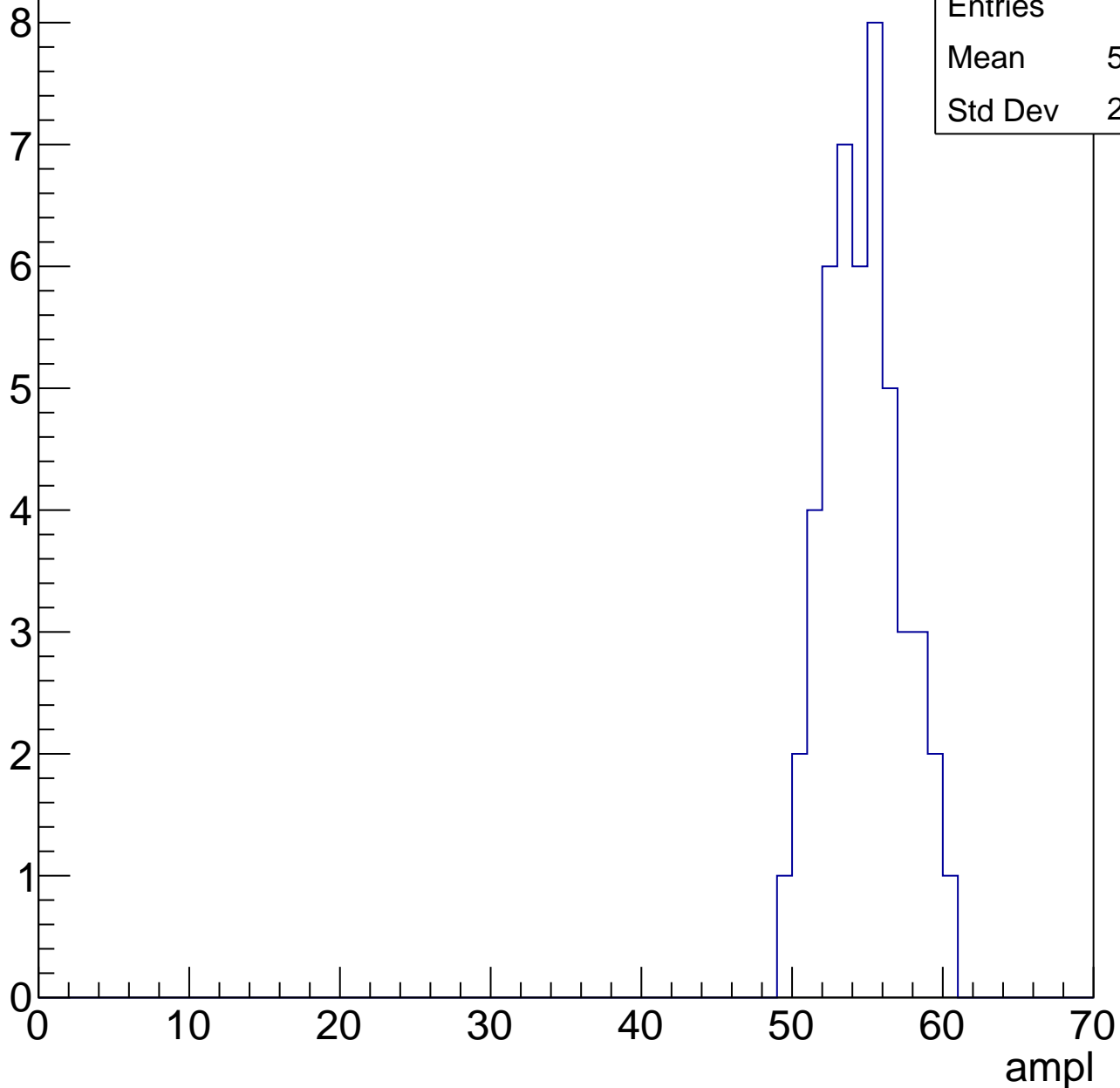


Entries	74
Mean	47.78
Std Dev	2.965

# B1L000S, U24-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	48
Mean	54.23
Std Dev	2.543

# B1L000S, U24-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

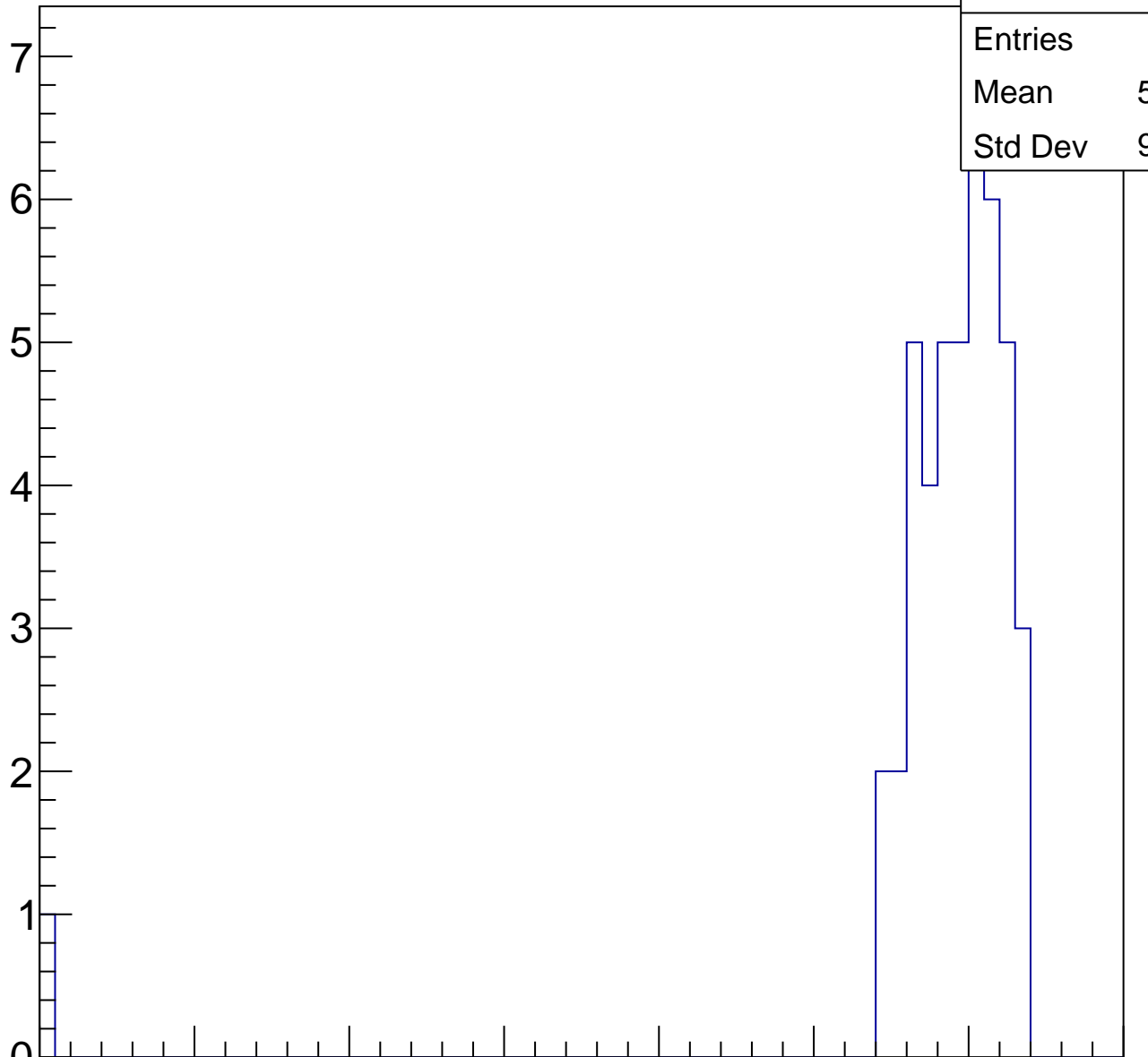
Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	57.69
Std Dev	9.038

ampl

0 10 20 30 40 50 60 70

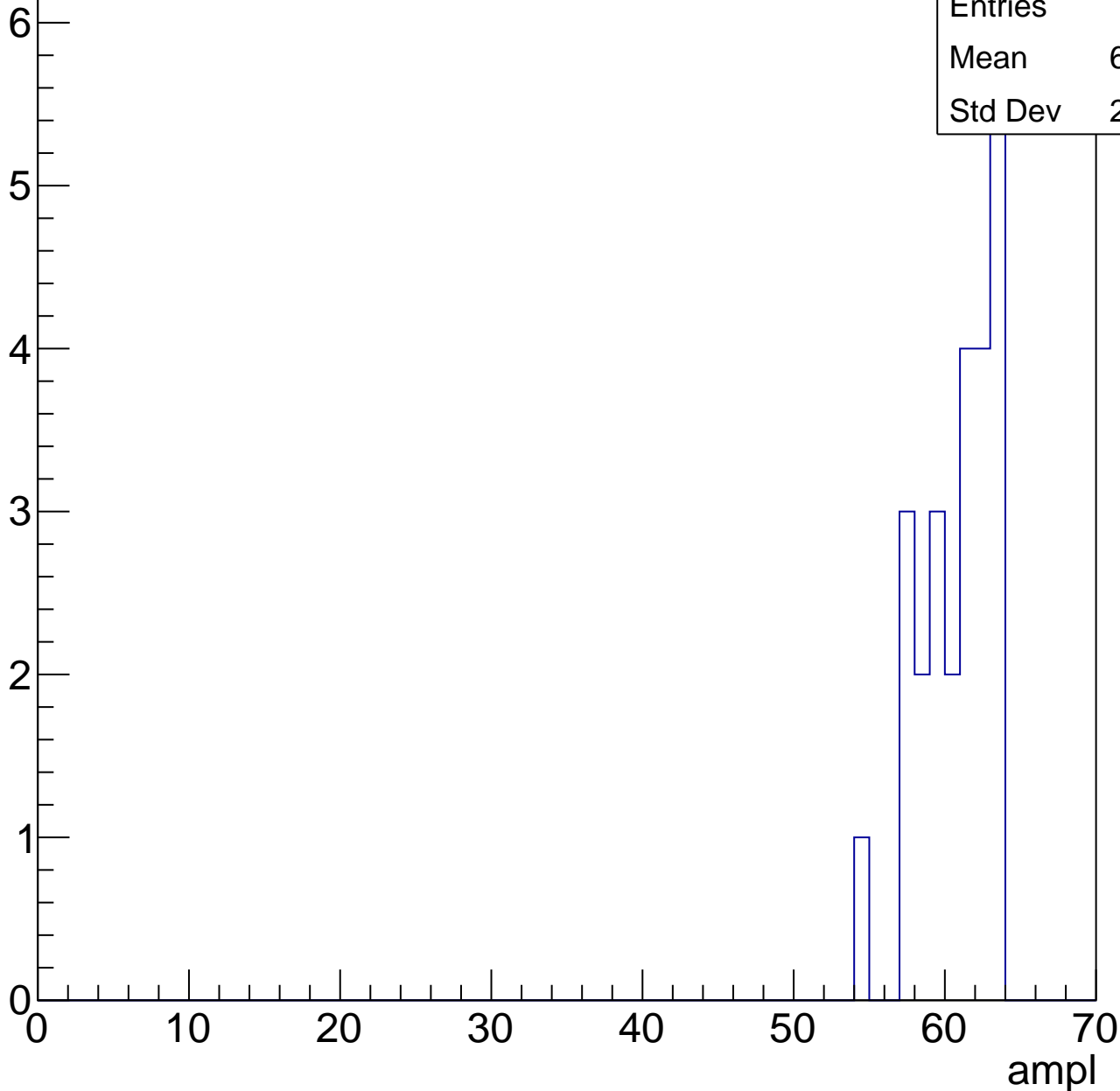


# B1L000S, U24-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	60.32
Std Dev	2.412





# B1L000S, U24-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch93, adc0

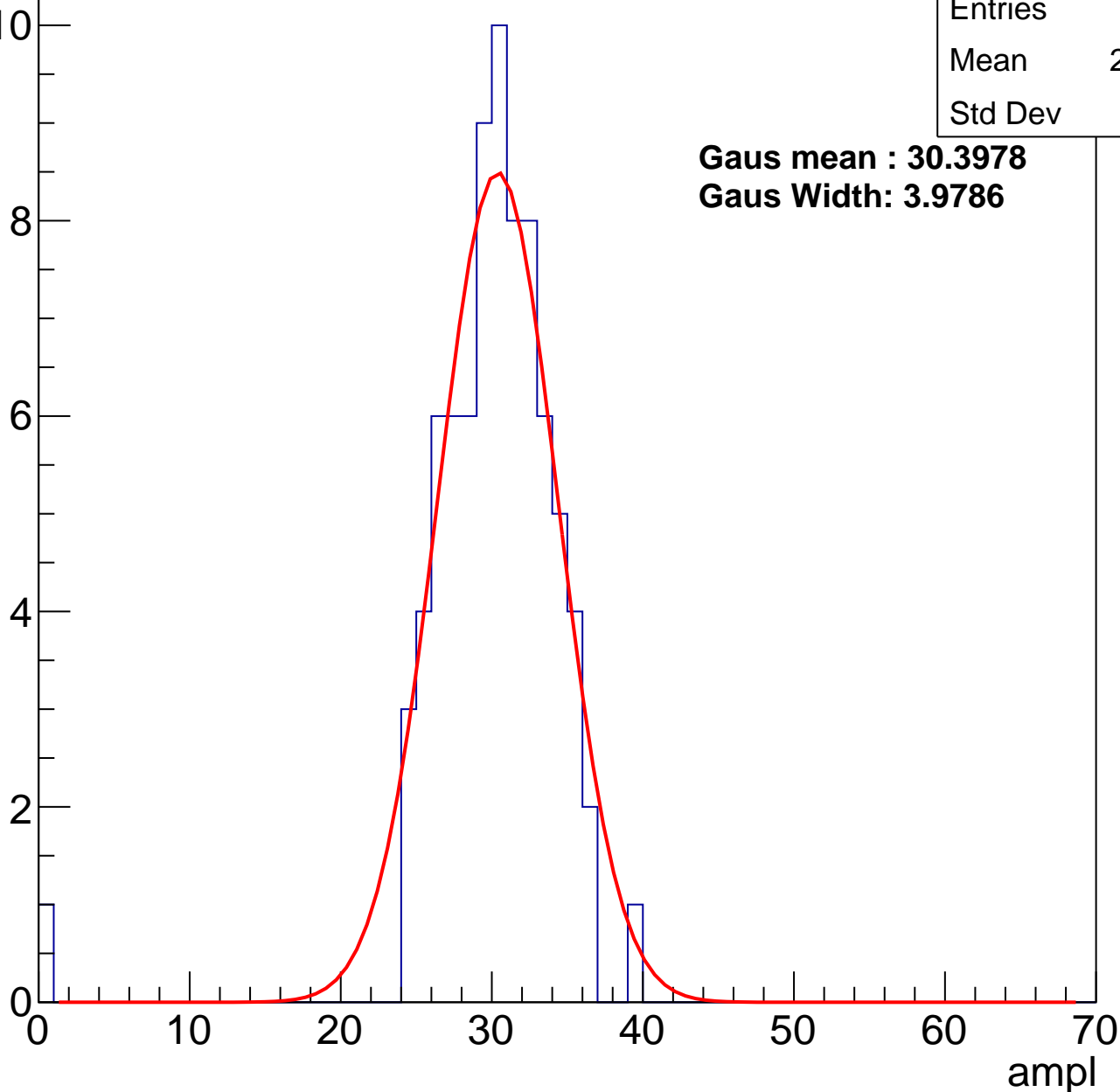
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	29.65
Std Dev	4.65

**Gaus mean : 30.3978**

**Gaus Width: 3.9786**



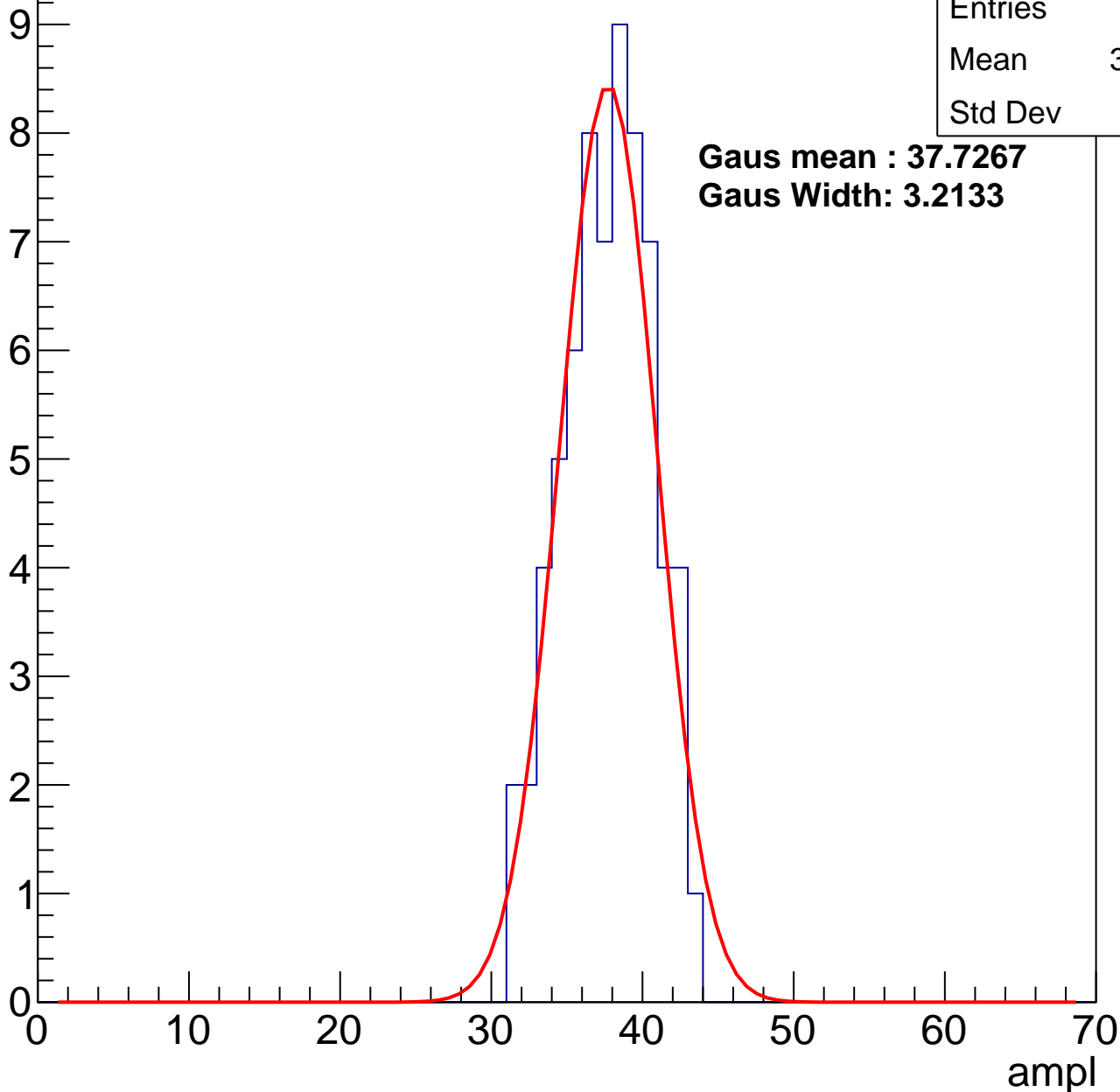
# B1L000S, U24-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	37.22
Std Dev	2.9

**Gaus mean : 37.7267**  
**Gaus Width: 3.2133**



# B1L000S, U24-ch93, adc2

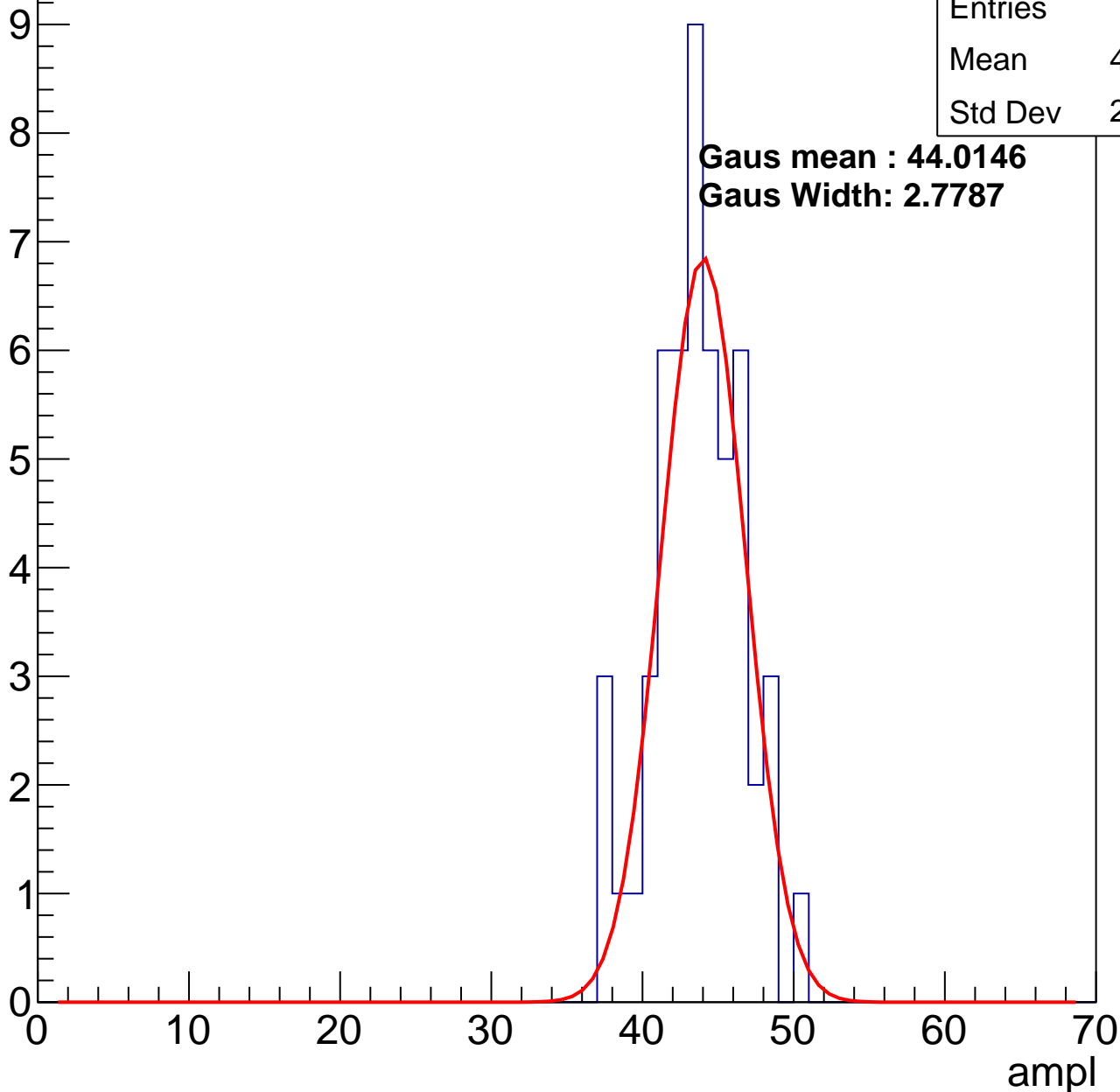
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	43.19
Std Dev	2.909

**Gaus mean : 44.0146**

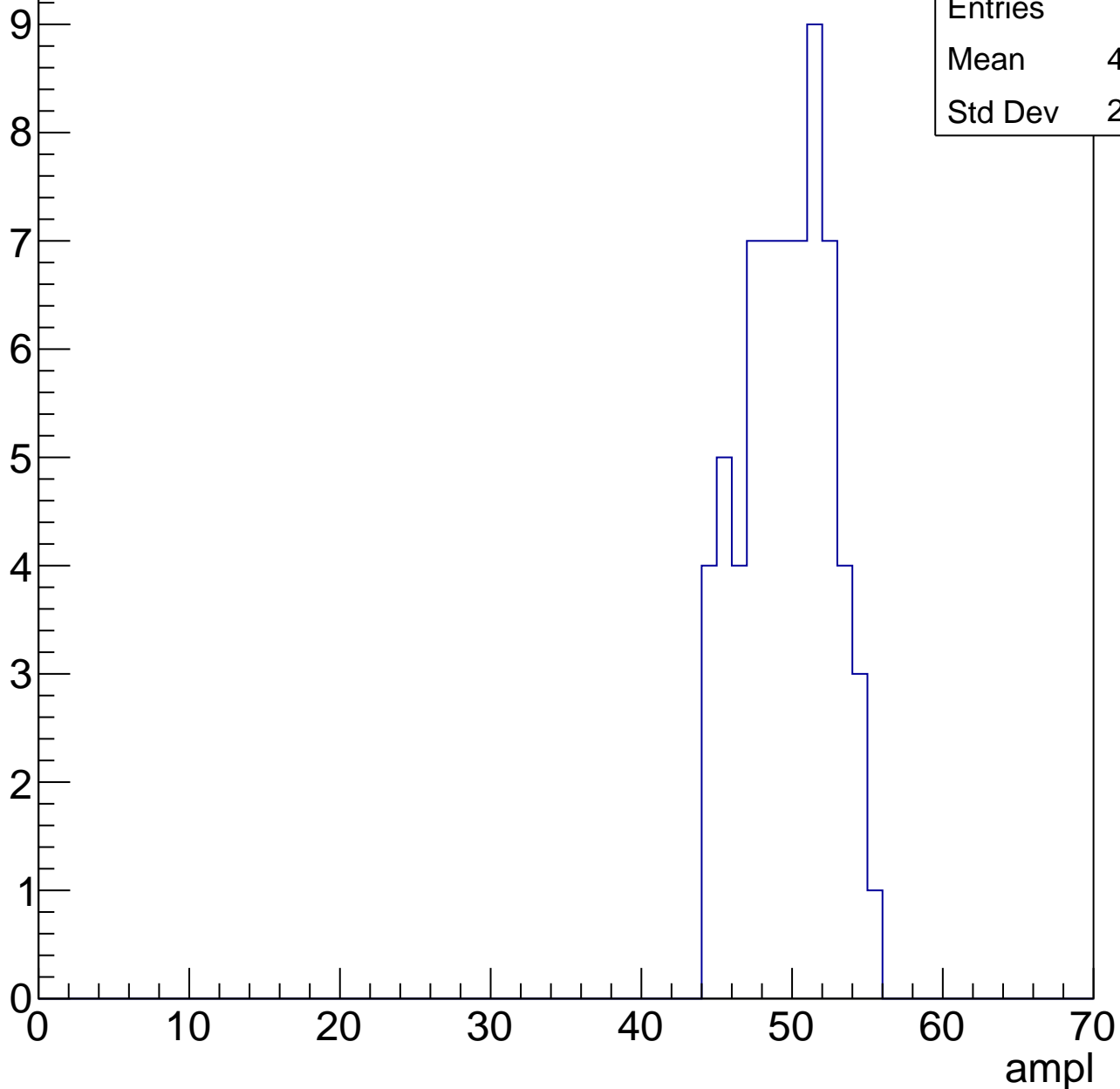
**Gaus Width: 2.7787**



# B1L000S, U24-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



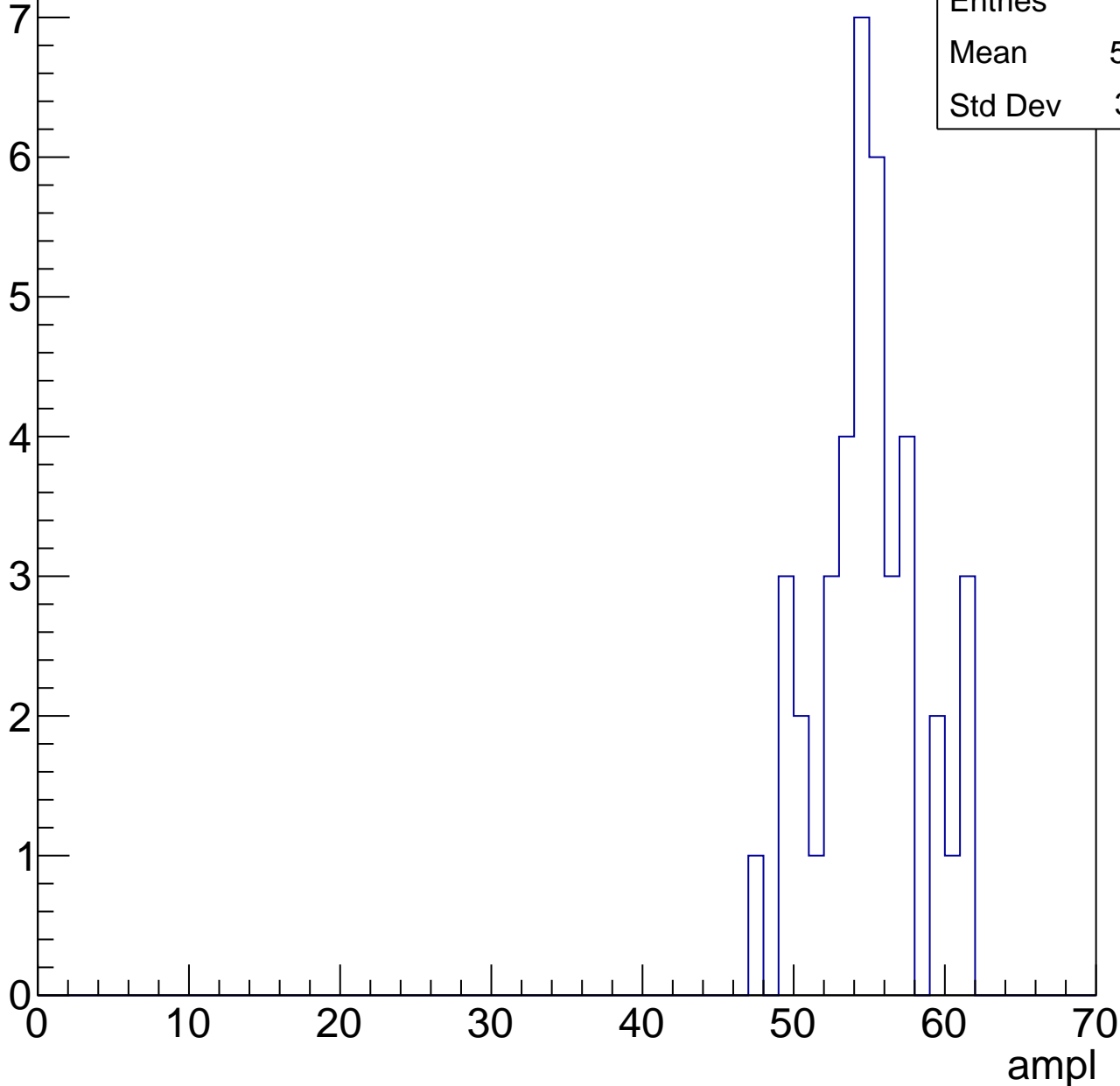
Entries	65
Mean	49.15
Std Dev	2.857

# B1L000S, U24-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	54.45
Std Dev	3.391

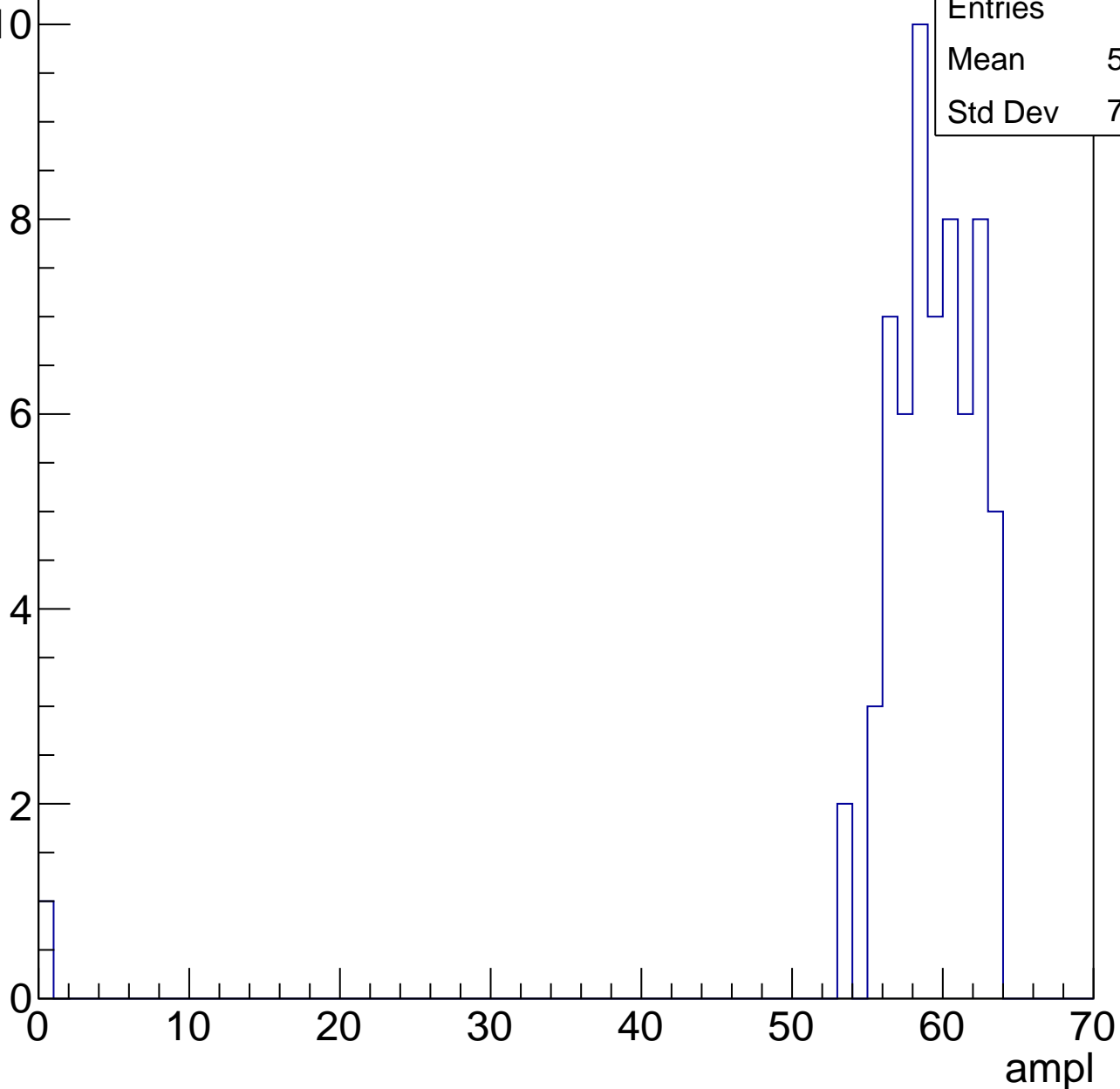


# B1L000S, U24-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

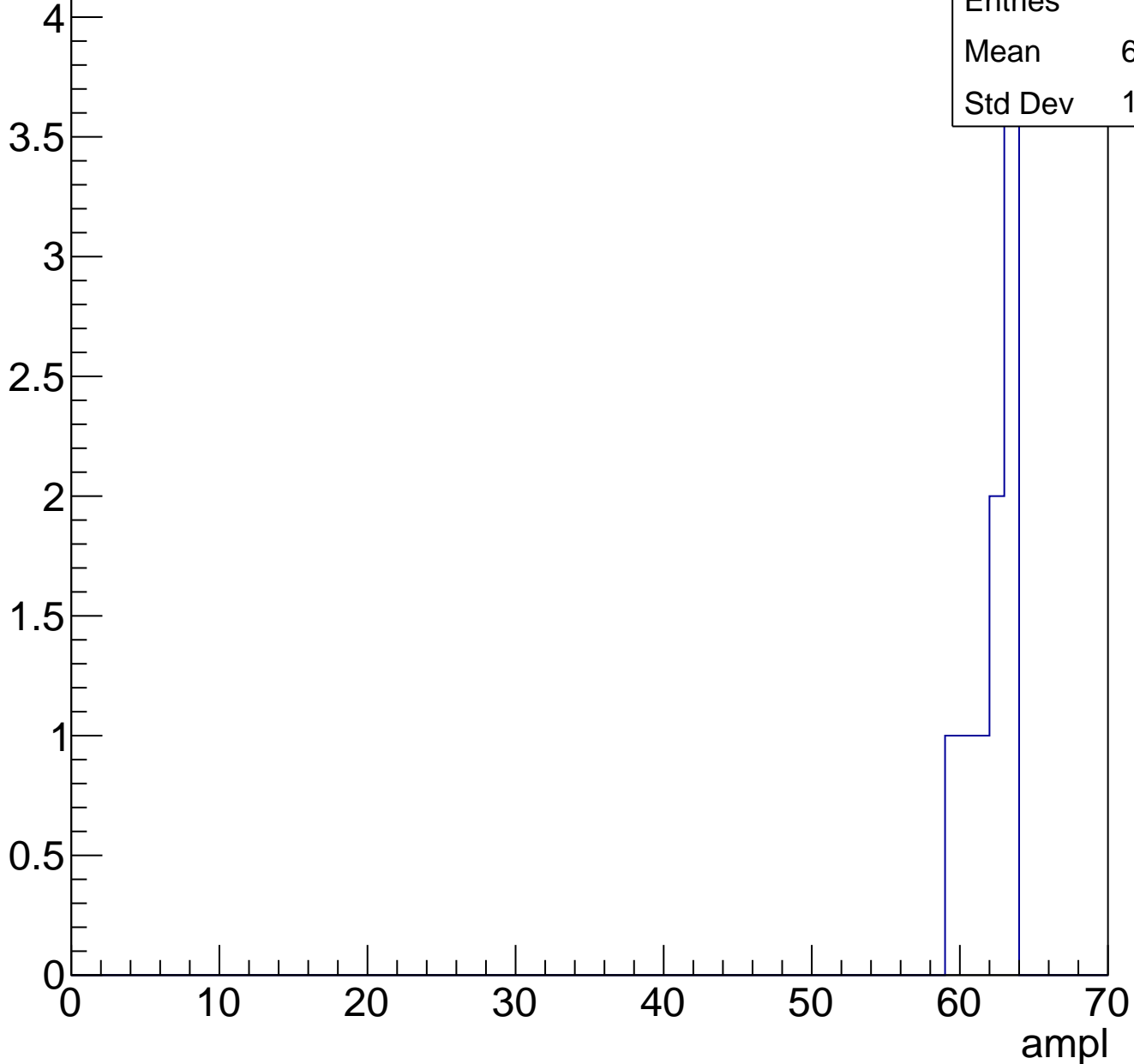
Entries	63
Mean	58.02
Std Dev	7.788



# B1L000S, U24-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U24-ch94, adc0

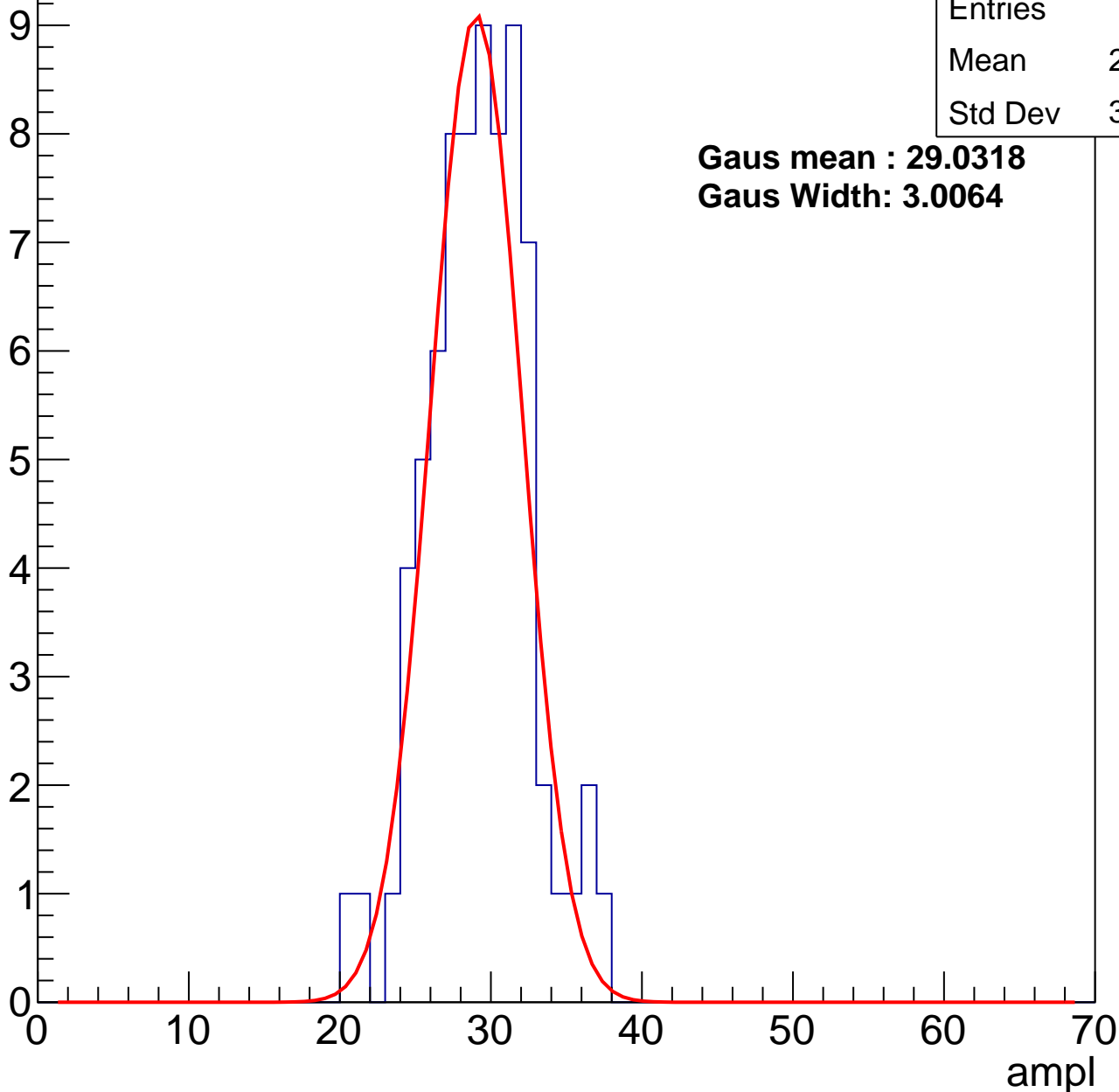
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	28.77
Std Dev	3.315

**Gaus mean : 29.0318**

**Gaus Width: 3.0064**

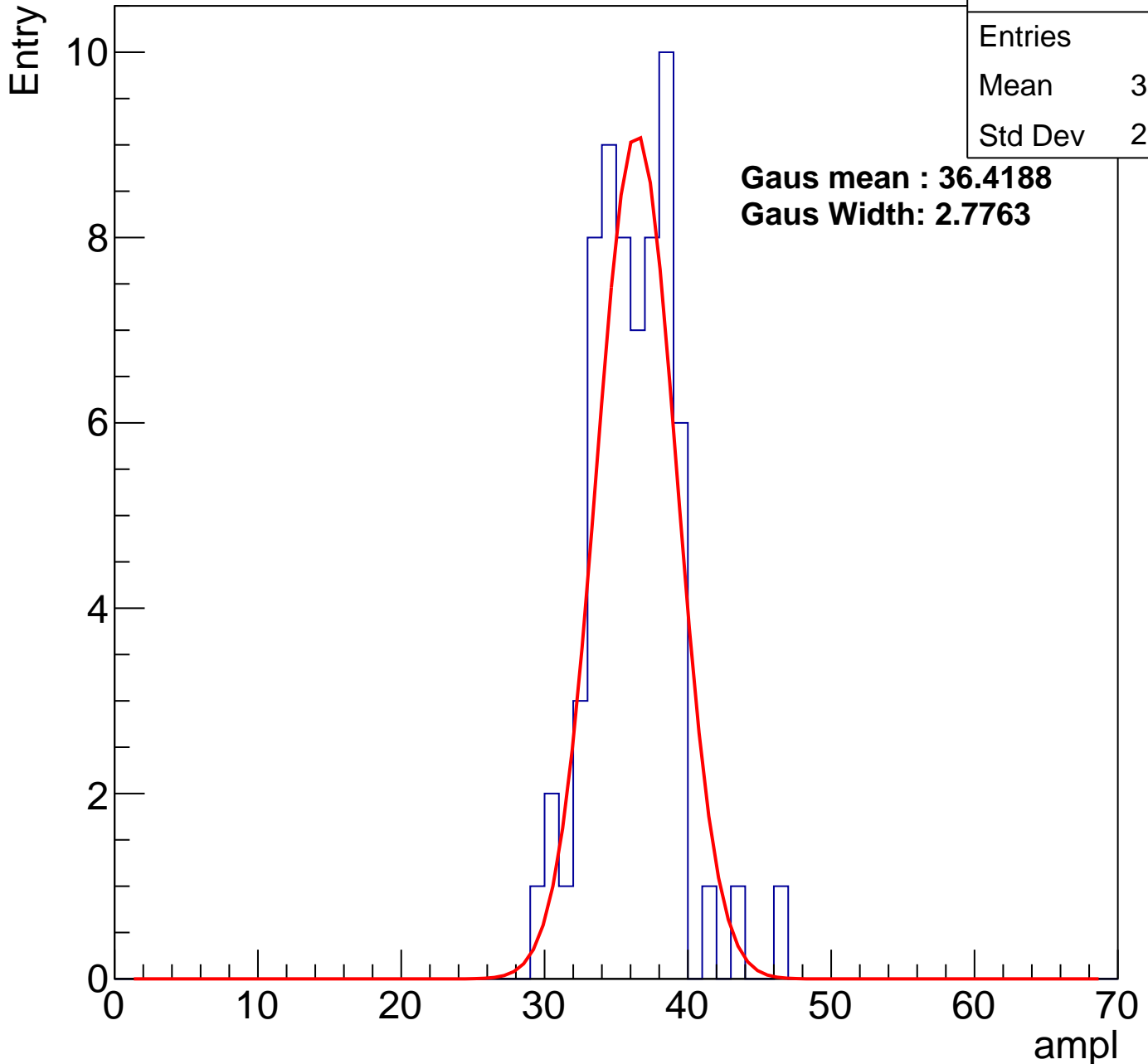


# B1L000S, U24-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	35.73
Std Dev	2.967

**Gaus mean : 36.4188**  
**Gaus Width: 2.7763**



# B1L000S, U24-ch94, adc2

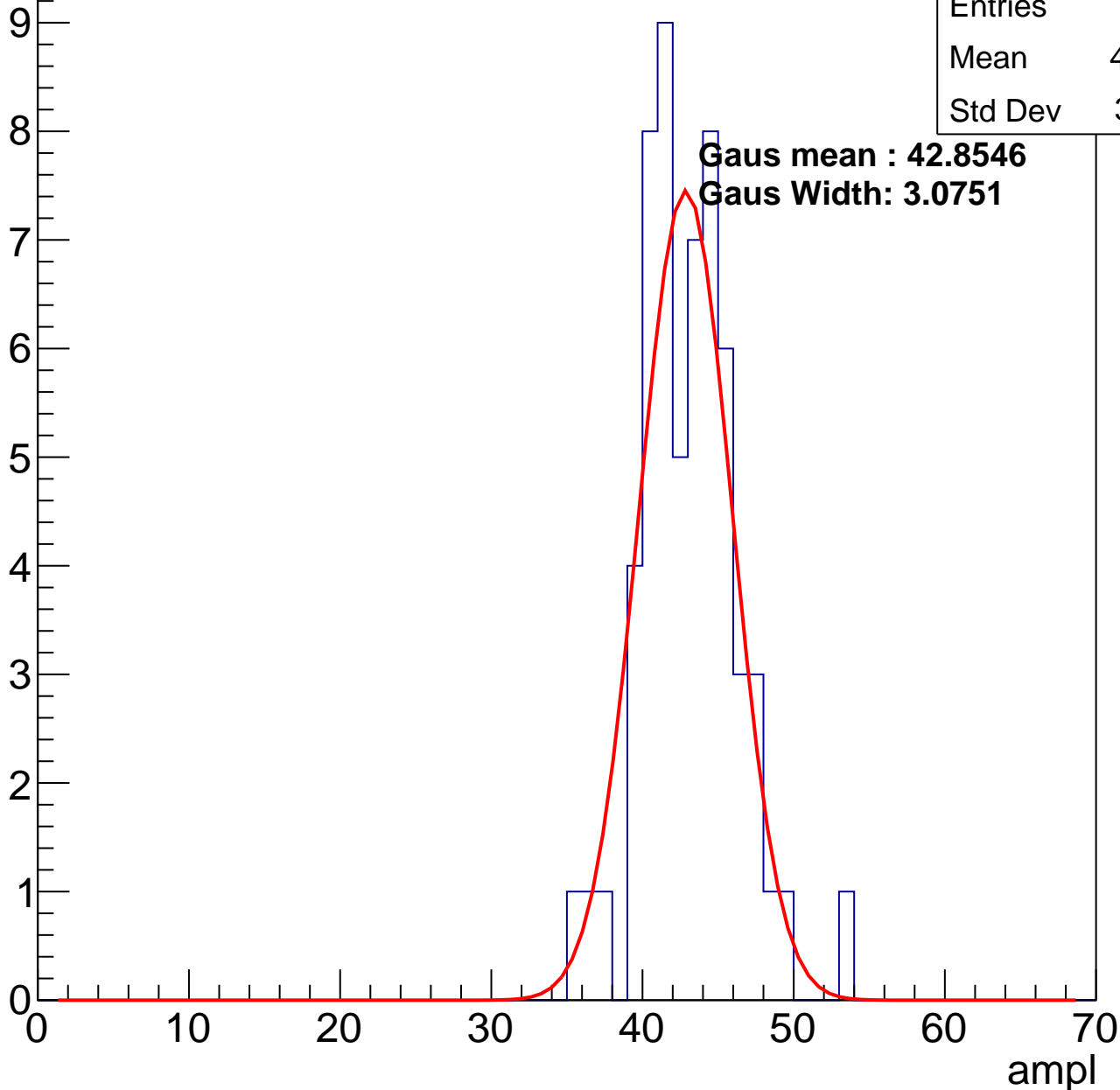
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.63
Std Dev	3.151

**Gaus mean : 42.8546**

**Gaus Width: 3.0751**

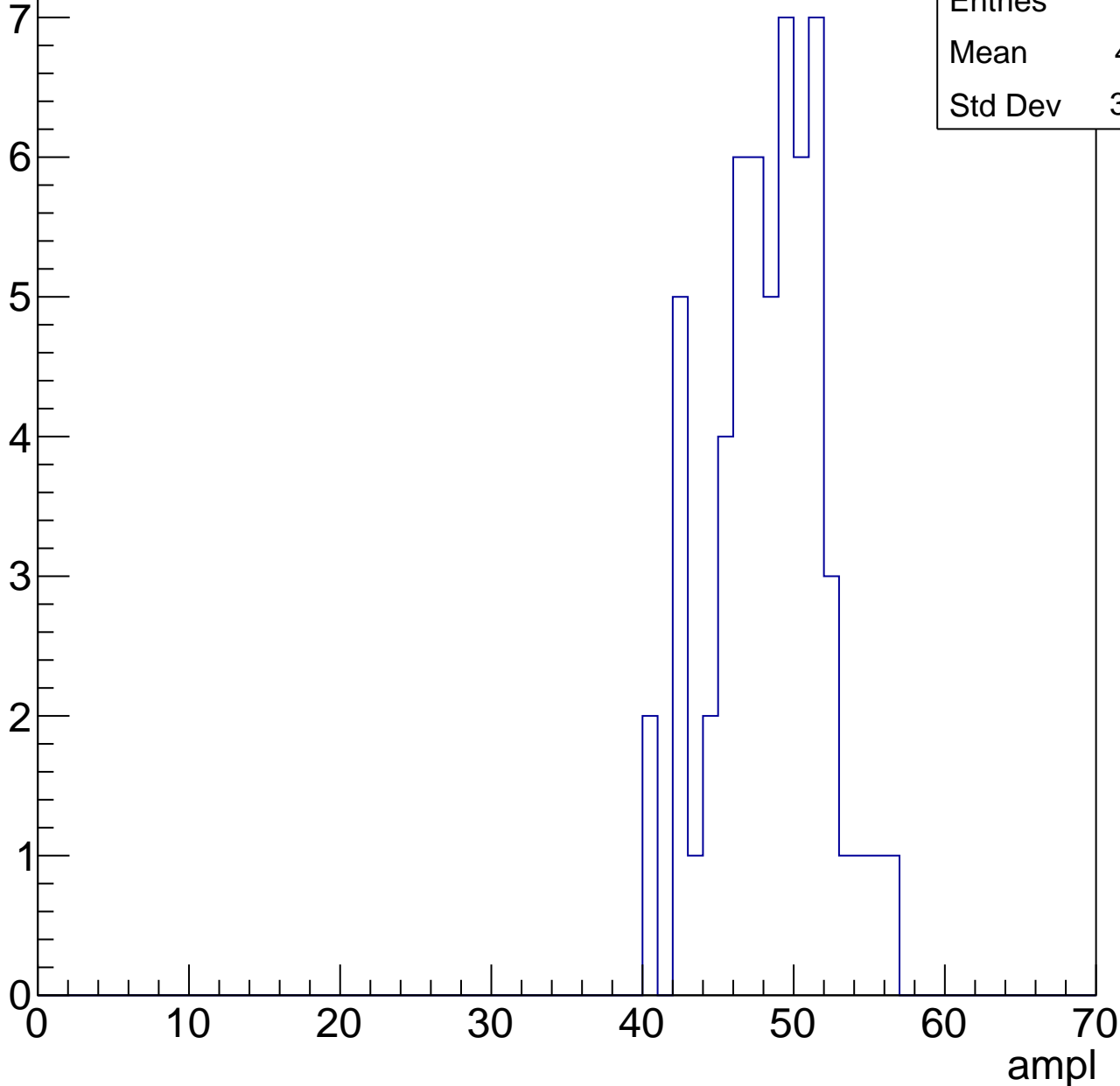


# B1L000S, U24-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	47.81
Std Dev	3.584

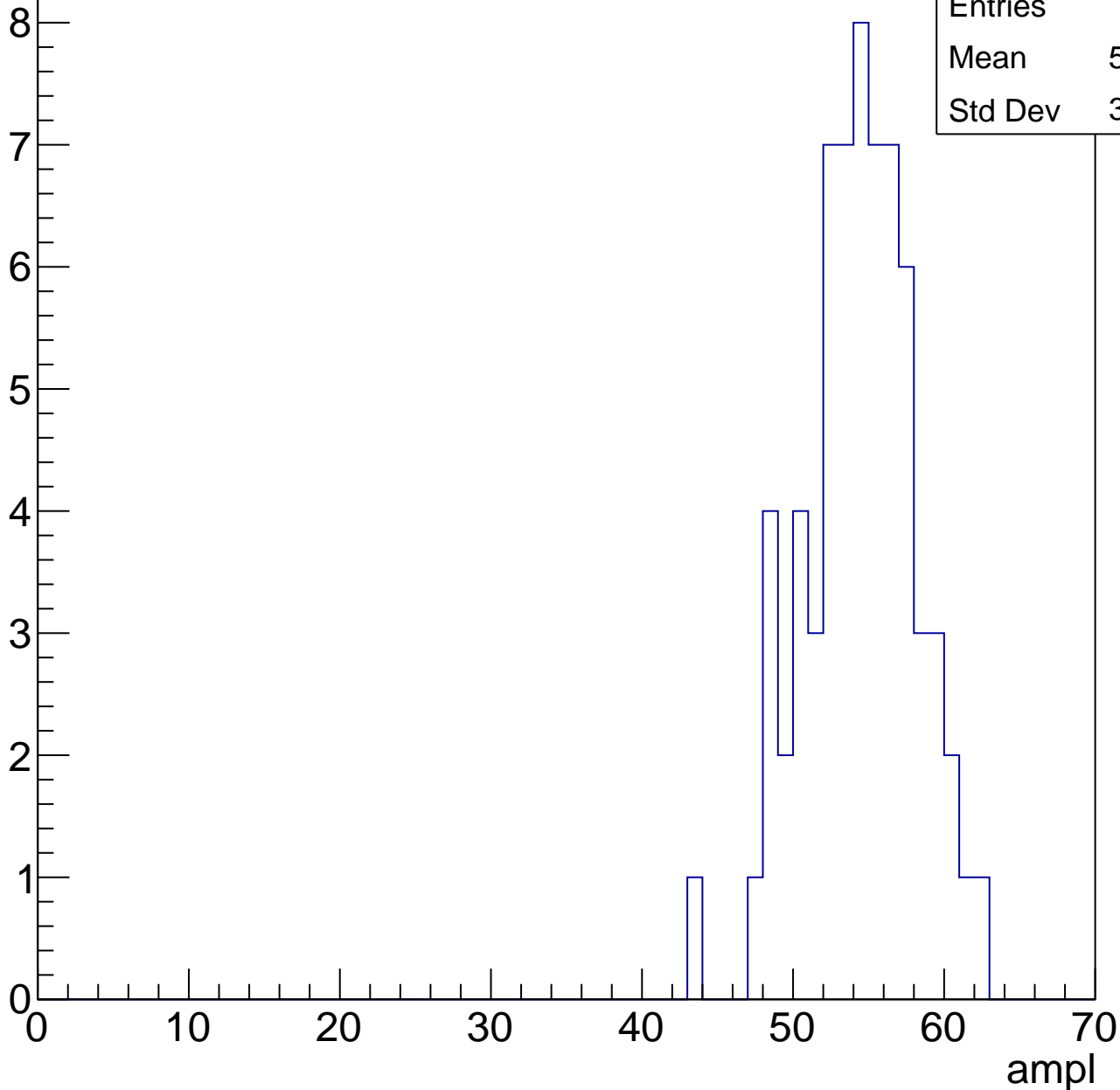


# B1L000S, U24-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	53.93
Std Dev	3.638

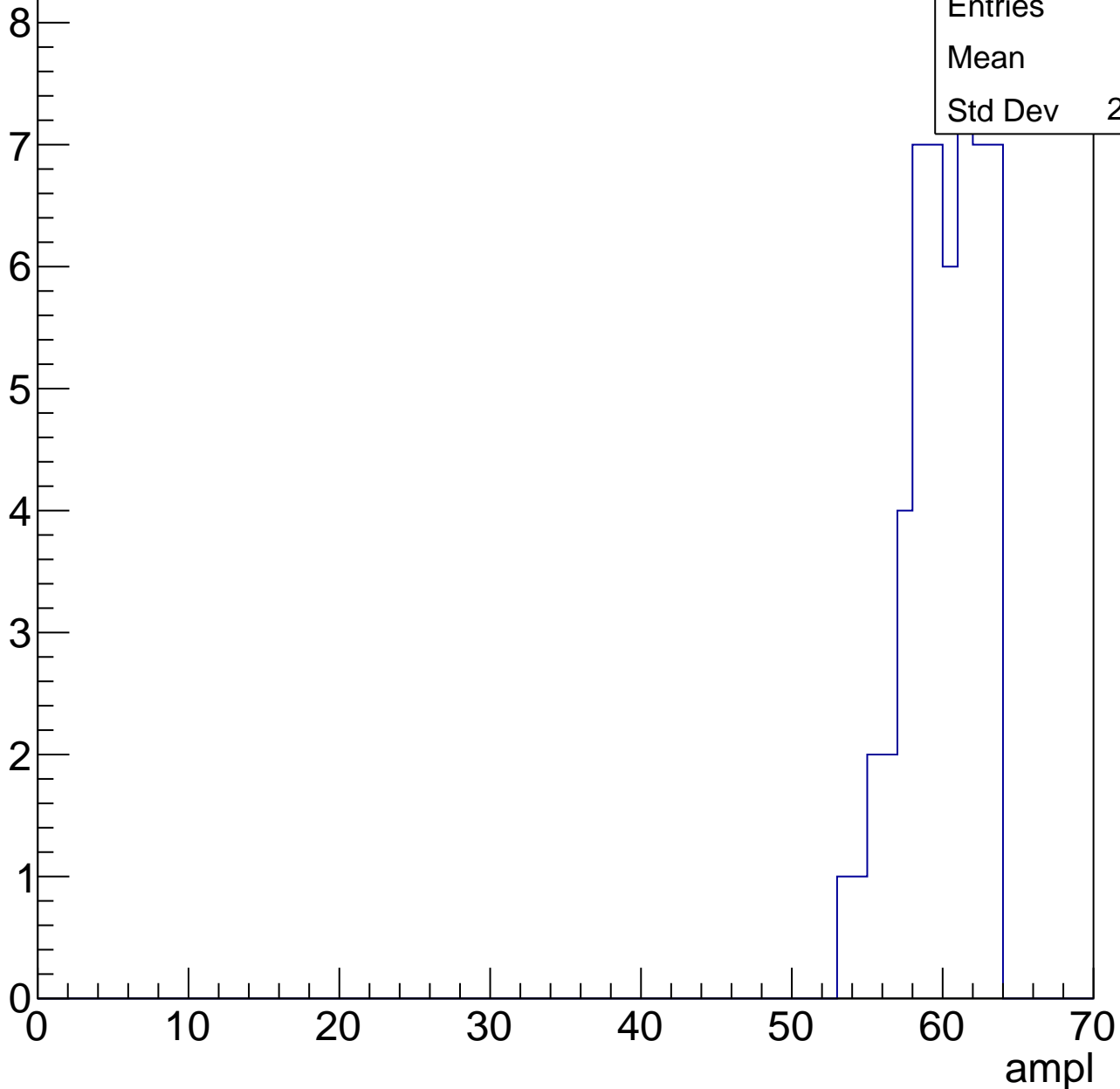


# B1L000S, U24-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	59.6
Std Dev	2.513



# B1L000S, U24-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

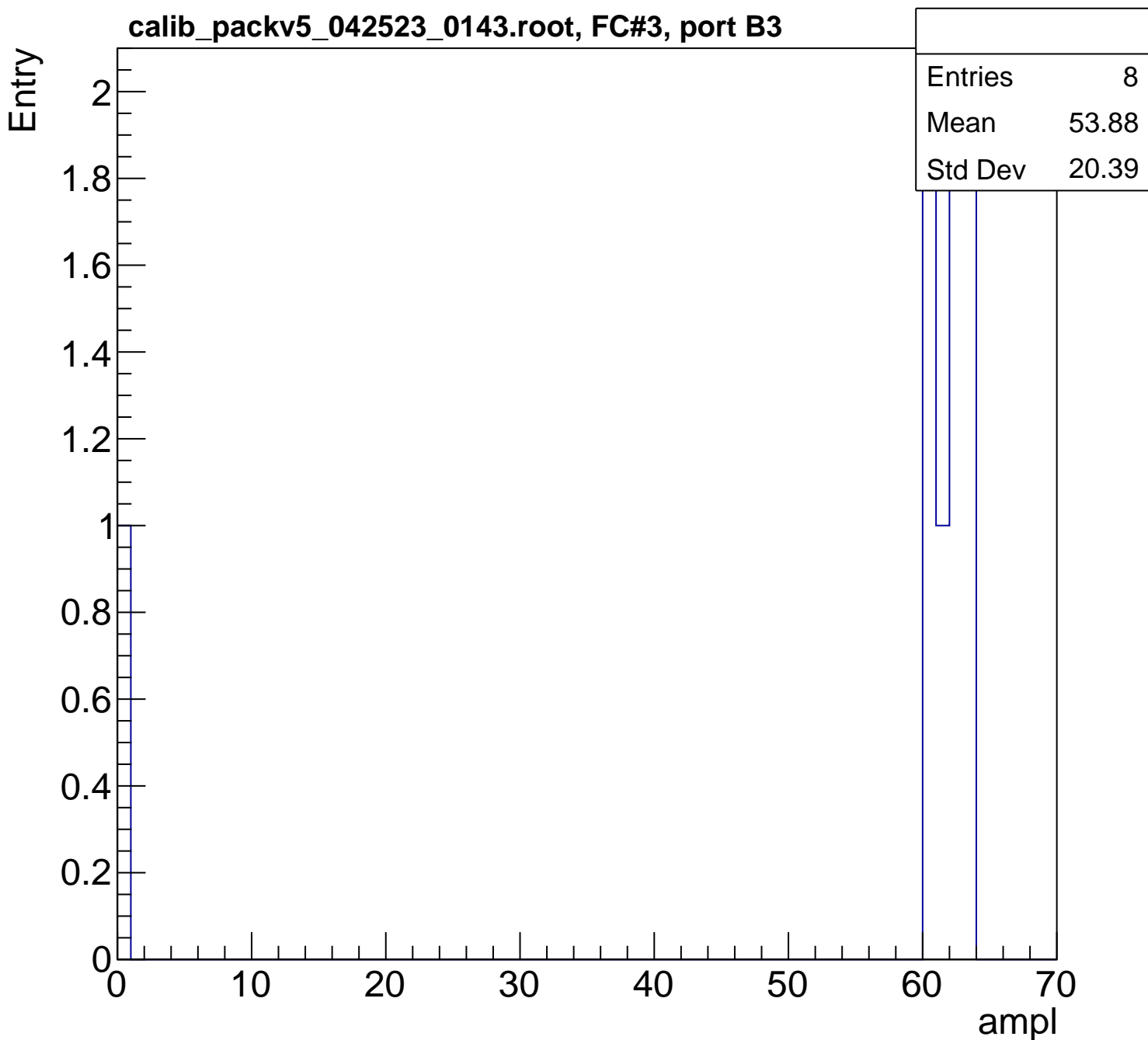
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.88
Std Dev	20.39

0 10 20 30 40 50 60 70

ampl





# B1L000S, U24-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U24-ch95, adc0

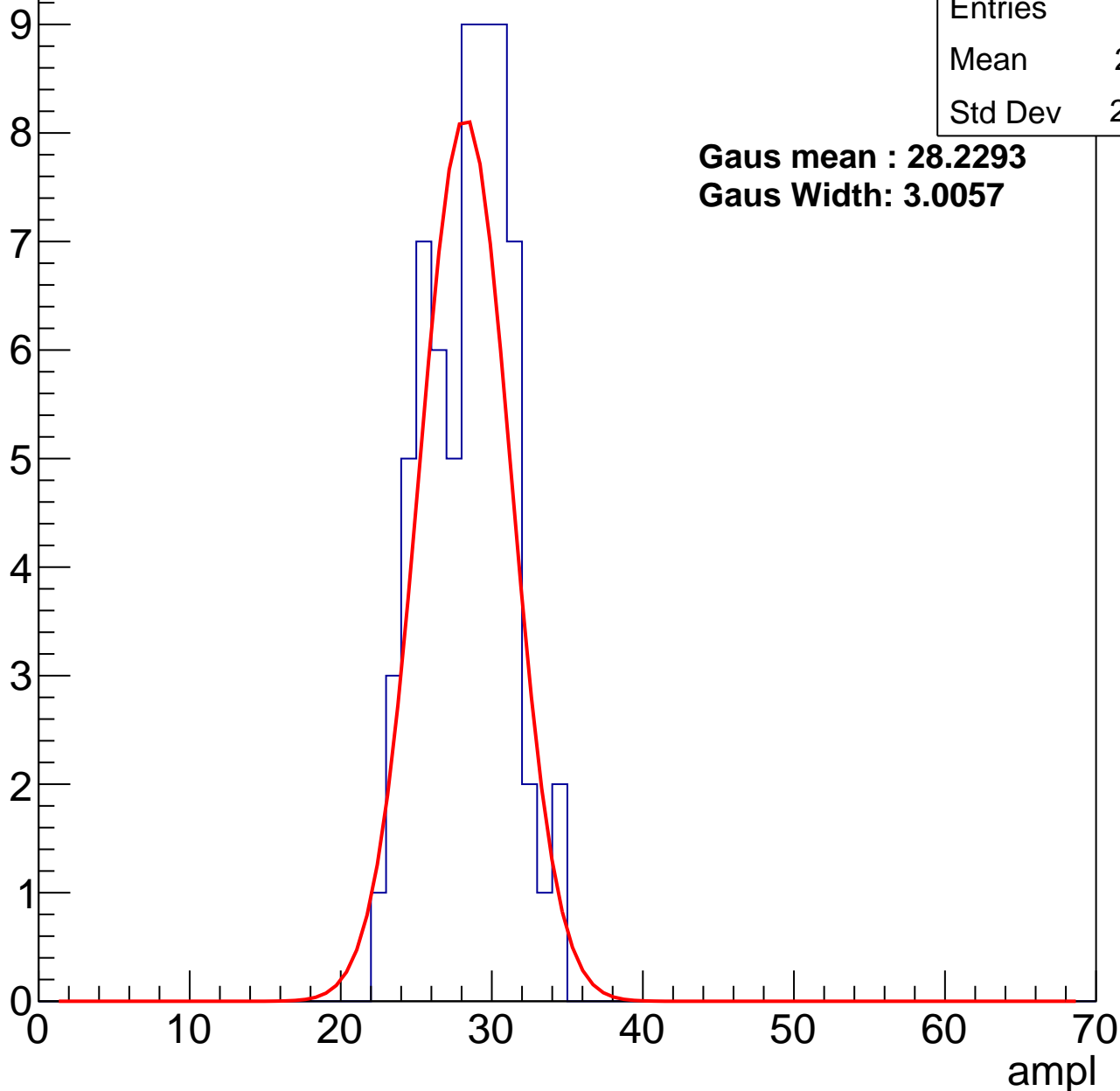
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.91
Std Dev	2.805

**Gaus mean : 28.2293**

**Gaus Width: 3.0057**



# B1L000S, U24-ch95, adc1

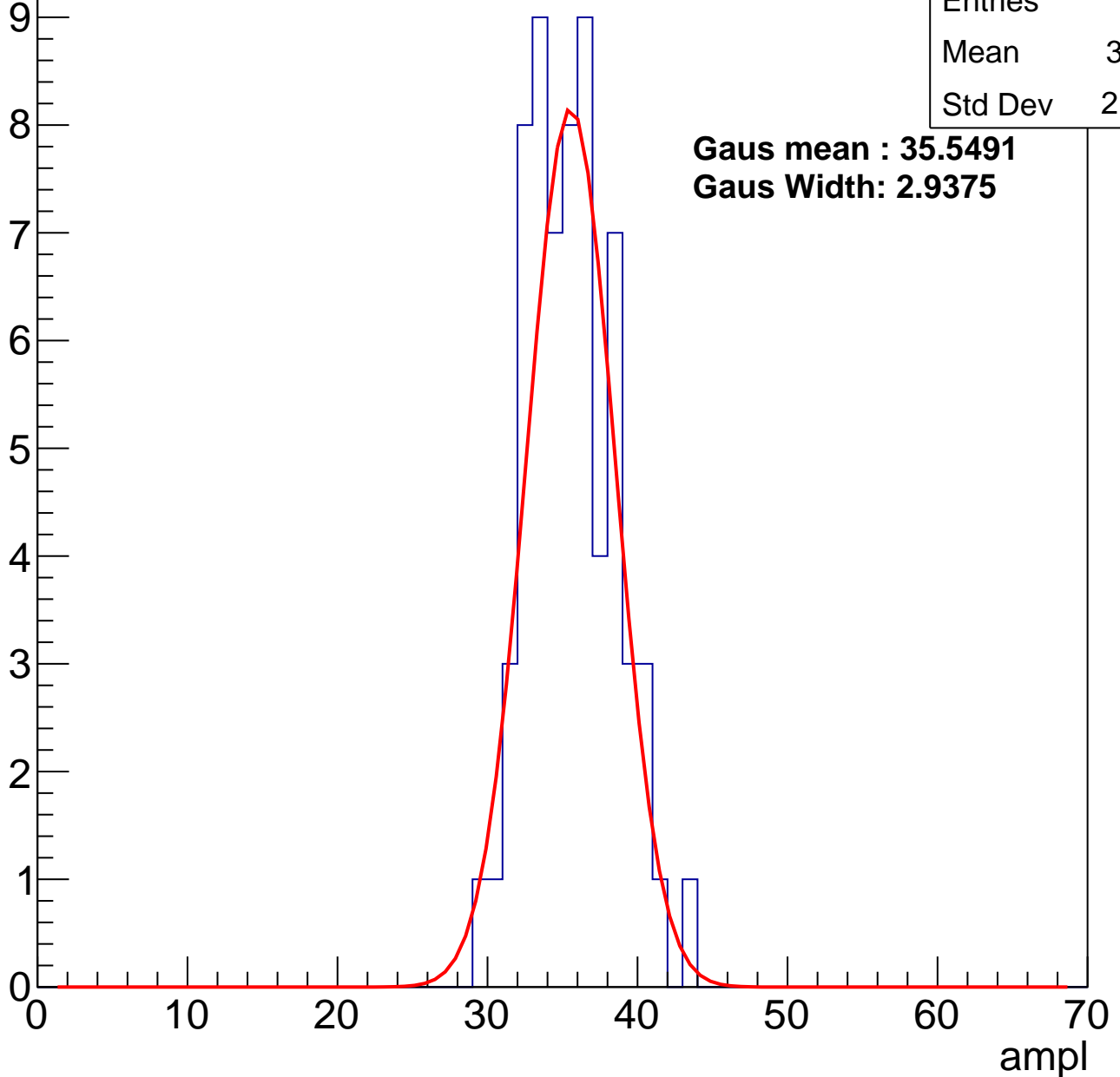
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.11
Std Dev	2.867

**Gaus mean : 35.5491**

**Gaus Width: 2.9375**



# B1L000S, U24-ch95, adc2

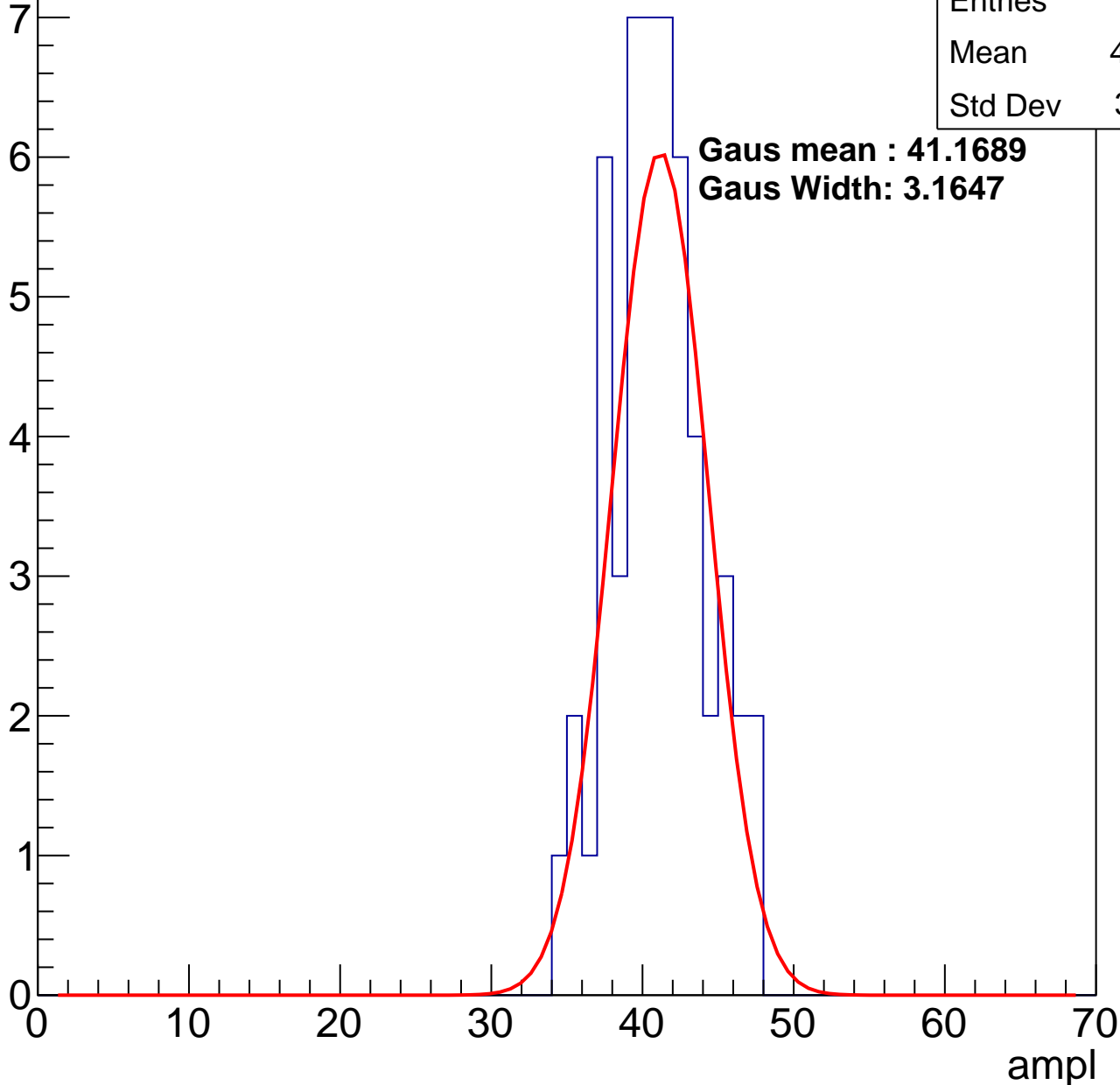
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.55
Std Dev	3.081

**Gaus mean : 41.1689**

**Gaus Width: 3.1647**

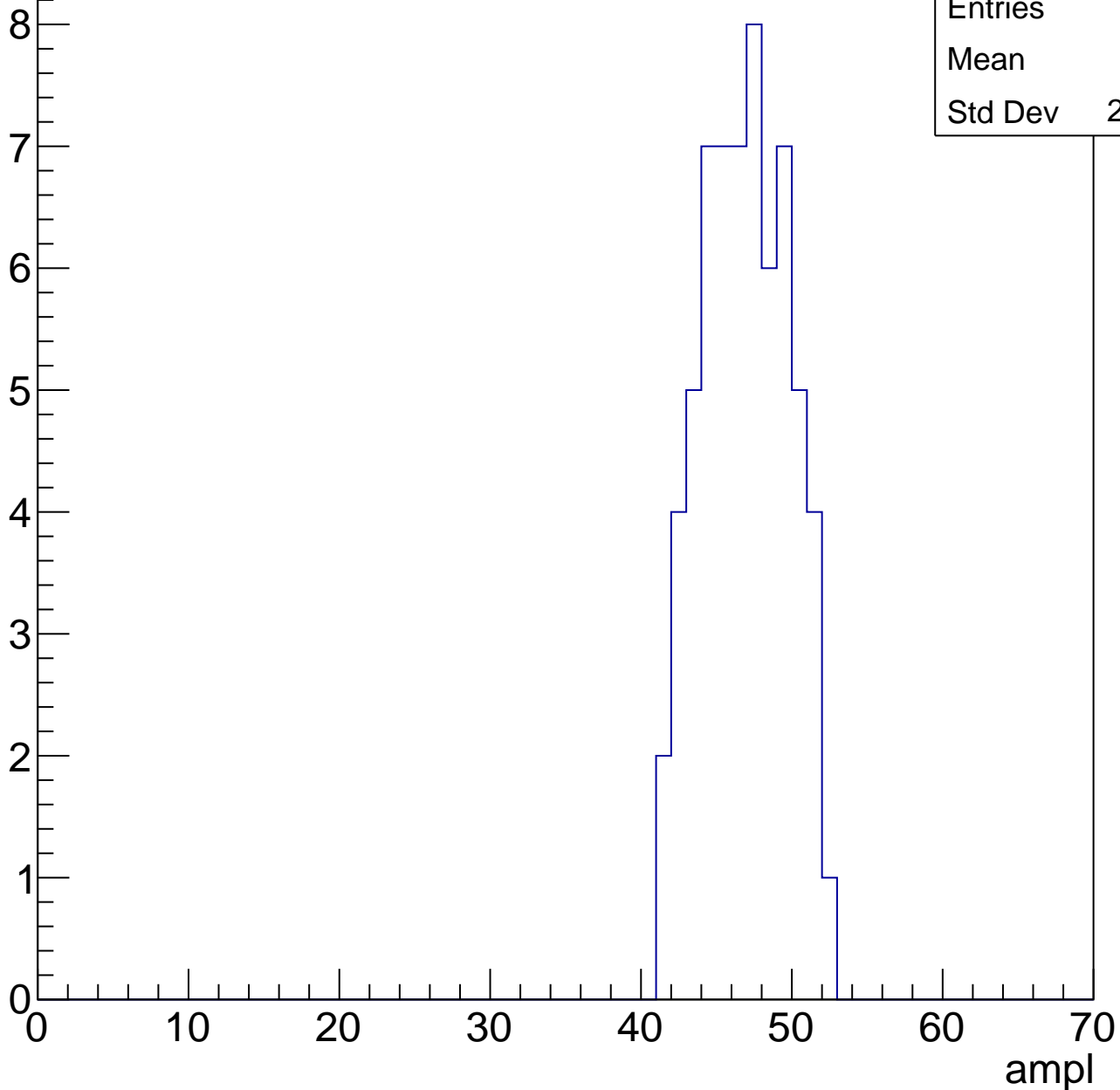


# B1L000S, U24-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

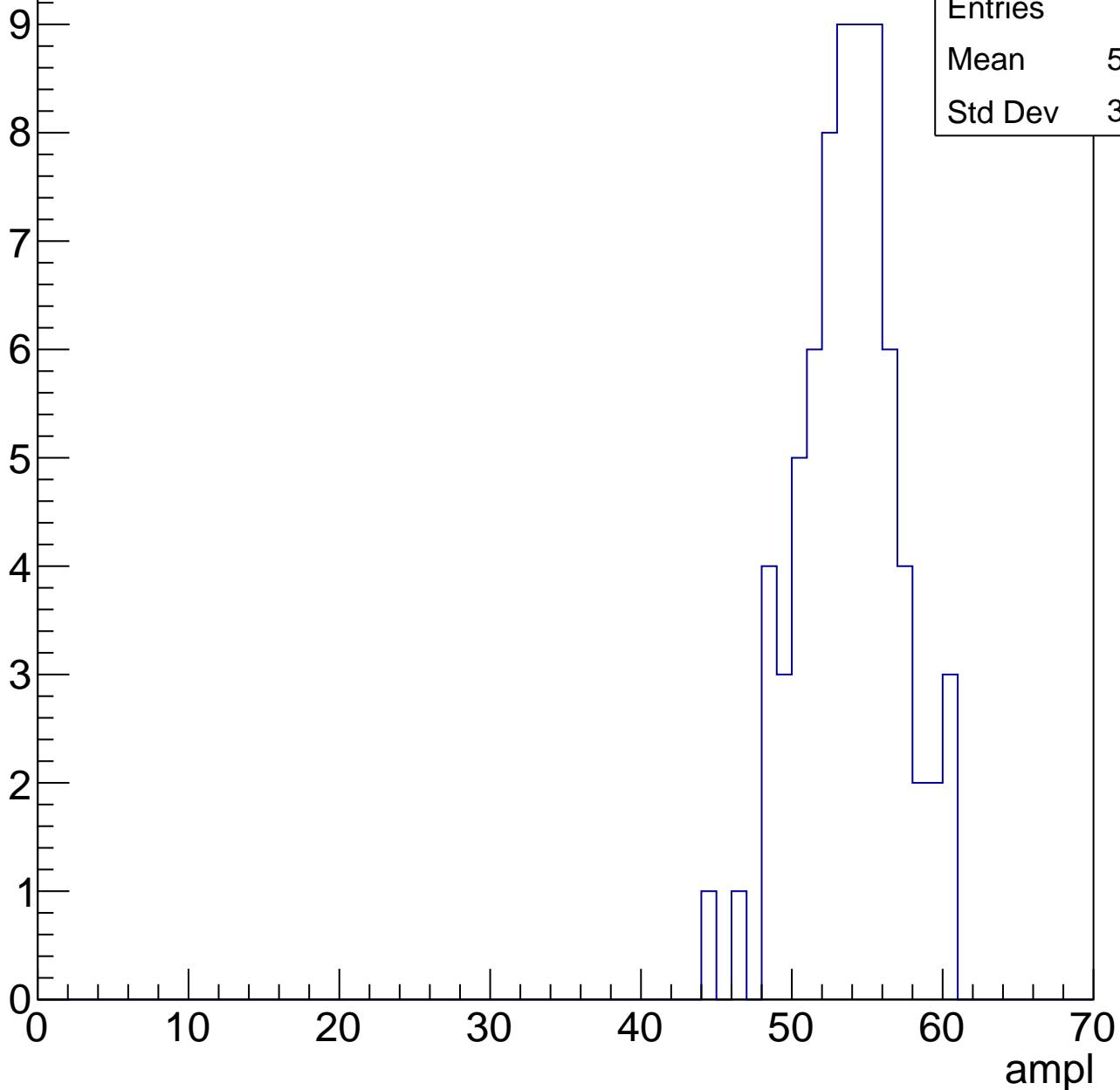
Entries	63
Mean	46.4
Std Dev	2.803



# B1L000S, U24-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

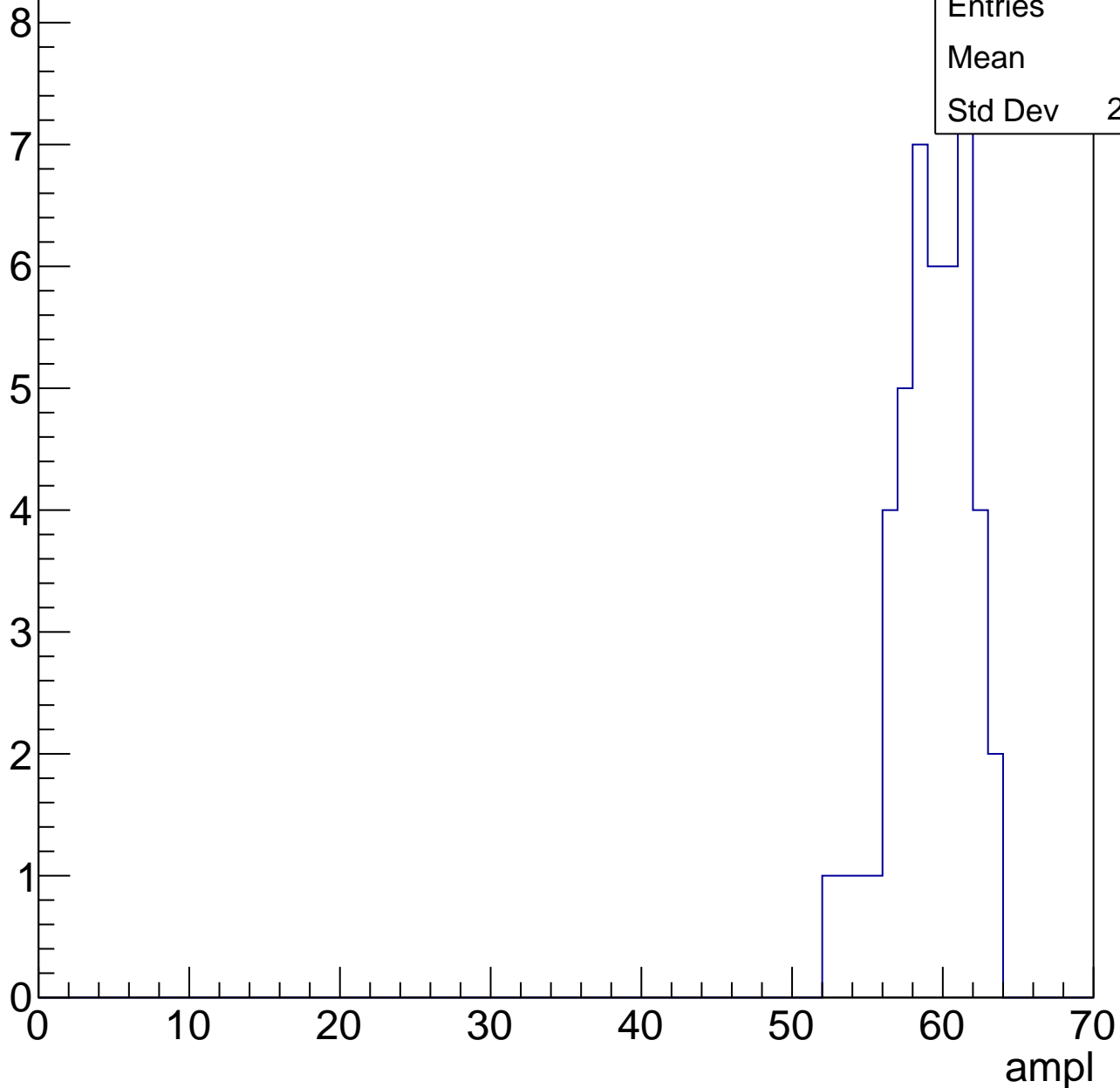
Entry



# B1L000S, U24-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

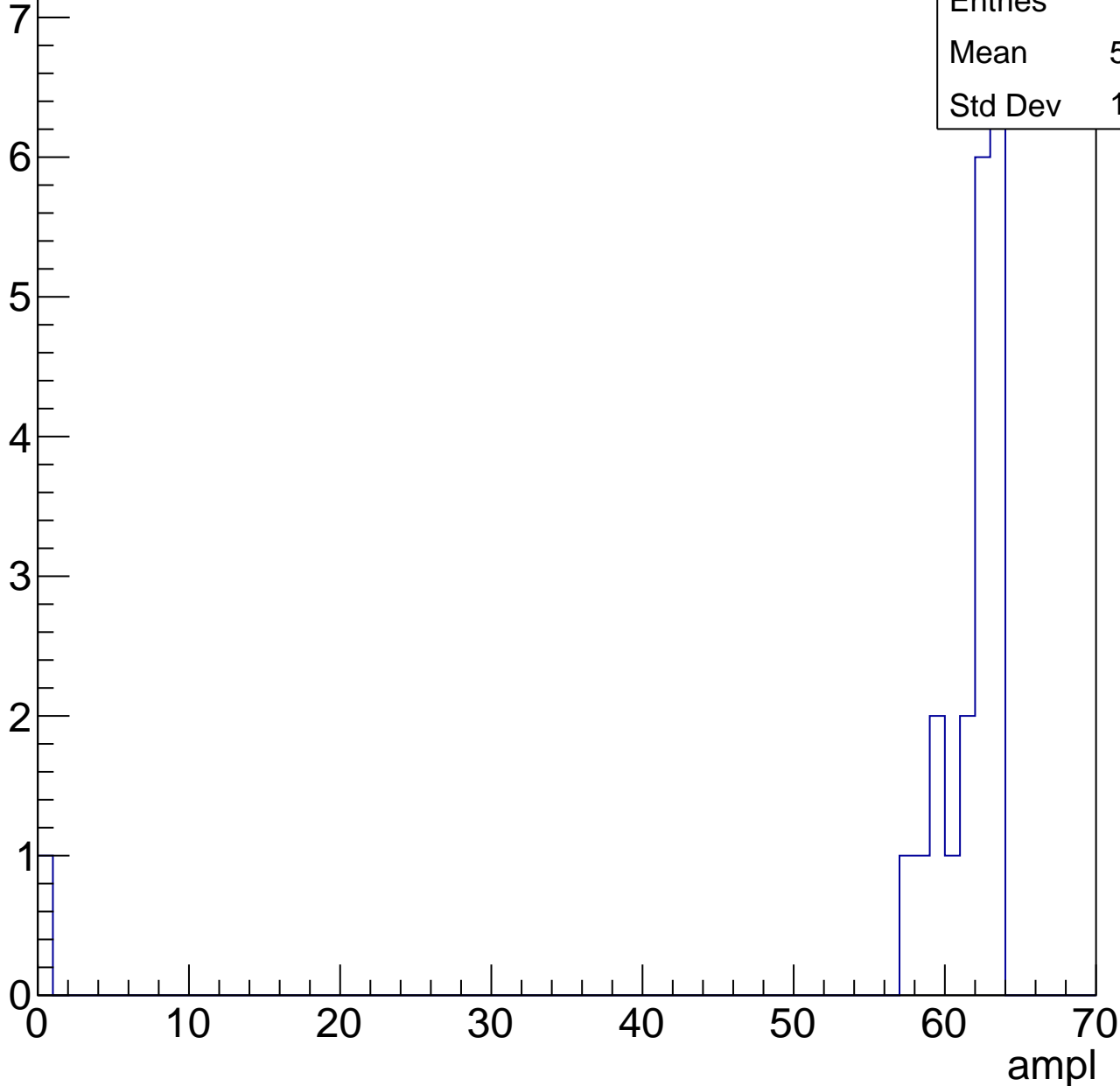


# B1L000S, U24-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.48
Std Dev	13.19

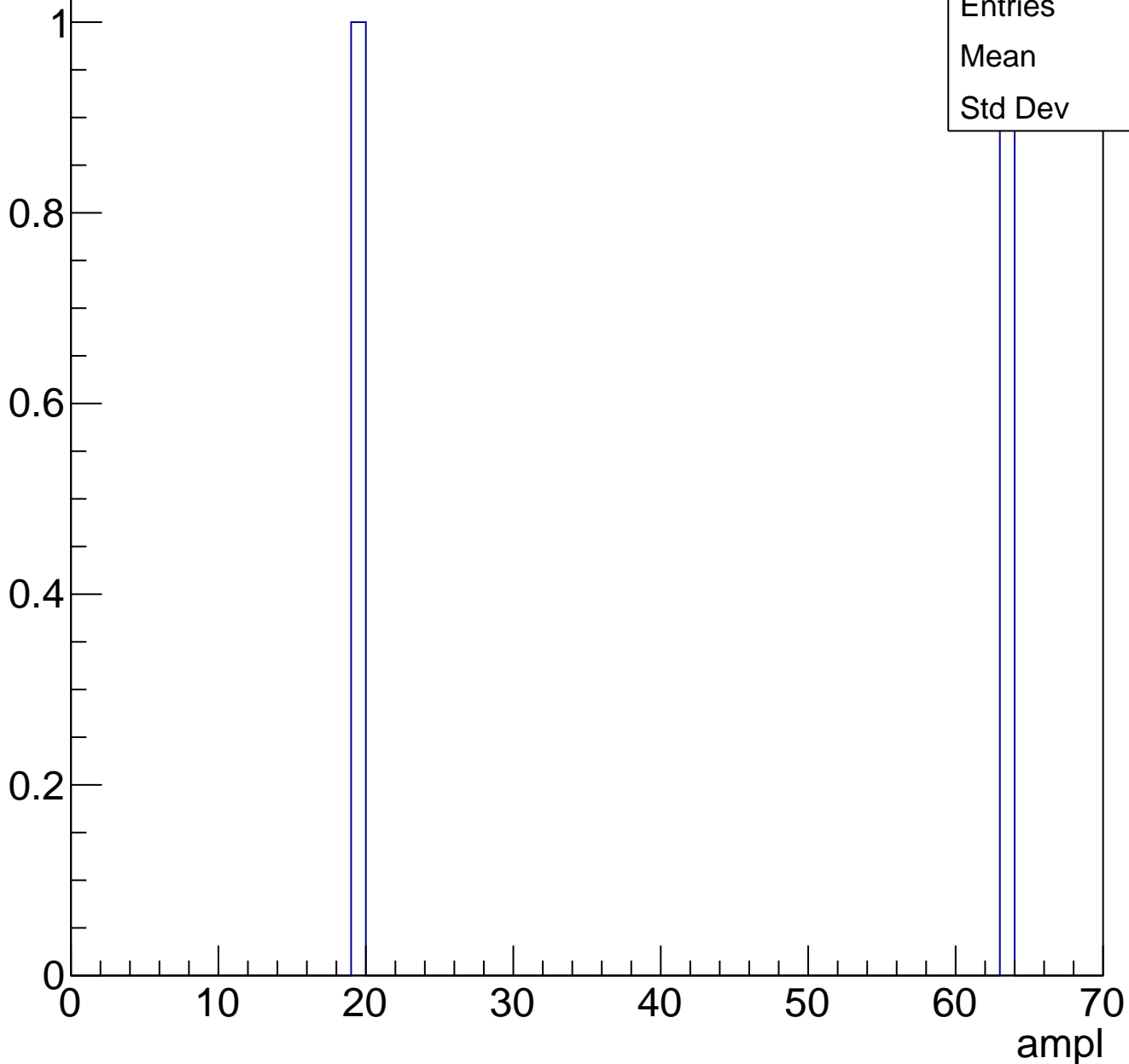




# B1L000S, U24-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch96, adc0

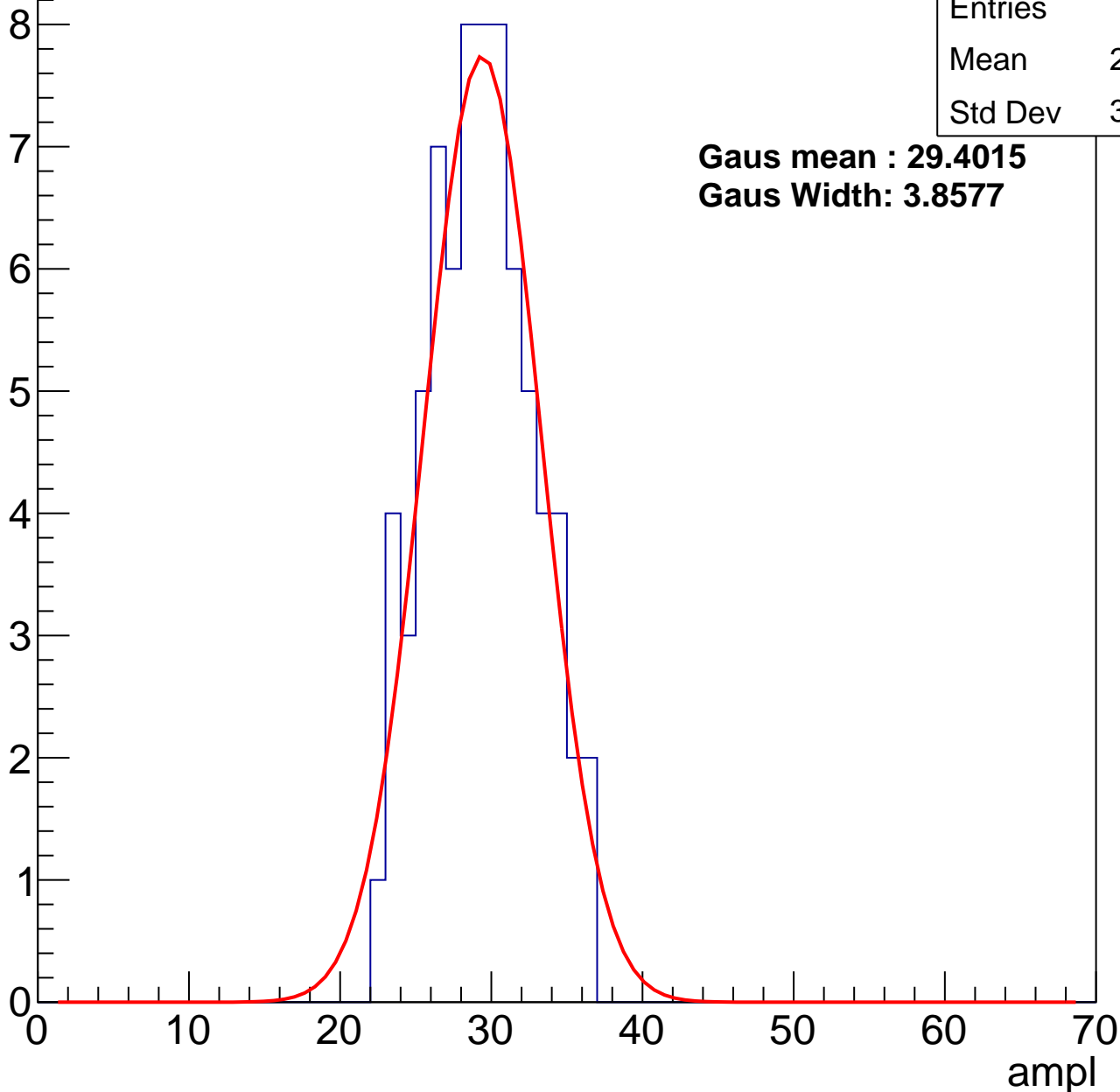
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.86
Std Dev	3.418

**Gaus mean : 29.4015**

**Gaus Width: 3.8577**



# B1L000S, U24-ch96, adc1

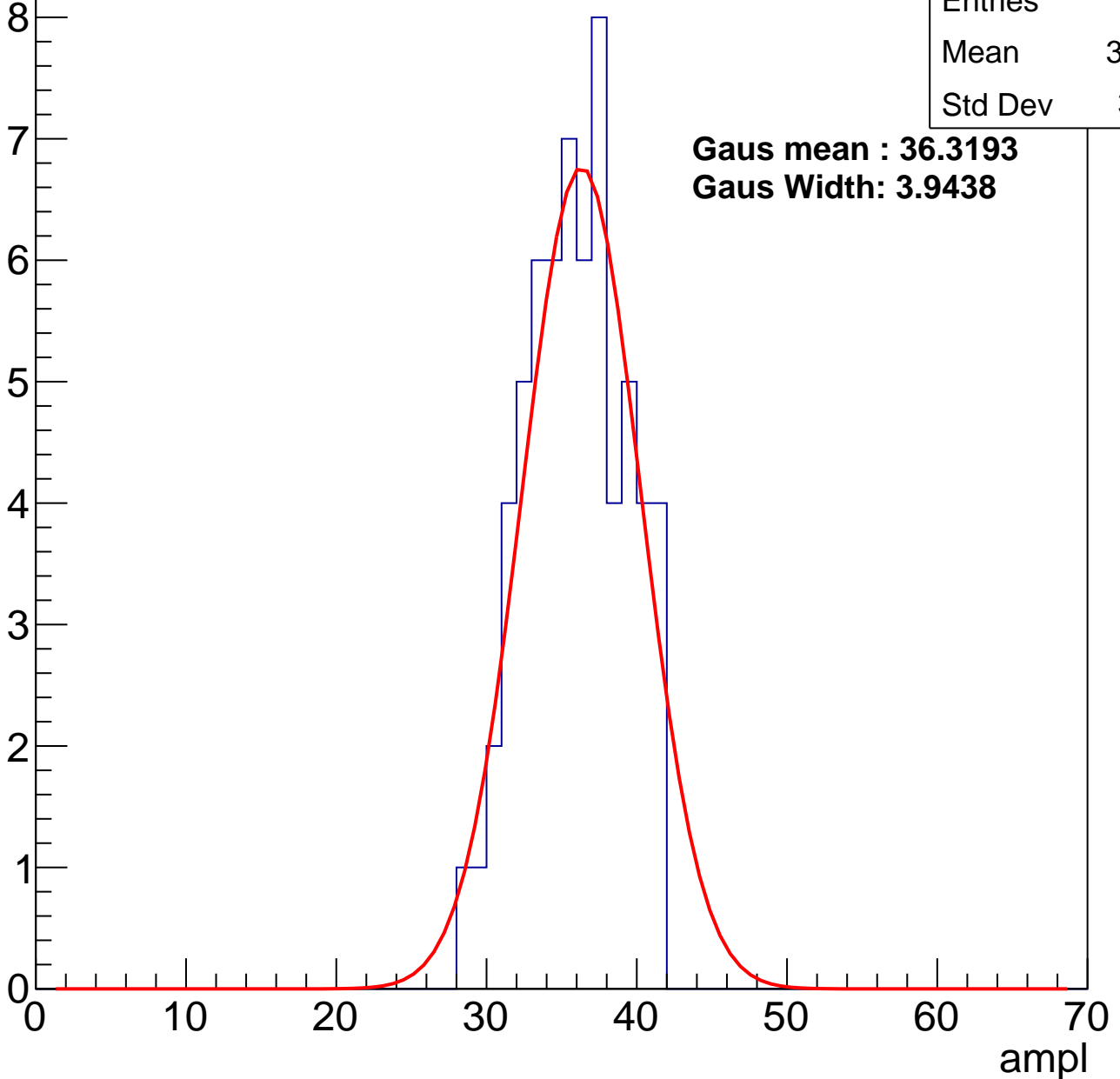
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.41
Std Dev	3.24

**Gaus mean : 36.3193**

**Gaus Width: 3.9438**



# B1L000S, U24-ch96, adc2

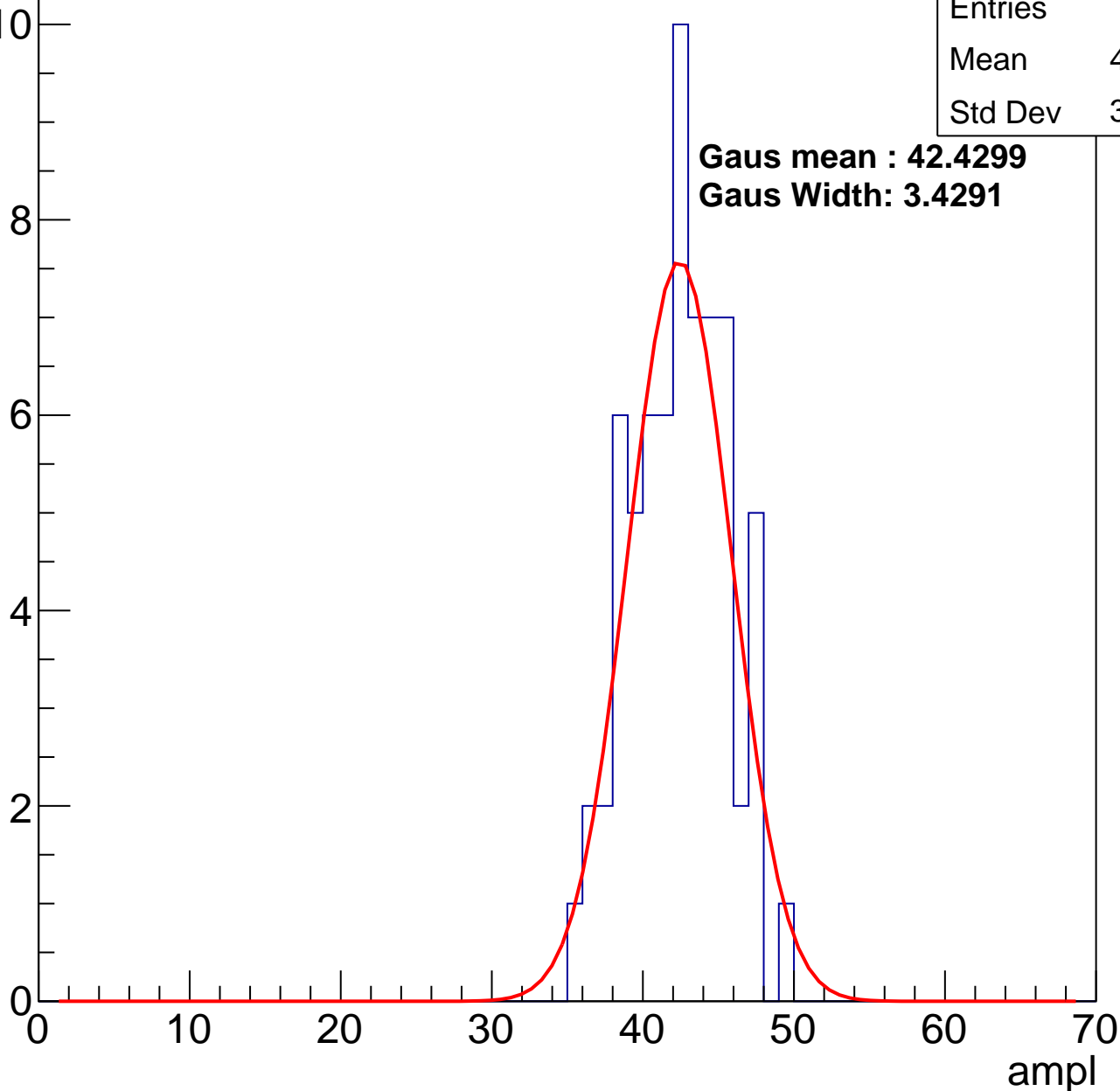
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.94
Std Dev	3.105

**Gaus mean : 42.4299**

**Gaus Width: 3.4291**



# B1L000S, U24-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

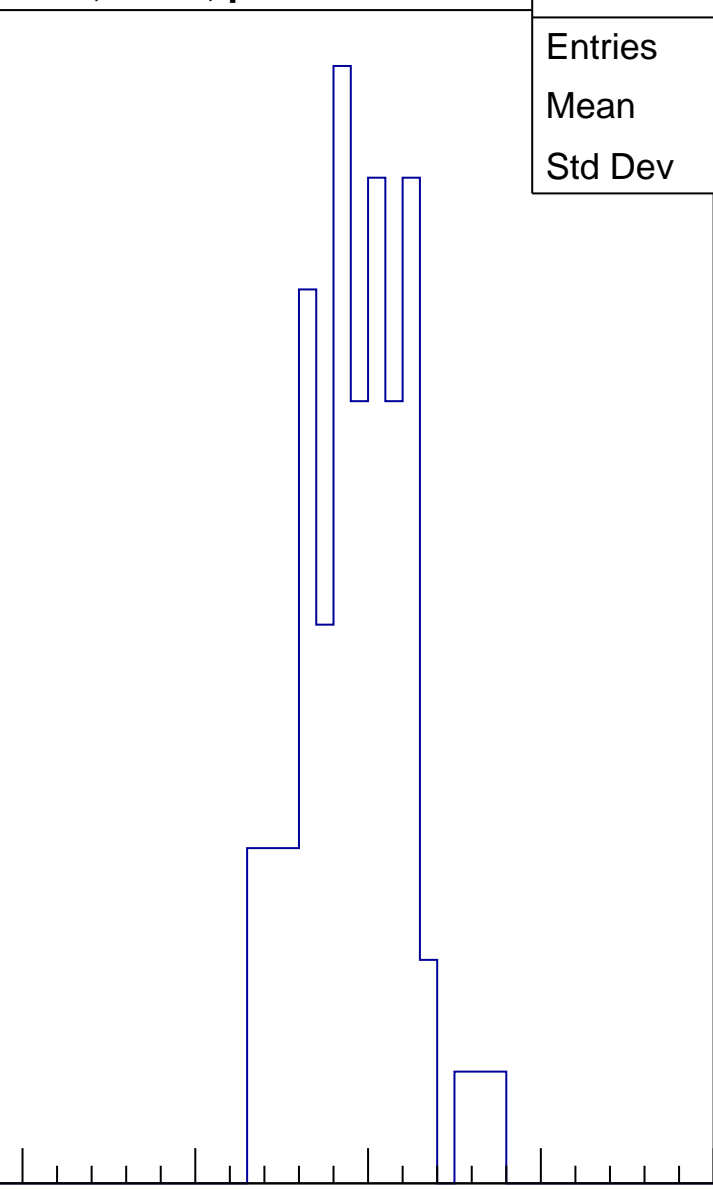
Entries	69
Mean	48.86
Std Dev	3.023

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

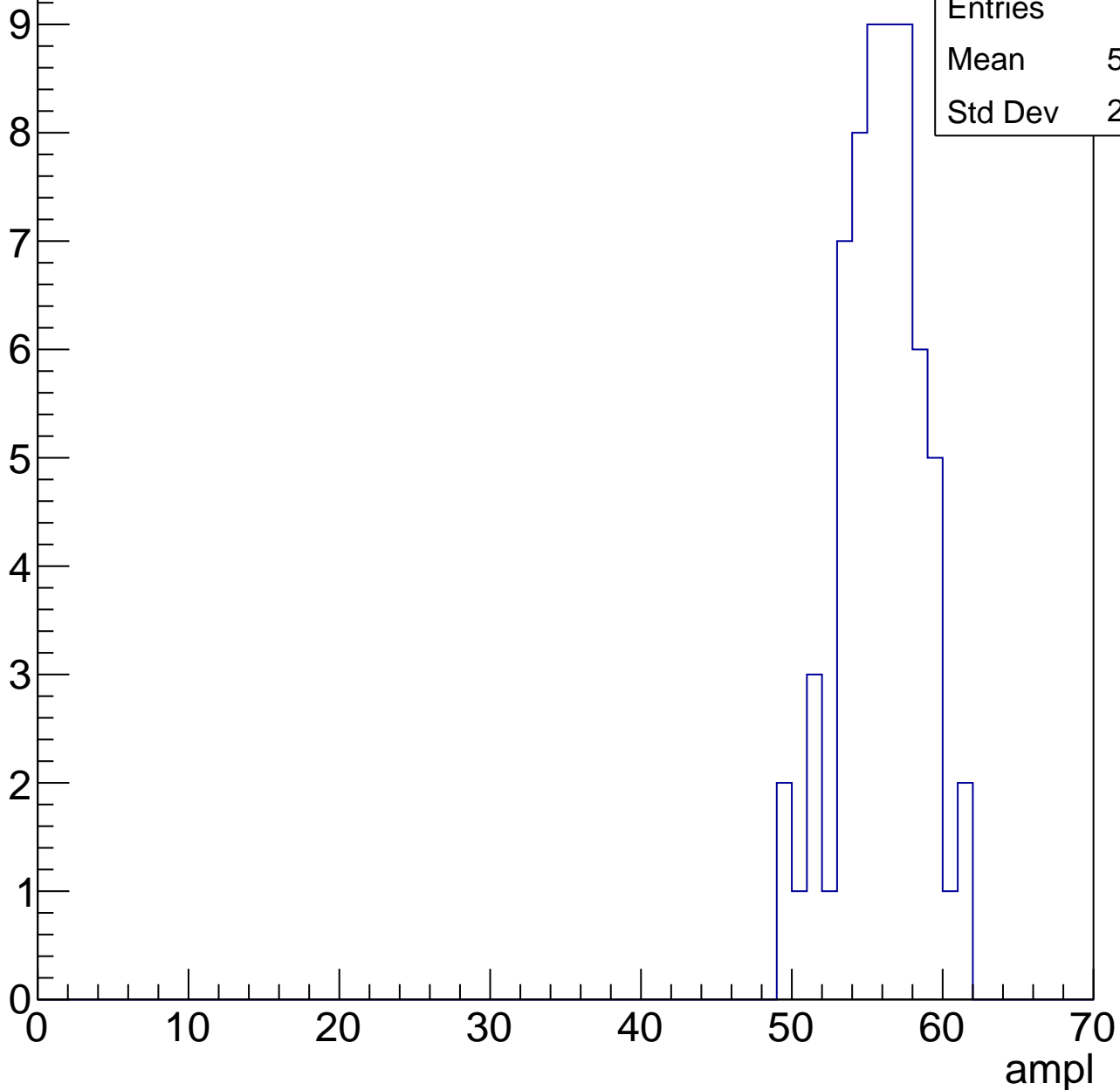


# B1L000S, U24-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	55.44
Std Dev	2.683

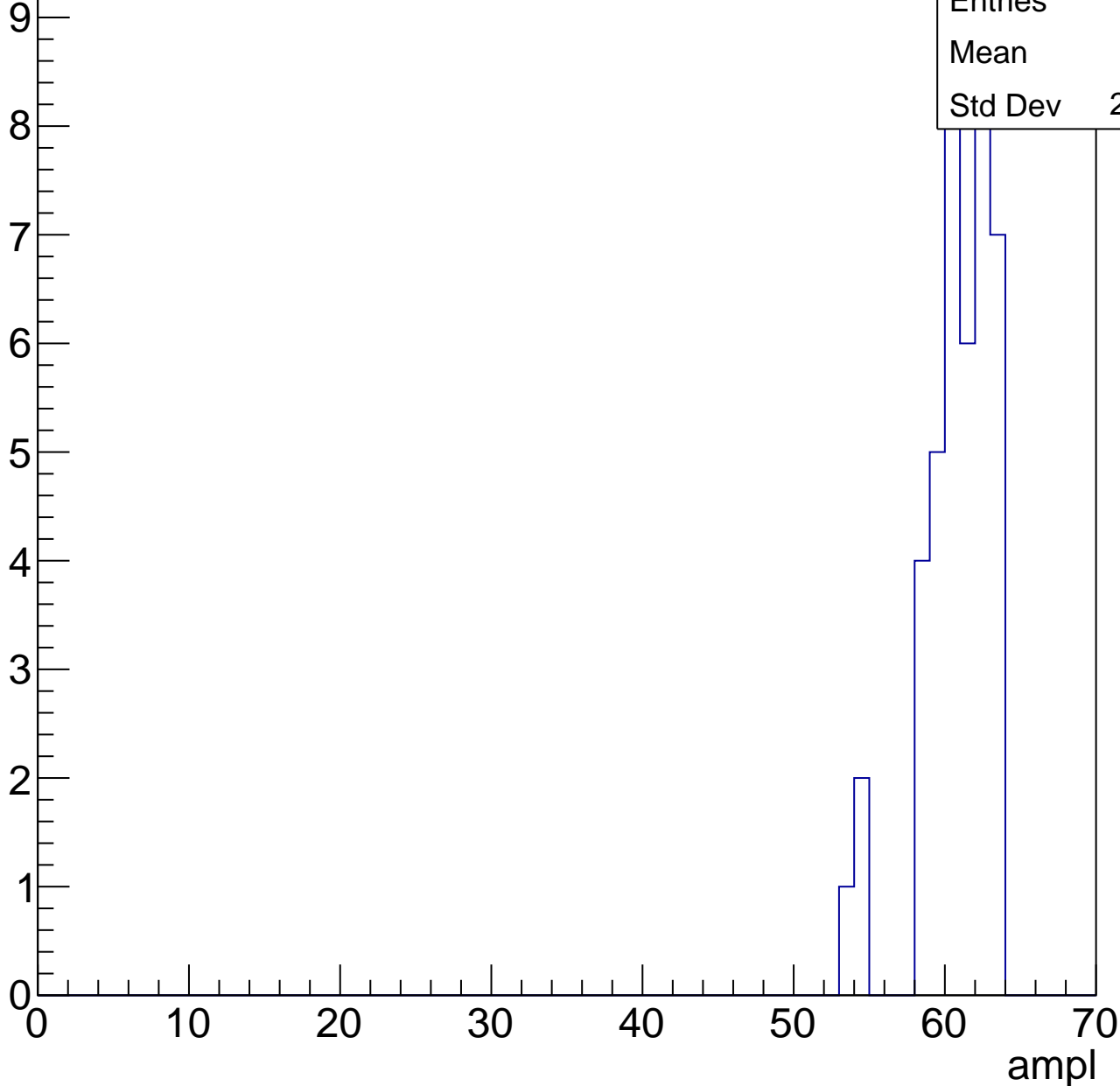


# B1L000S, U24-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

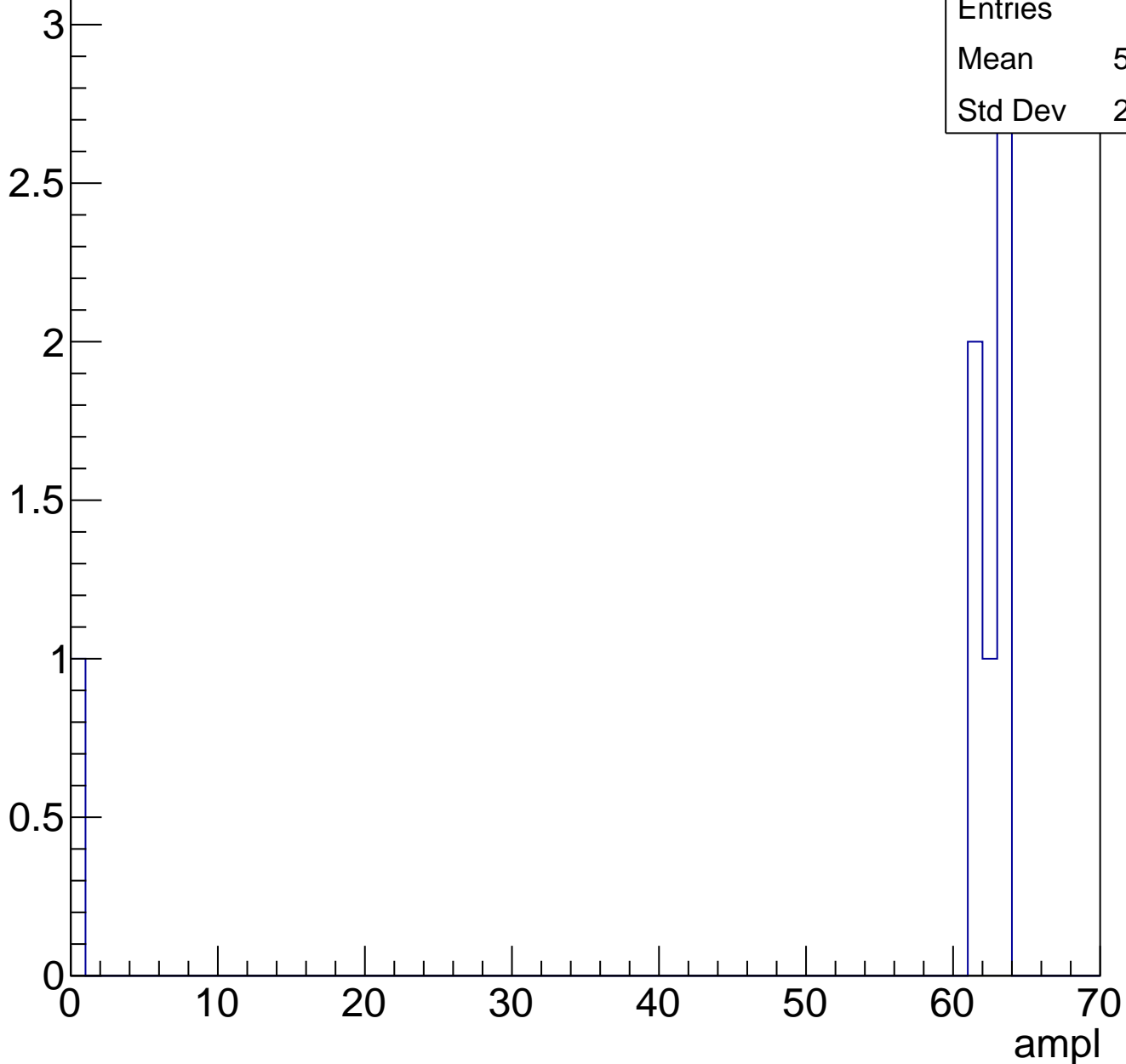
Entries	43
Mean	60.3
Std Dev	2.378



# B1L000S, U24-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch97, adc0

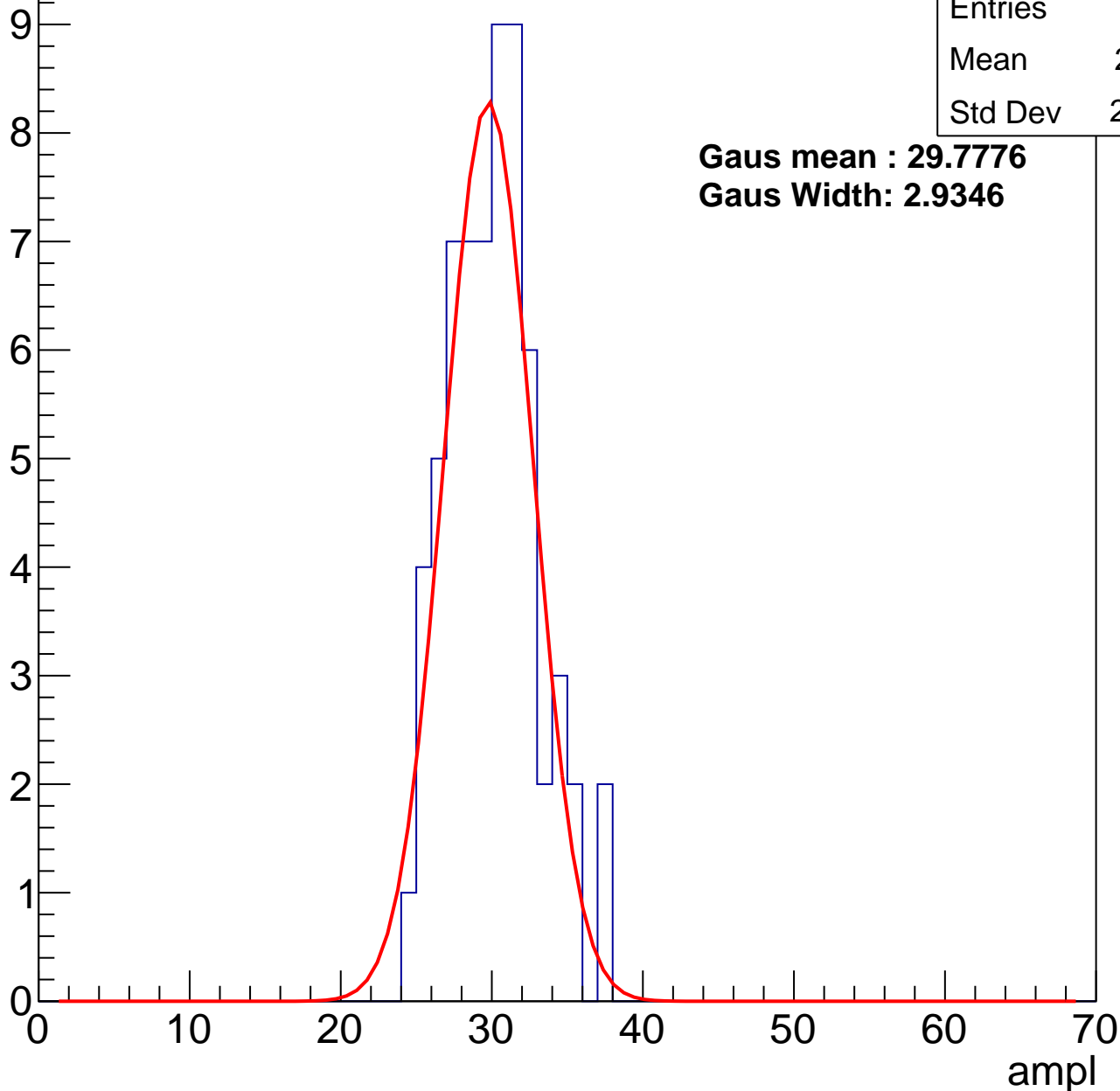
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.61
Std Dev	2.935

**Gaus mean : 29.7776**

**Gaus Width: 2.9346**



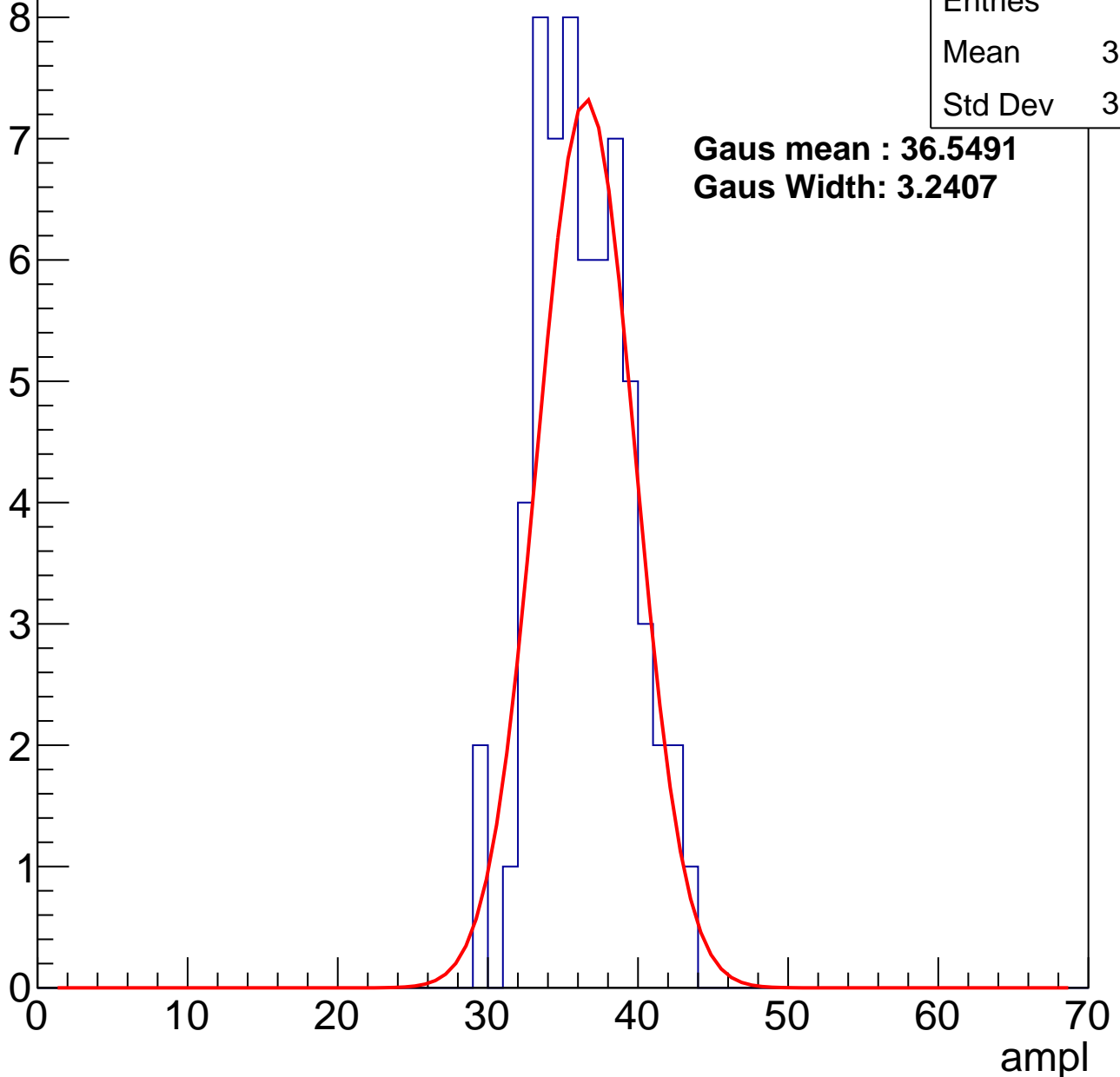
# B1L000S, U24-ch97, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.92
Std Dev	3.092

**Gaus mean : 36.5491**  
**Gaus Width: 3.2407**



# B1L000S, U24-ch97, adc2

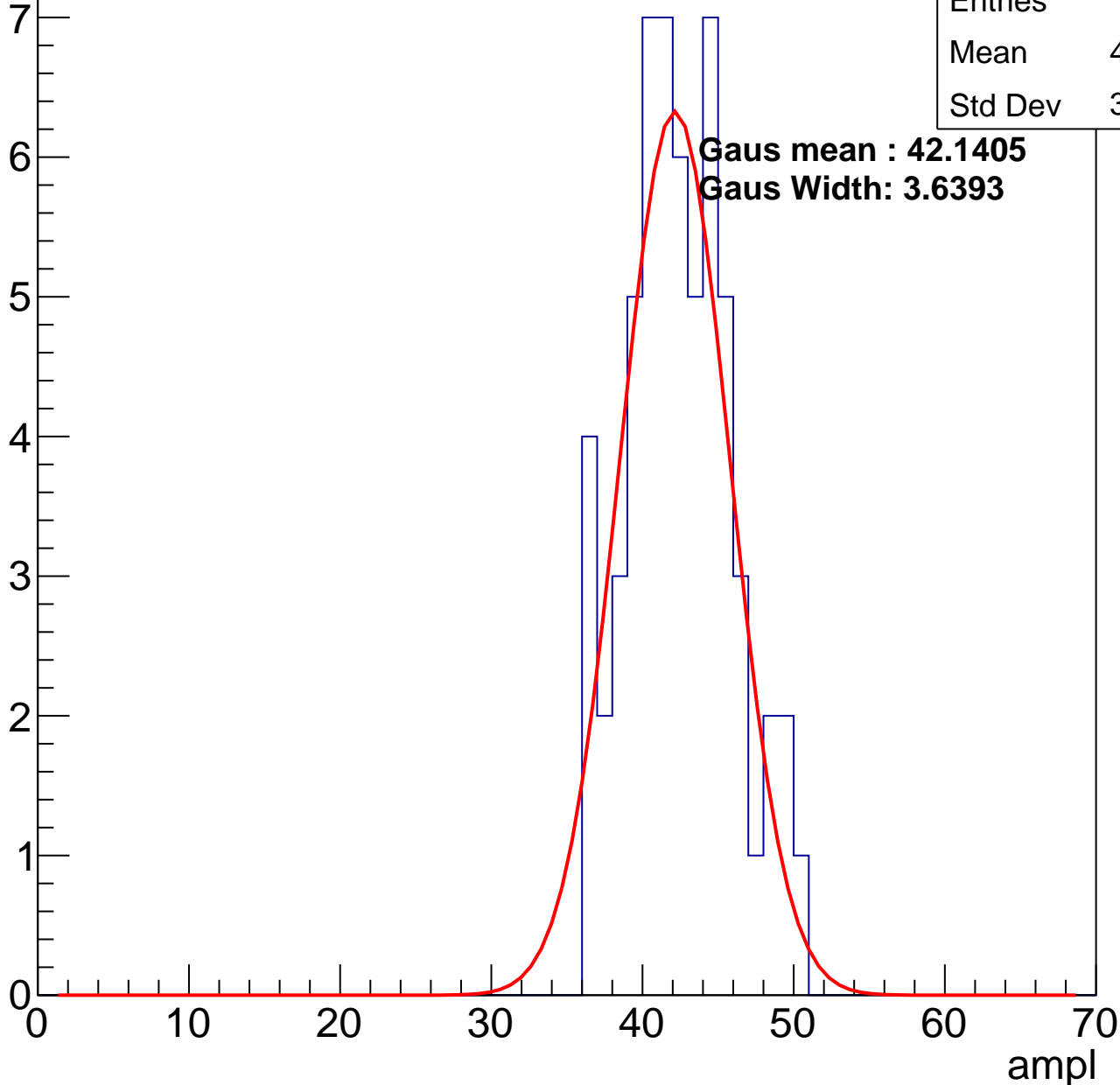
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.05
Std Dev	3.432

**Gaus mean : 42.1405**

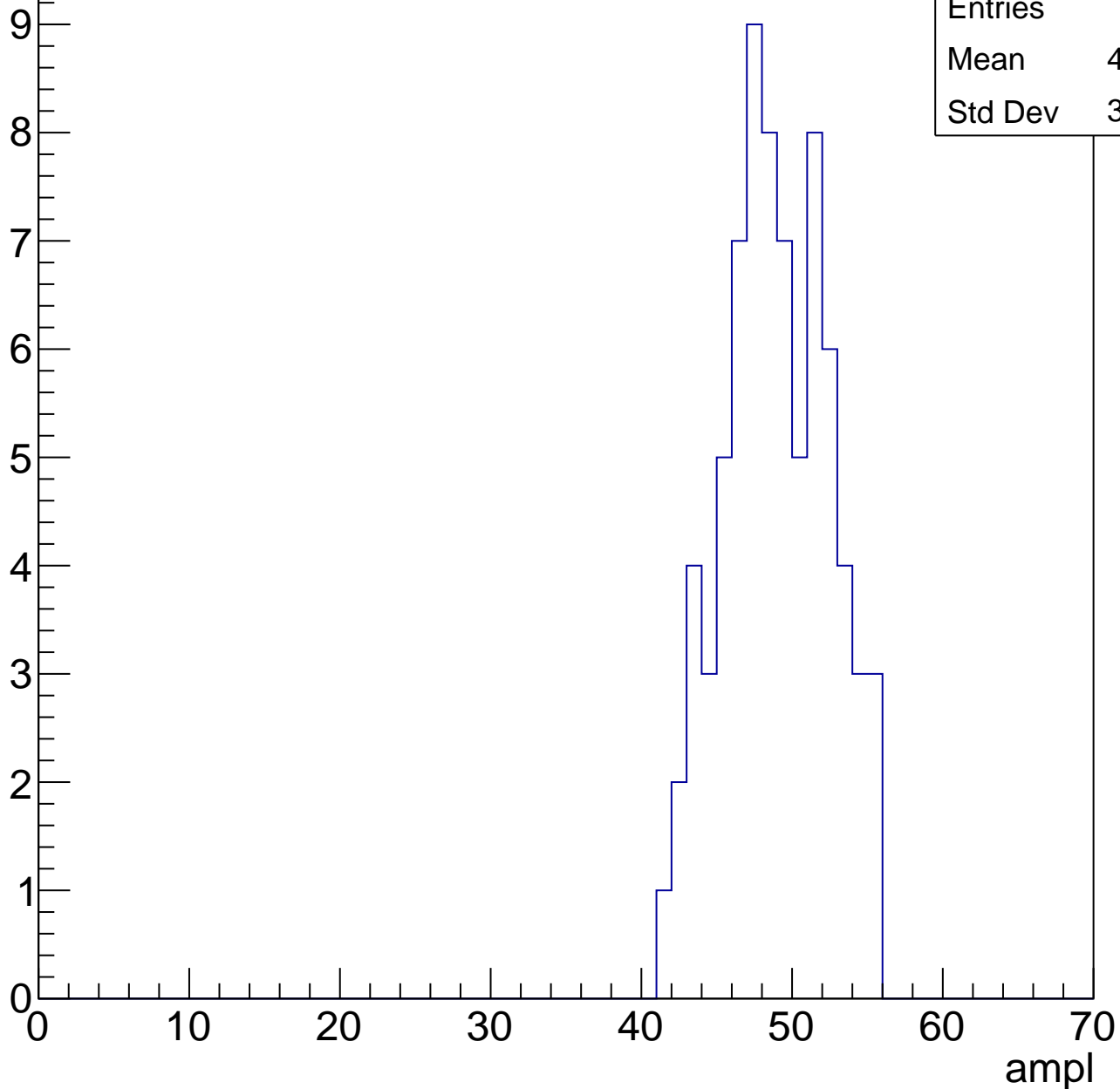
**Gaus Width: 3.6393**



# B1L000S, U24-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



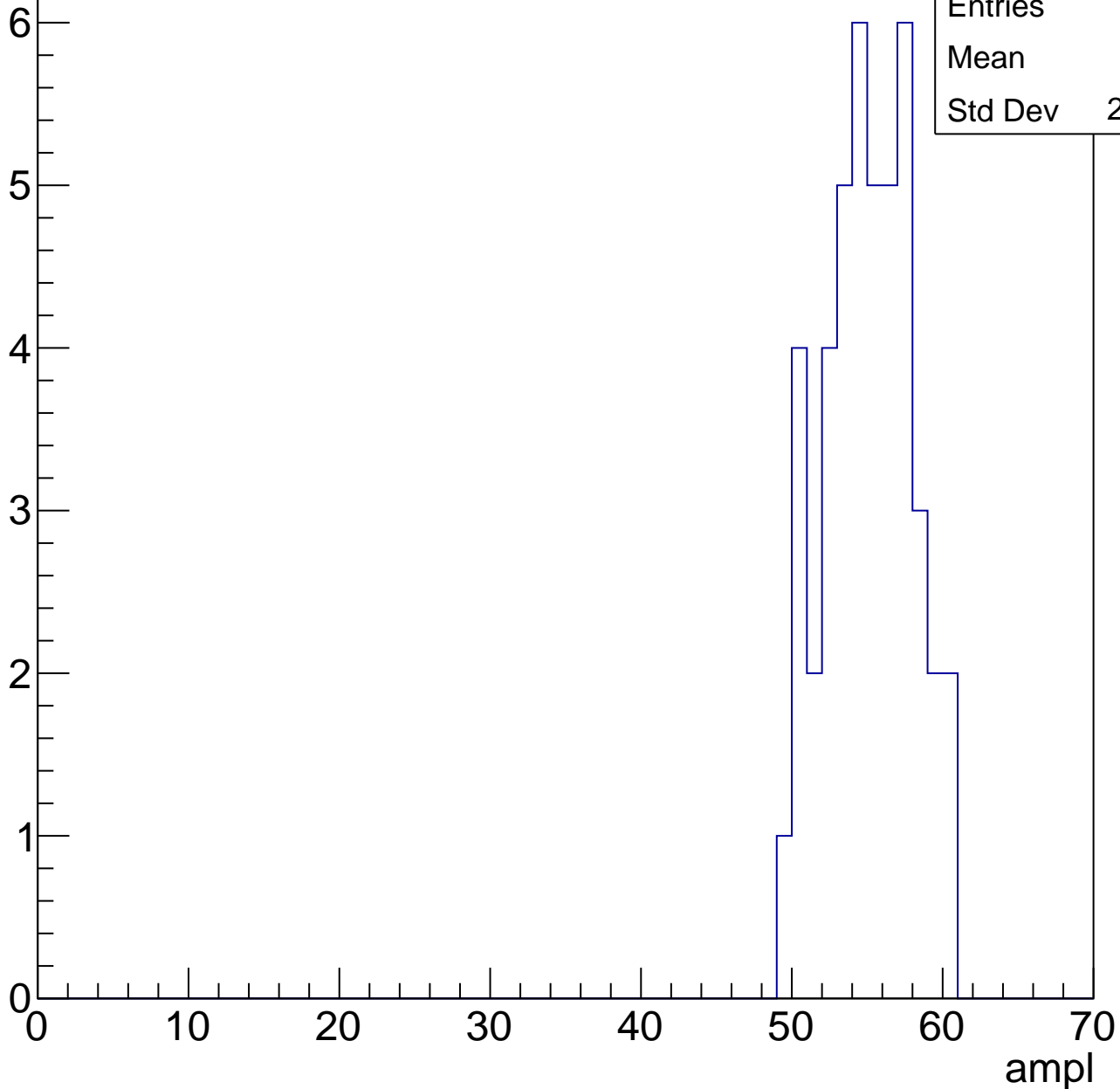
Entries	75
Mean	48.47
Std Dev	3.434

# B1L000S, U24-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	54.6
Std Dev	2.832

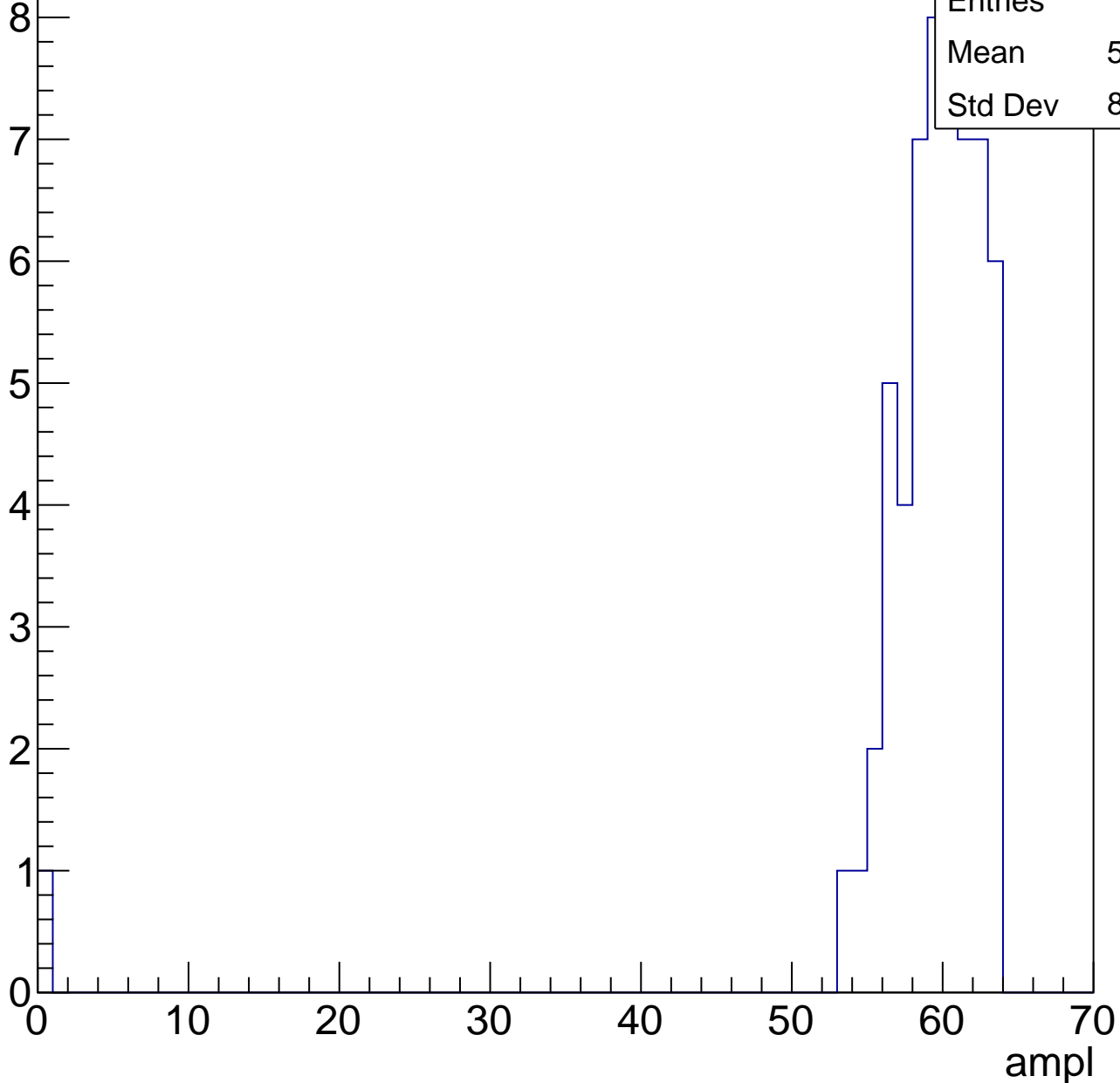


# B1L000S, U24-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

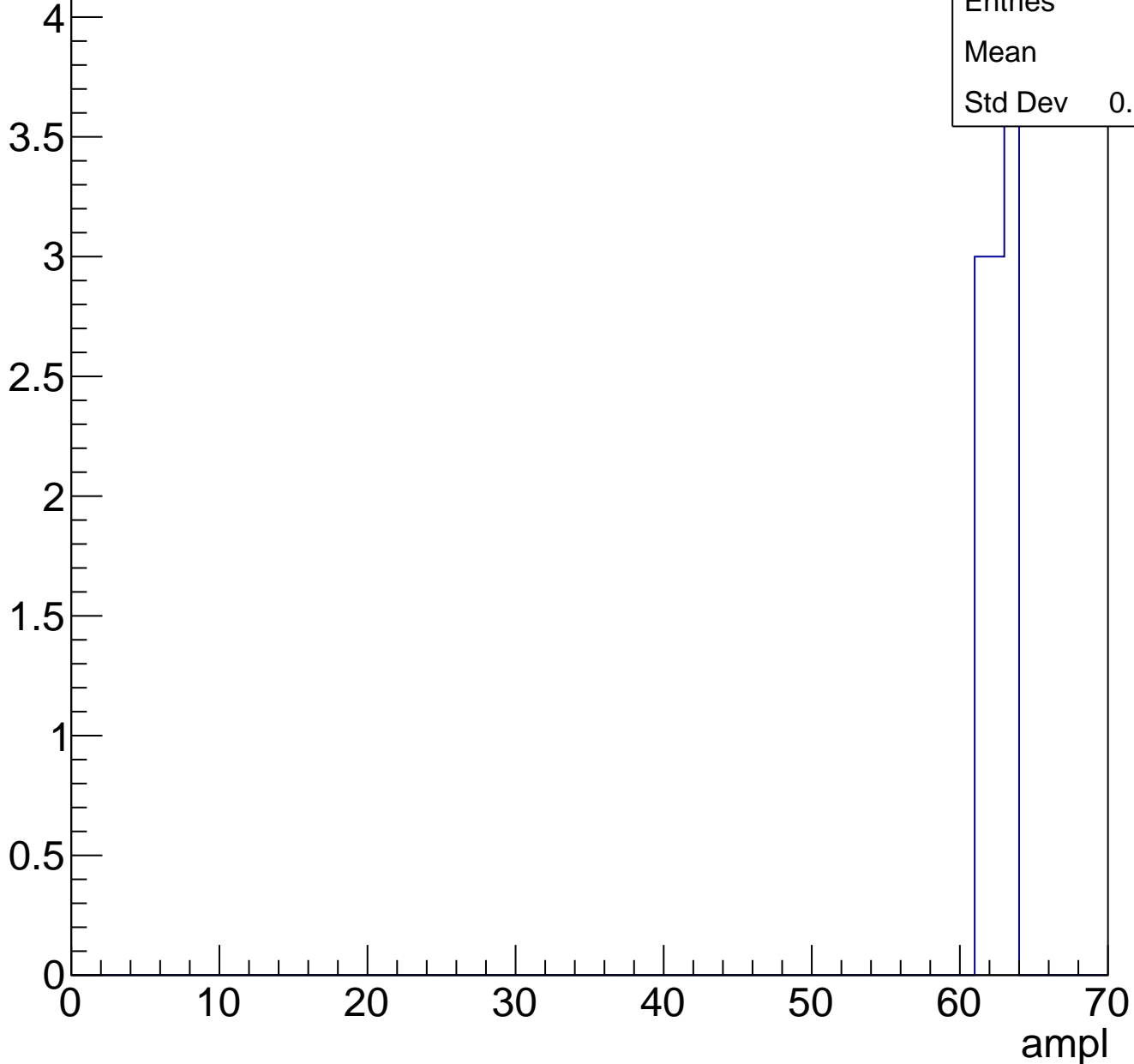
Entries	57
Mean	58.28
Std Dev	8.173



# B1L000S, U24-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U24-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.05
Std Dev	4.75

**Gaus mean : 29.3334**

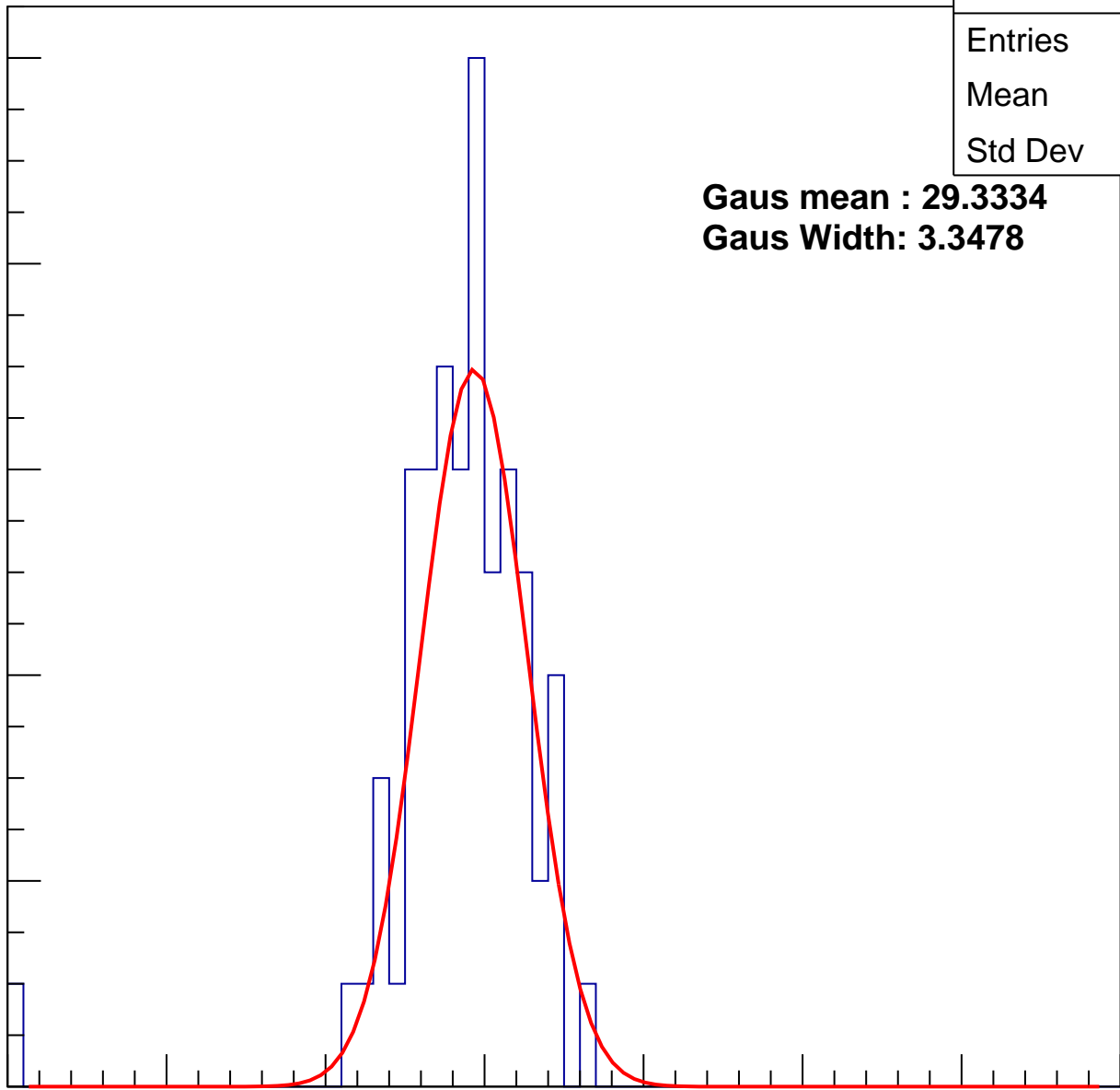
**Gaus Width: 3.3478**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



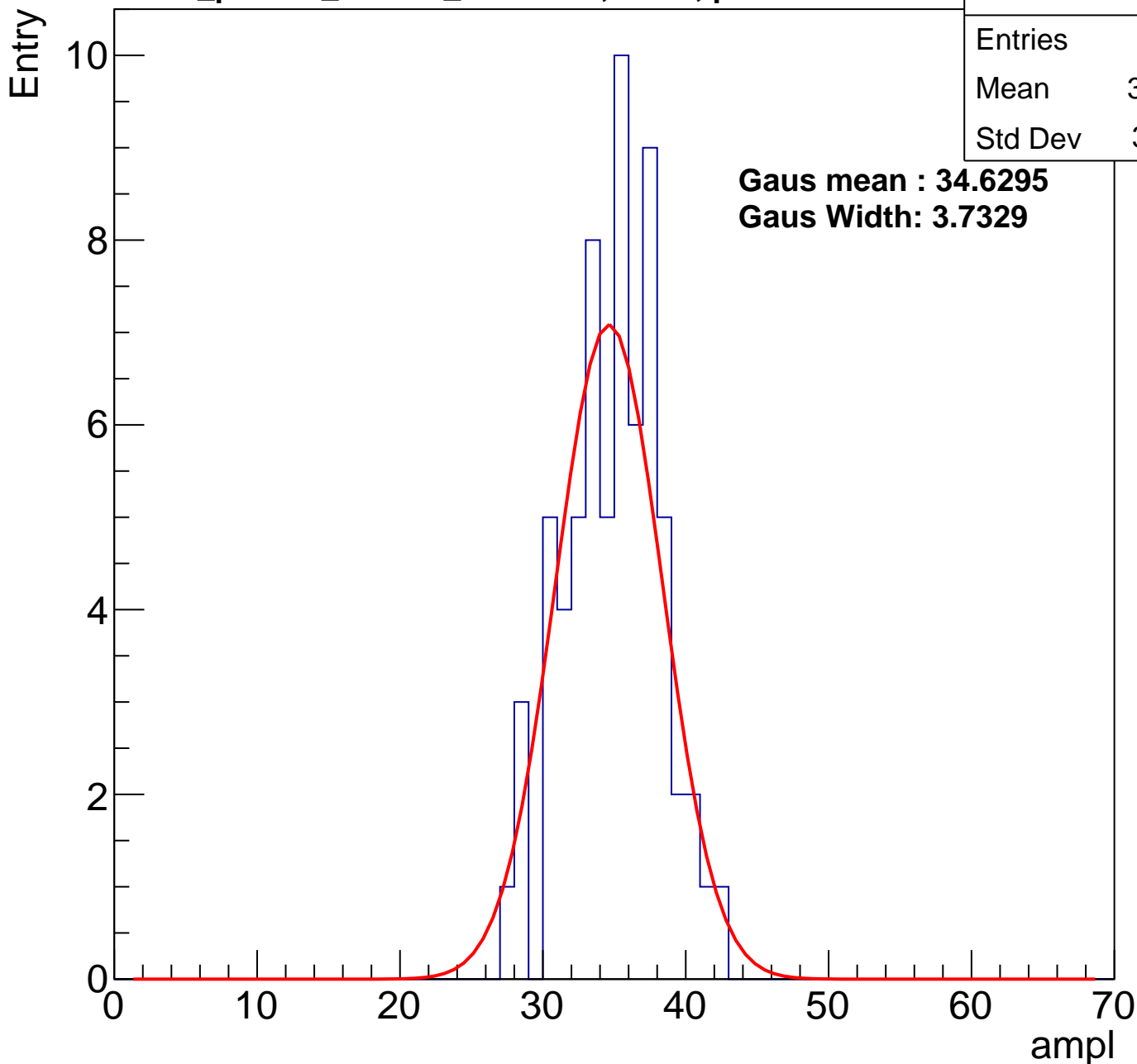
# B1L000S, U24-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	34.46
Std Dev	3.271

**Gaus mean : 34.6295**

**Gaus Width: 3.7329**



# B1L000S, U24-ch98, adc2

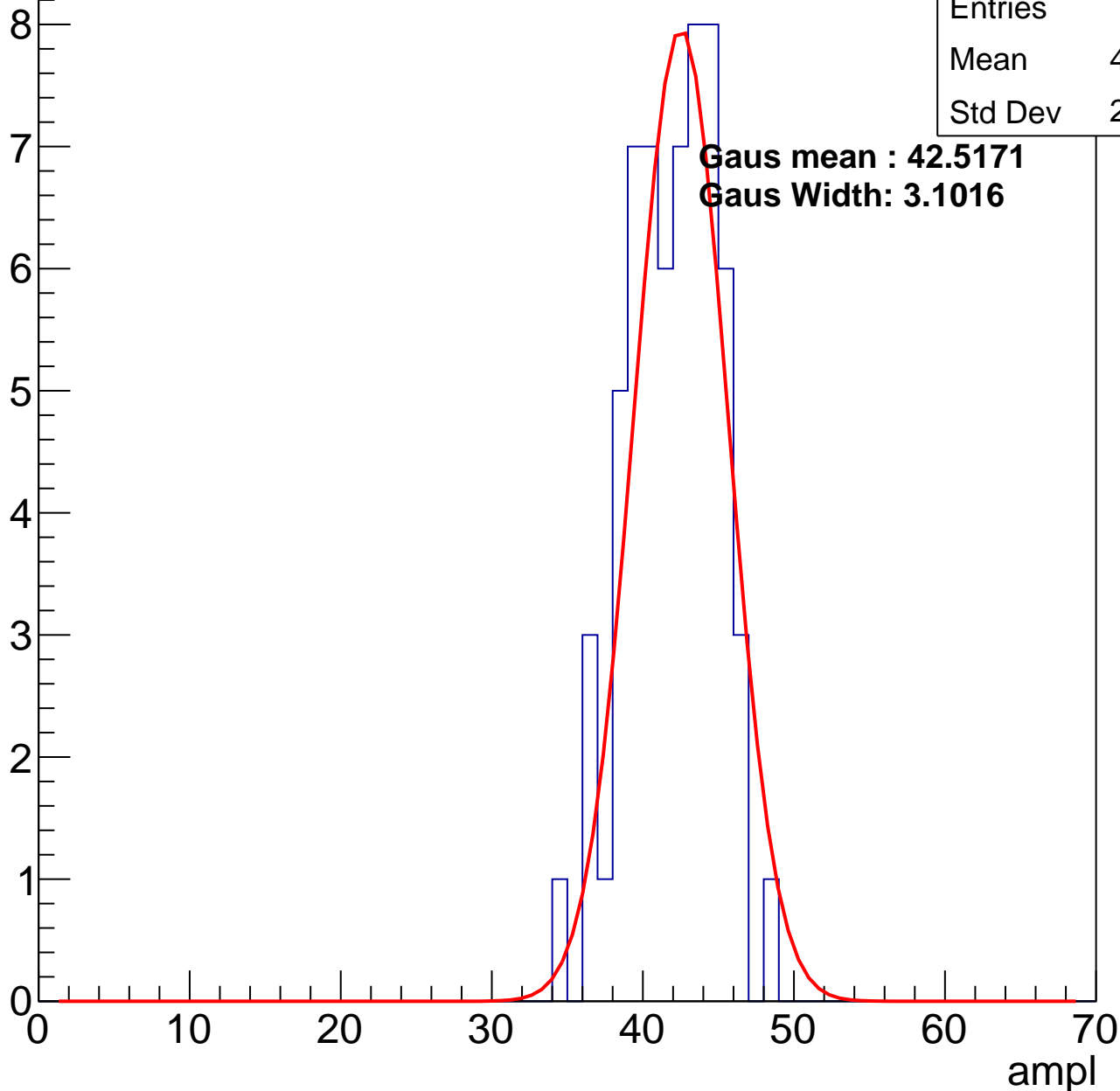
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.49
Std Dev	2.916

**Gaus mean : 42.5171**

**Gaus Width: 3.1016**

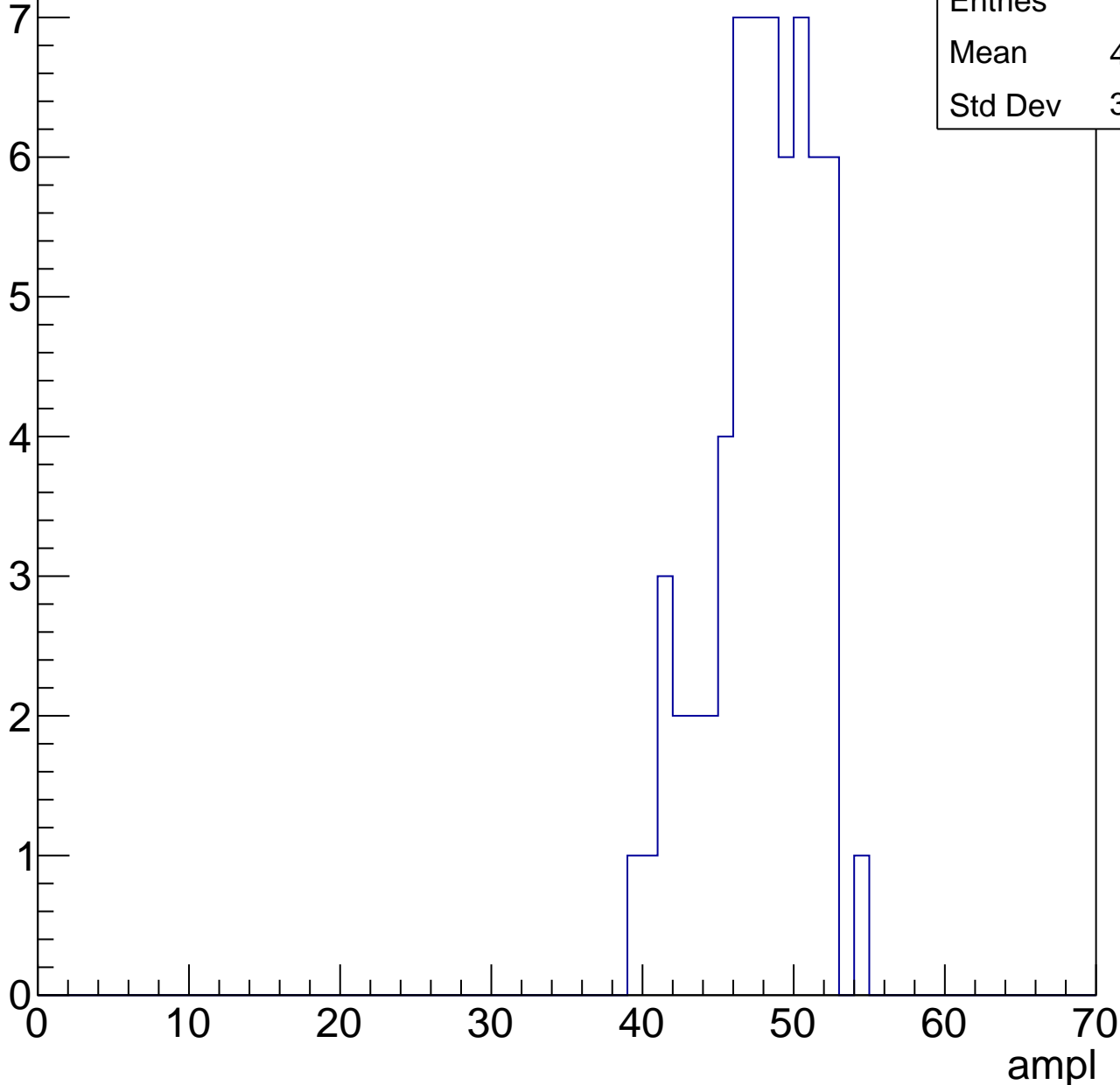


# B1L000S, U24-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

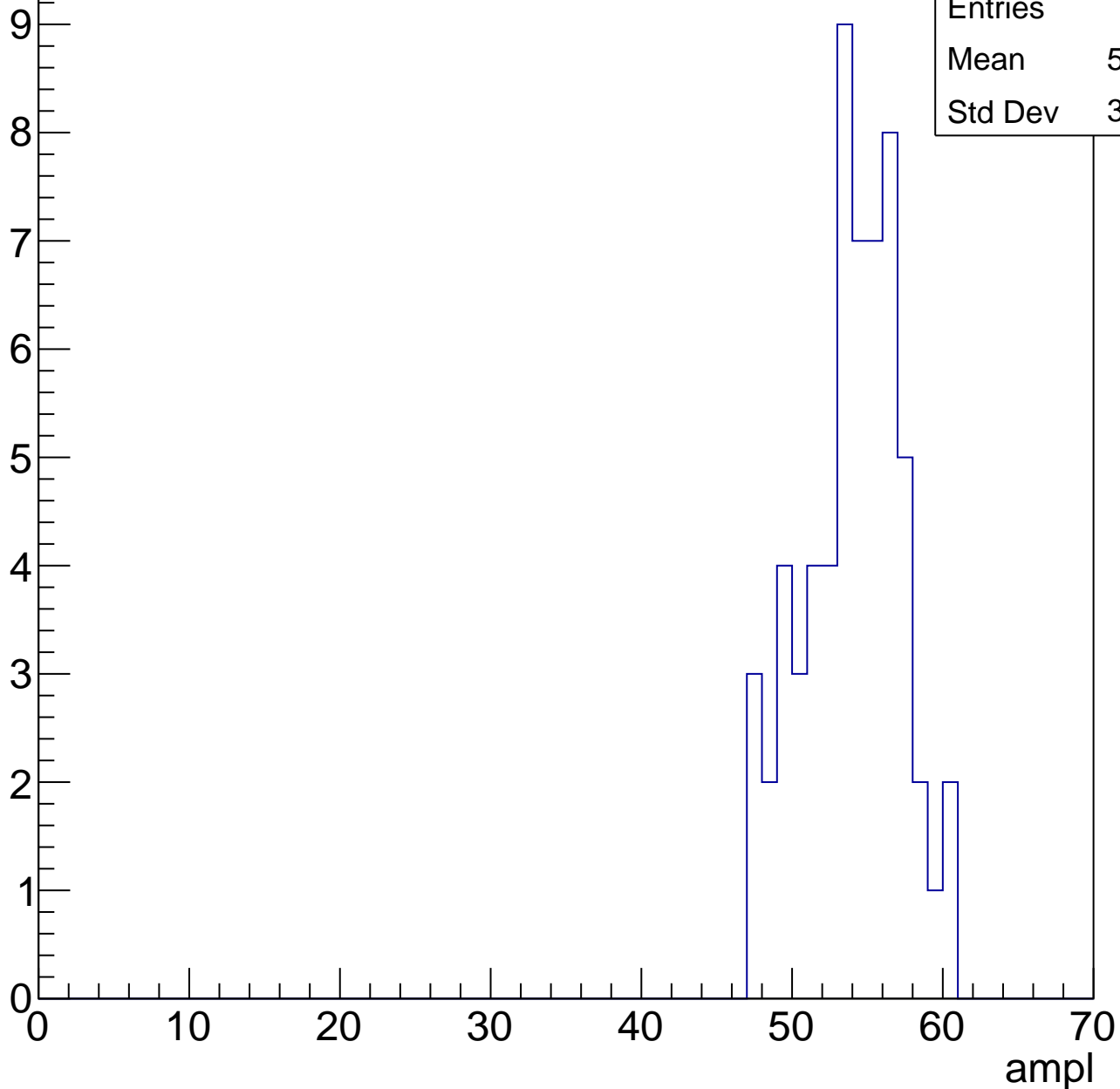
Entries	62
Mean	47.47
Std Dev	3.406



# B1L000S, U24-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

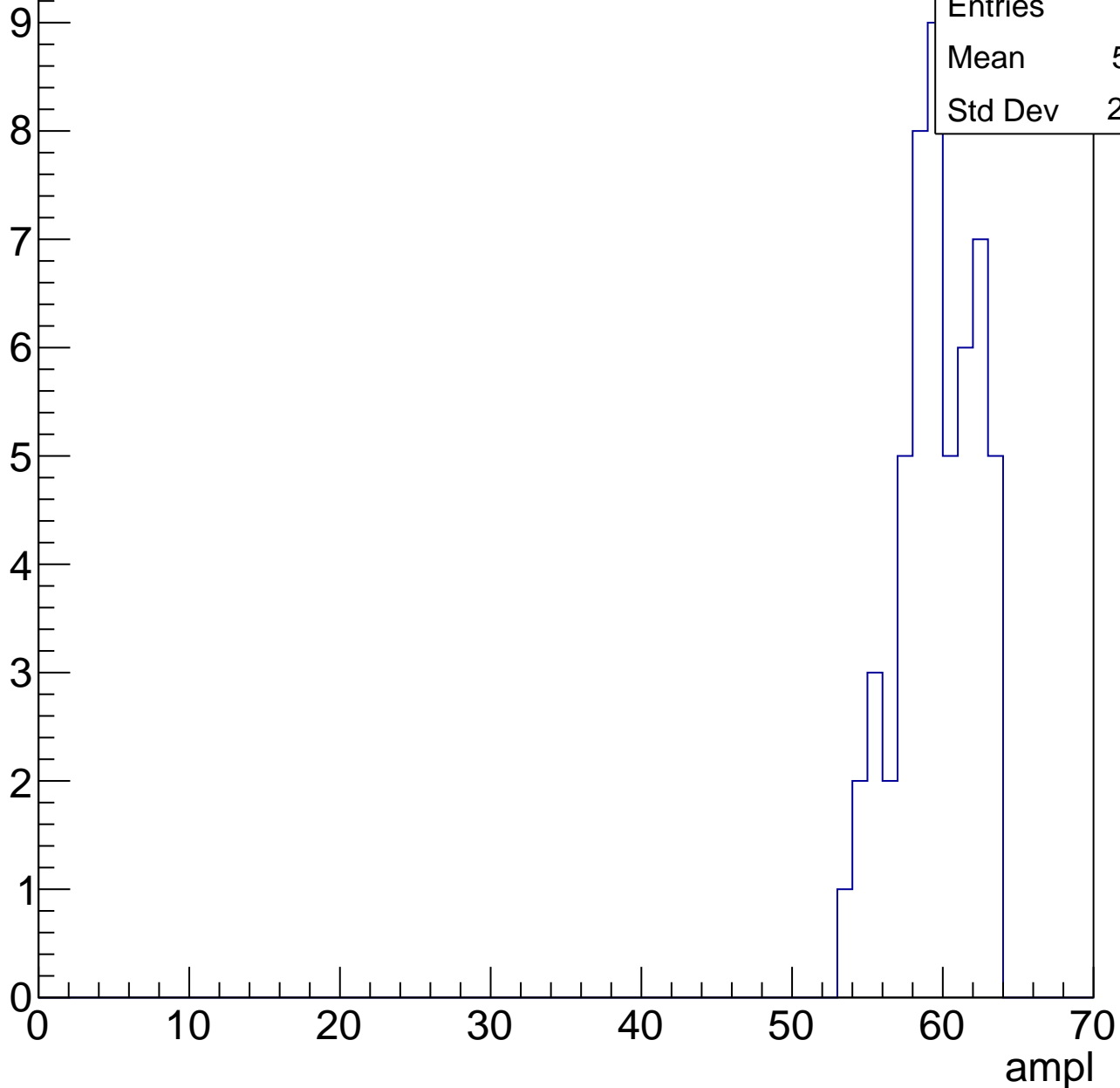


Entries	61
Mean	53.49
Std Dev	3.202

# B1L000S, U24-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

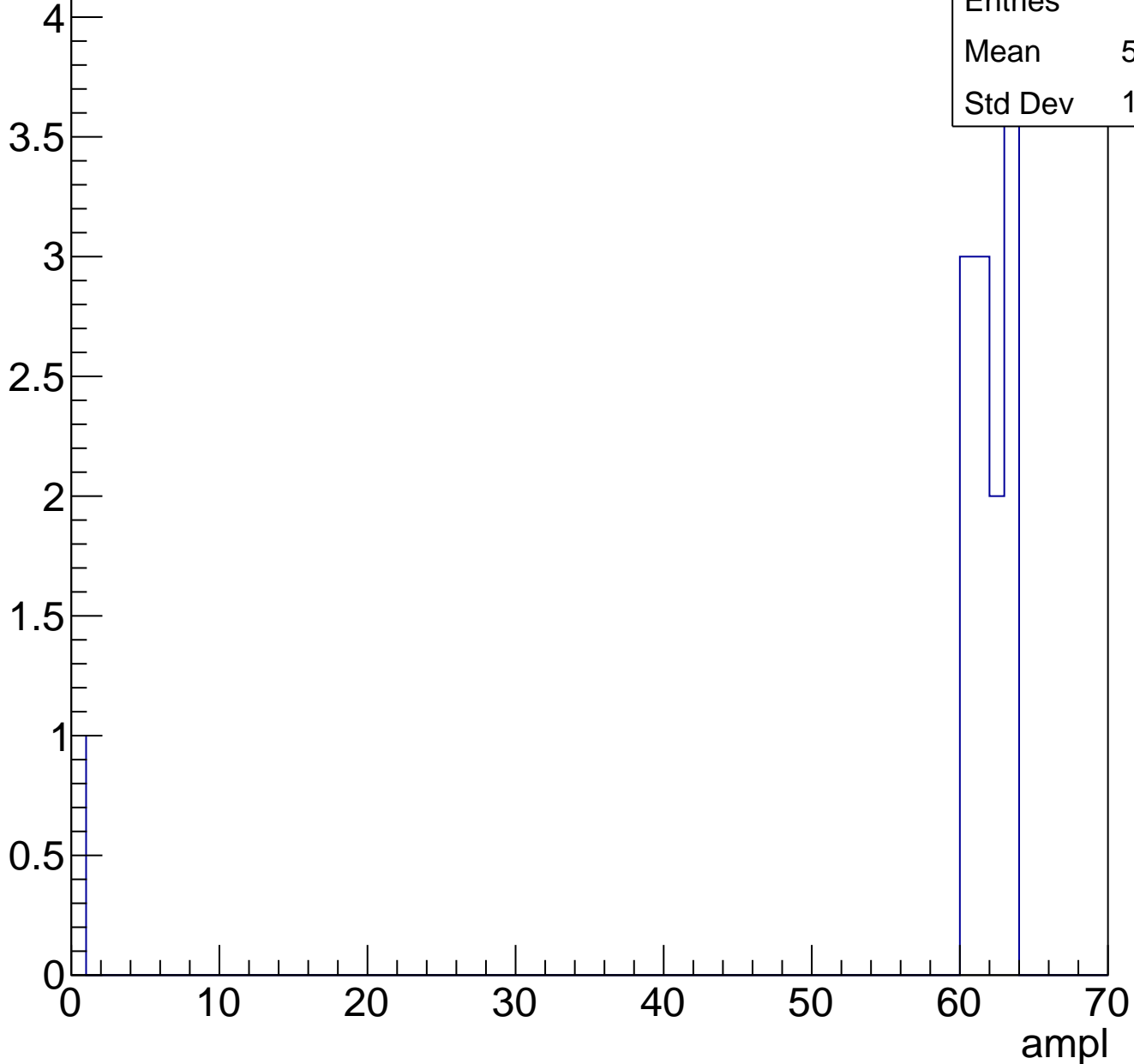
Entry



# B1L000S, U24-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

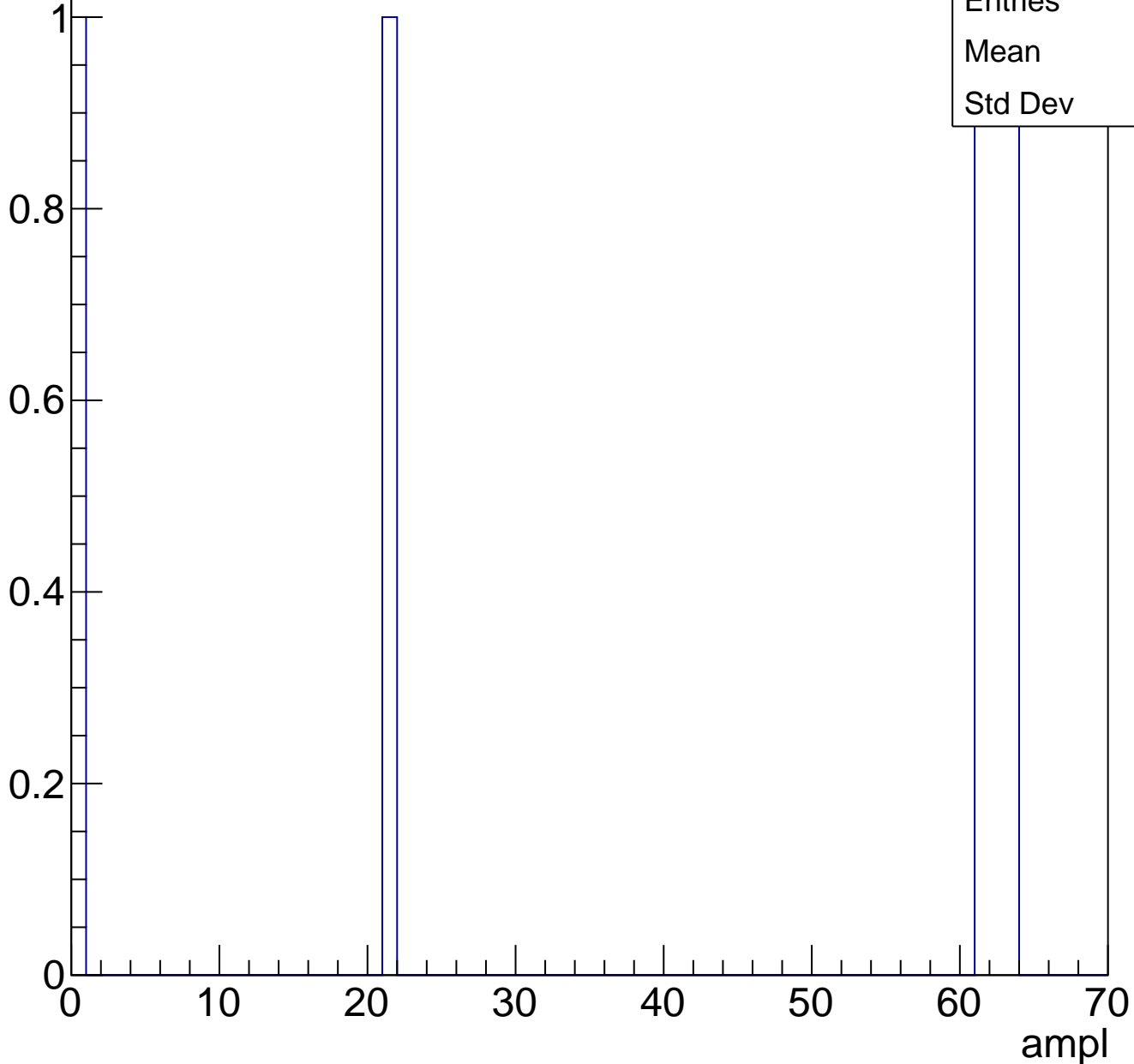




# B1L000S, U24-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch99, adc0

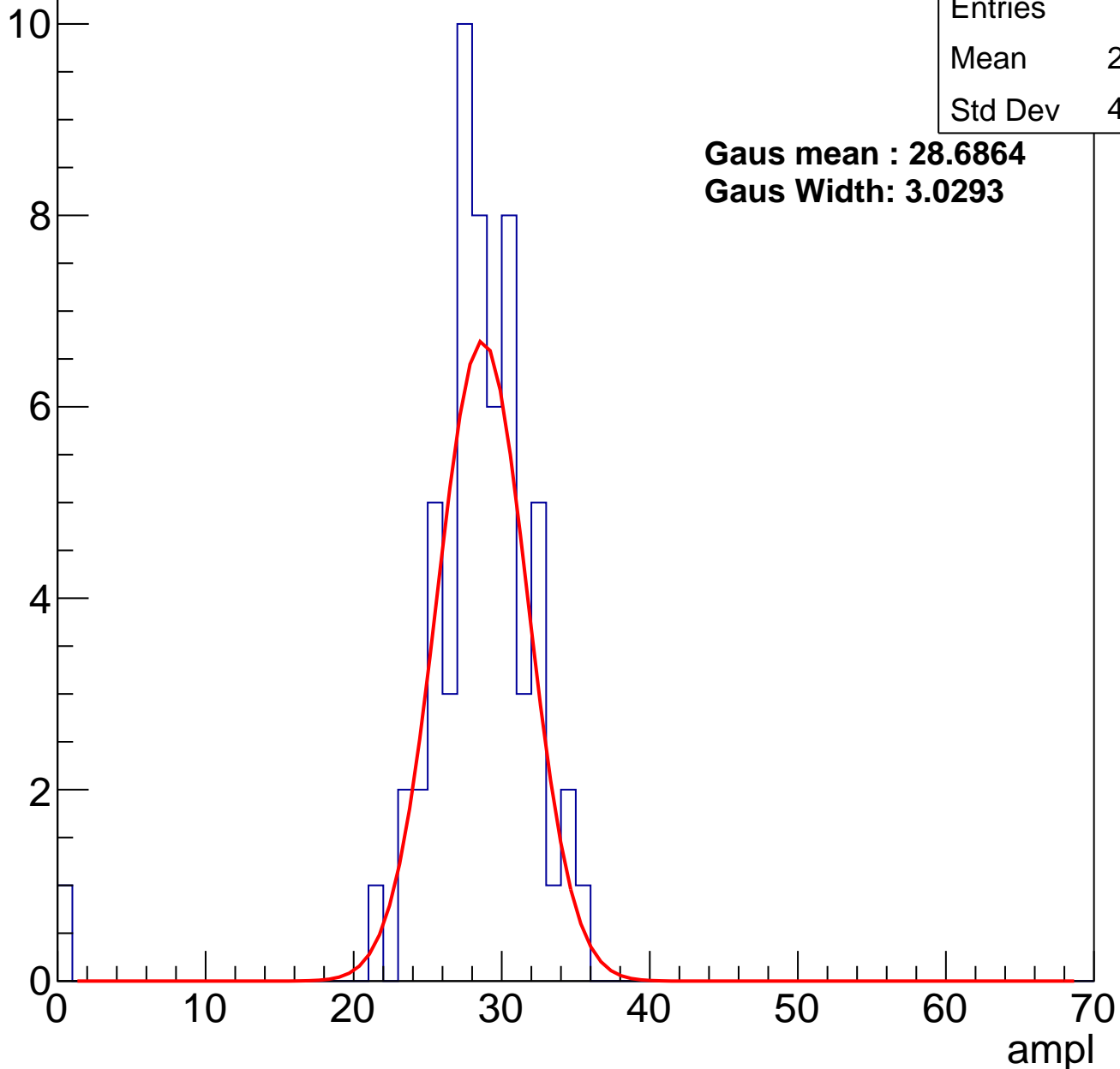
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	27.84
Std Dev	4.682

**Gaus mean : 28.6864**

**Gaus Width: 3.0293**

Entry



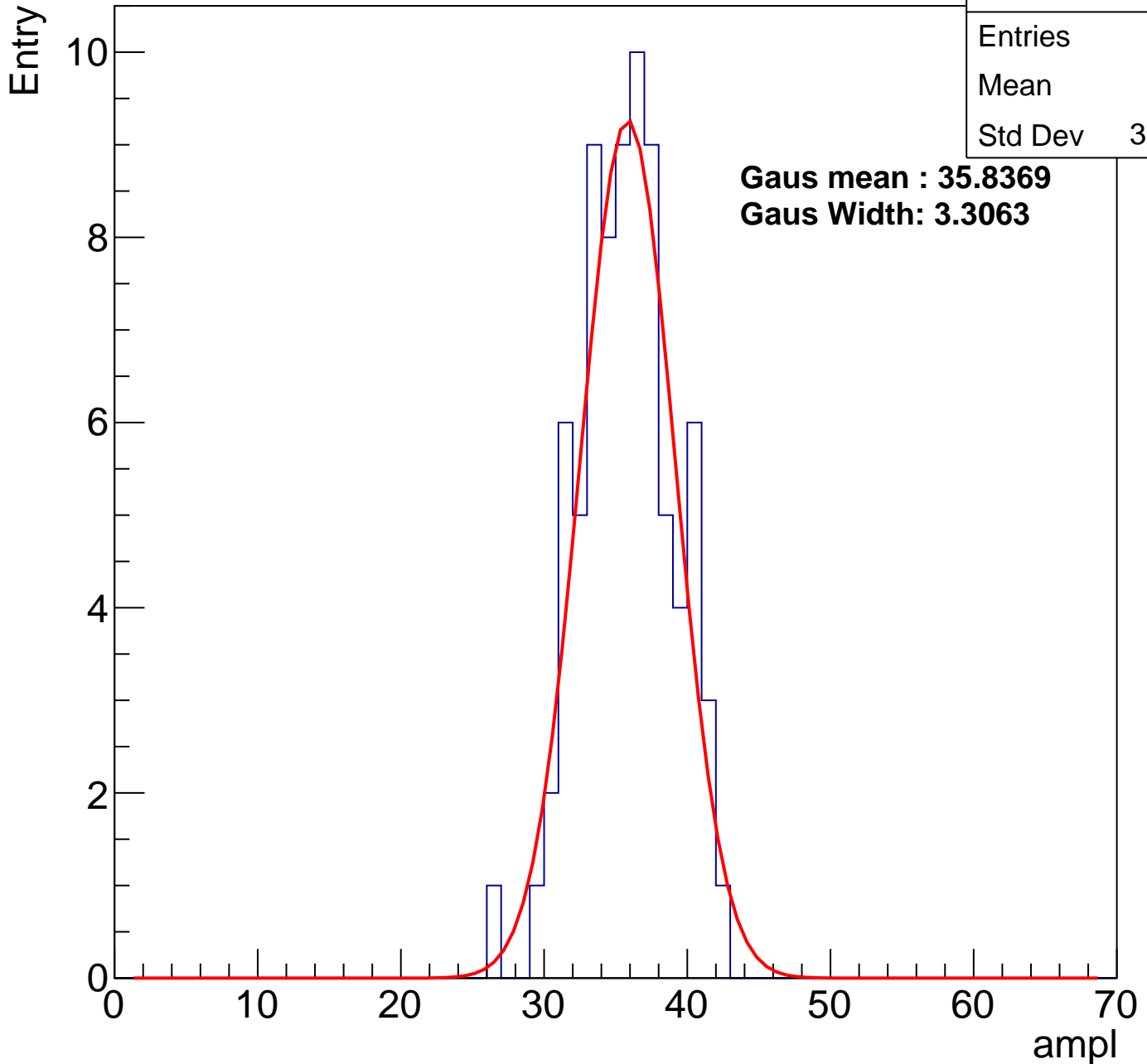
# B1L000S, U24-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.3
Std Dev	3.196

**Gaus mean : 35.8369**

**Gaus Width: 3.3063**



# B1L000S, U24-ch99, adc2

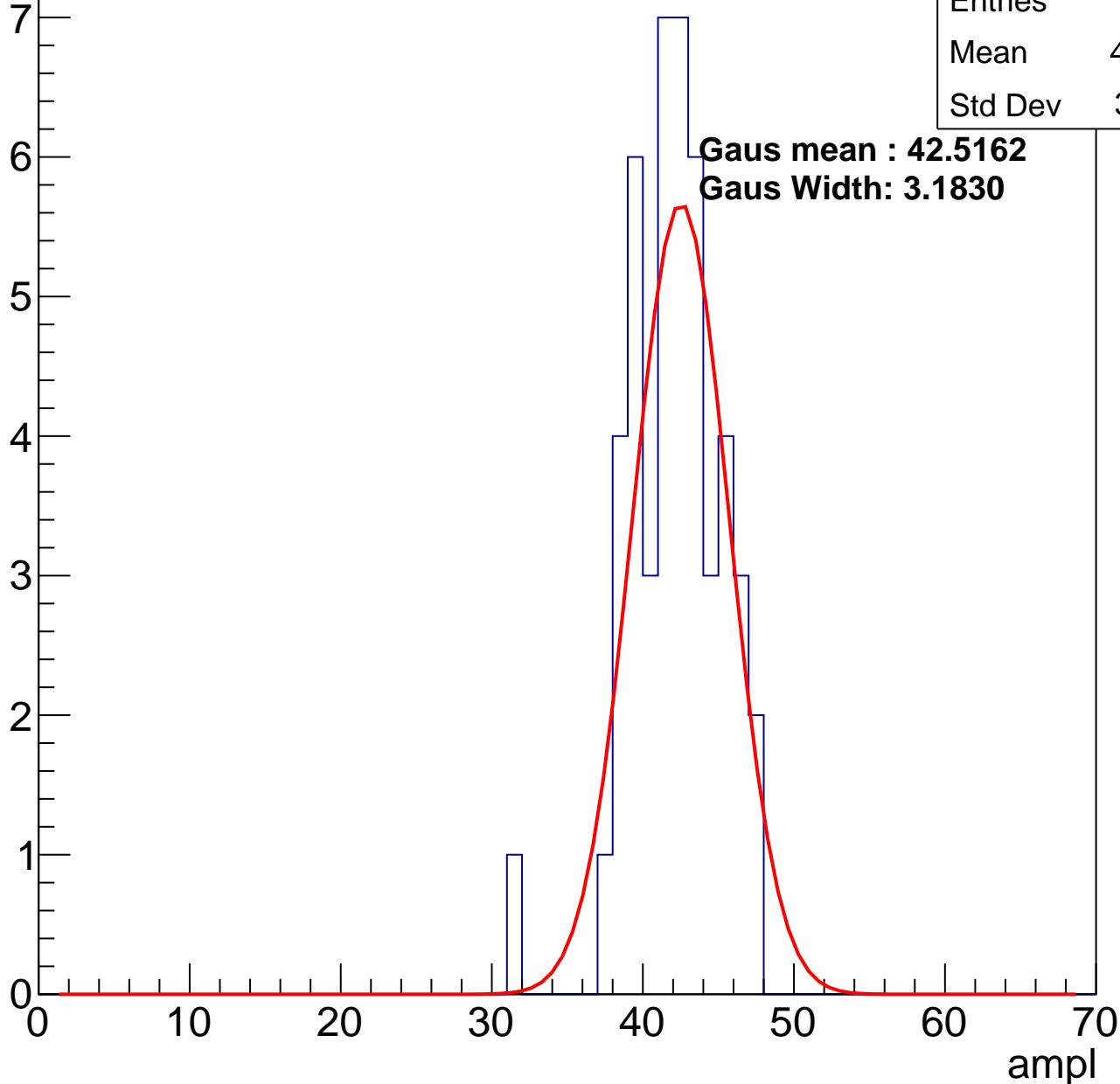
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	41.64
Std Dev	3.021

**Gaus mean : 42.5162**

**Gaus Width: 3.1830**

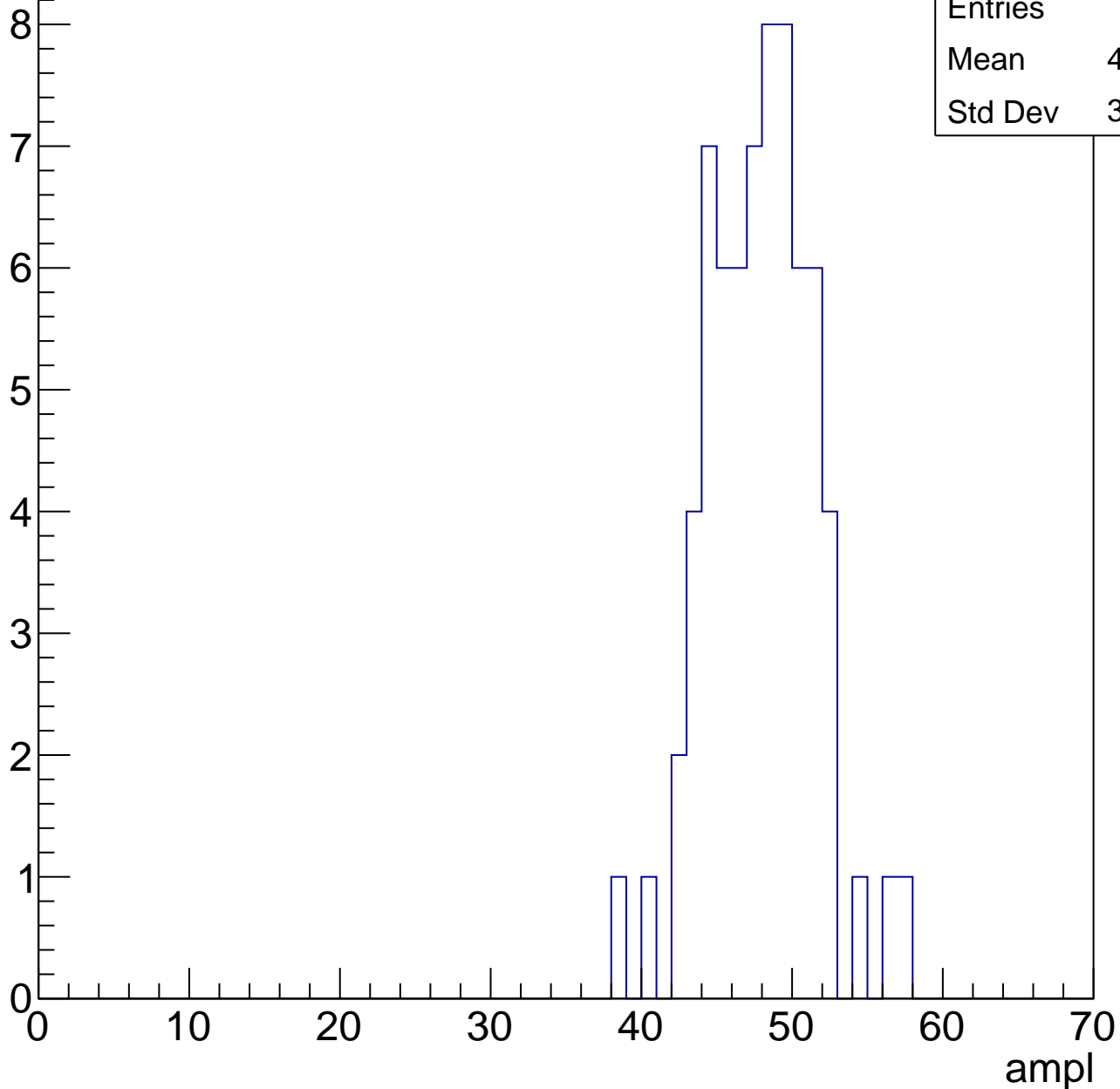


# B1L000S, U24-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.45
Std Dev	3.495



# B1L000S, U24-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	54.15
Std Dev	3.41

Entry

10

8

6

4

2

0

0

10

20

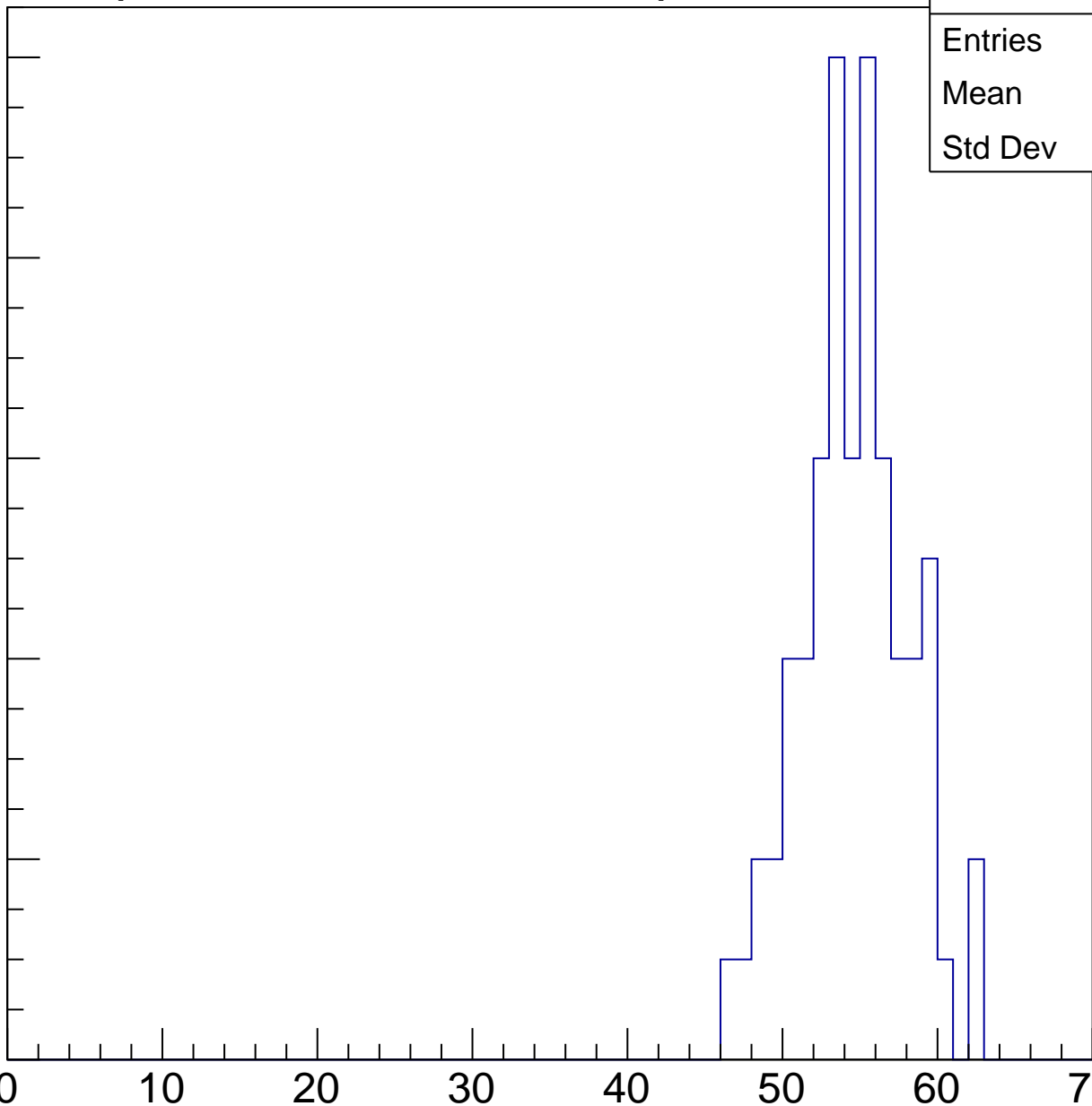
30

40

50

60

ampl

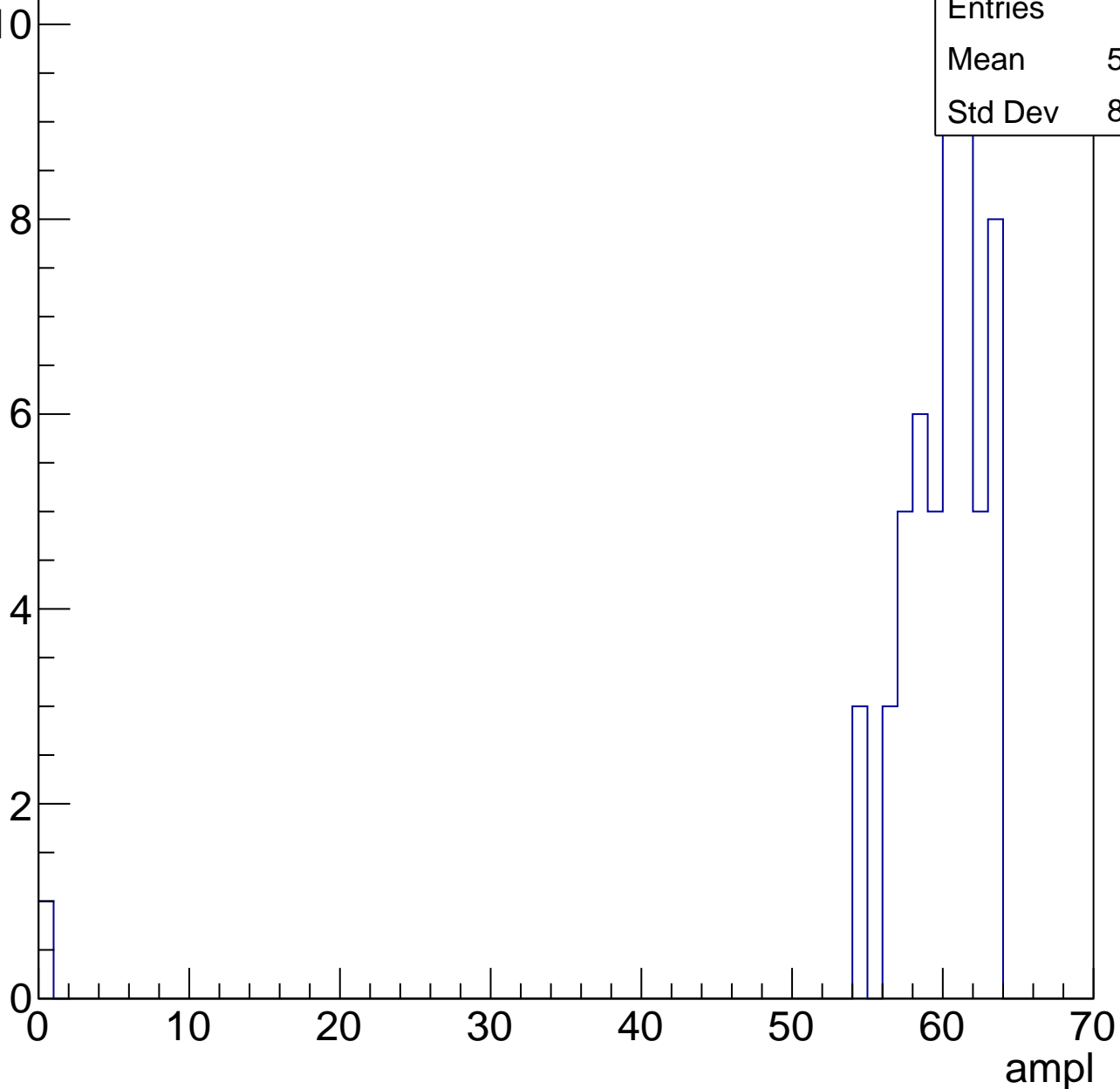


# B1L000S, U24-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.58
Std Dev	8.335



# B1L000S, U24-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch100, adc0

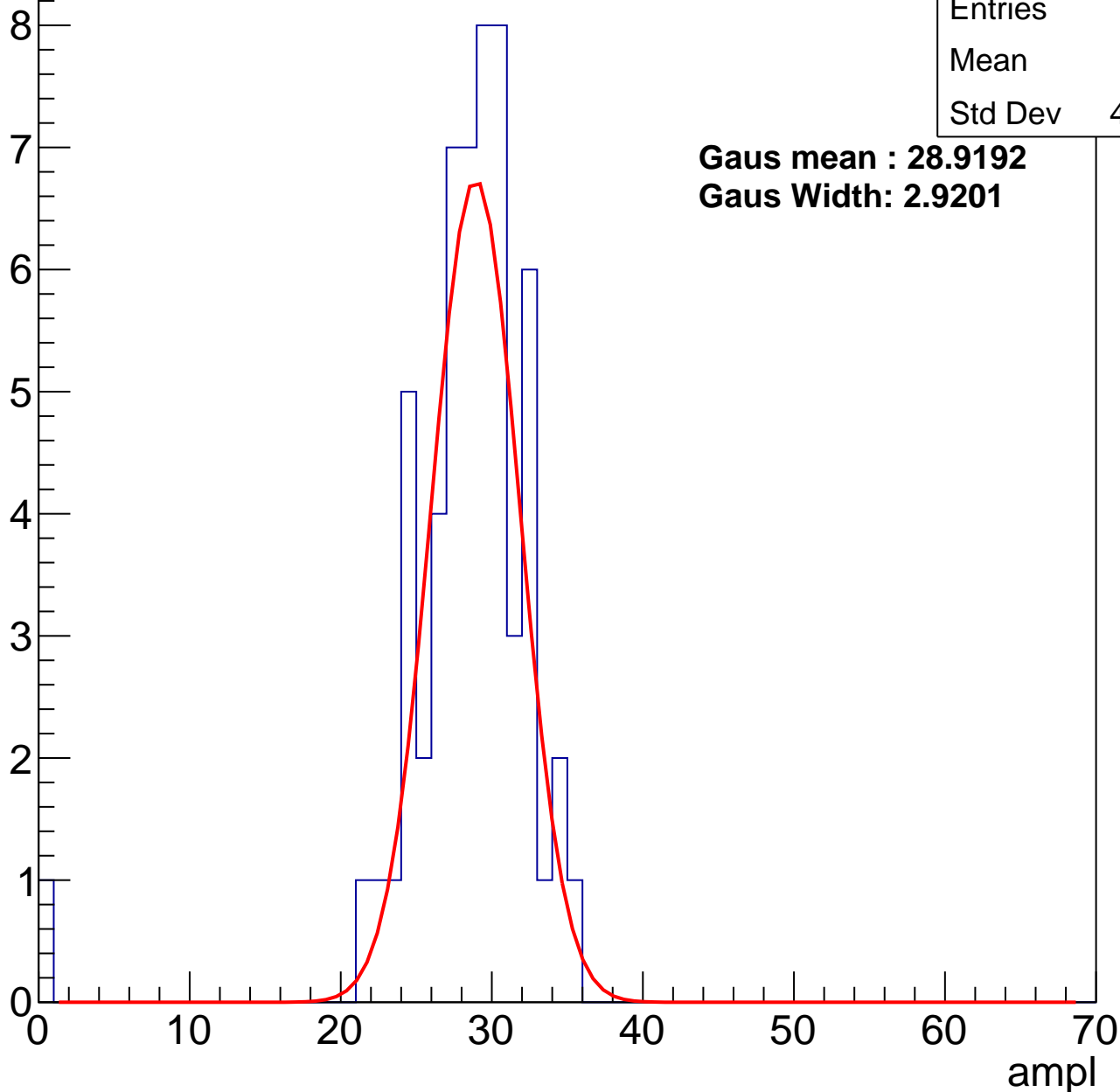
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.9
Std Dev	4.777

**Gaus mean : 28.9192**

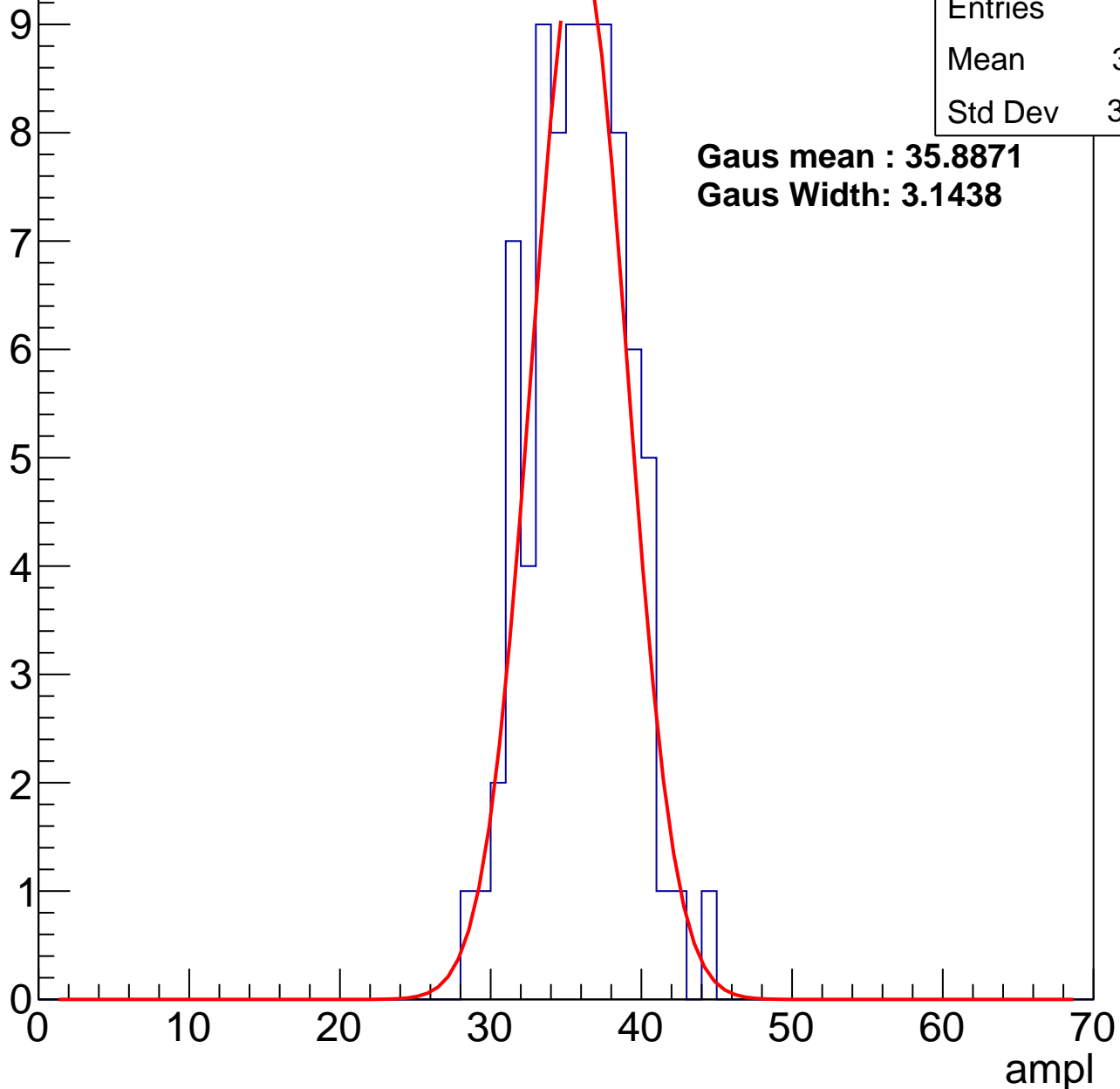
**Gaus Width: 2.9201**



# B1L000S, U24-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	81
Mean	35.41
Std Dev	3.177

**Gaus mean : 35.8871**

**Gaus Width: 3.1438**

# B1L000S, U24-ch100, adc2

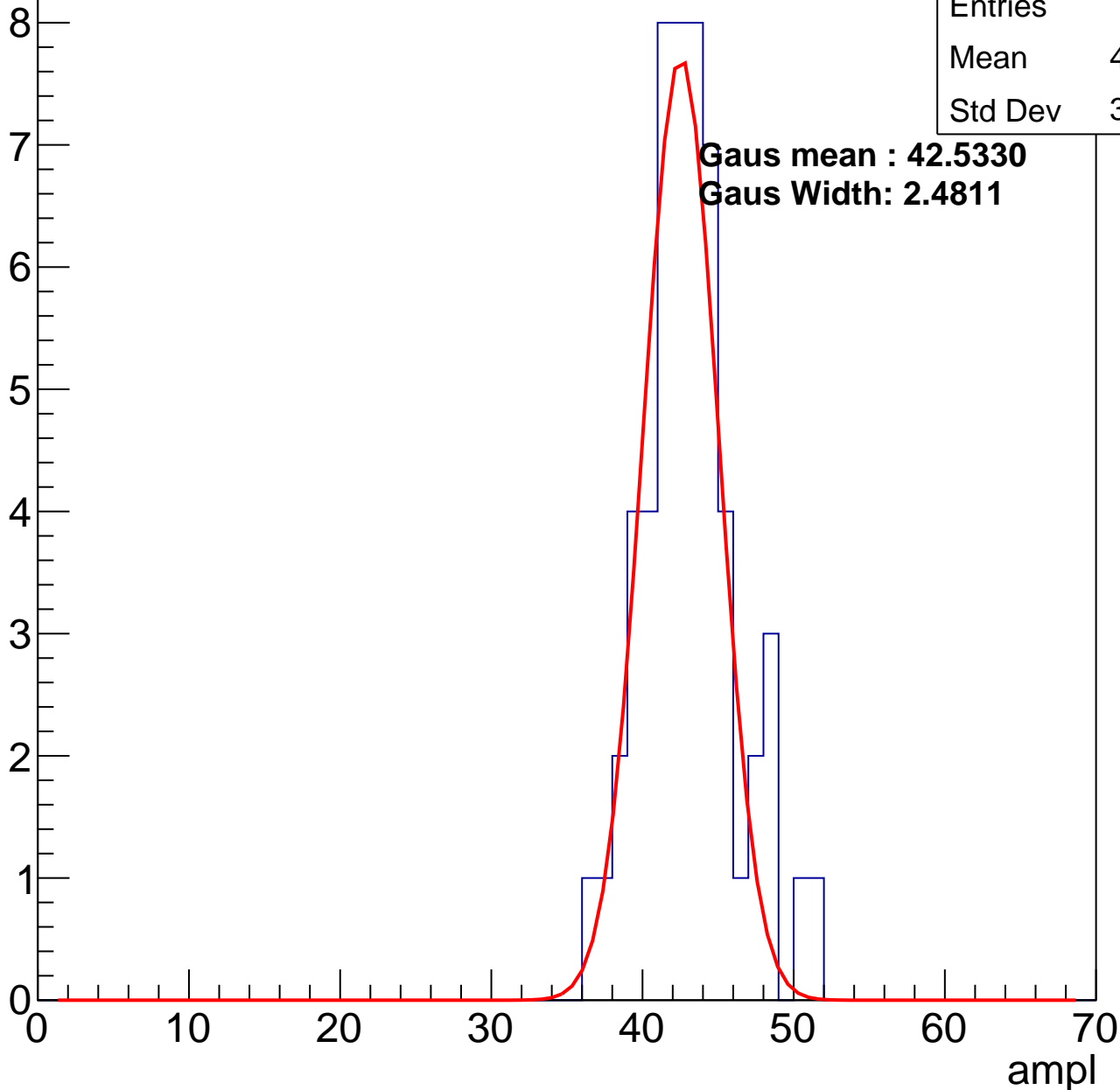
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.65
Std Dev	3.076

**Gaus mean : 42.5330**

**Gaus Width: 2.4811**

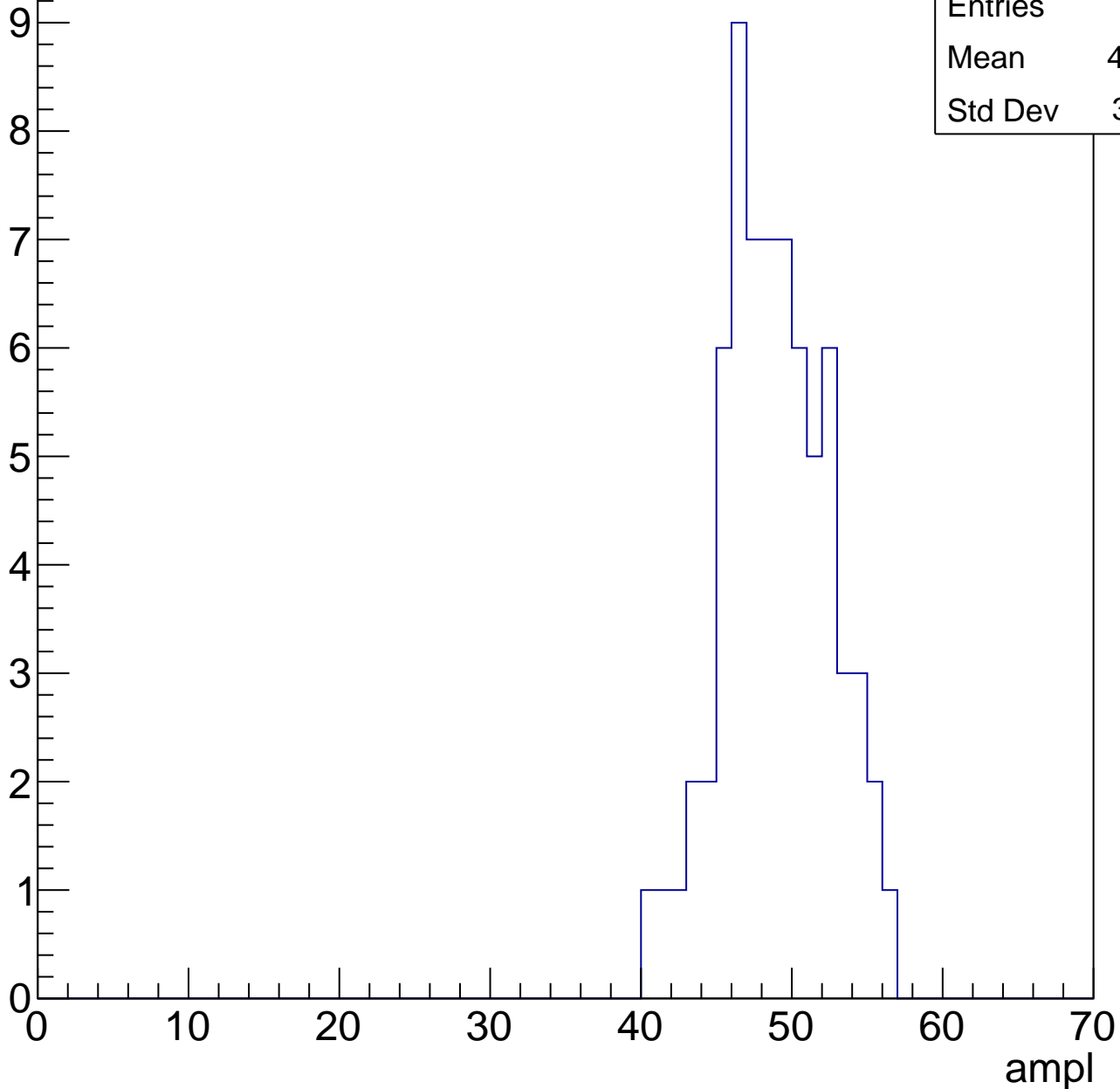


# B1L000S, U24-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.45
Std Dev	3.471

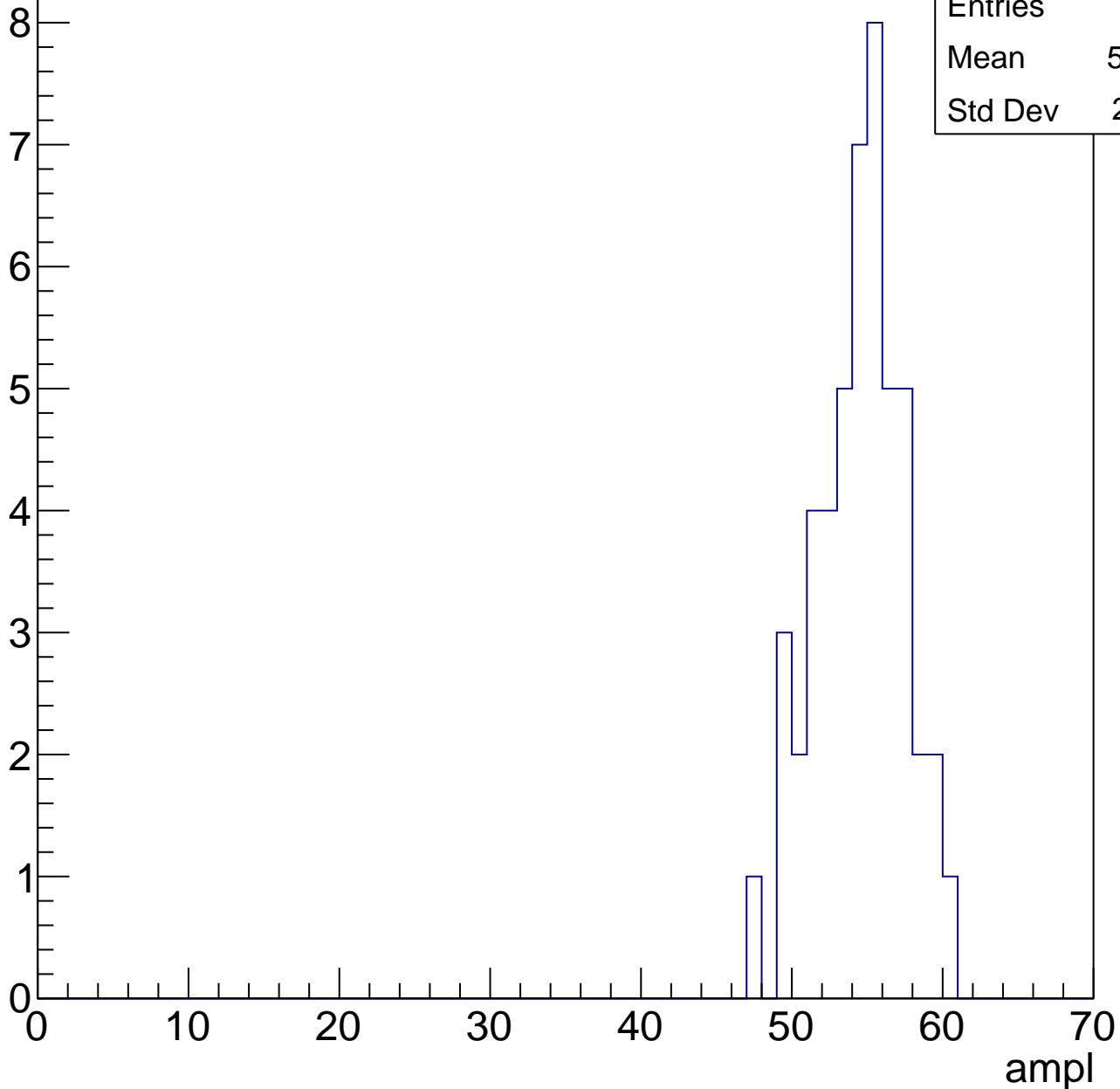


# B1L000S, U24-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	54.04
Std Dev	2.871

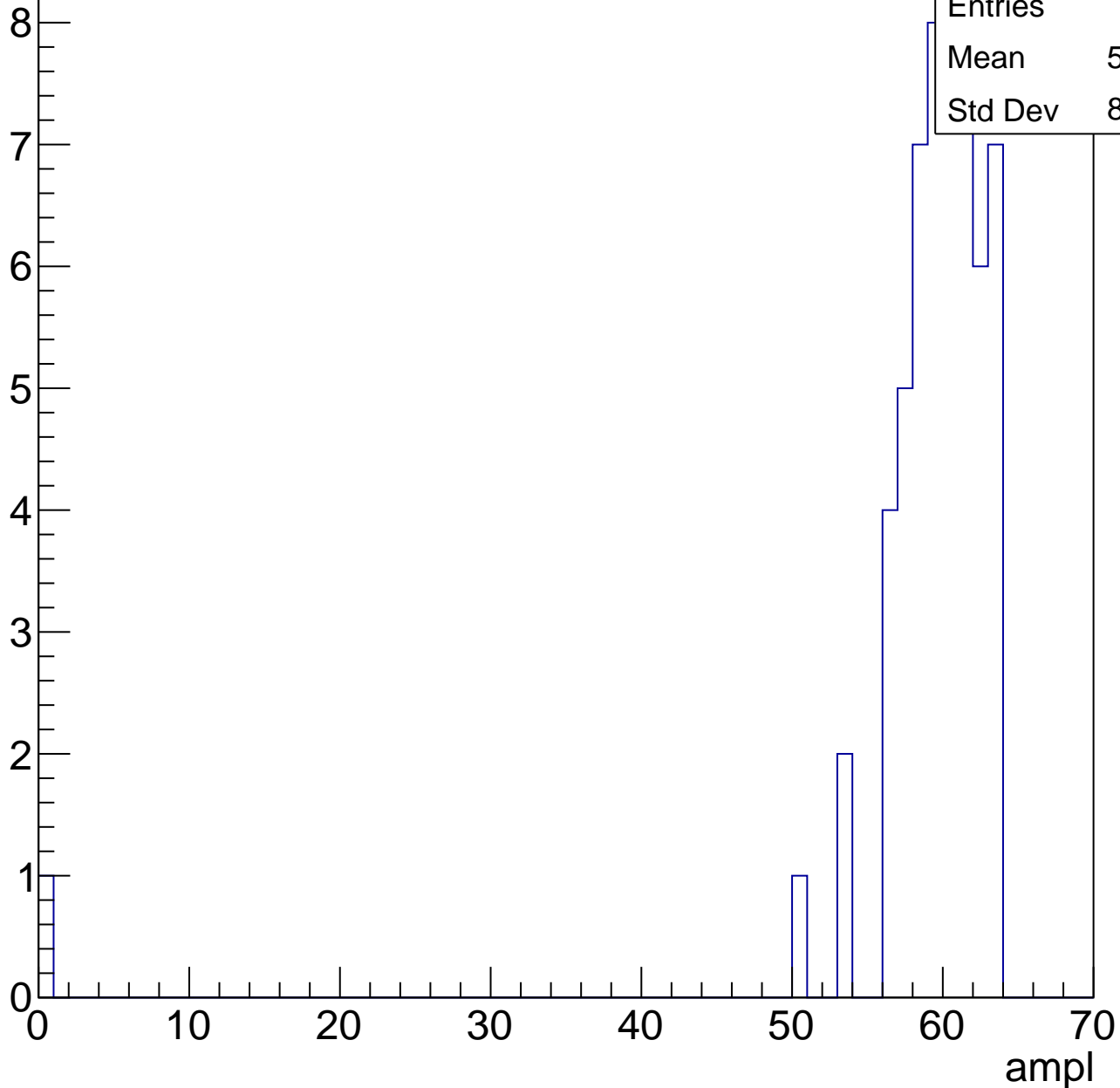


# B1L000S, U24-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

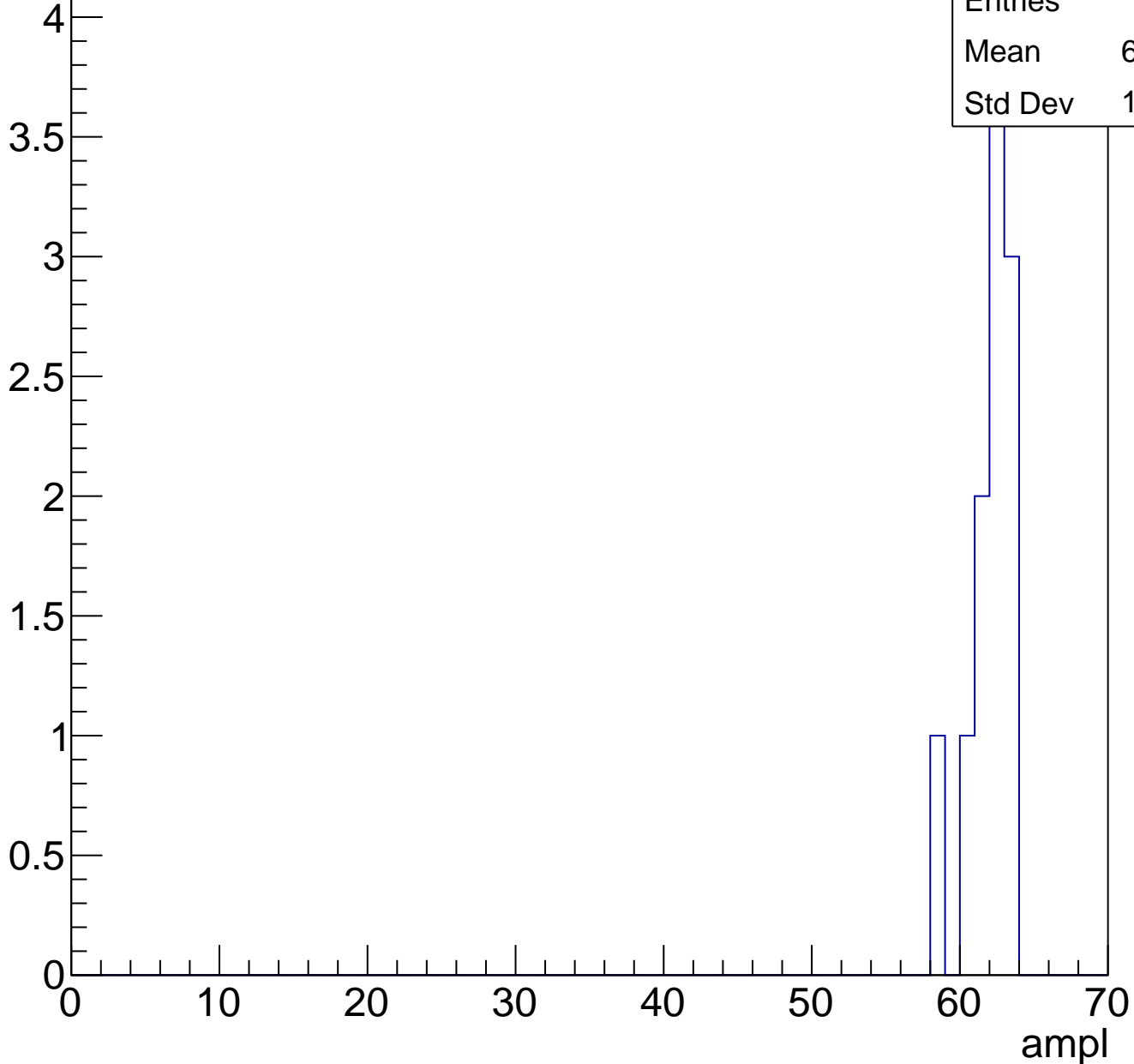
Entries	57
Mean	58.32
Std Dev	8.247



# B1L000S, U24-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch101, adc0

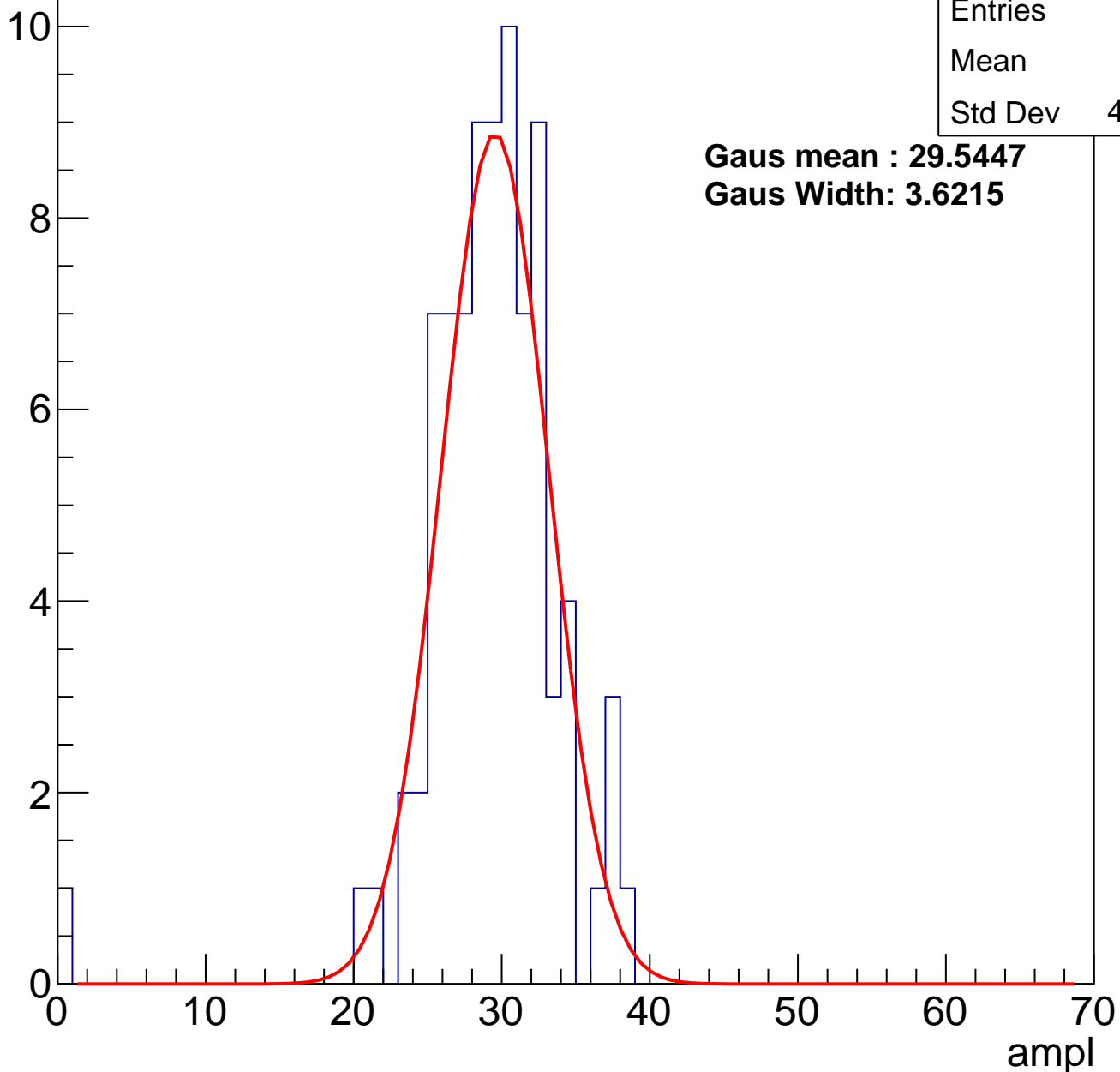
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	28.8
Std Dev	4.763

**Gaus mean : 29.5447**

**Gaus Width: 3.6215**

Entry



# B1L000S, U24-ch101, adc1

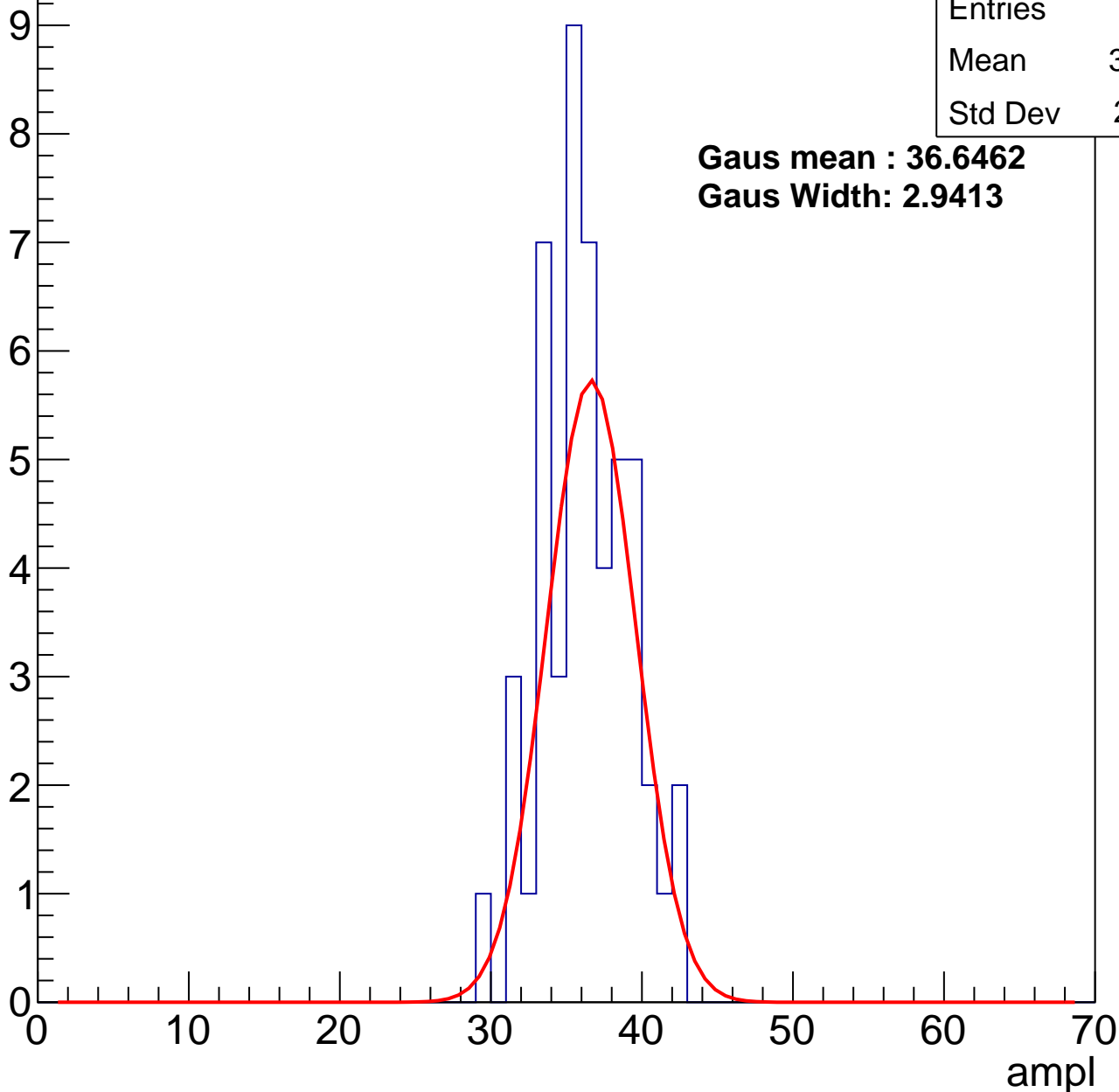
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	35.84
Std Dev	2.901

**Gaus mean : 36.6462**

**Gaus Width: 2.9413**



# B1L000S, U24-ch101, adc2

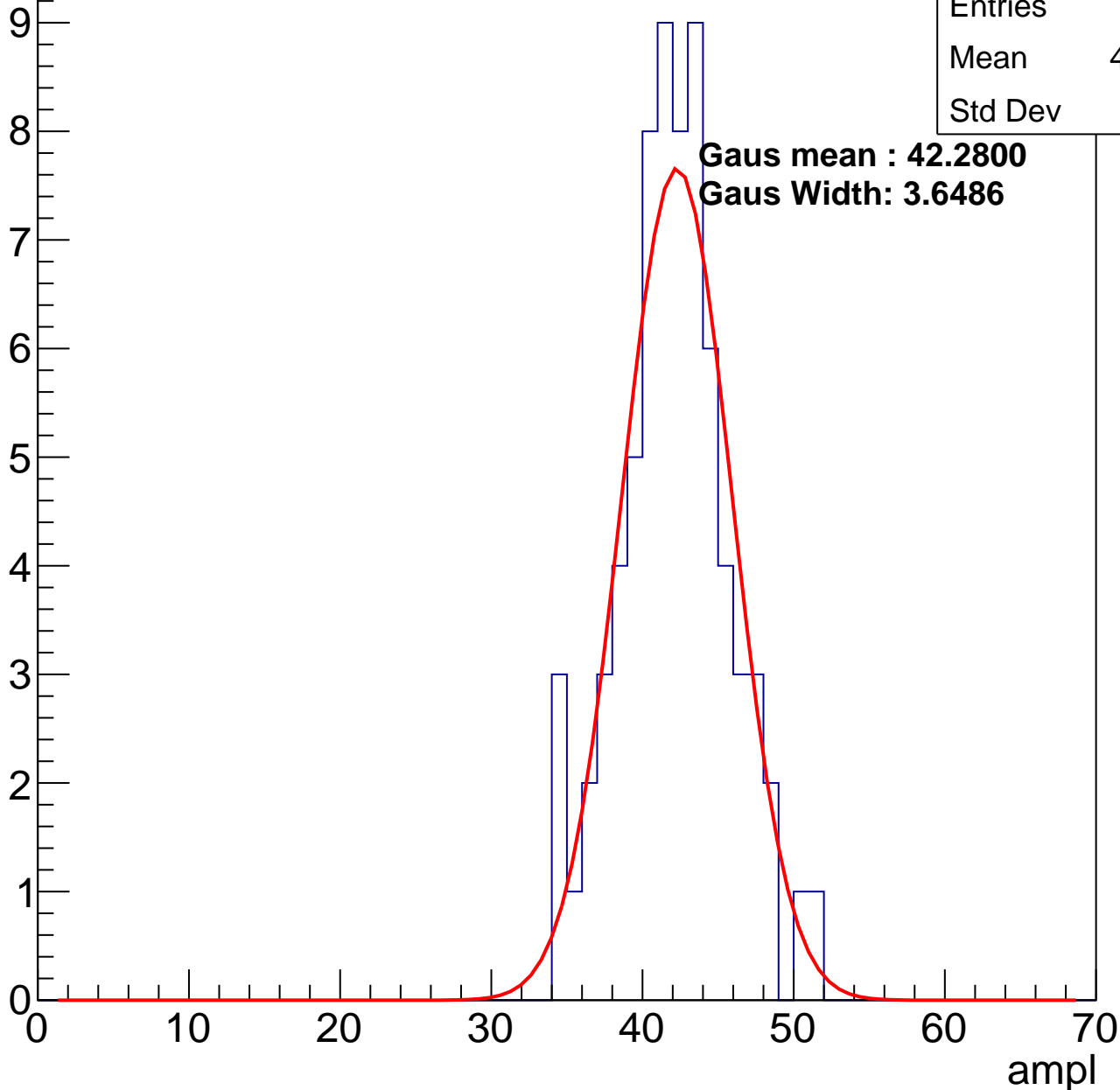
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.65
Std Dev	3.61

**Gaus mean : 42.2800**

**Gaus Width: 3.6486**

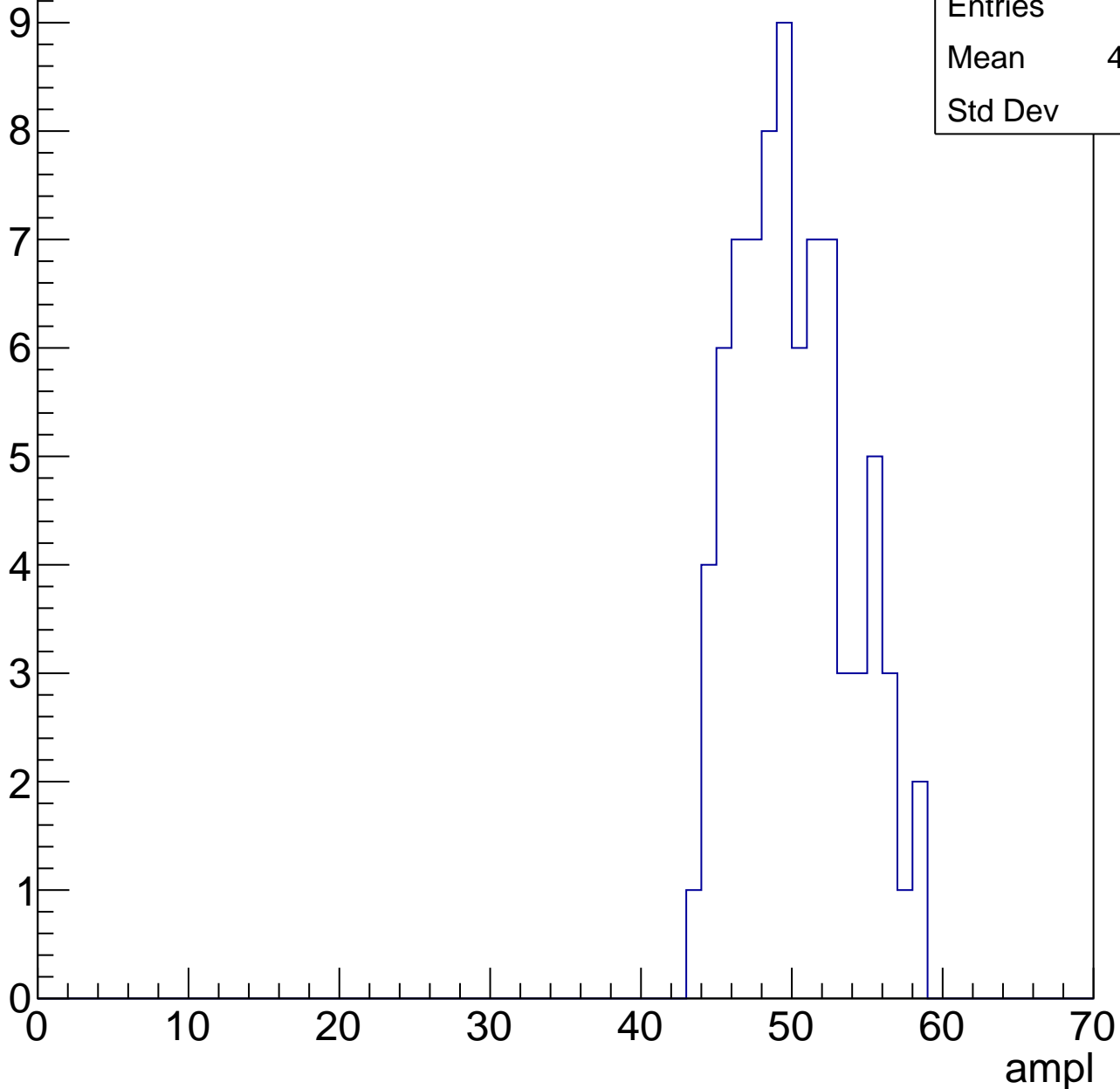


# B1L000S, U24-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	49.66
Std Dev	3.68

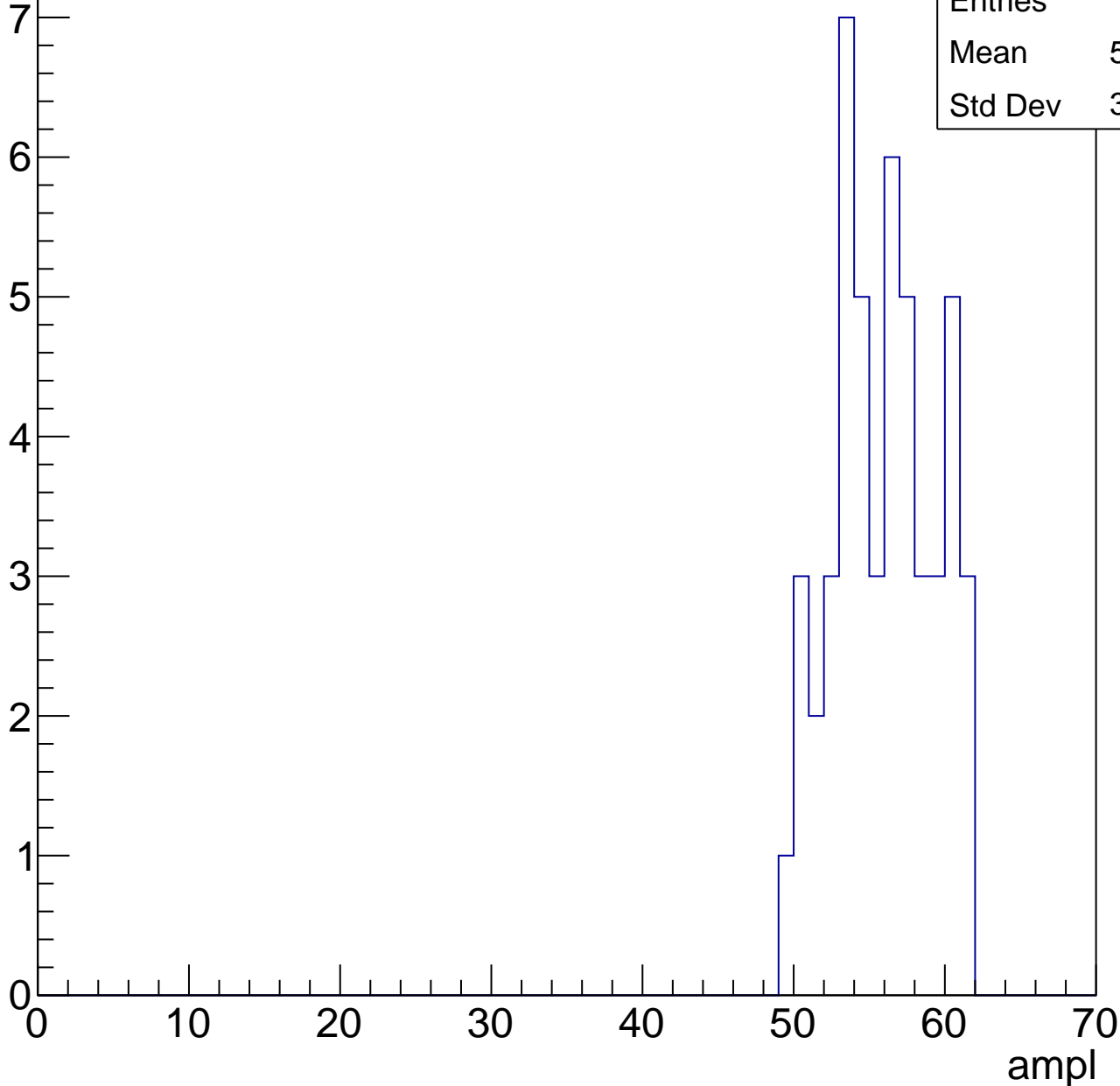


# B1L000S, U24-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	55.47
Std Dev	3.277

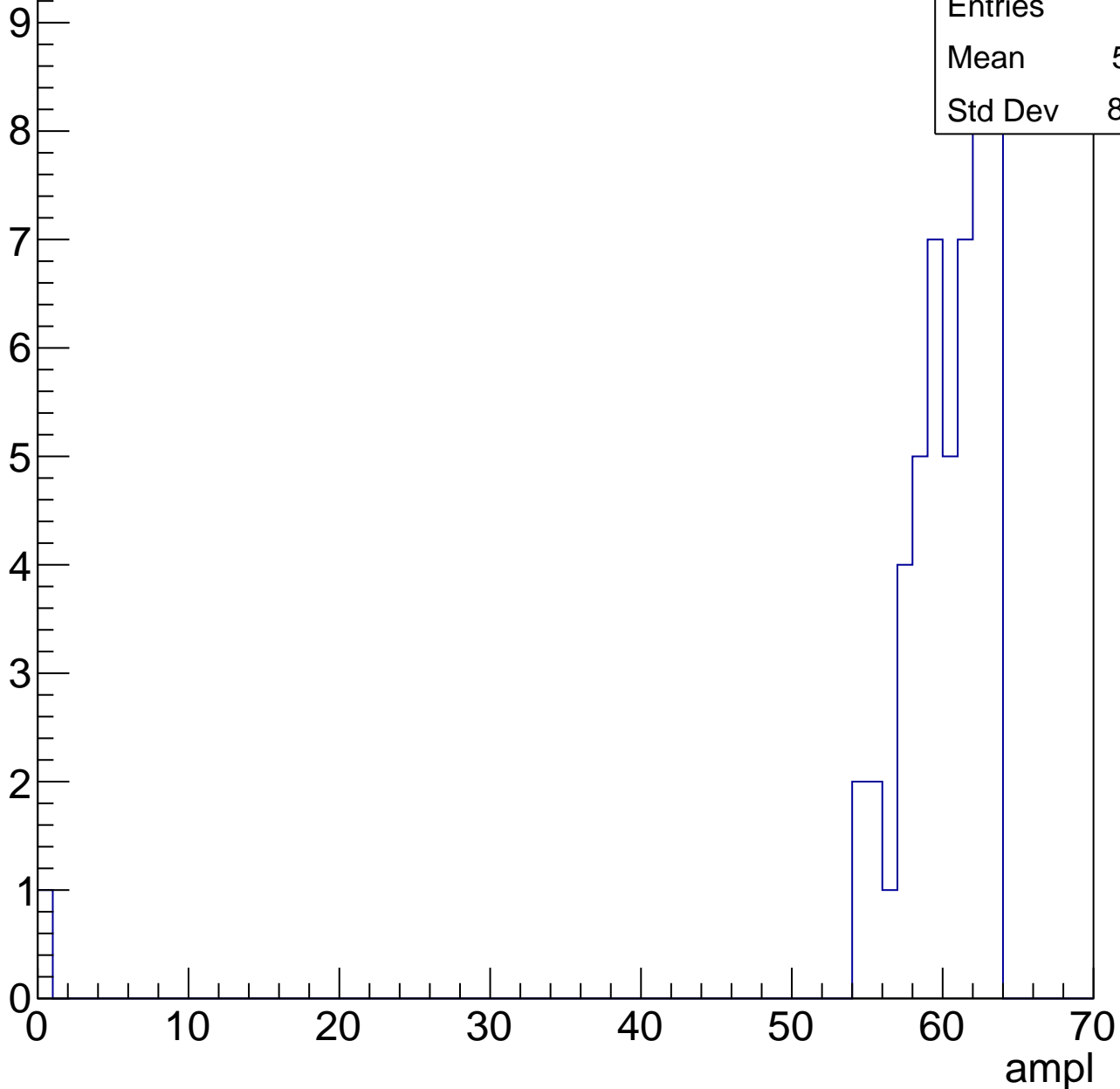


# B1L000S, U24-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.71
Std Dev	8.667



# B1L000S, U24-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

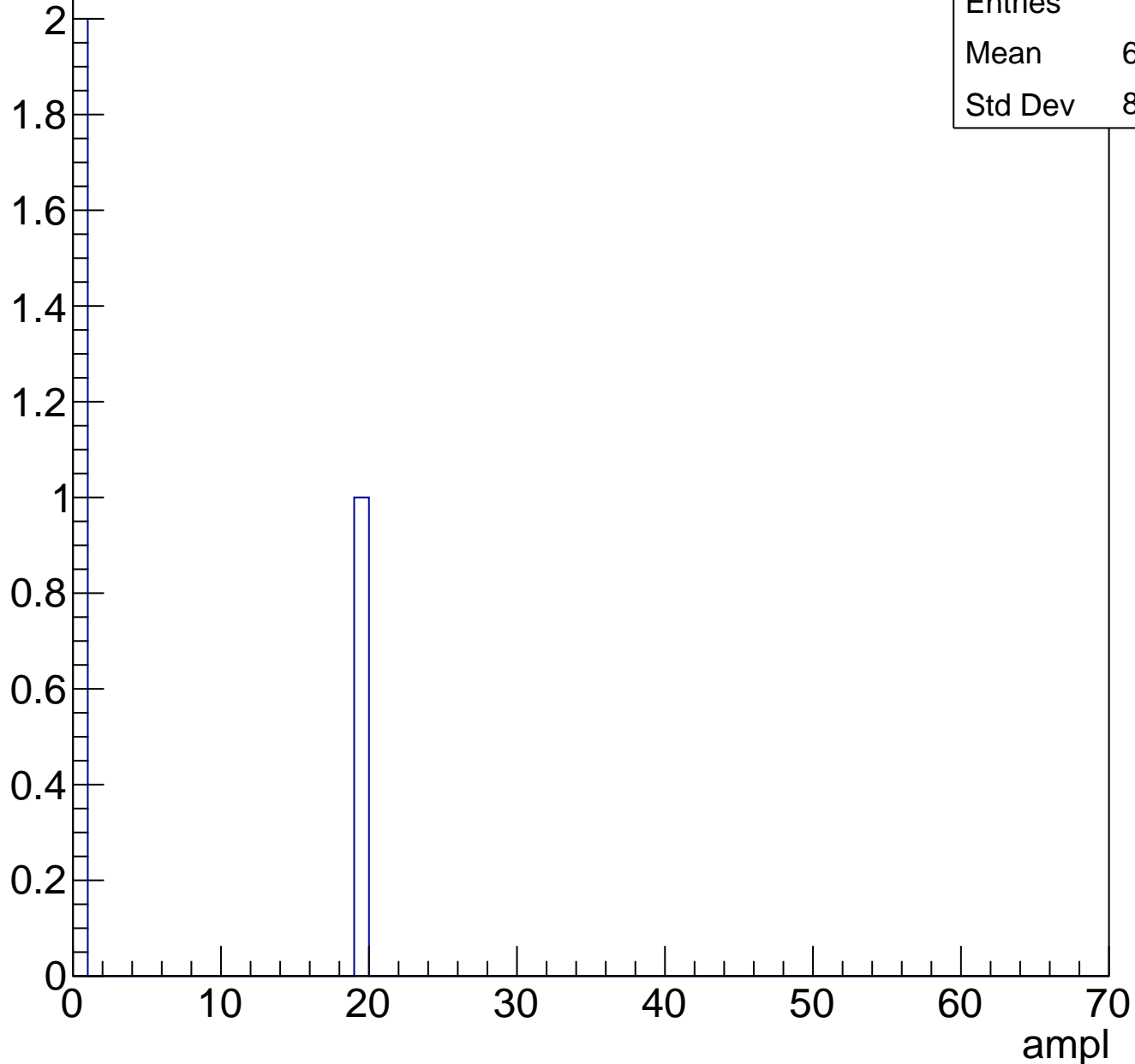




# B1L000S, U24-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	6.333
Std Dev	8.957

# B1L000S, U24-ch102, adc0

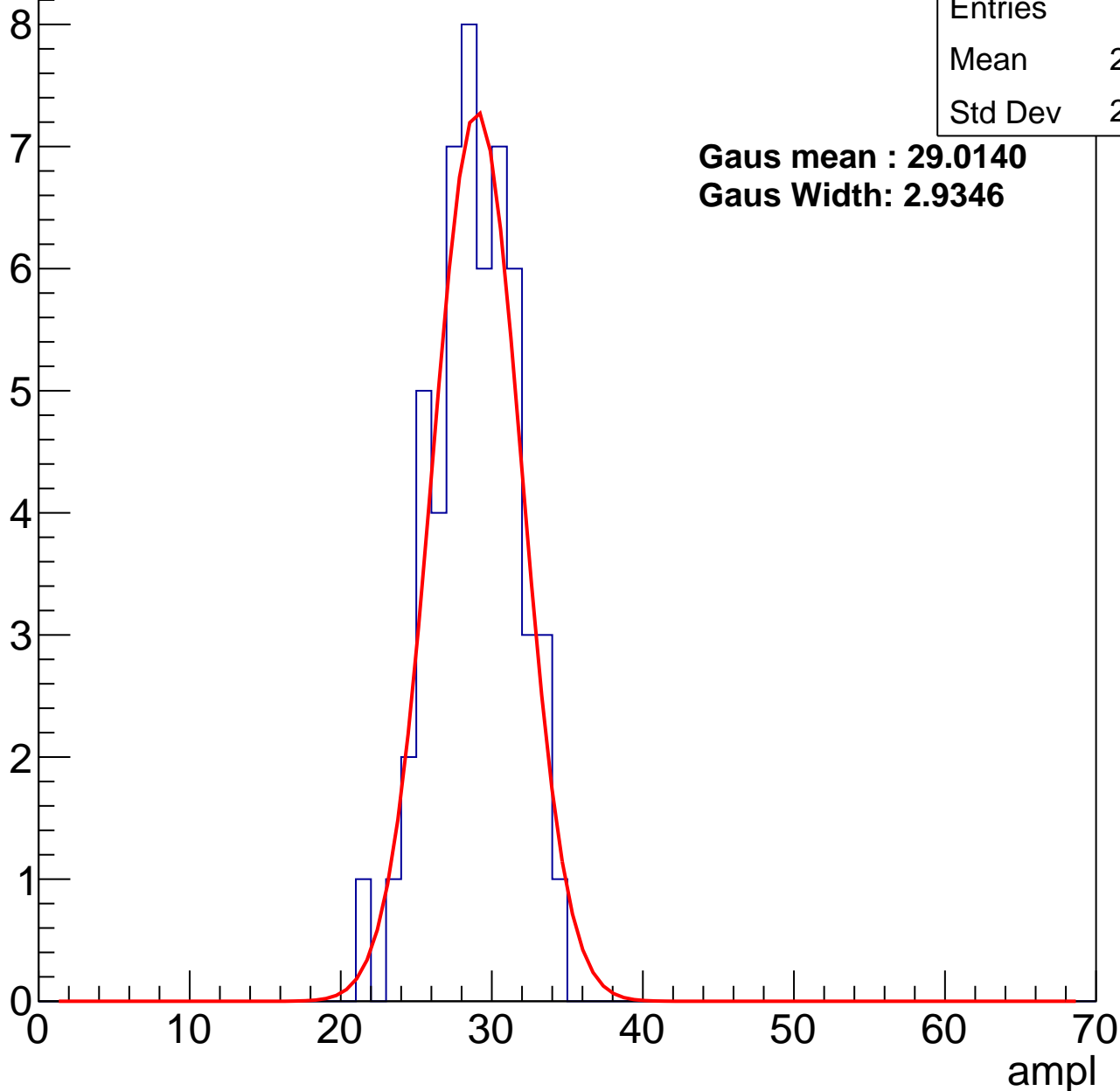
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	28.39
Std Dev	2.765

**Gaus mean : 29.0140**

**Gaus Width: 2.9346**



# B1L000S, U24-ch102, adc1

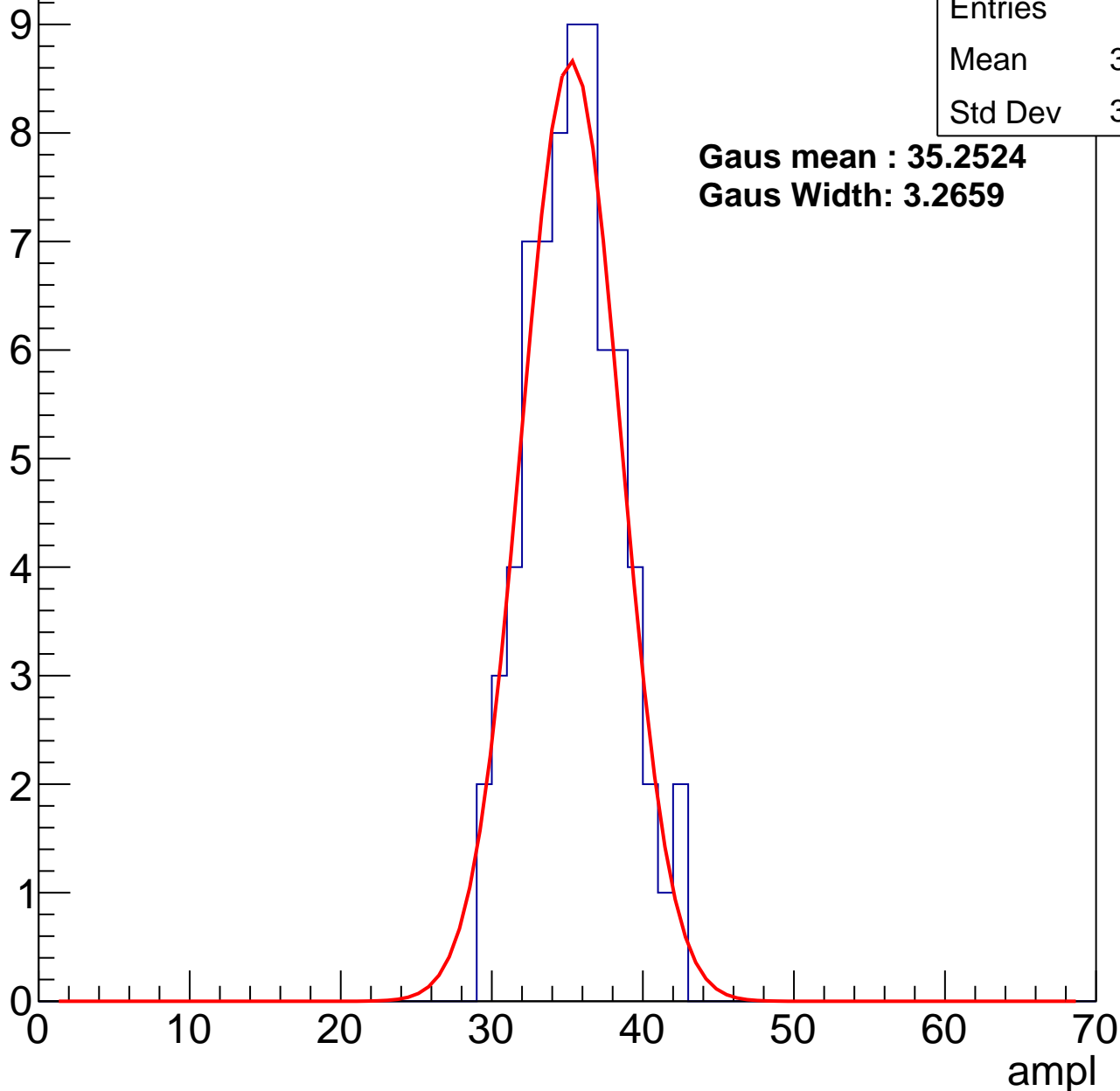
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	34.99
Std Dev	3.035

**Gaus mean : 35.2524**

**Gaus Width: 3.2659**



# B1L000S, U24-ch102, adc2

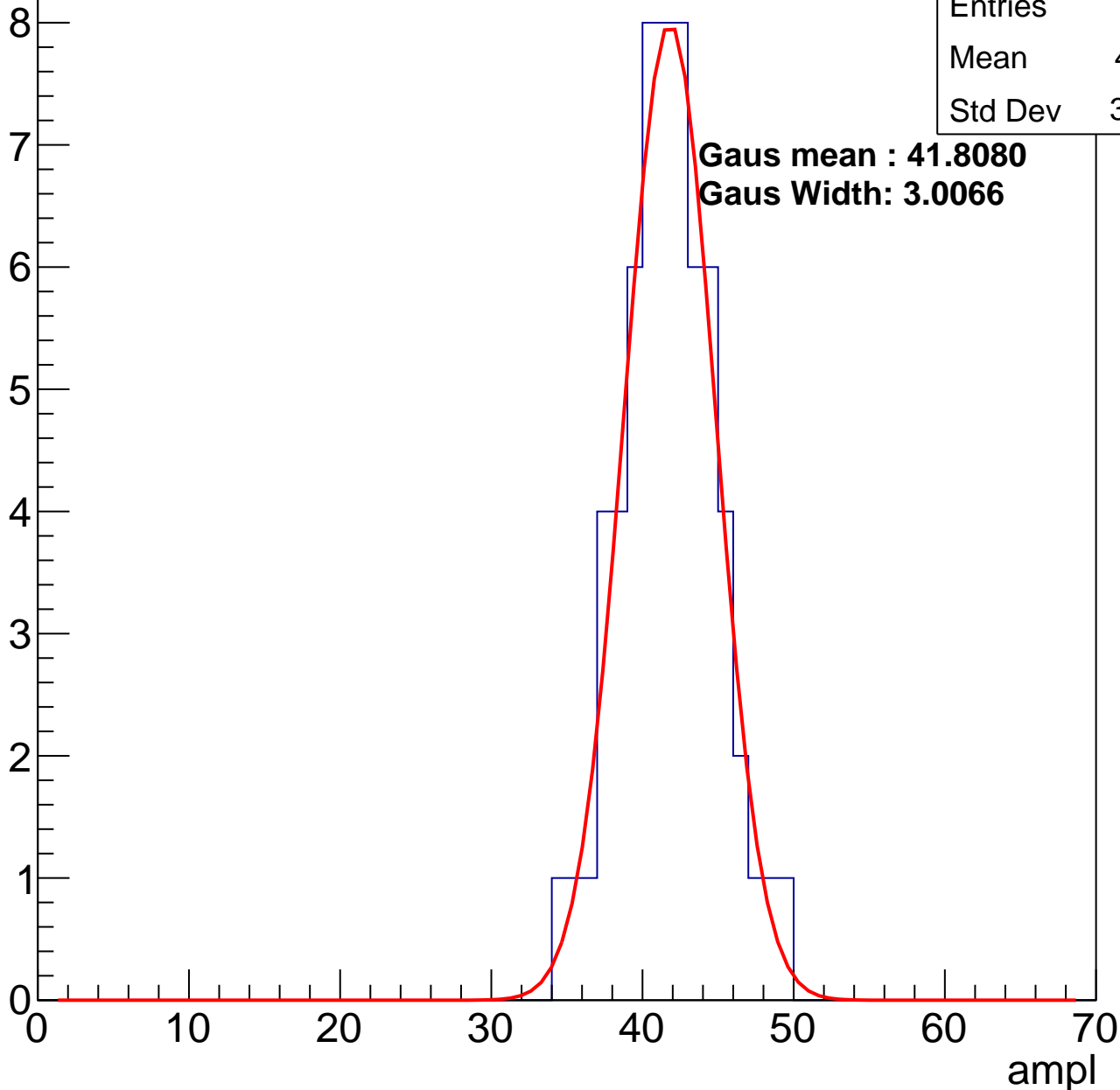
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.31
Std Dev	3.072

**Gaus mean : 41.8080**

**Gaus Width: 3.0066**

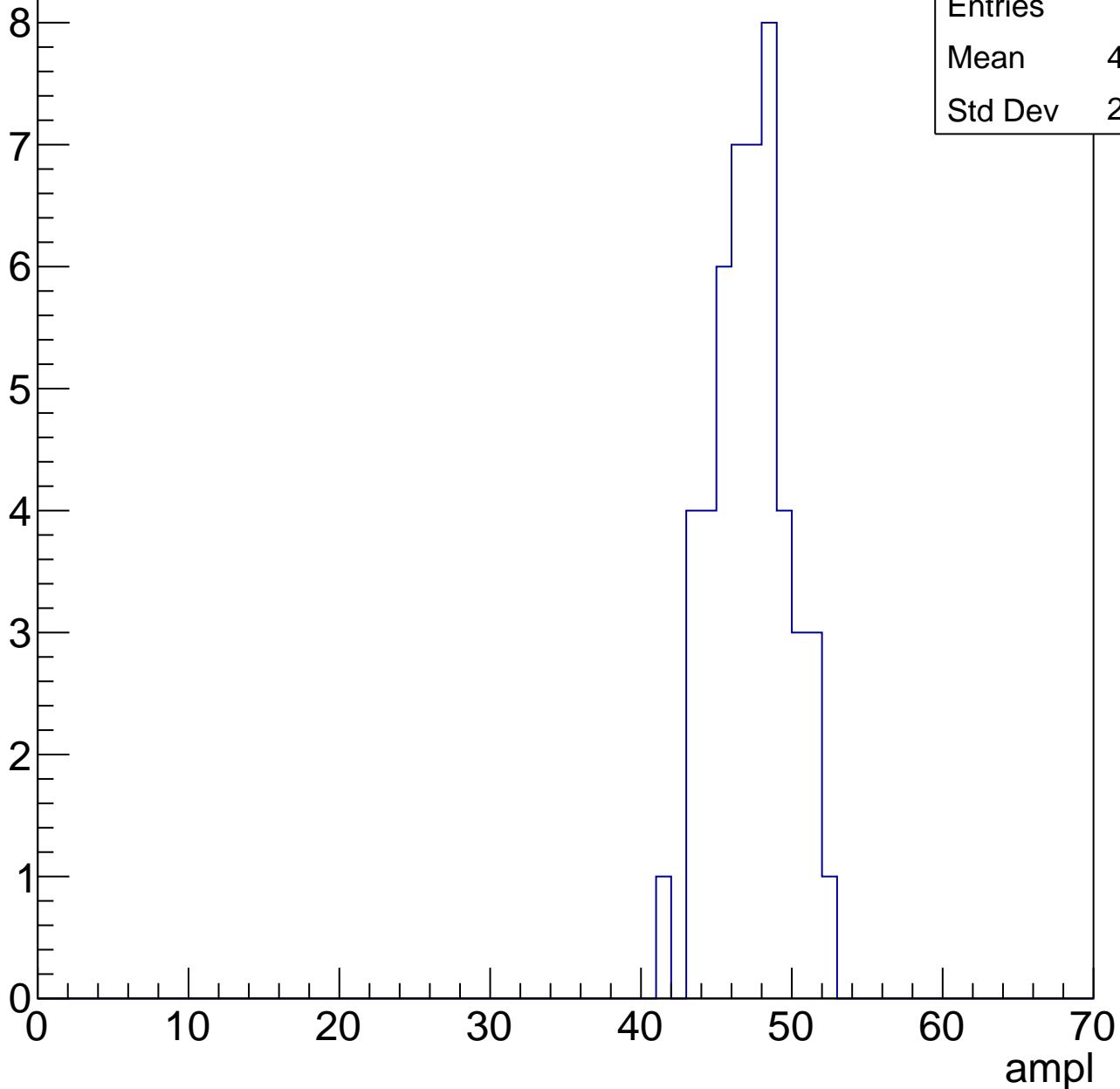


# B1L000S, U24-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	46.77
Std Dev	2.452

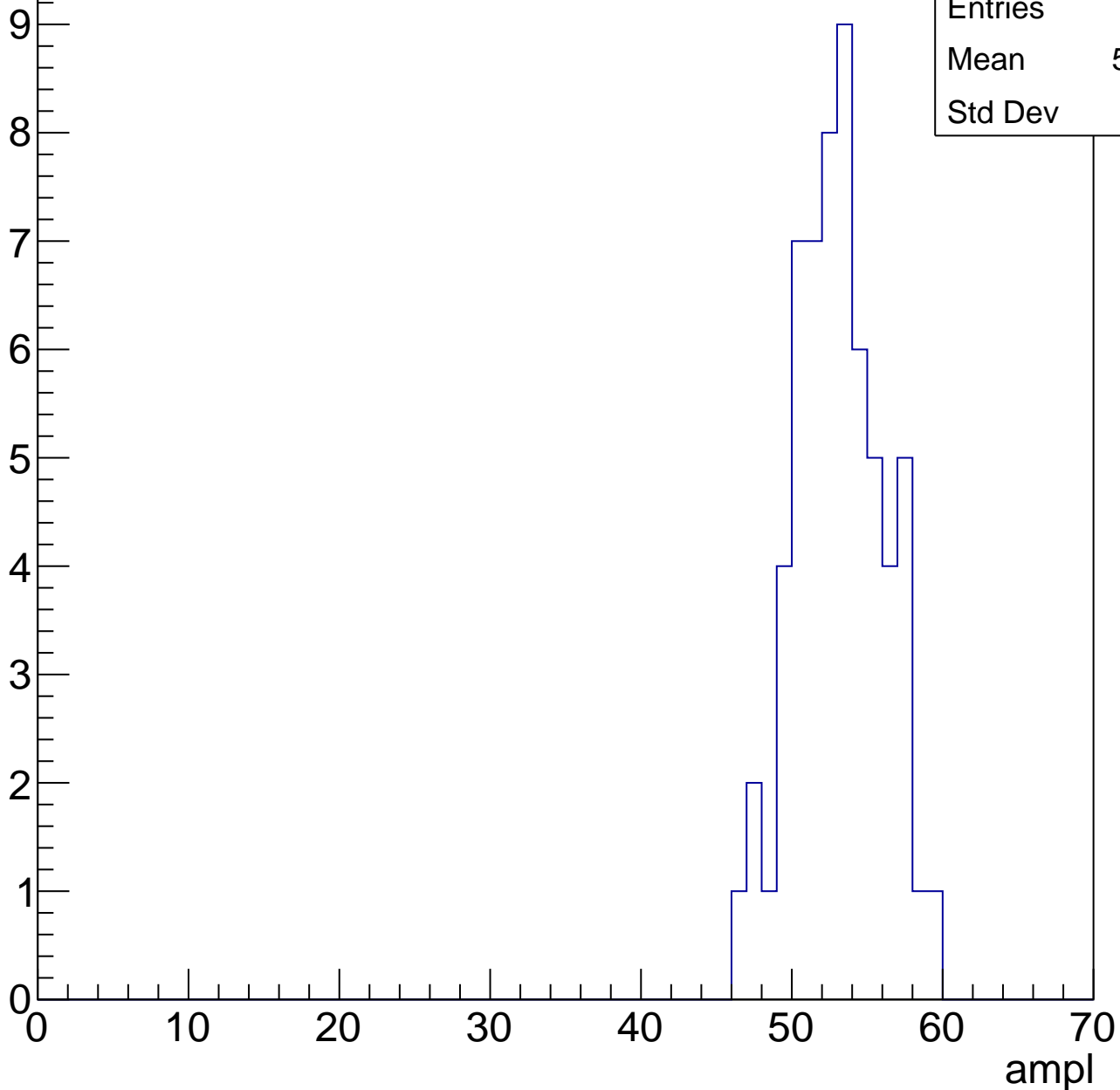


# B1L000S, U24-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	52.61
Std Dev	2.87

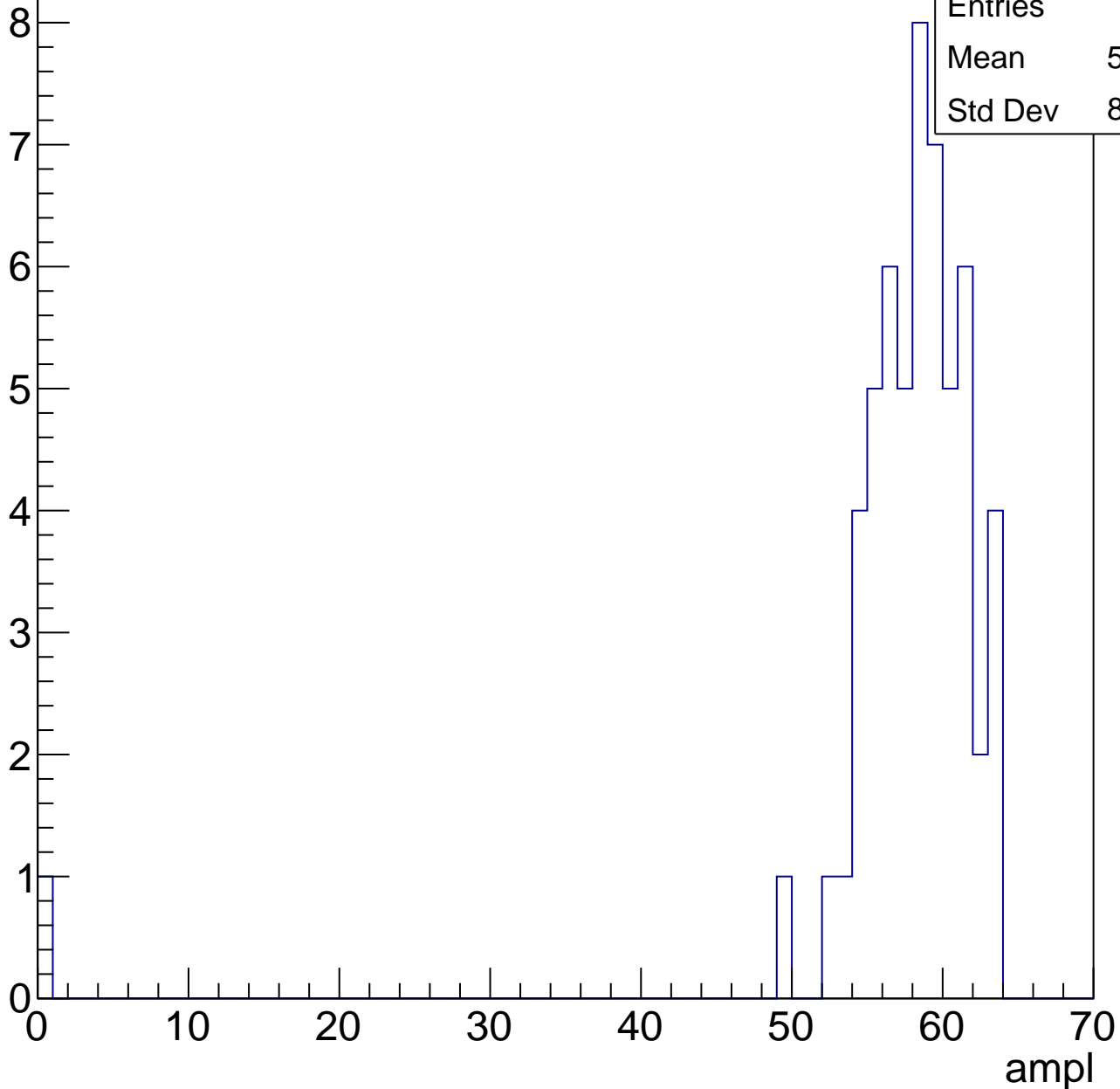


# B1L000S, U24-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	56.88
Std Dev	8.222

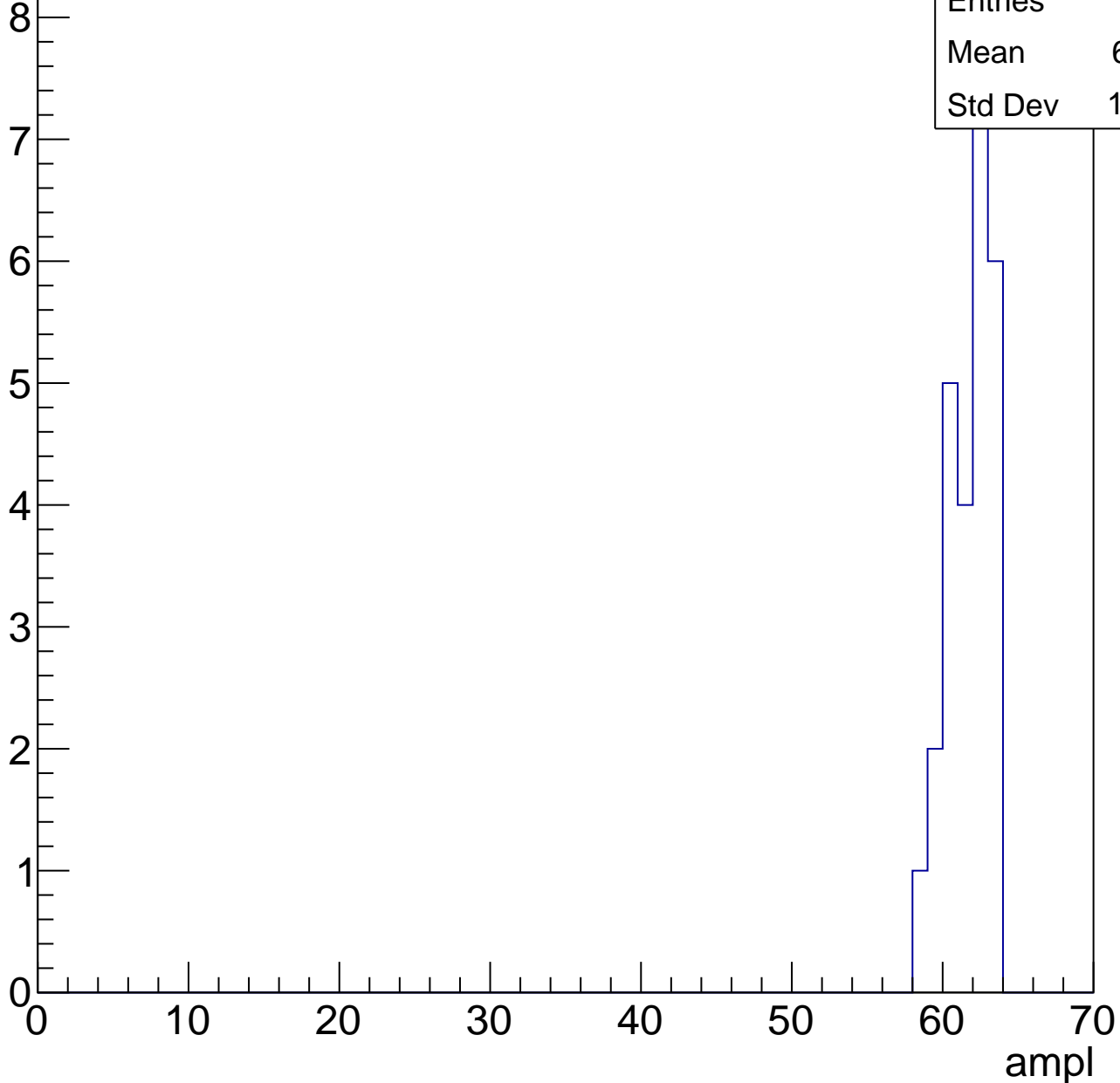


# B1L000S, U24-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	61.31
Std Dev	1.408





# B1L000S, U24-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch103, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	29.01
Std Dev	3.585

**Gaus mean : 29.2866**

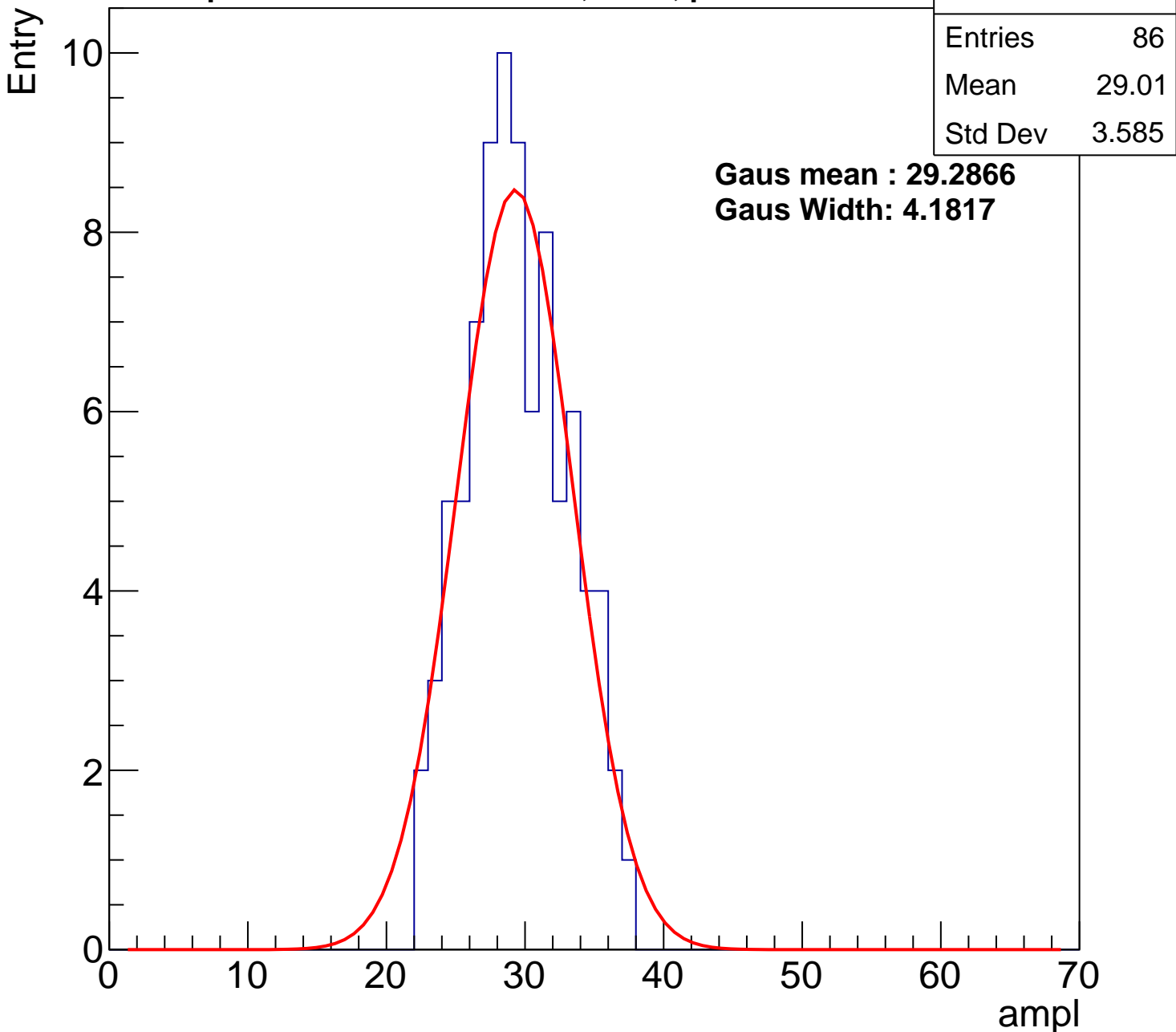
**Gaus Width: 4.1817**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch103, adc1

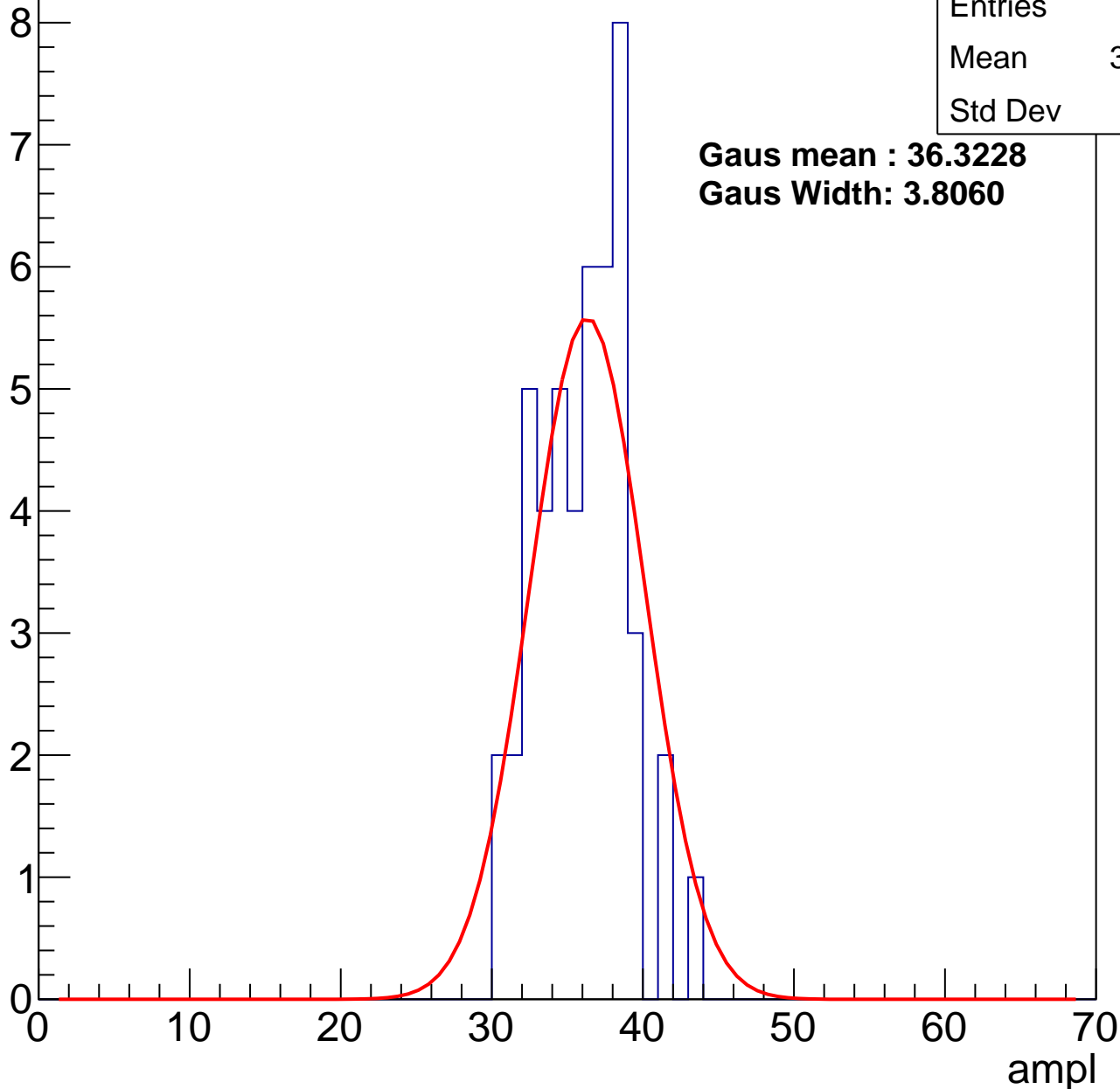
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	35.58
Std Dev	2.95

**Gaus mean : 36.3228**

**Gaus Width: 3.8060**



# B1L000S, U24-ch103, adc2

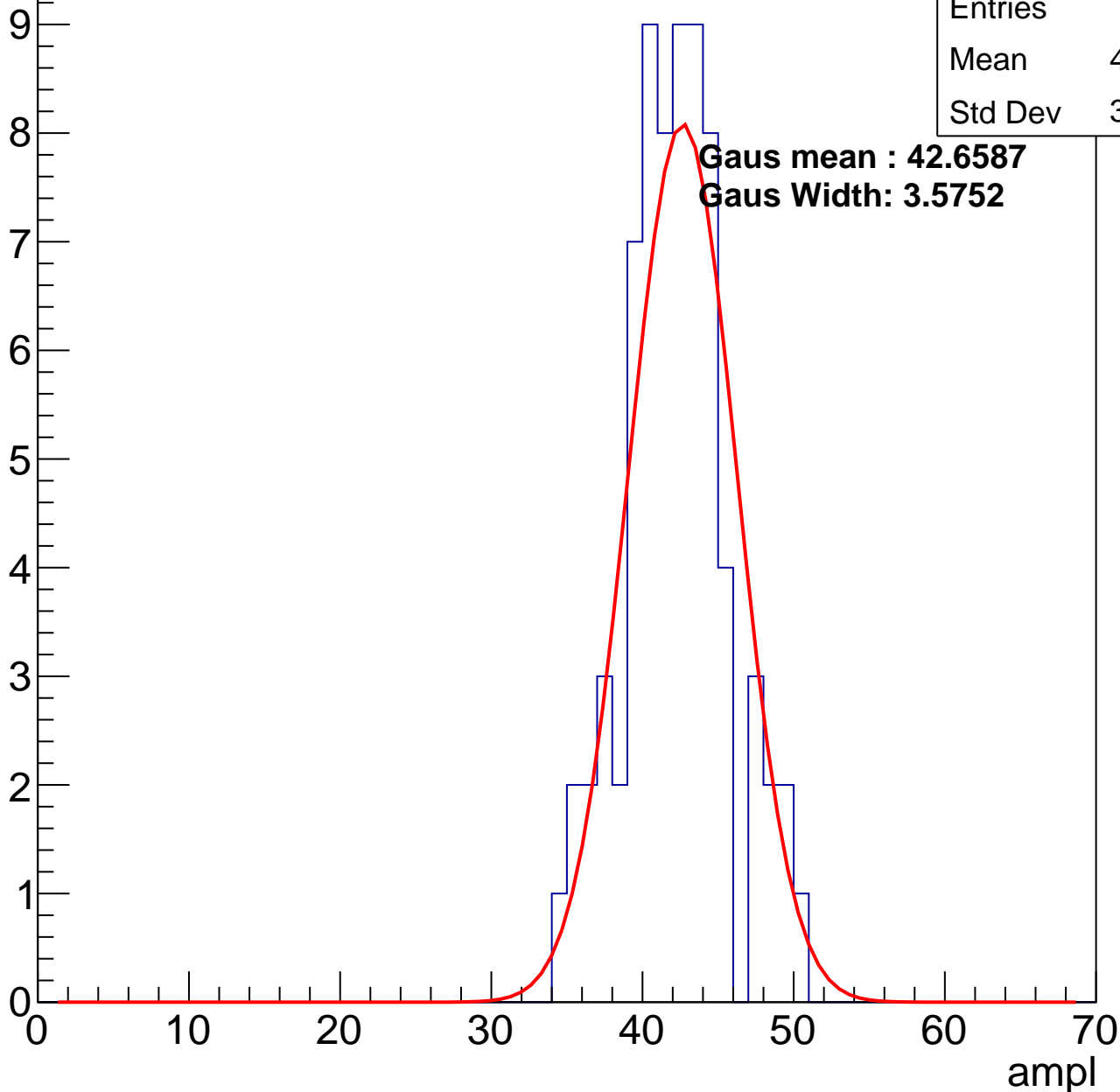
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.75
Std Dev	3.394

**Gaus mean : 42.6587**

**Gaus Width: 3.5752**

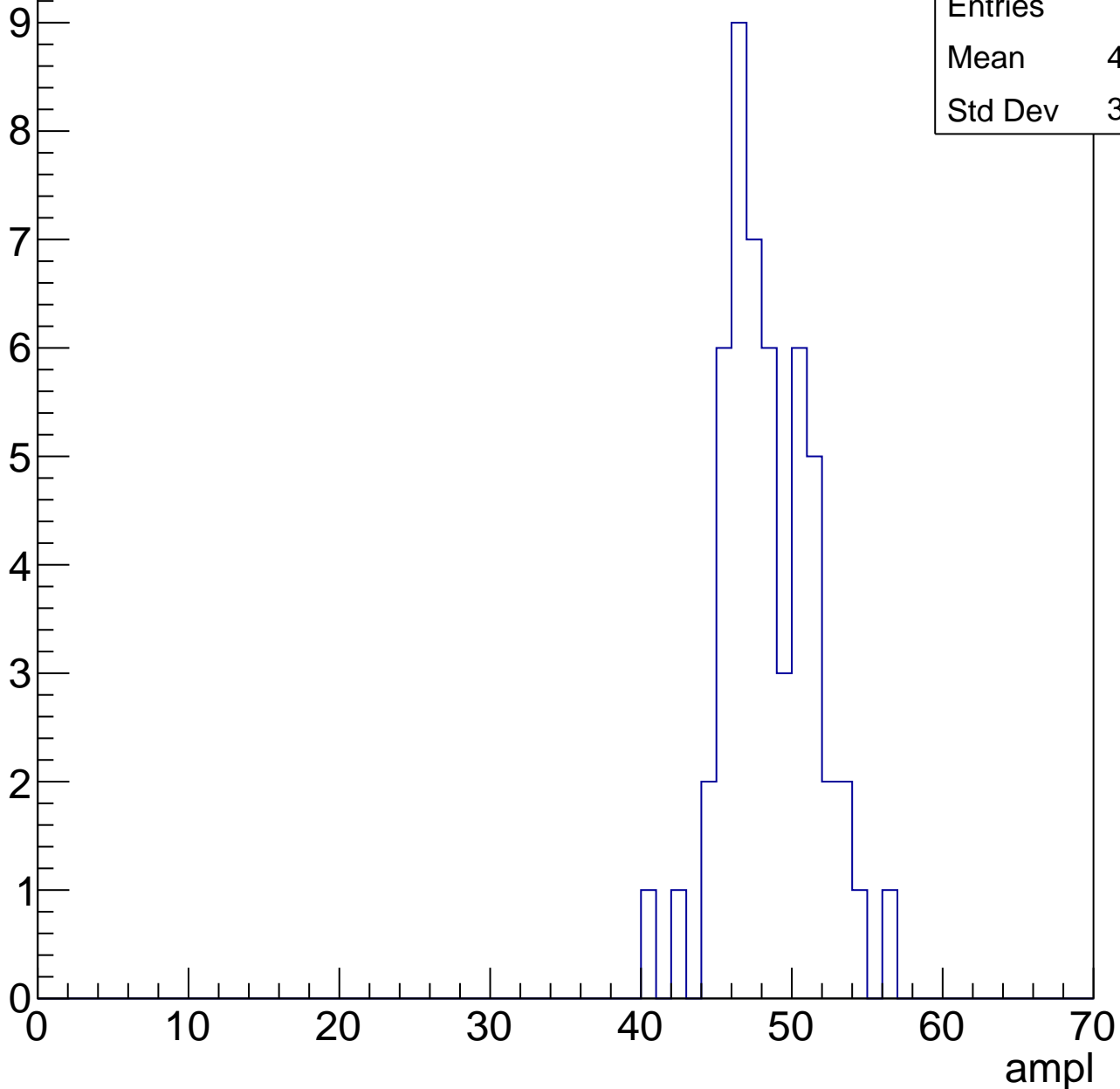


# B1L000S, U24-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	47.94
Std Dev	3.047

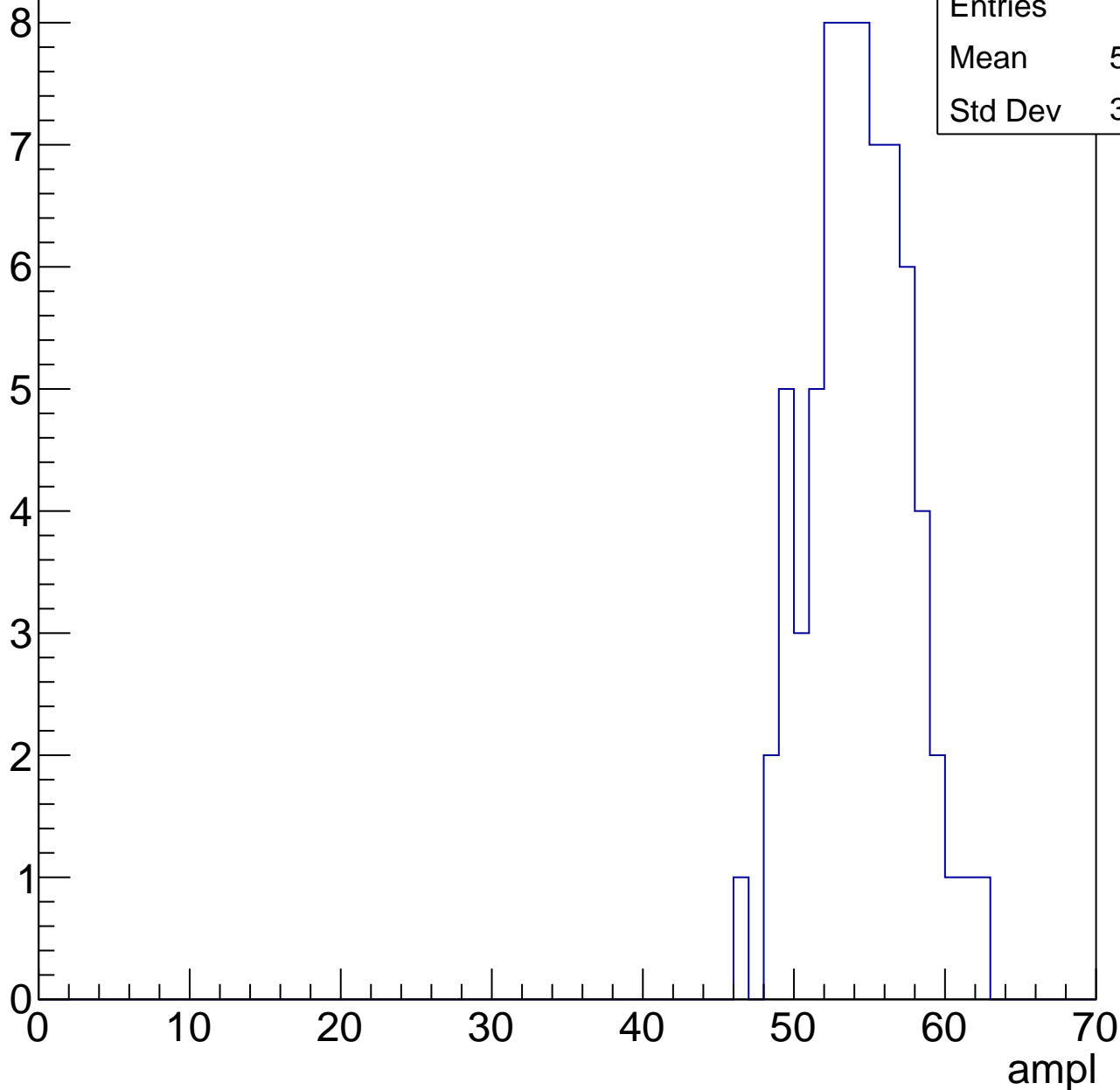


# B1L000S, U24-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	53.86
Std Dev	3.285

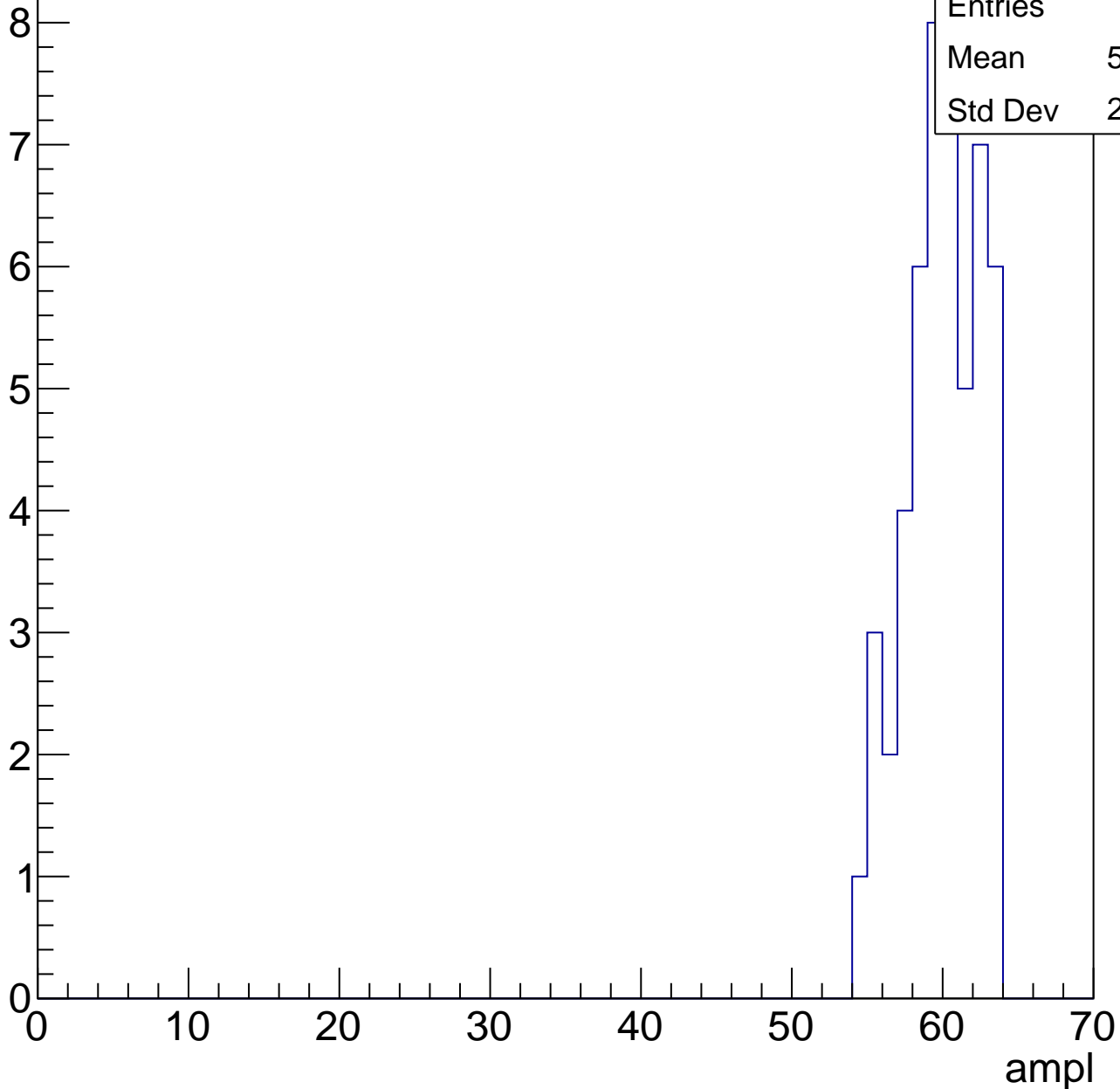


# B1L000S, U24-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

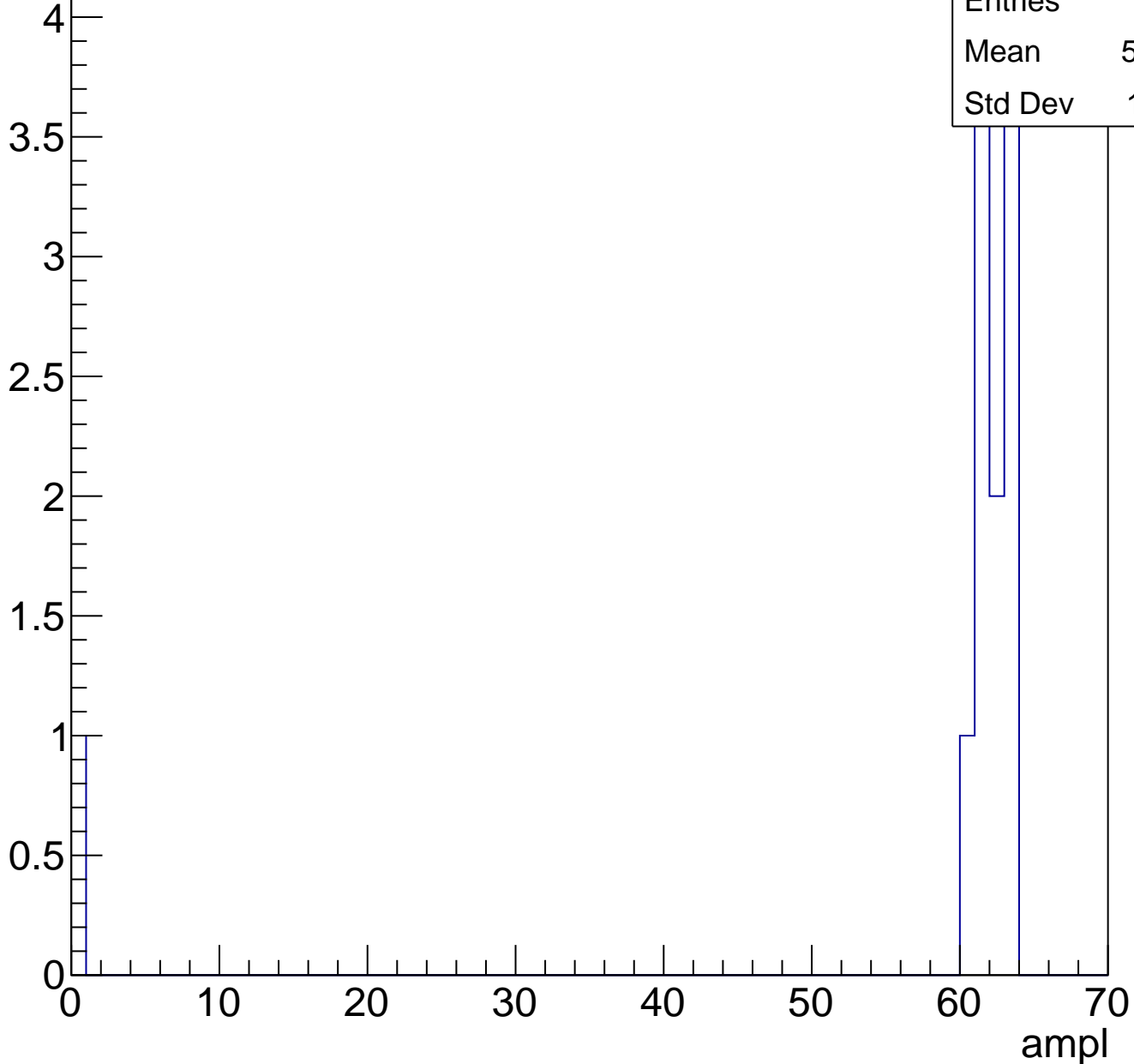
Entries	50
Mean	59.52
Std Dev	2.394



# B1L000S, U24-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch104, adc0

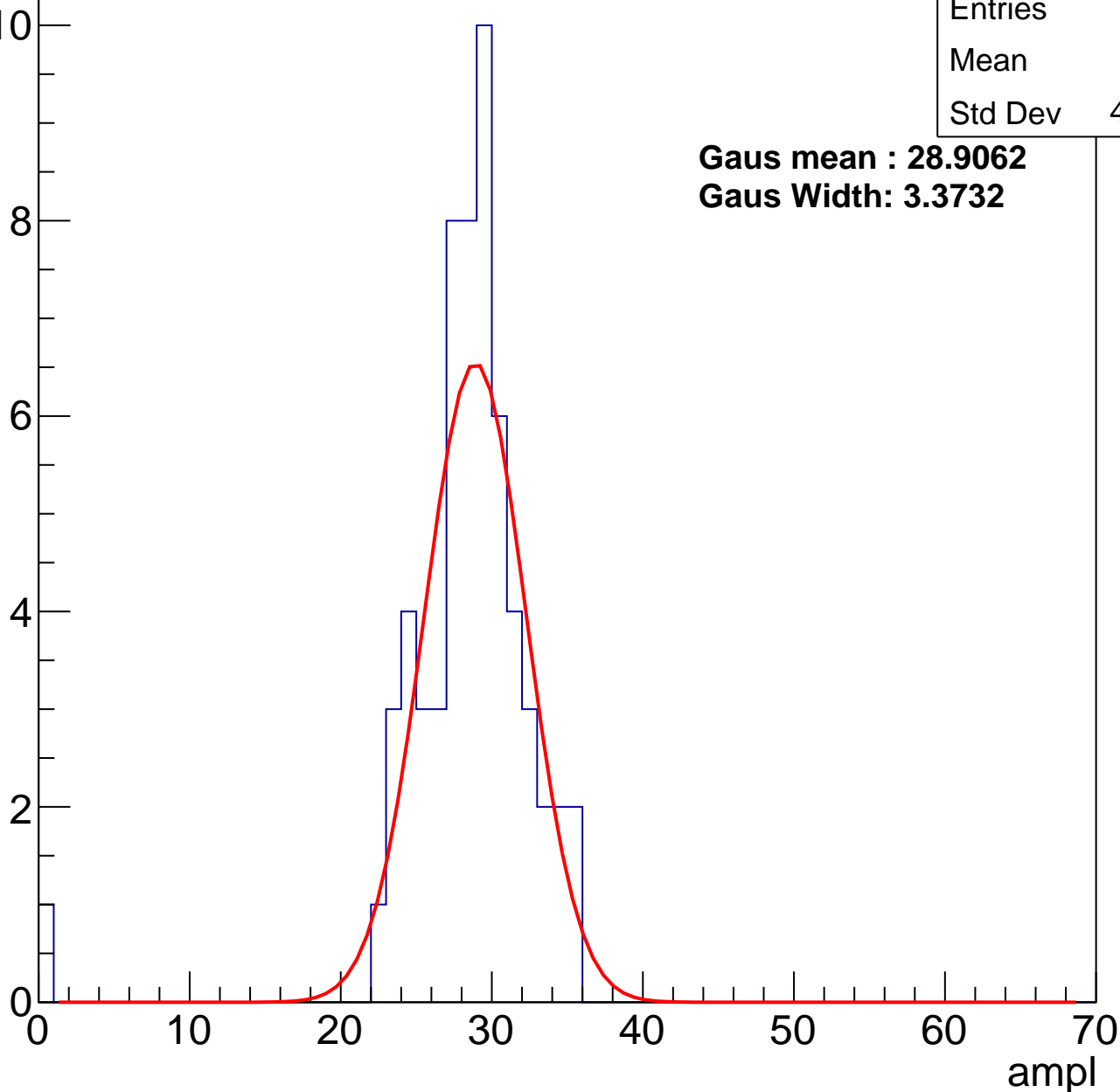
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.9
Std Dev	4.732

**Gaus mean : 28.9062**

**Gaus Width: 3.3732**



# B1L000S, U24-ch104, adc1

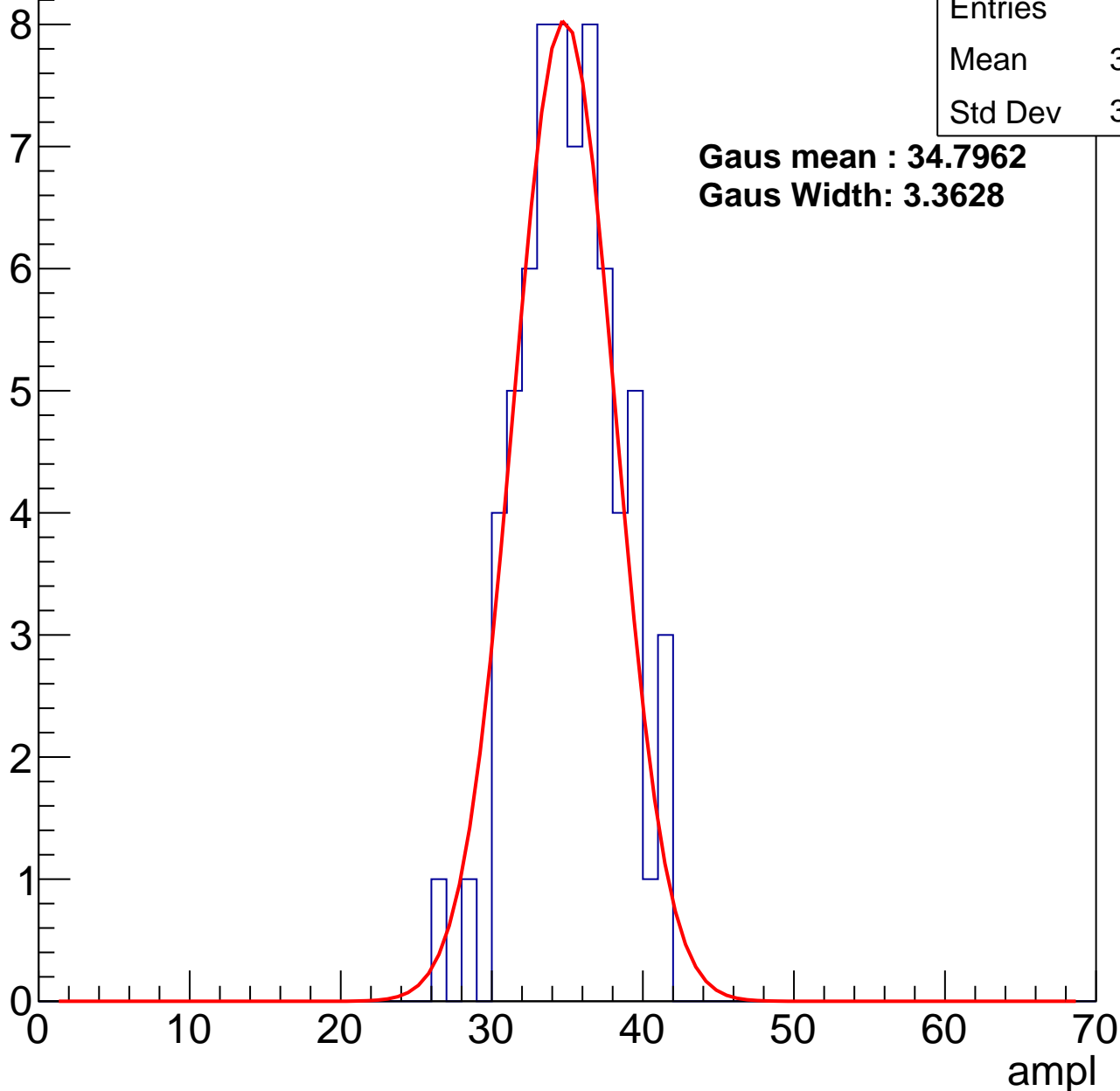
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.66
Std Dev	3.179

**Gaus mean : 34.7962**

**Gaus Width: 3.3628**



# B1L000S, U24-ch104, adc2

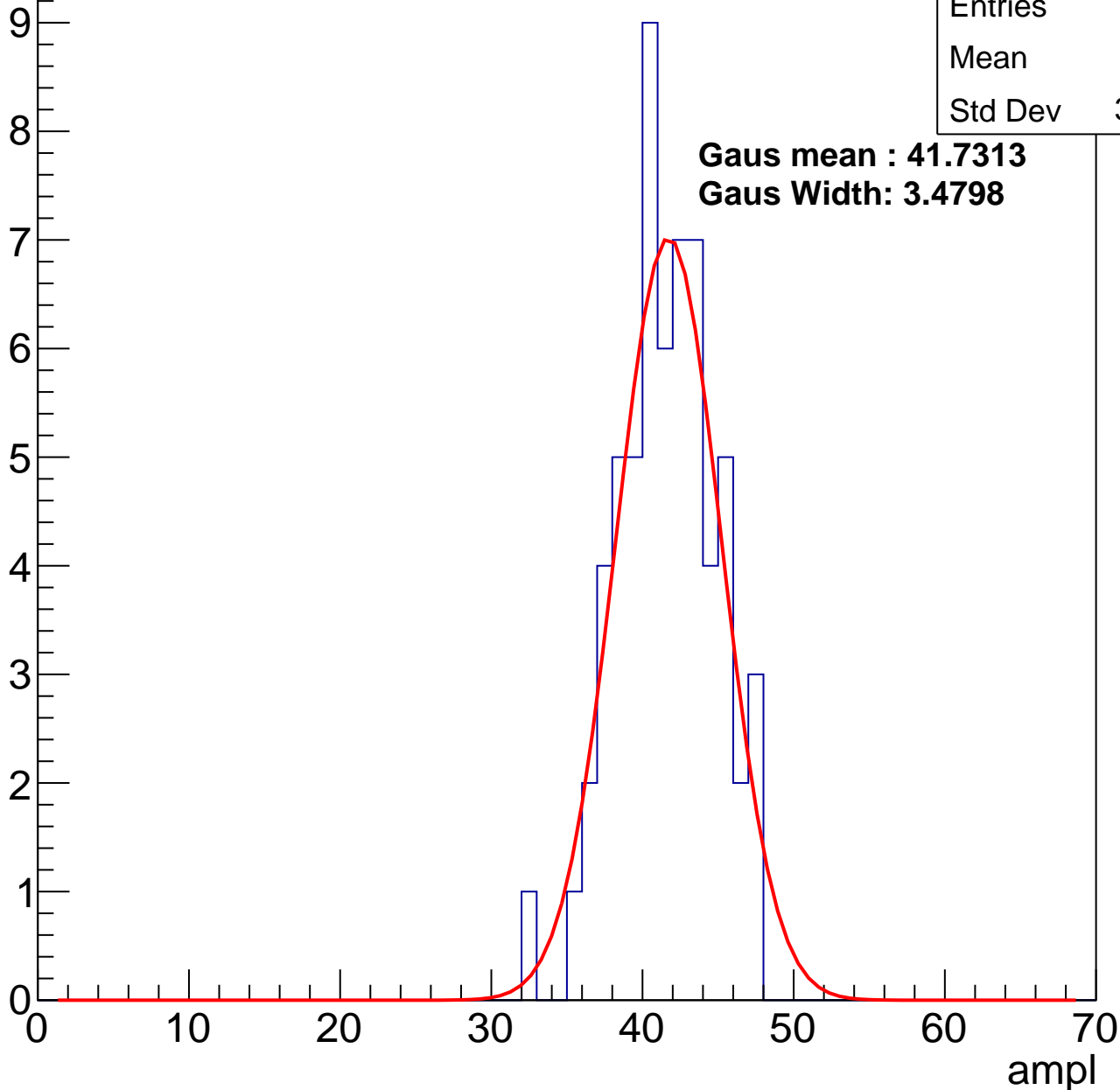
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.1
Std Dev	3.171

**Gaus mean : 41.7313**

**Gaus Width: 3.4798**

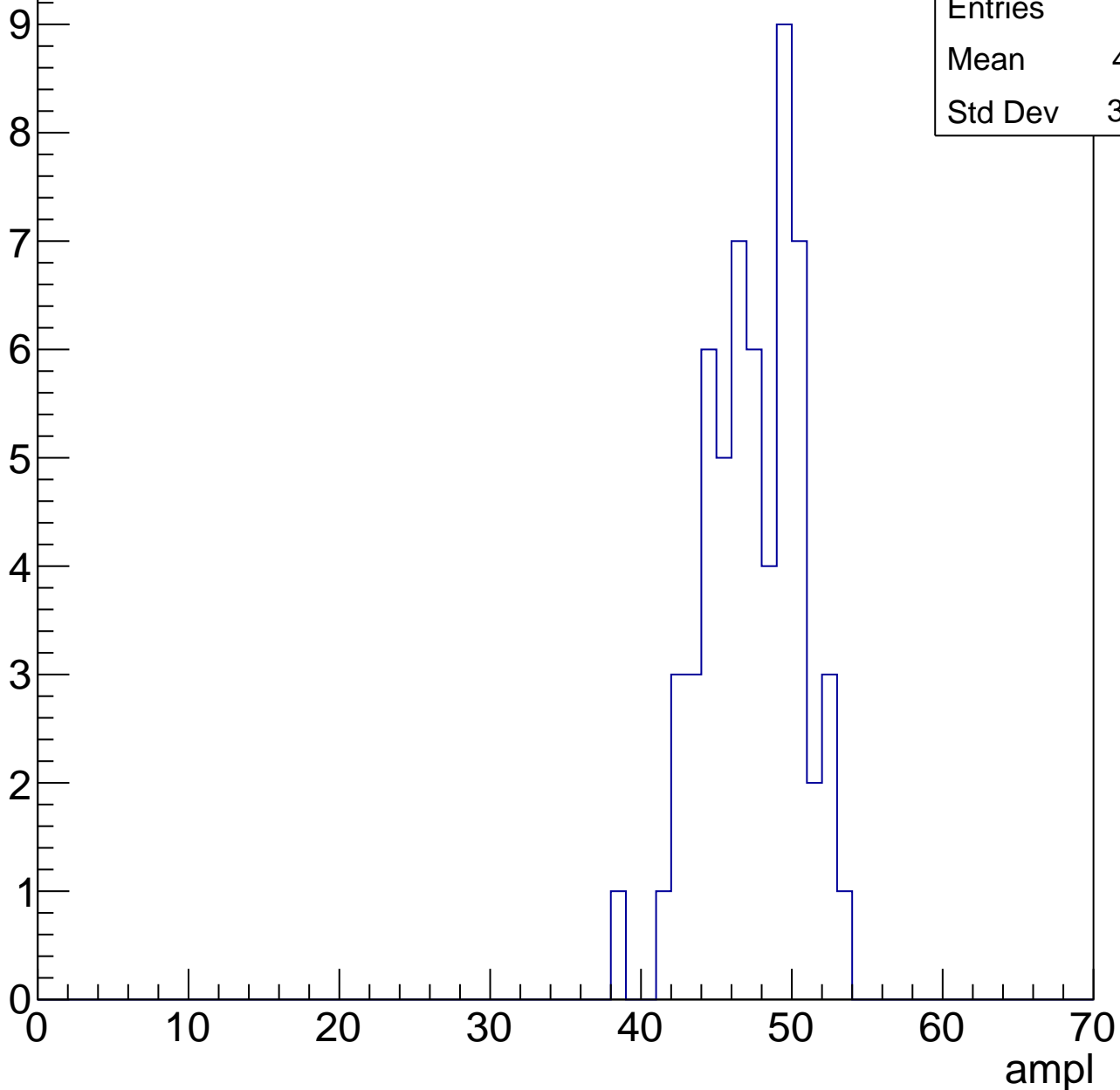


# B1L000S, U24-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.91
Std Dev	3.125

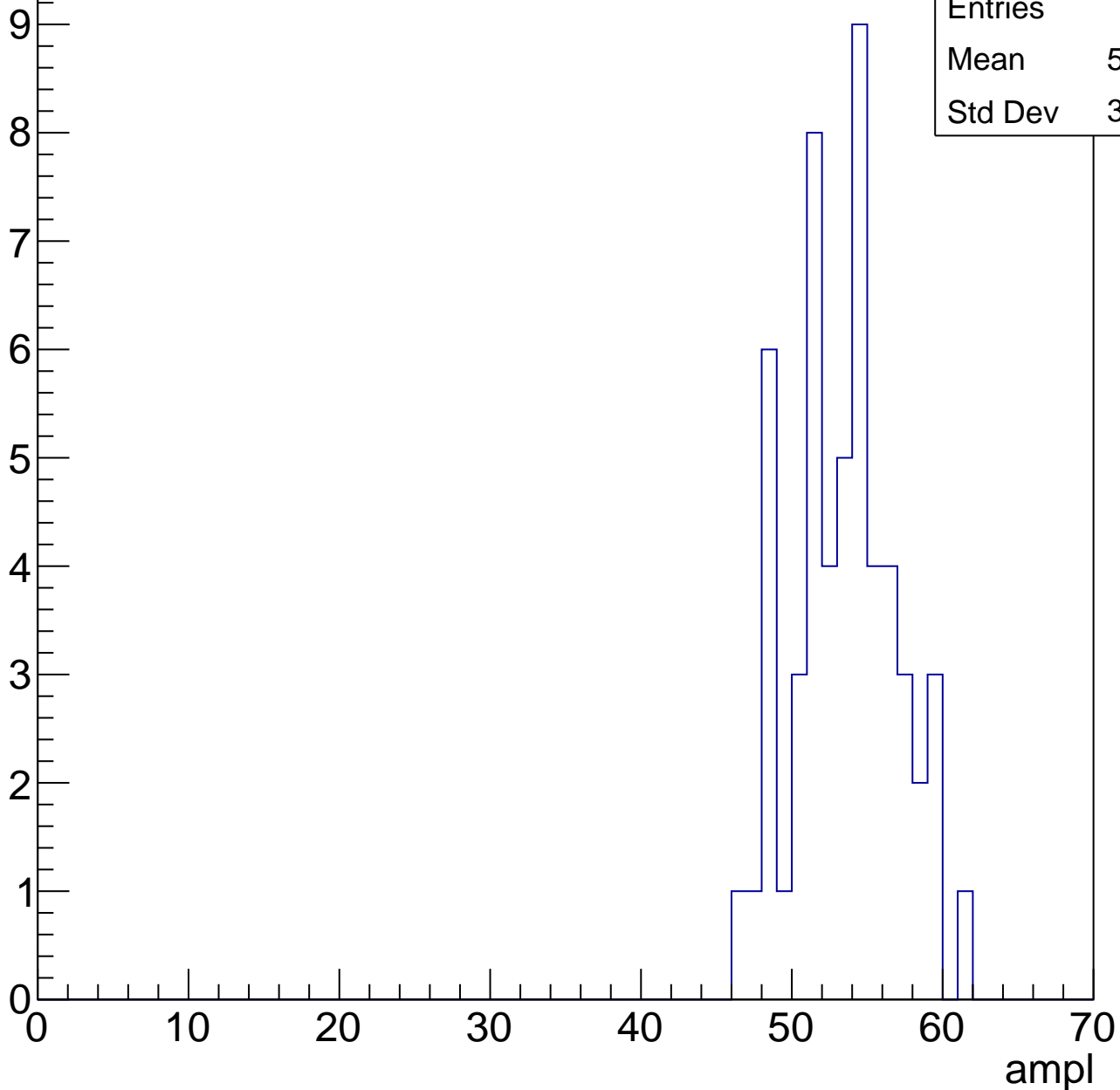


# B1L000S, U24-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	53.02
Std Dev	3.424

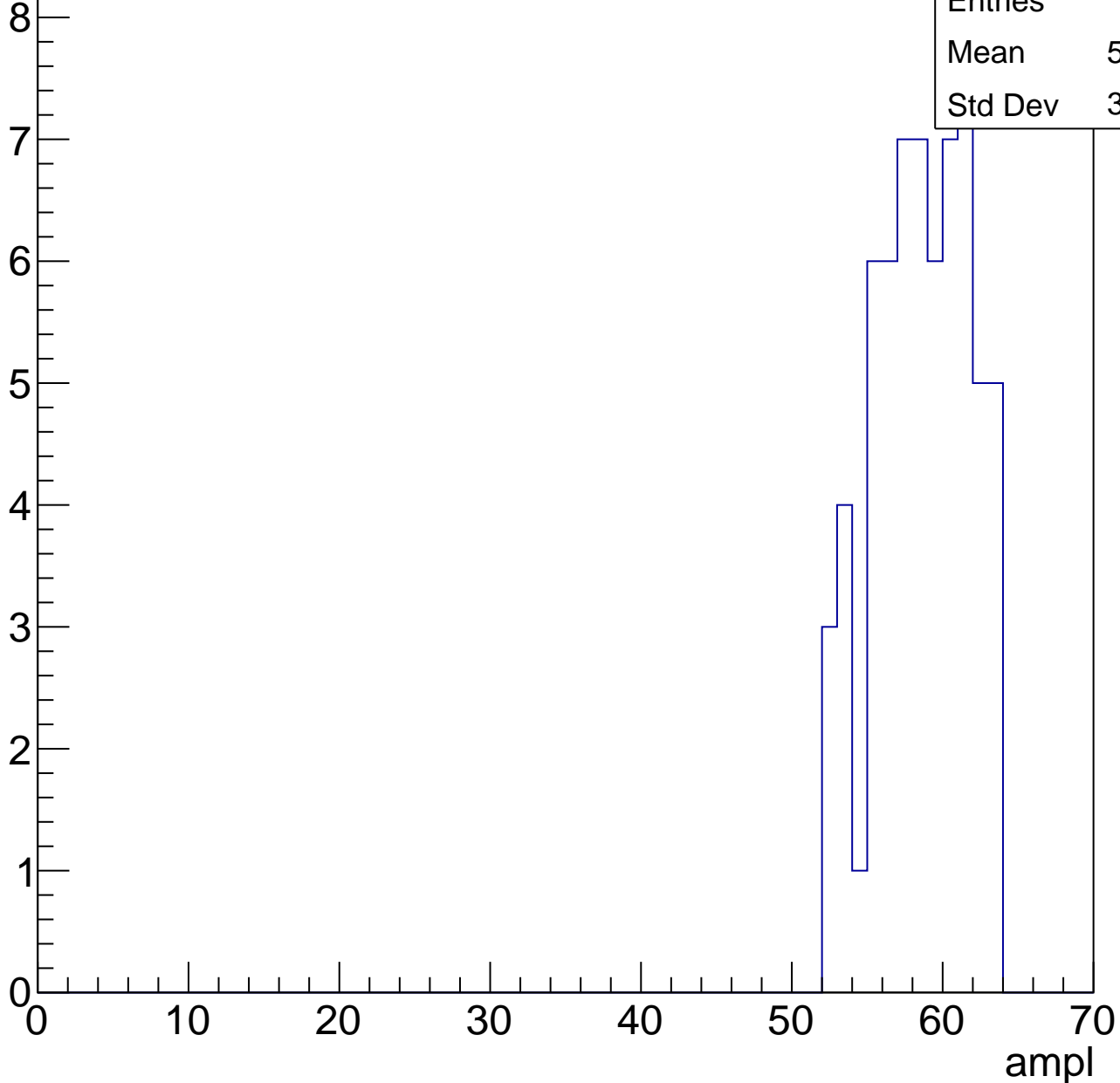


# B1L000S, U24-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	58.15
Std Dev	3.085

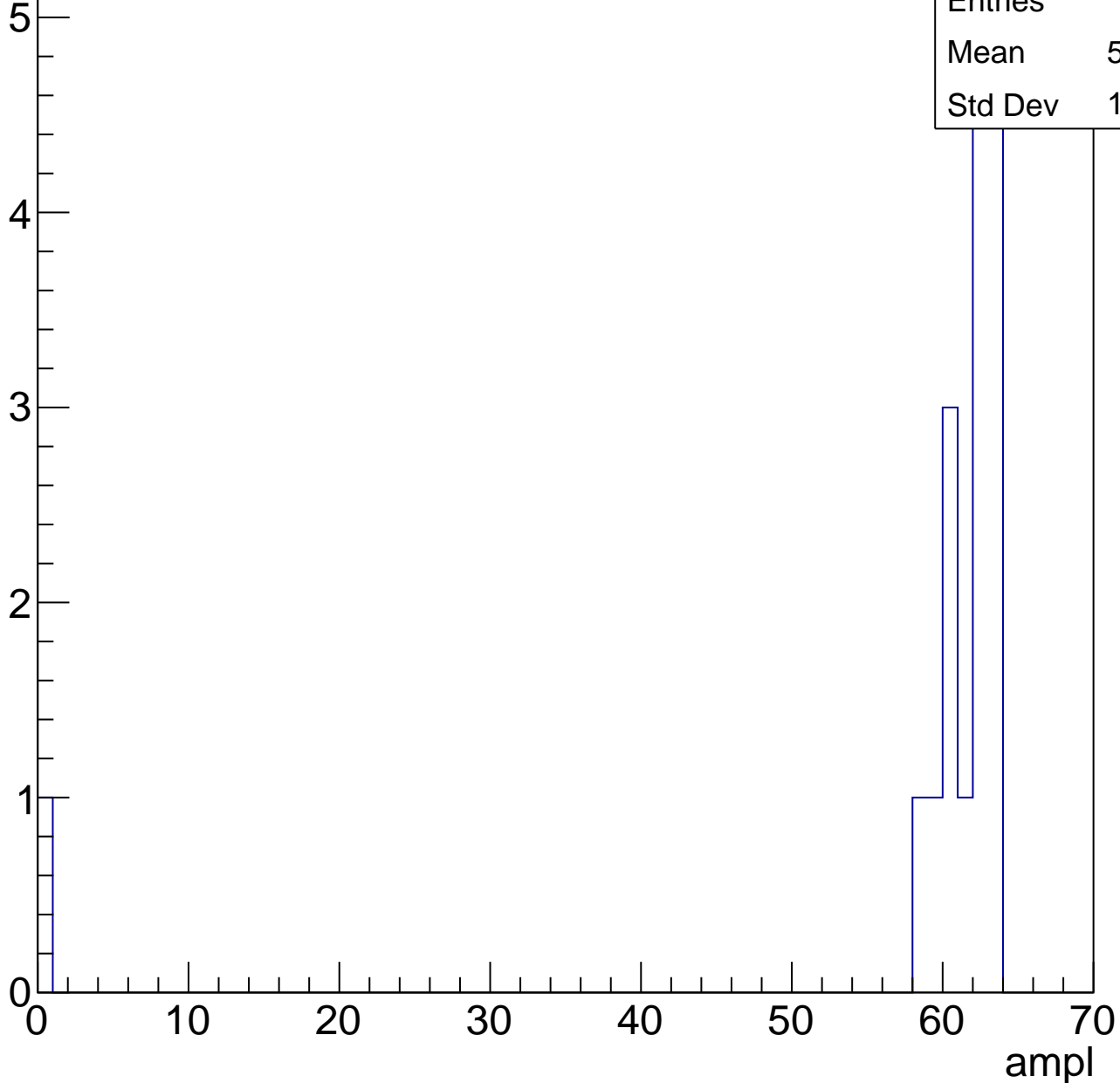


# B1L000S, U24-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.82
Std Dev	14.53





# B1L000S, U24-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch105, adc0

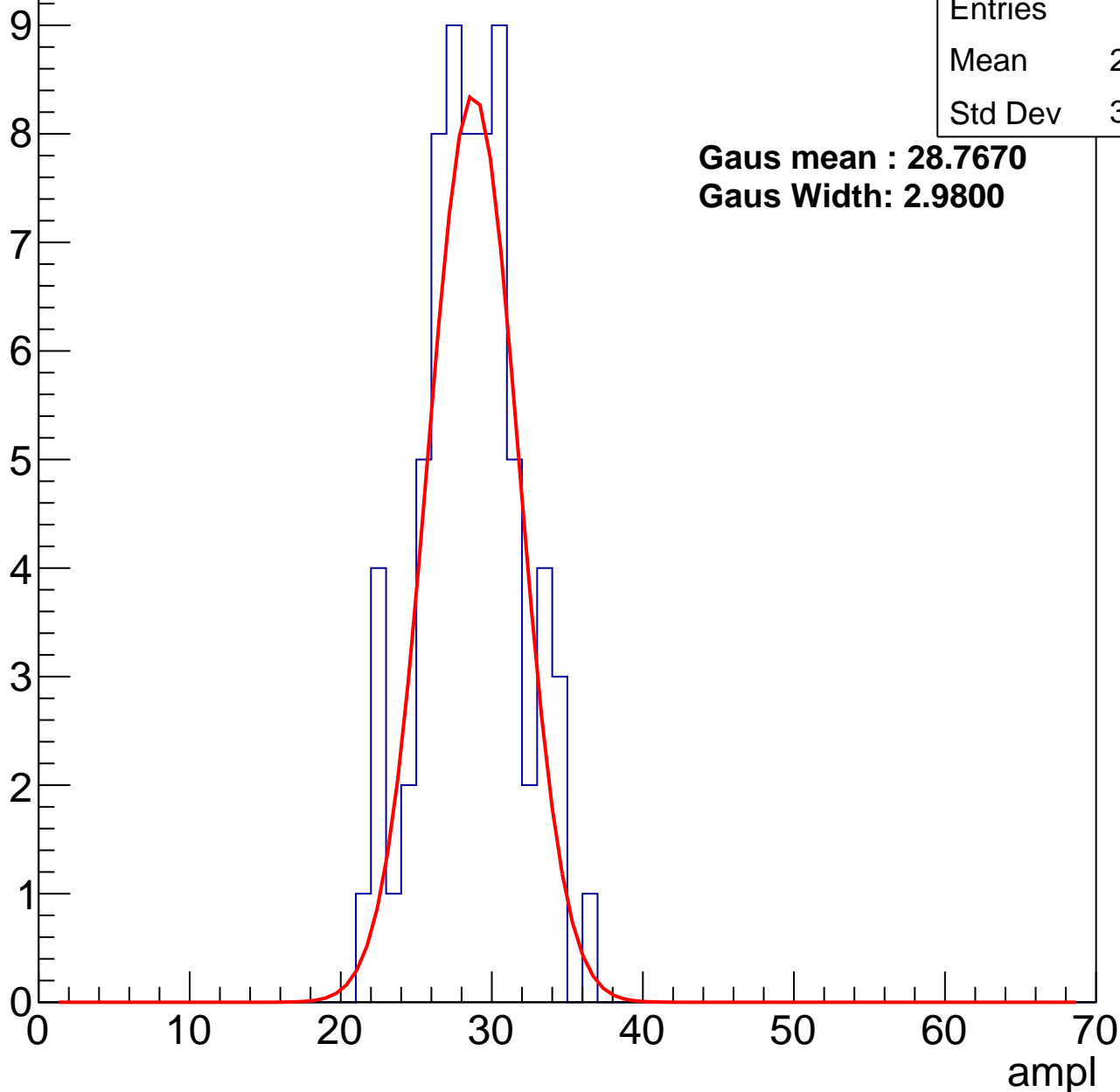
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.16
Std Dev	3.223

**Gaus mean : 28.7670**

**Gaus Width: 2.9800**



# B1L000S, U24-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	34.81
Std Dev	2.877

**Gaus mean : 35.1703**

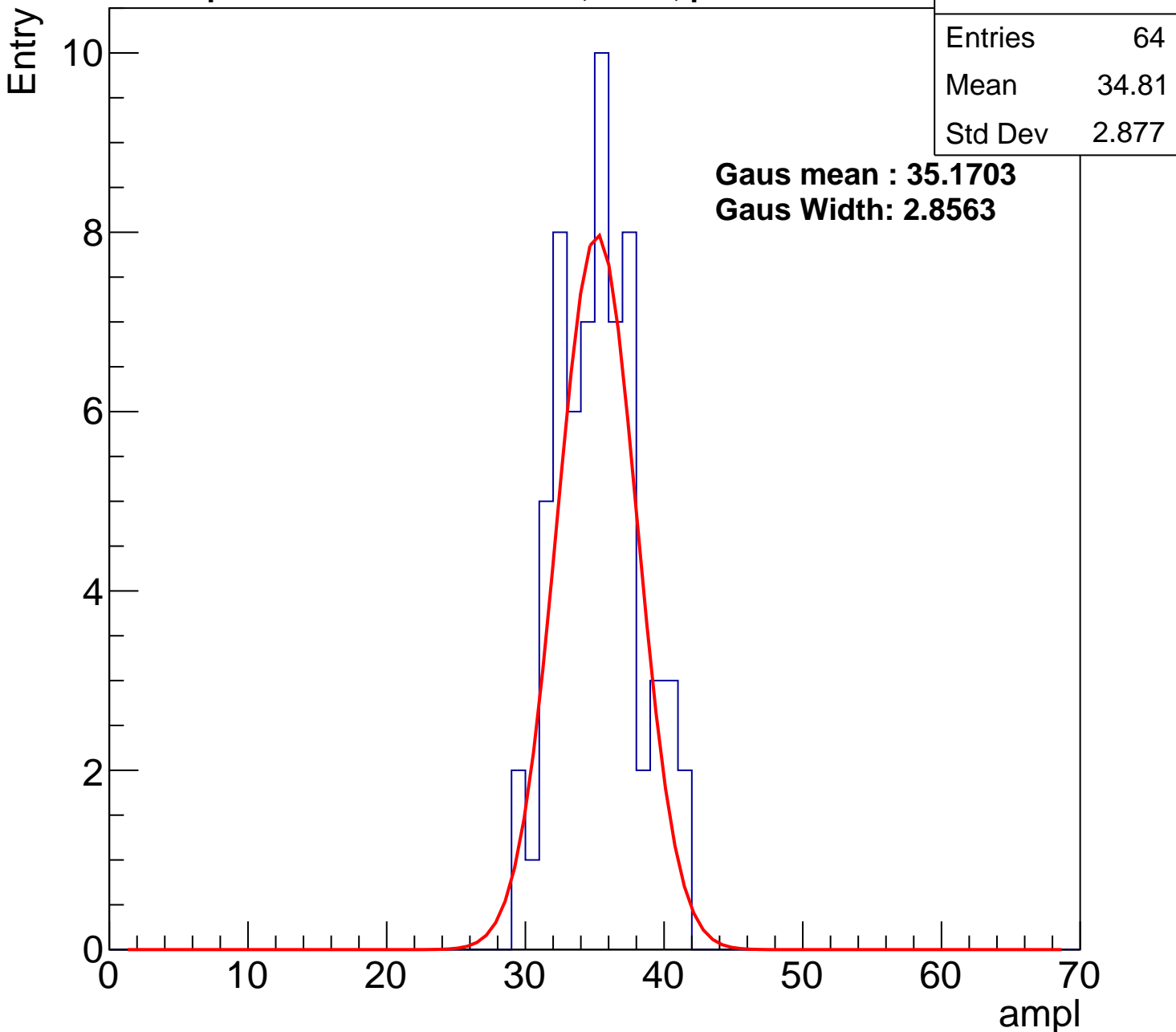
**Gaus Width: 2.8563**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch105, adc2

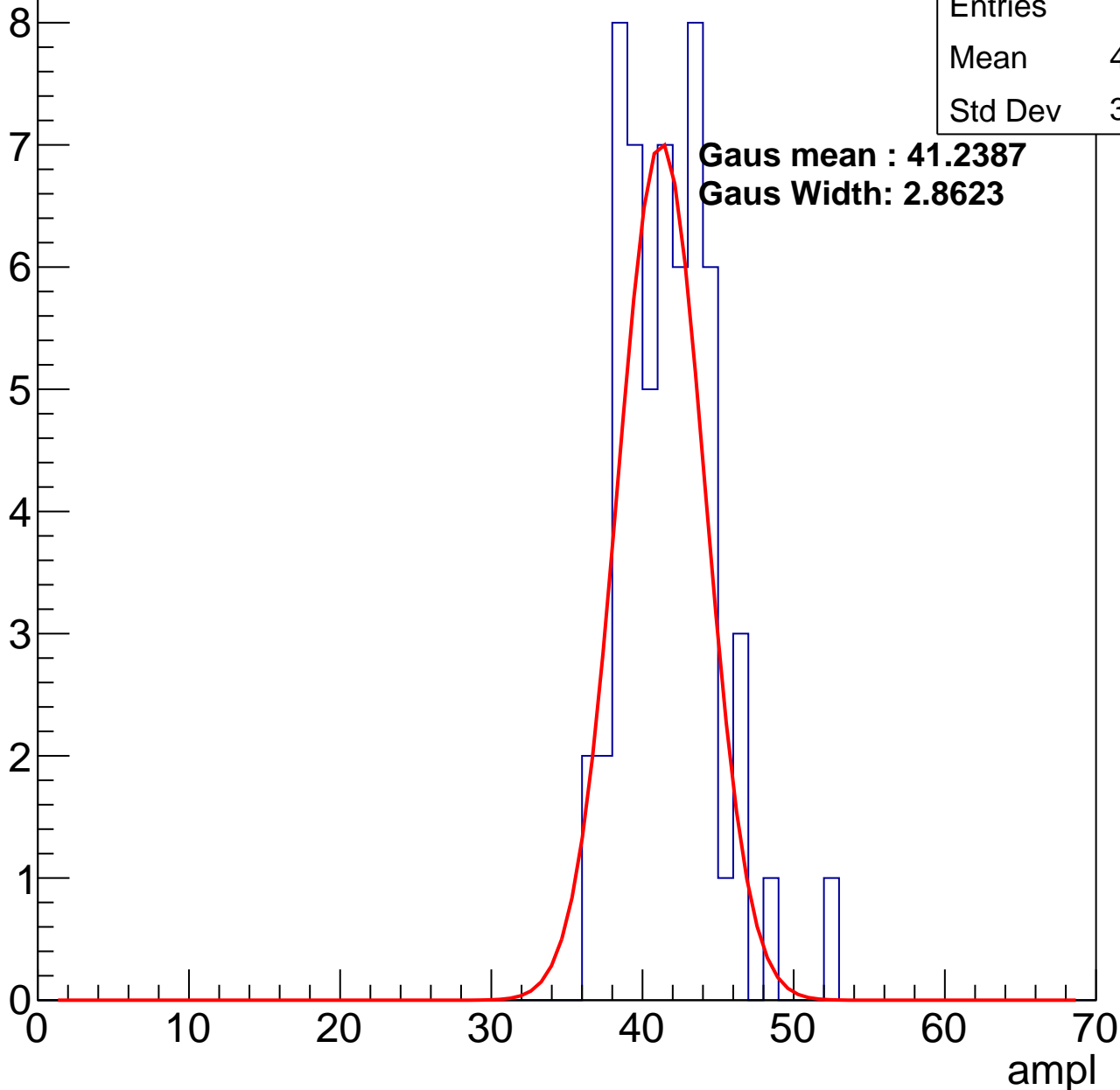
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.28
Std Dev	3.065

**Gaus mean : 41.2387**

**Gaus Width: 2.8623**



# B1L000S, U24-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

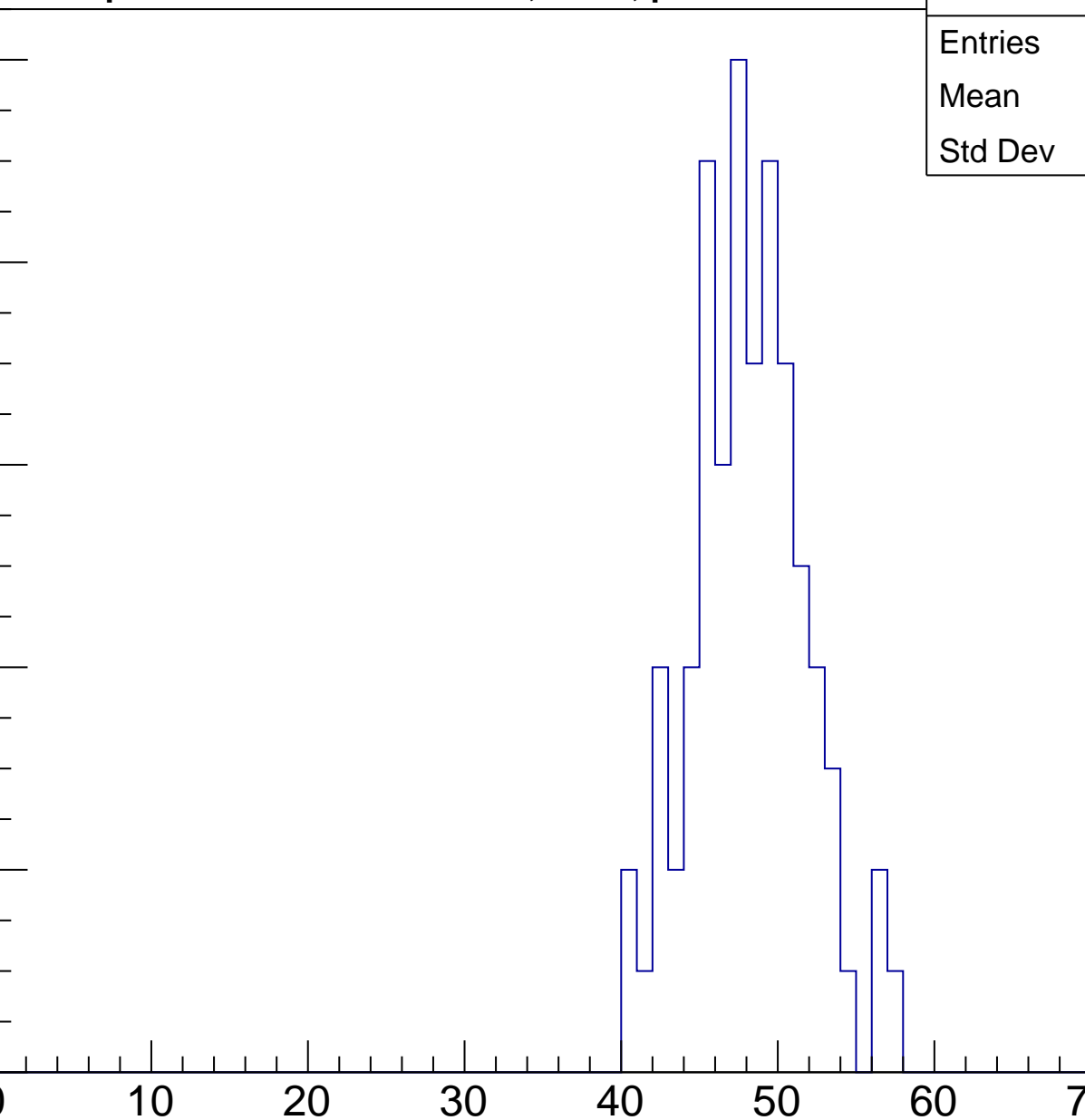
Entries	77
Mean	47.71
Std Dev	3.621

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

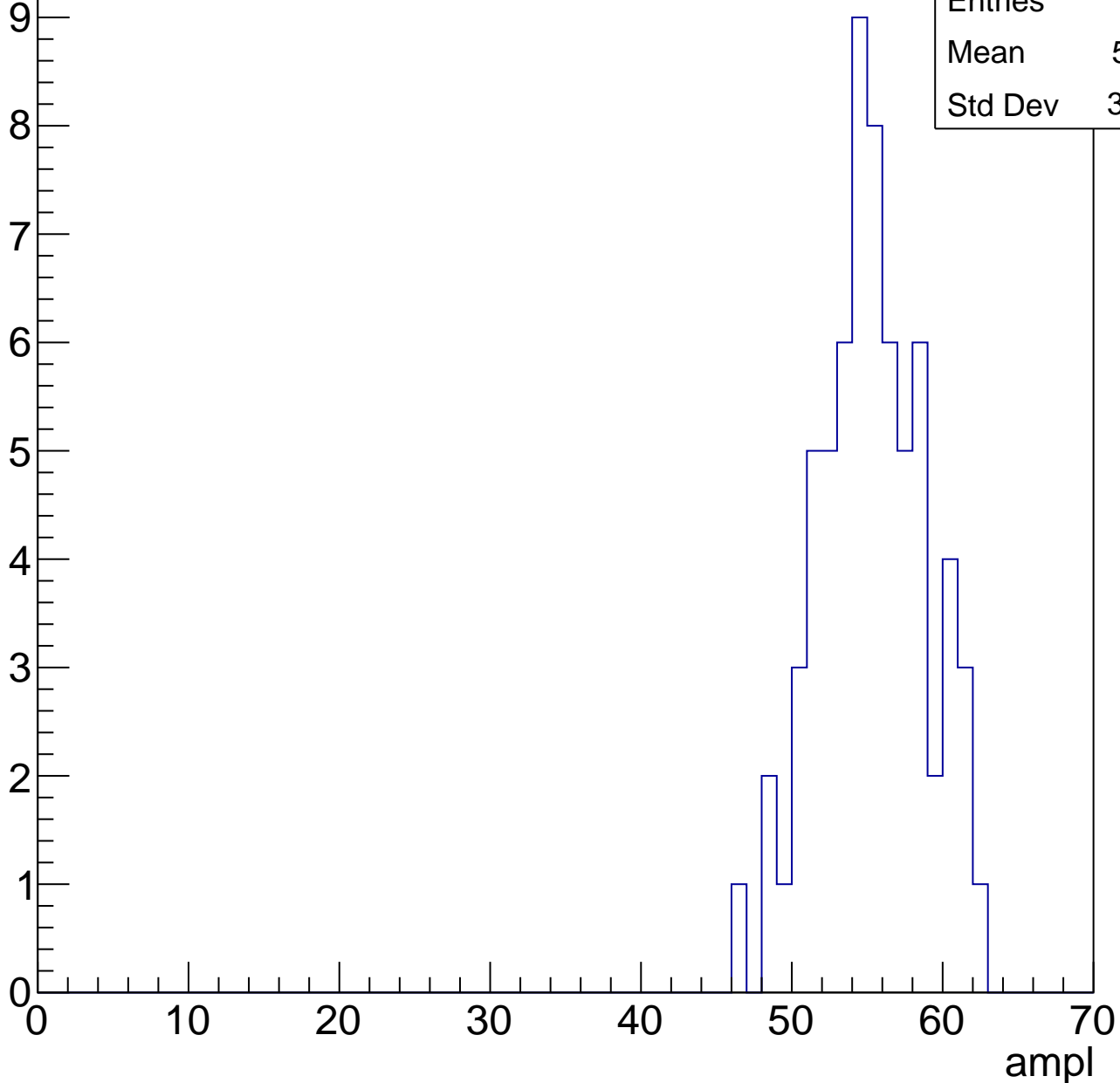


# B1L000S, U24-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	54.81
Std Dev	3.487

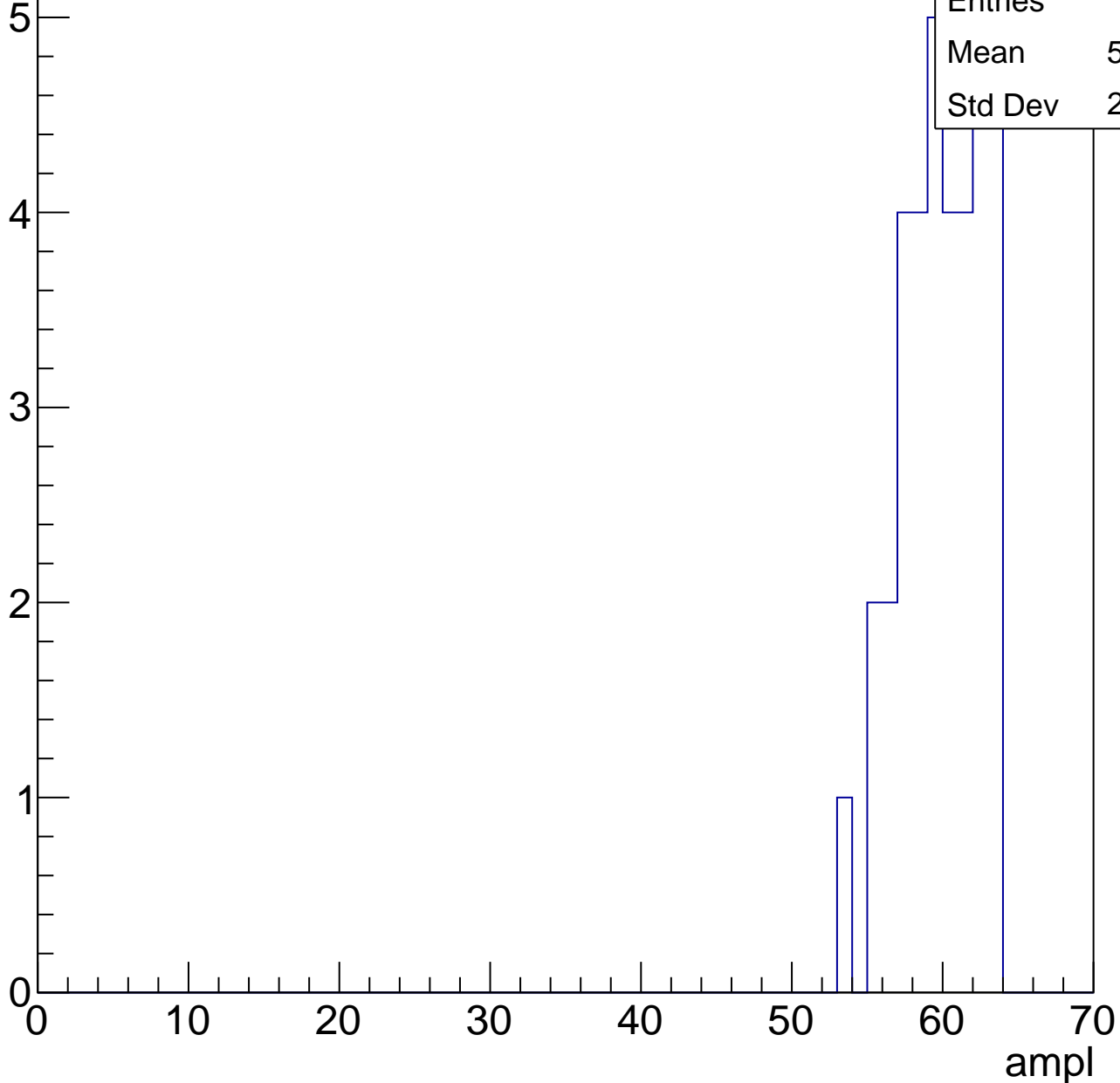


# B1L000S, U24-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	59.42
Std Dev	2.607

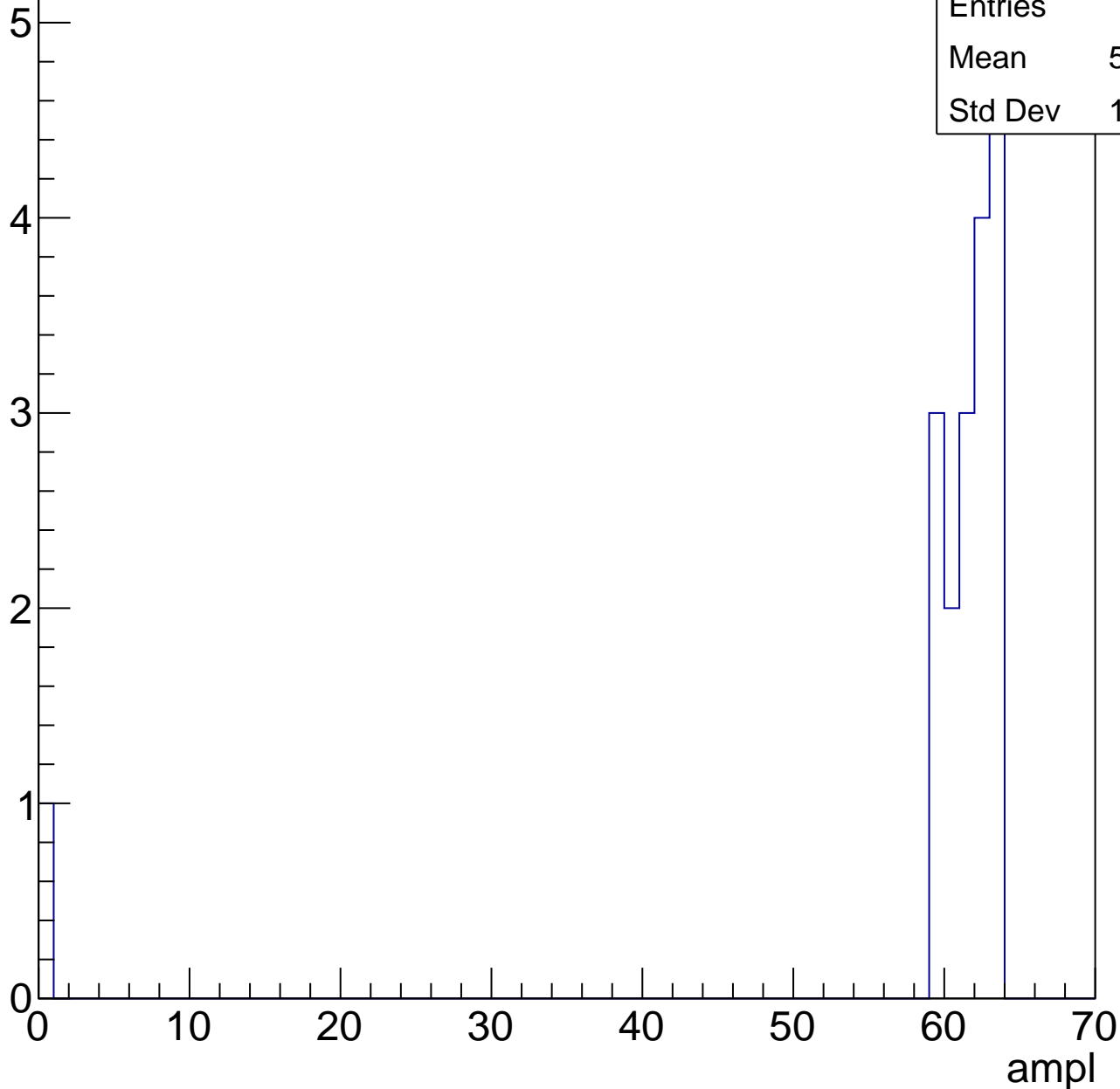


# B1L000S, U24-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.94
Std Dev	14.12





# B1L000S, U24-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch106, adc0

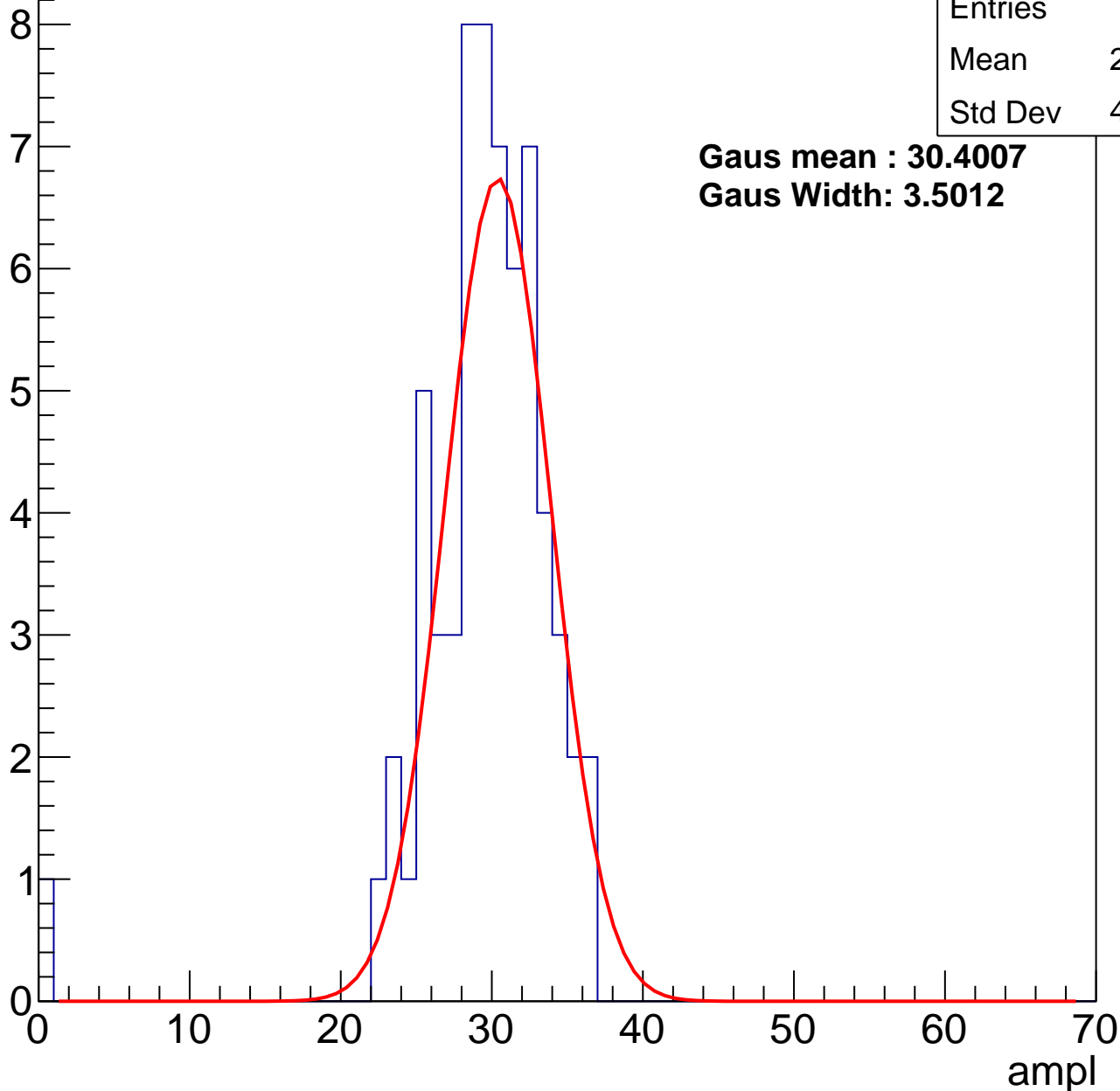
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.02
Std Dev	4.907

**Gaus mean : 30.4007**

**Gaus Width: 3.5012**



# B1L000S, U24-ch106, adc1

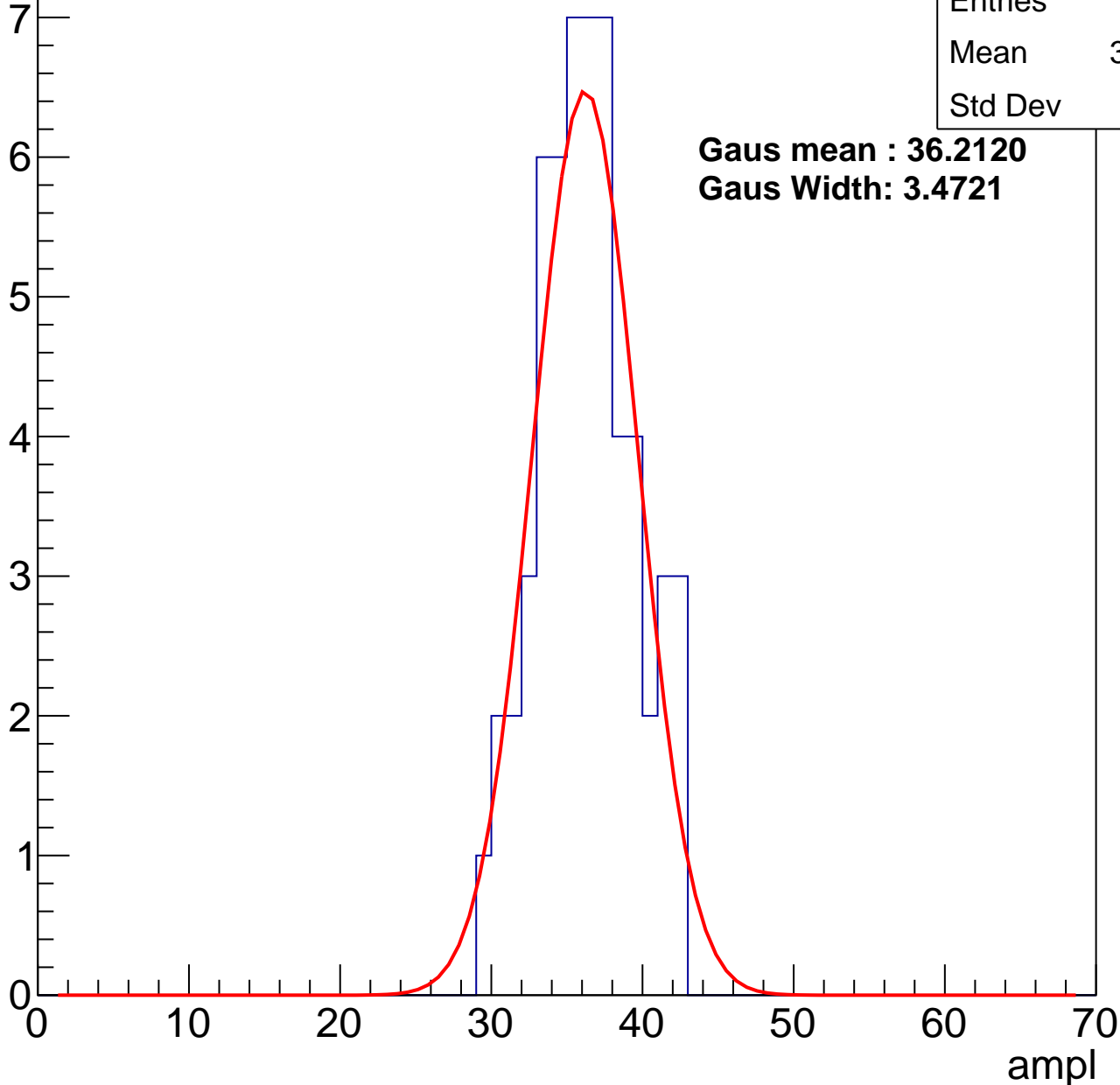
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.82
Std Dev	3.18

**Gaus mean : 36.2120**

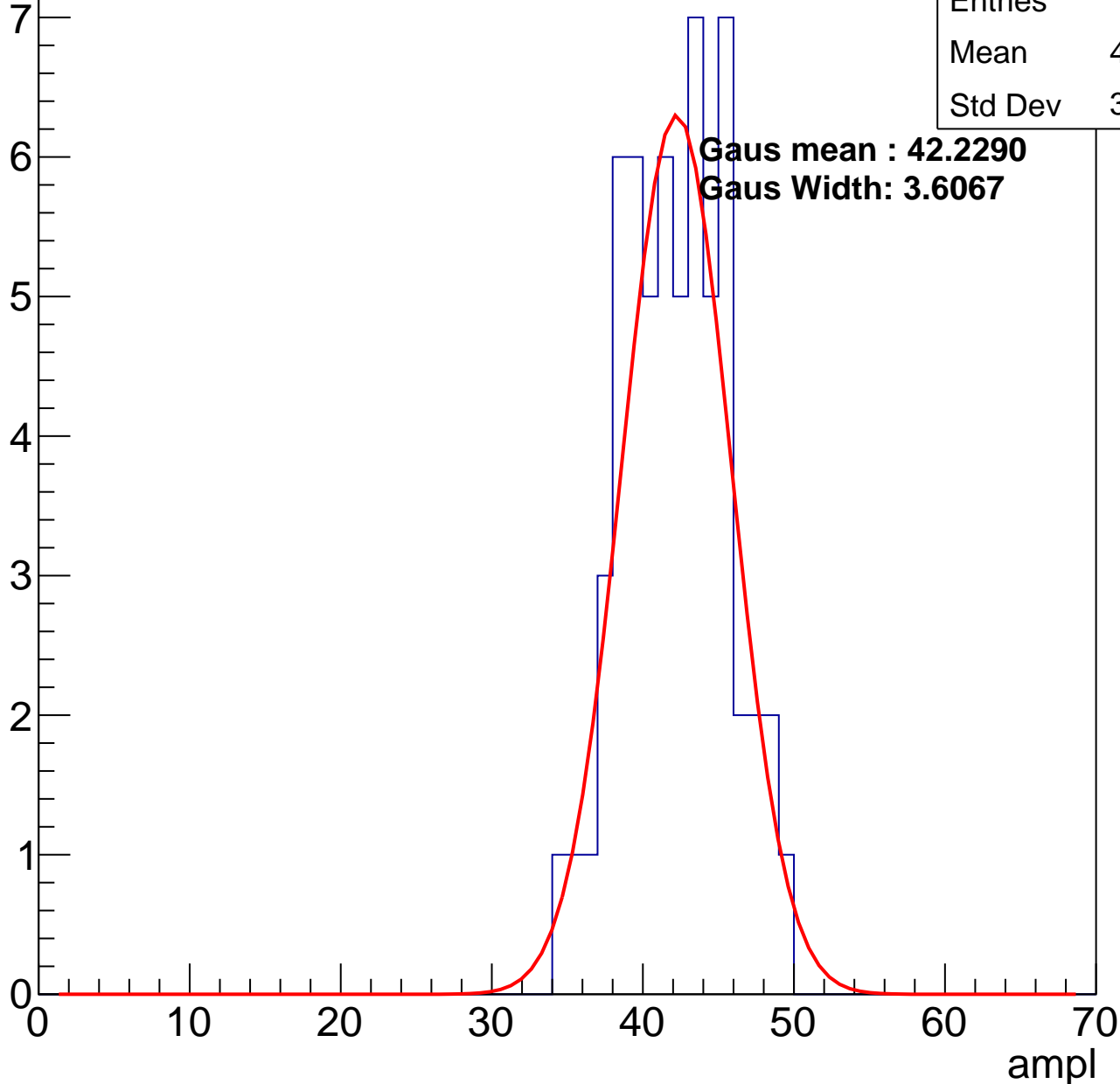
**Gaus Width: 3.4721**



# B1L000S, U24-ch106, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



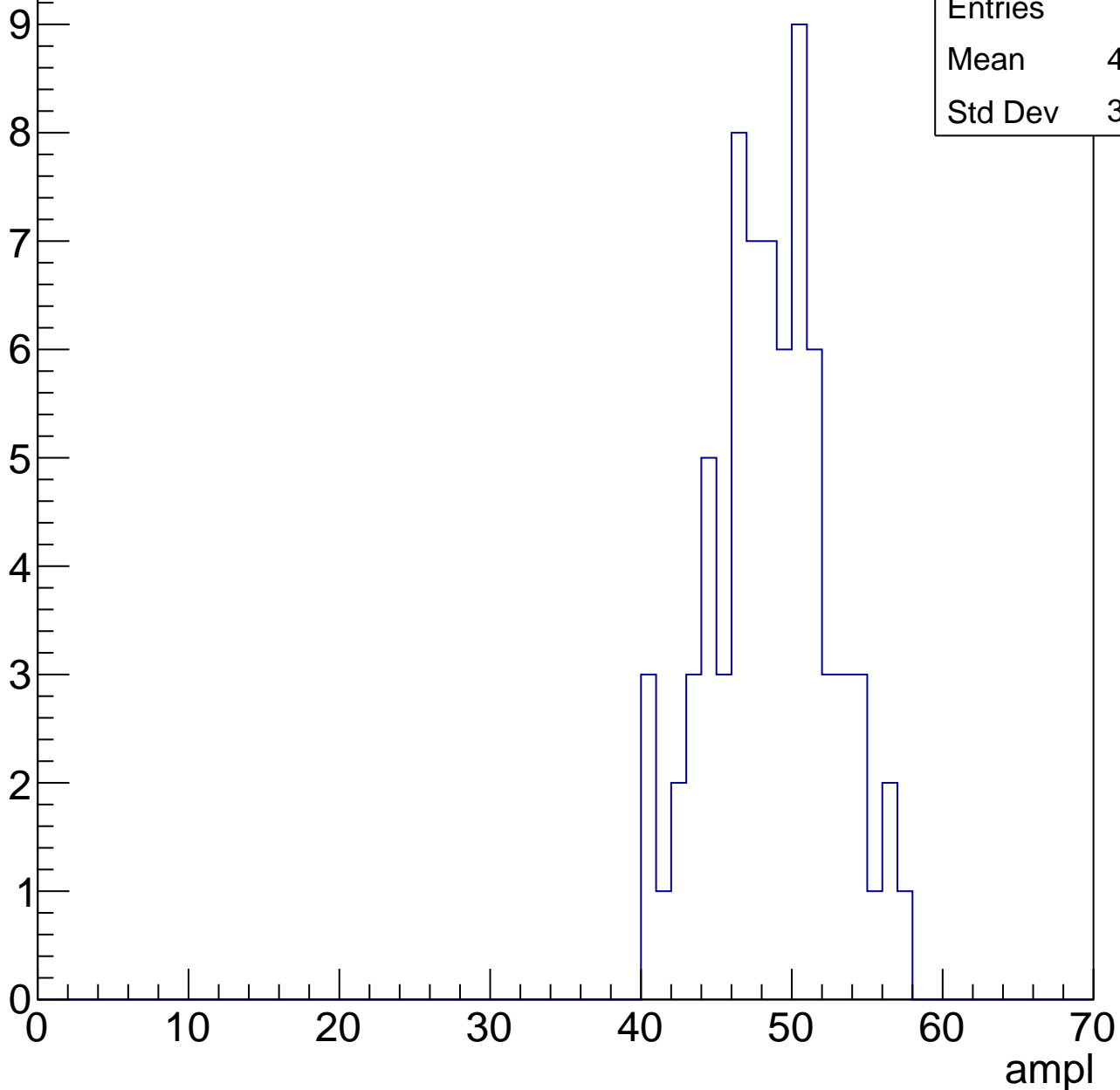
Entries	60
Mean	41.68
Std Dev	3.384

# B1L000S, U24-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	48.12
Std Dev	3.913

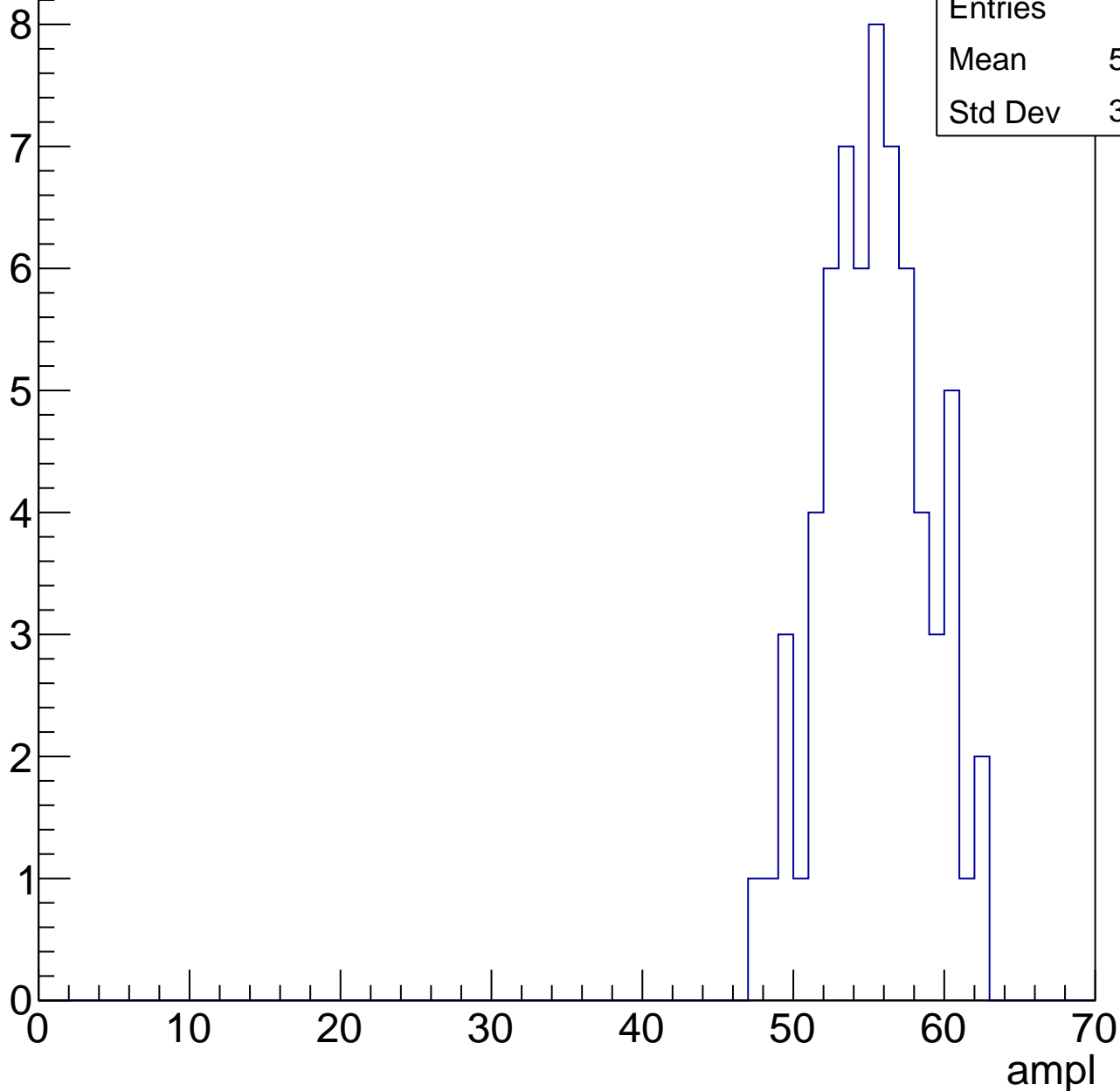


# B1L000S, U24-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.94
Std Dev	3.446

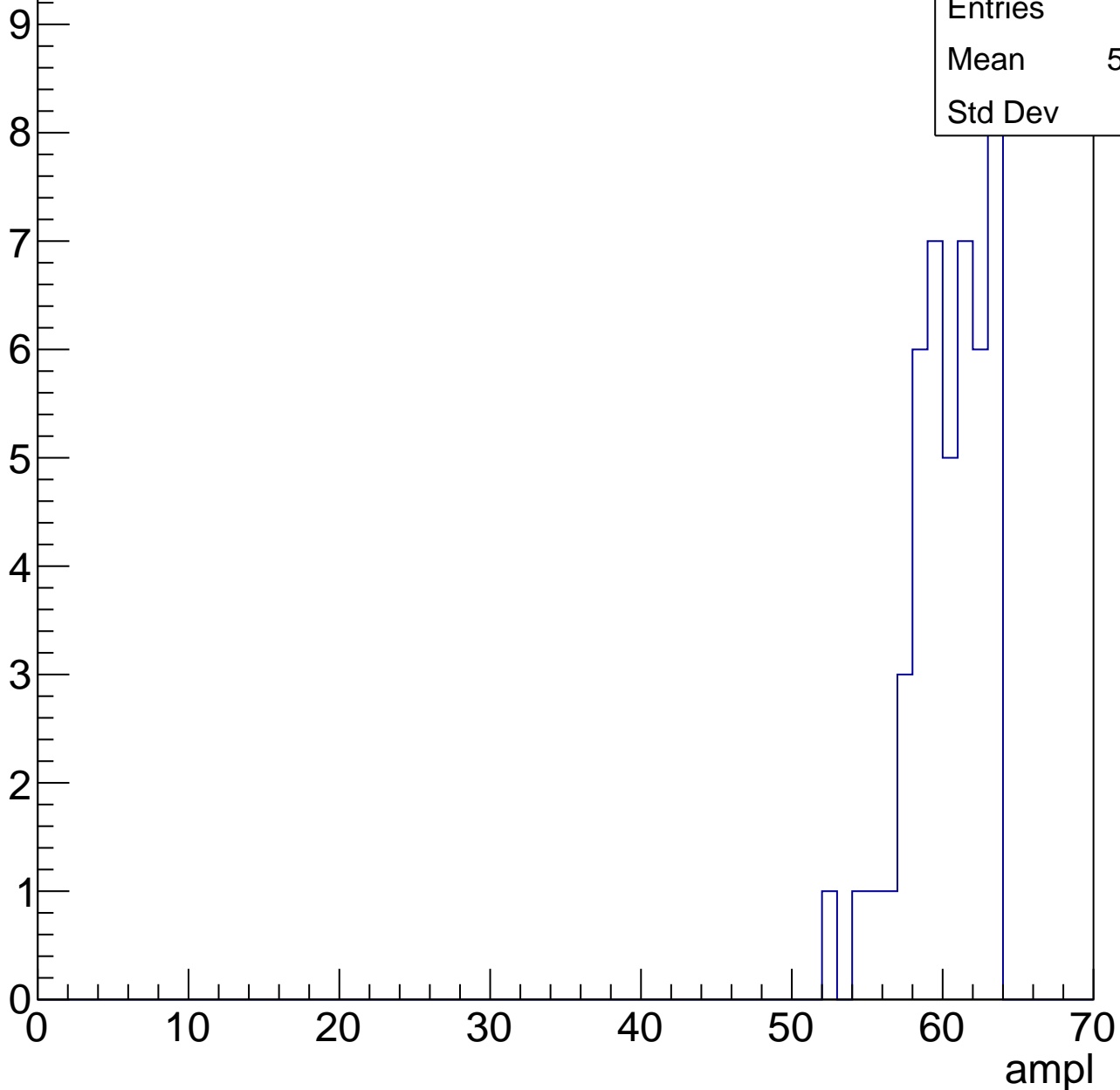


# B1L000S, U24-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	59.89
Std Dev	2.57



# B1L000S, U24-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

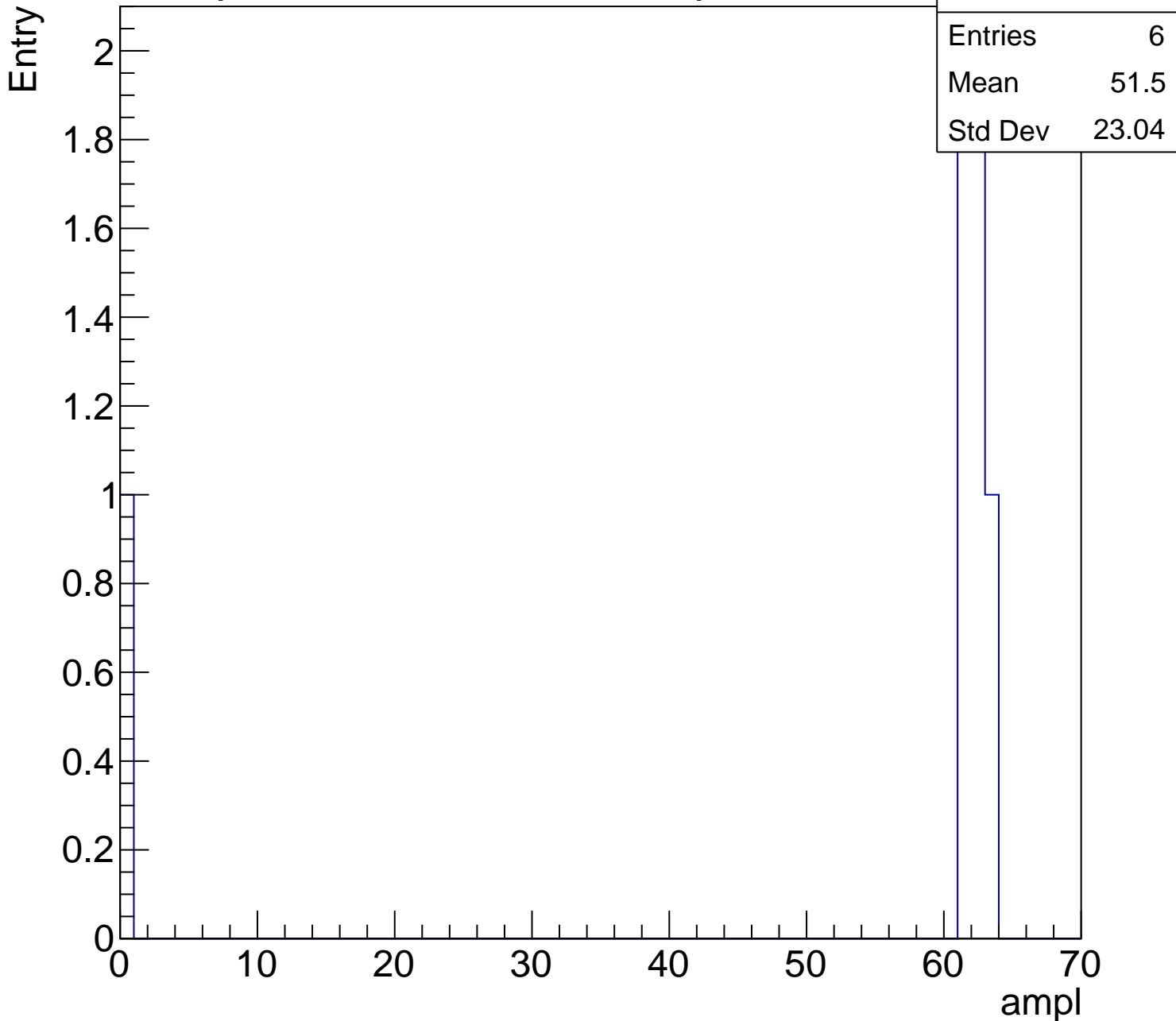
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.04

0 10 20 30 40 50 60 70

ampl





# B1L000S, U24-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch107, adc0

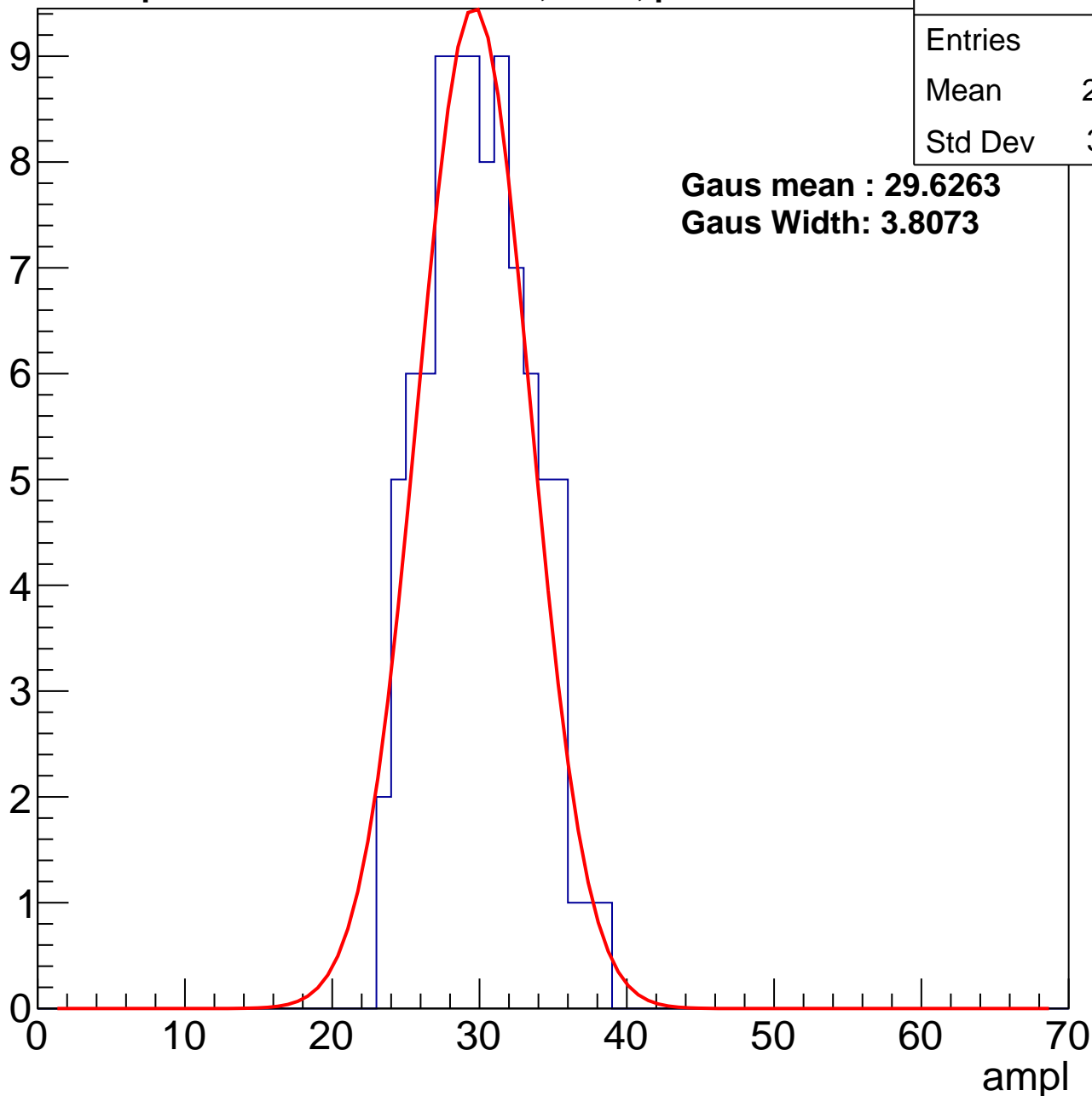
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	89
Mean	29.49
Std Dev	3.471

**Gaus mean : 29.6263**

**Gaus Width: 3.8073**



# B1L000S, U24-ch107, adc1

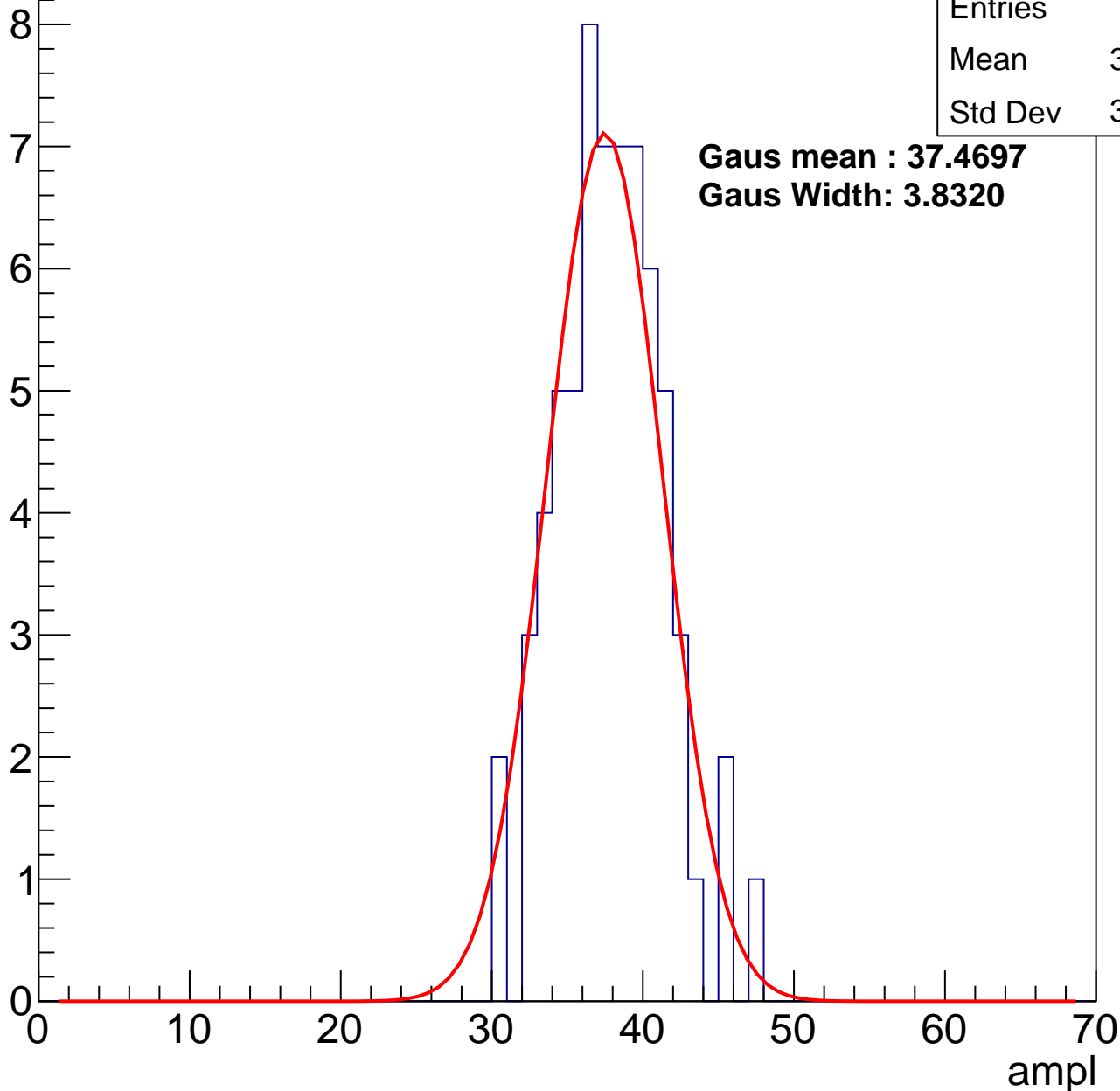
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	37.42
Std Dev	3.495

**Gaus mean : 37.4697**

**Gaus Width: 3.8320**



# B1L000S, U24-ch107, adc2

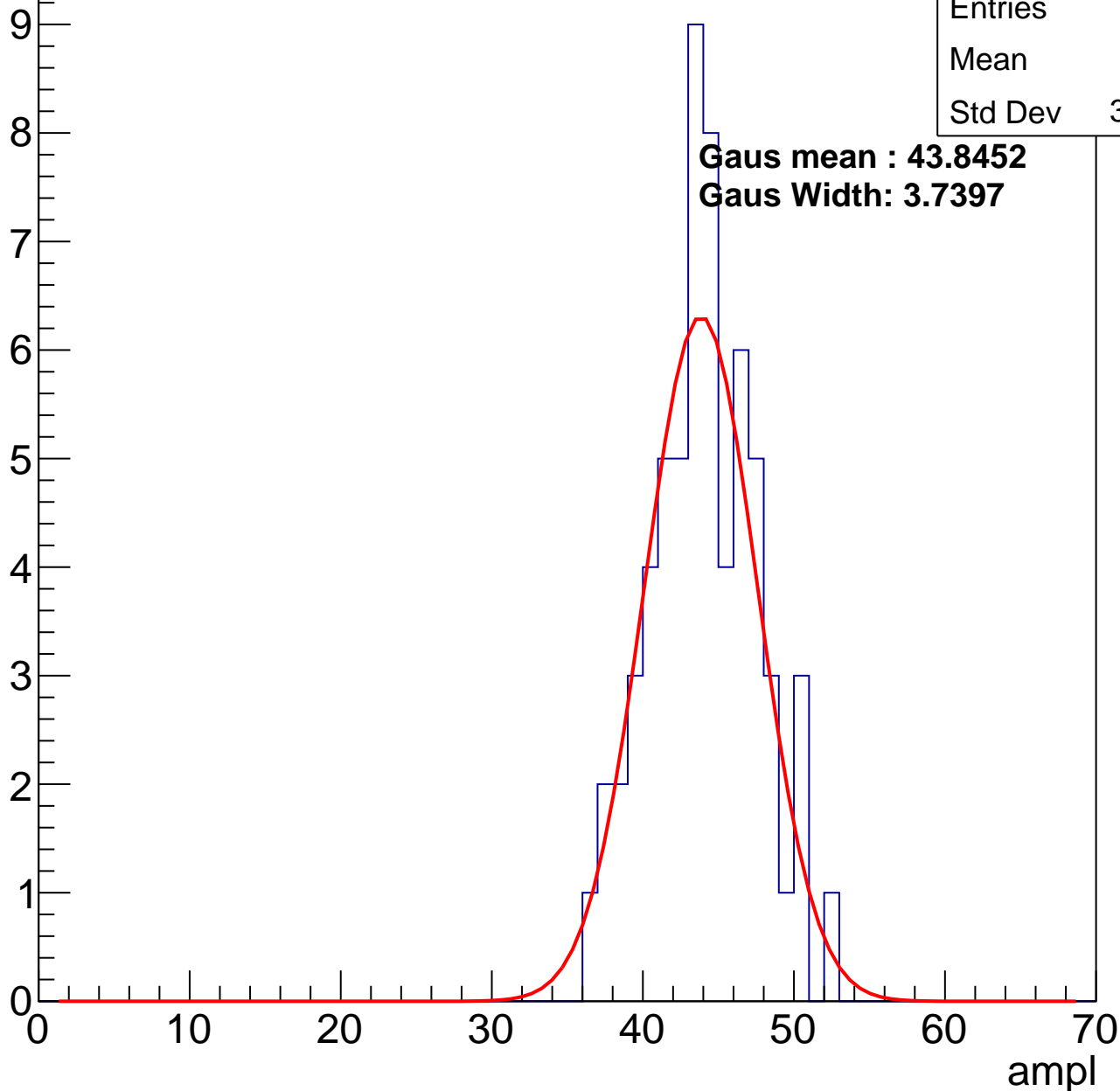
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.6
Std Dev	3.489

**Gaus mean : 43.8452**

**Gaus Width: 3.7397**

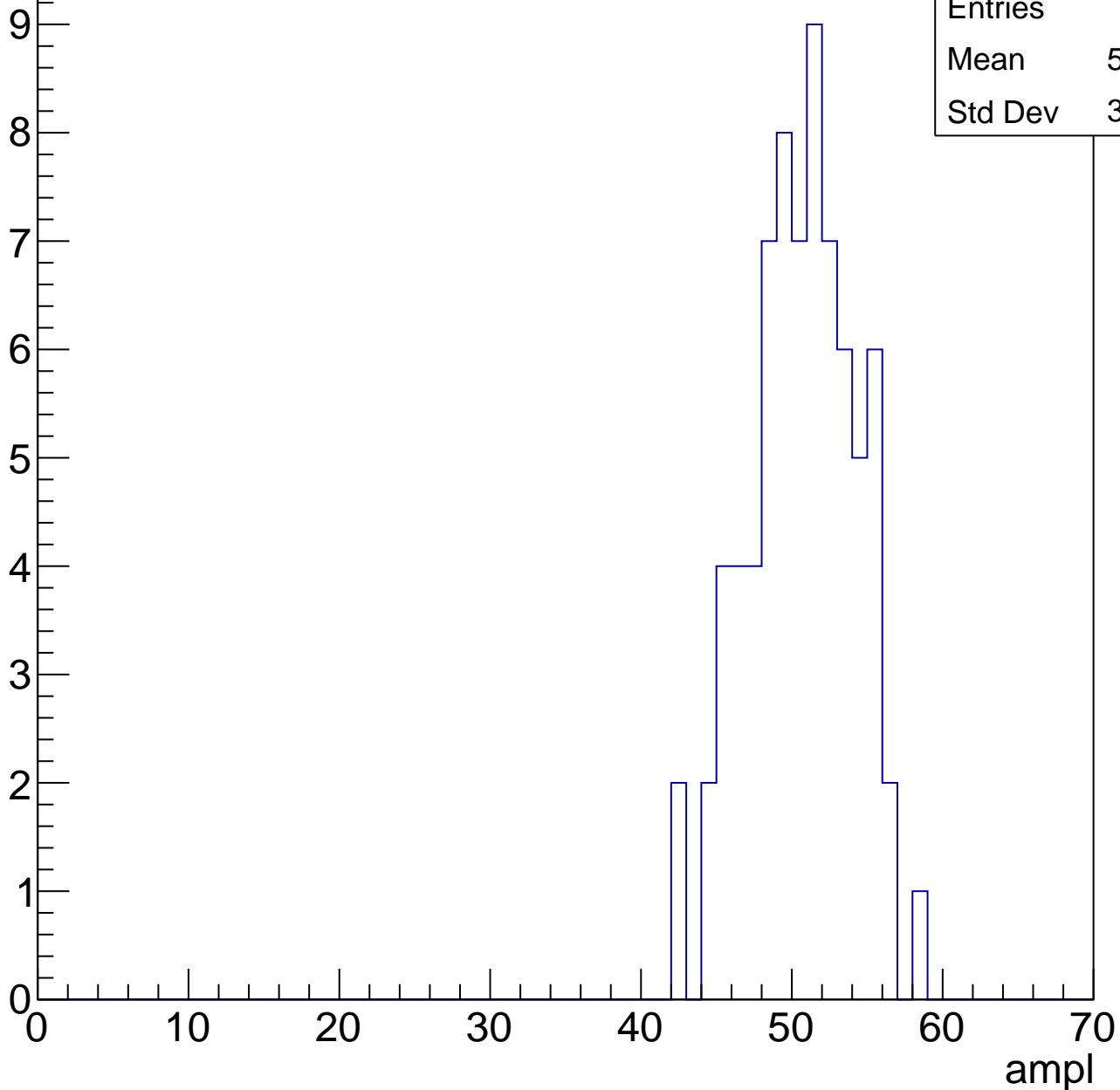


# B1L000S, U24-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	50.18
Std Dev	3.469



# B1L000S, U24-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

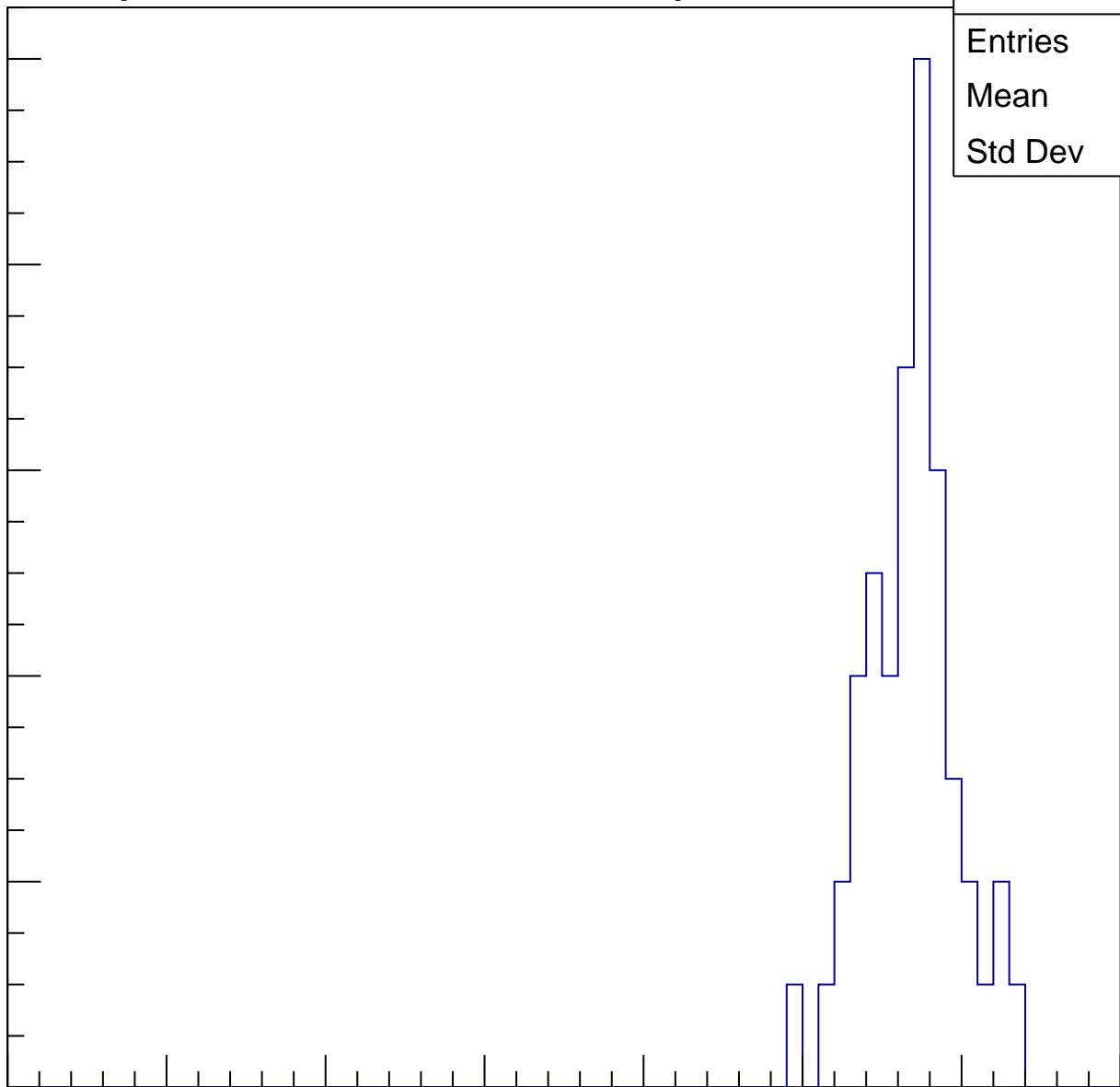
Entries	49
Mean	56.35
Std Dev	2.854

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

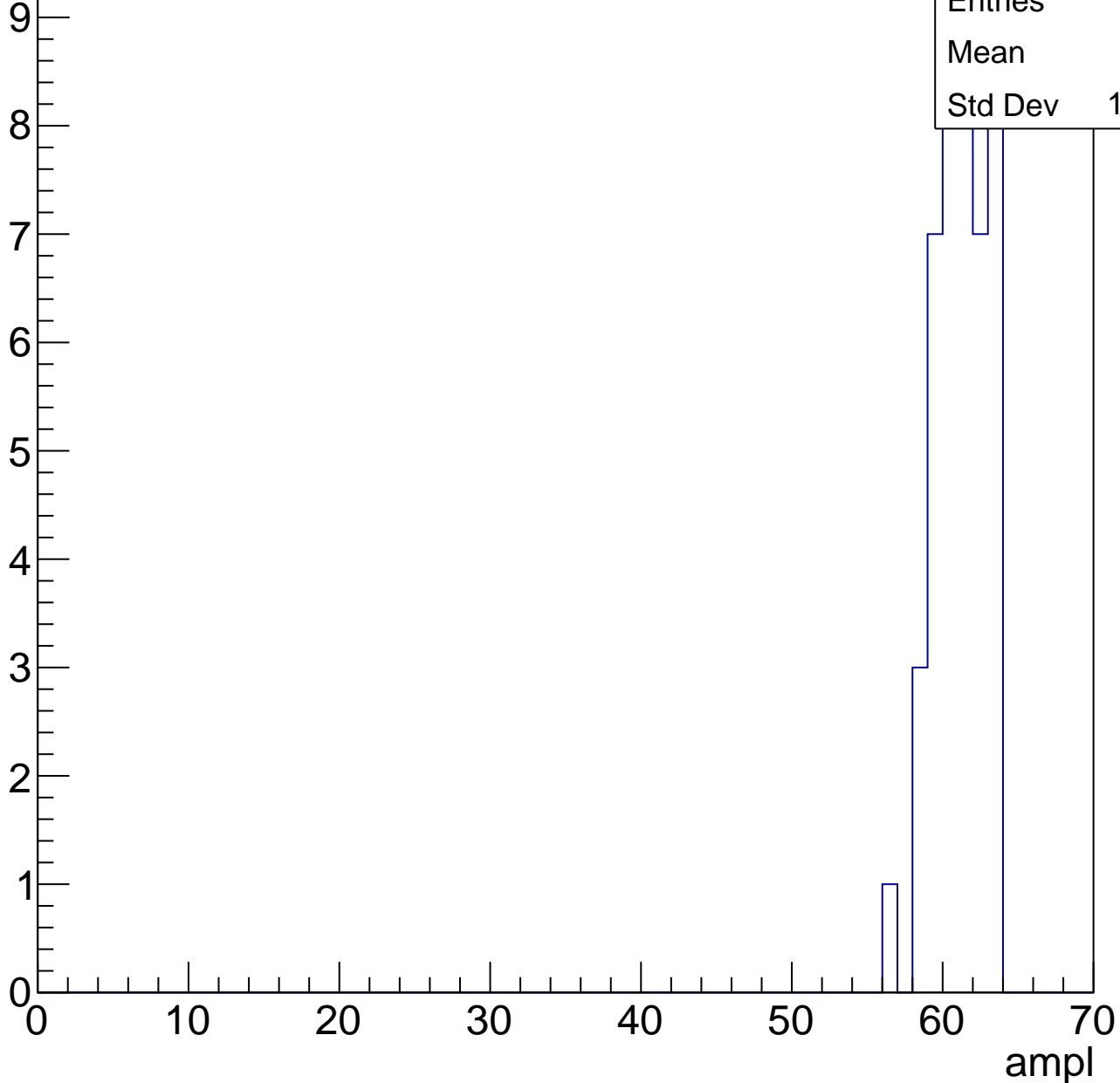


# B1L000S, U24-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	60.7
Std Dev	1.692



# B1L000S, U24-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch108, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	29.23
Std Dev	2.712

**Gaus mean : 29.5530**

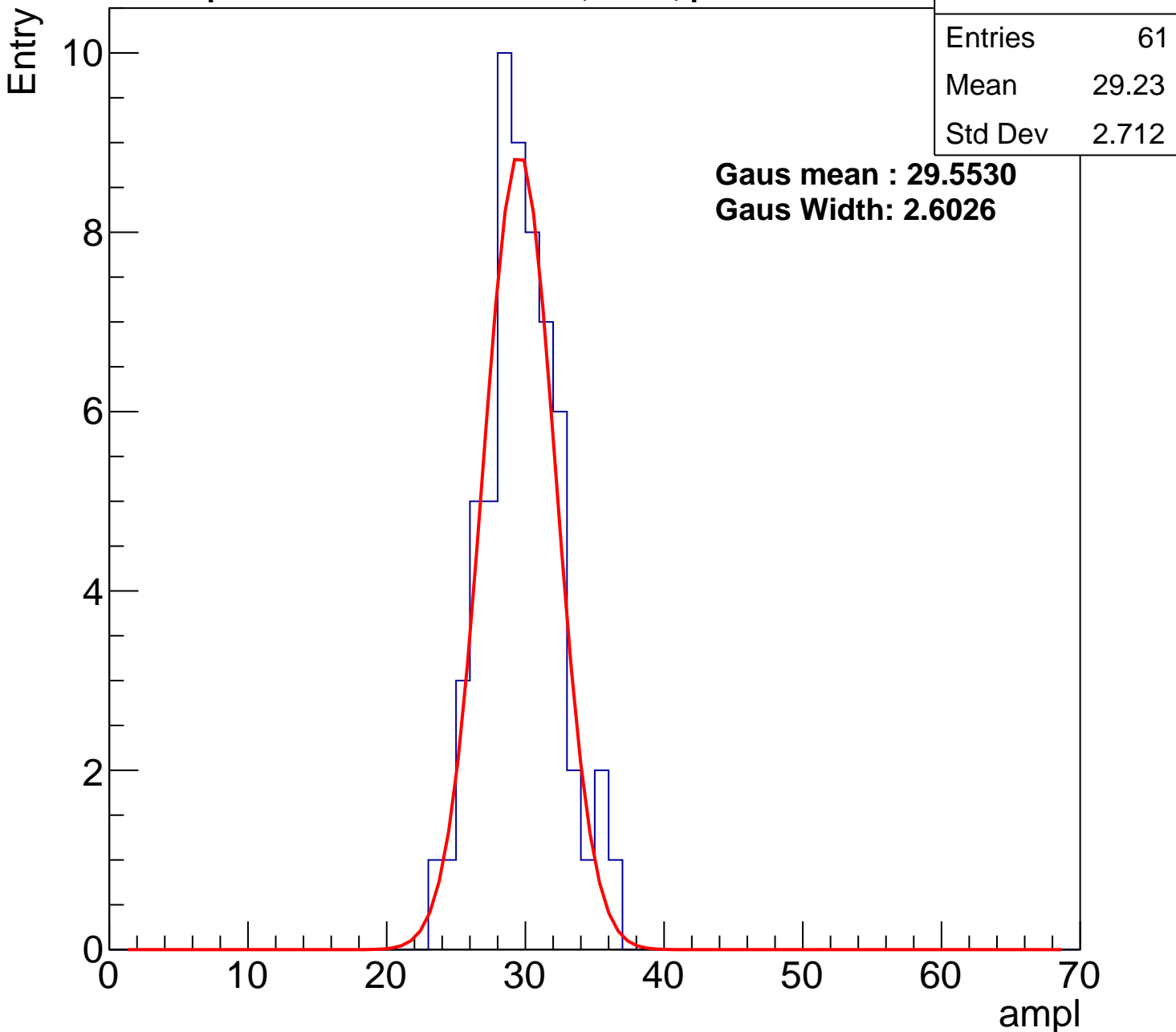
**Gaus Width: 2.6026**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch108, adc1

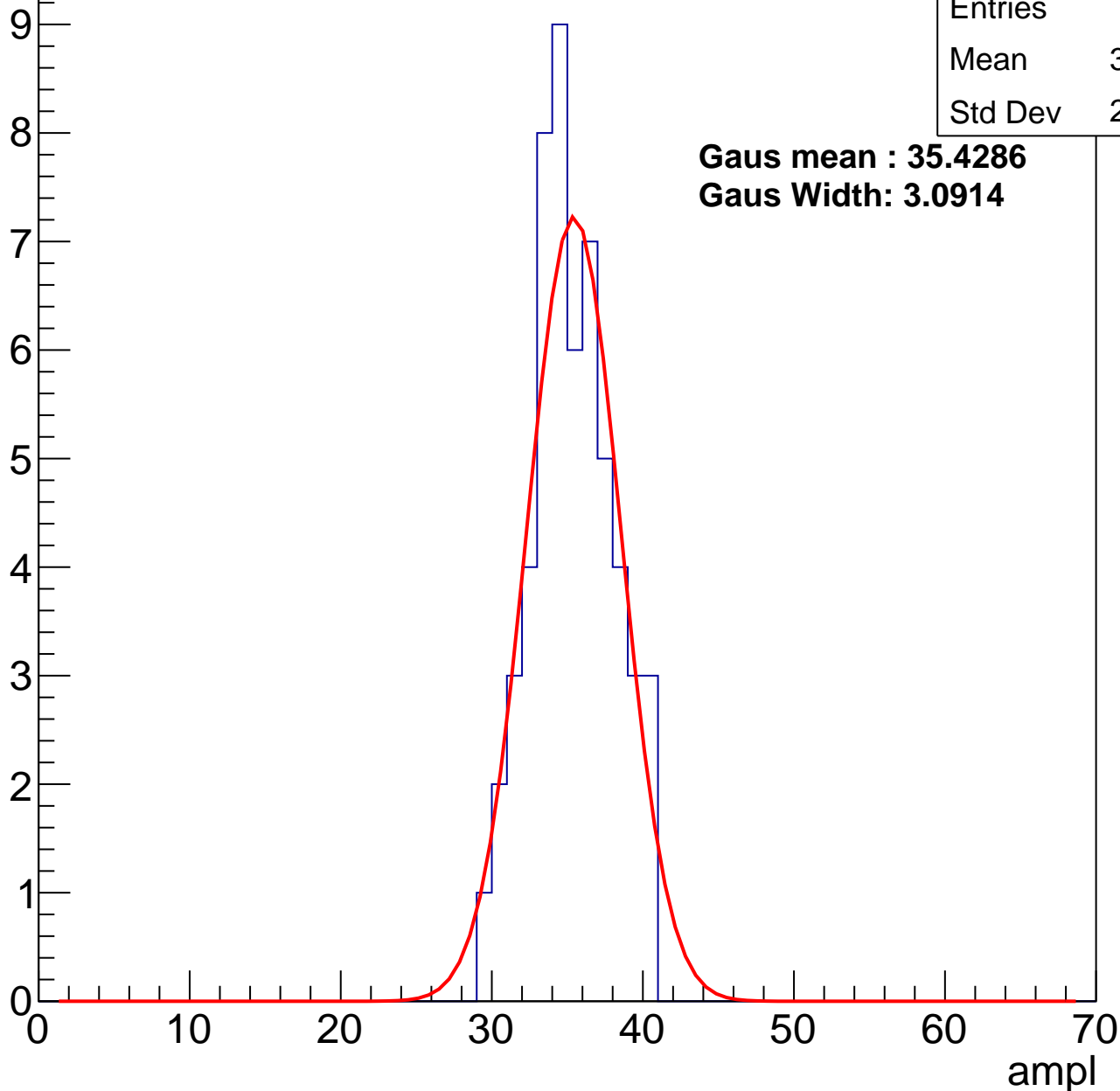
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.84
Std Dev	2.682

**Gaus mean : 35.4286**

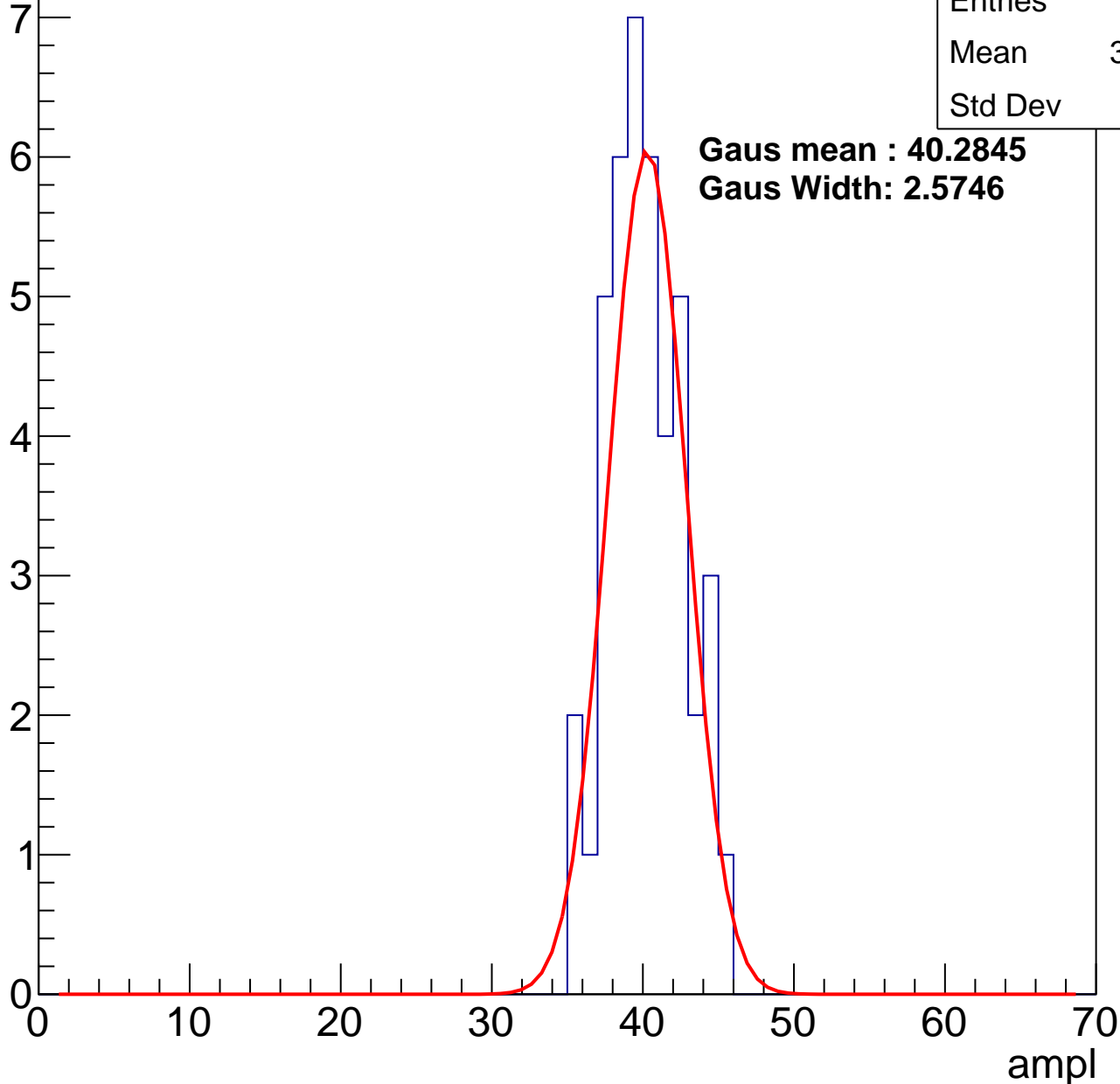
**Gaus Width: 3.0914**



# B1L000S, U24-ch108, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

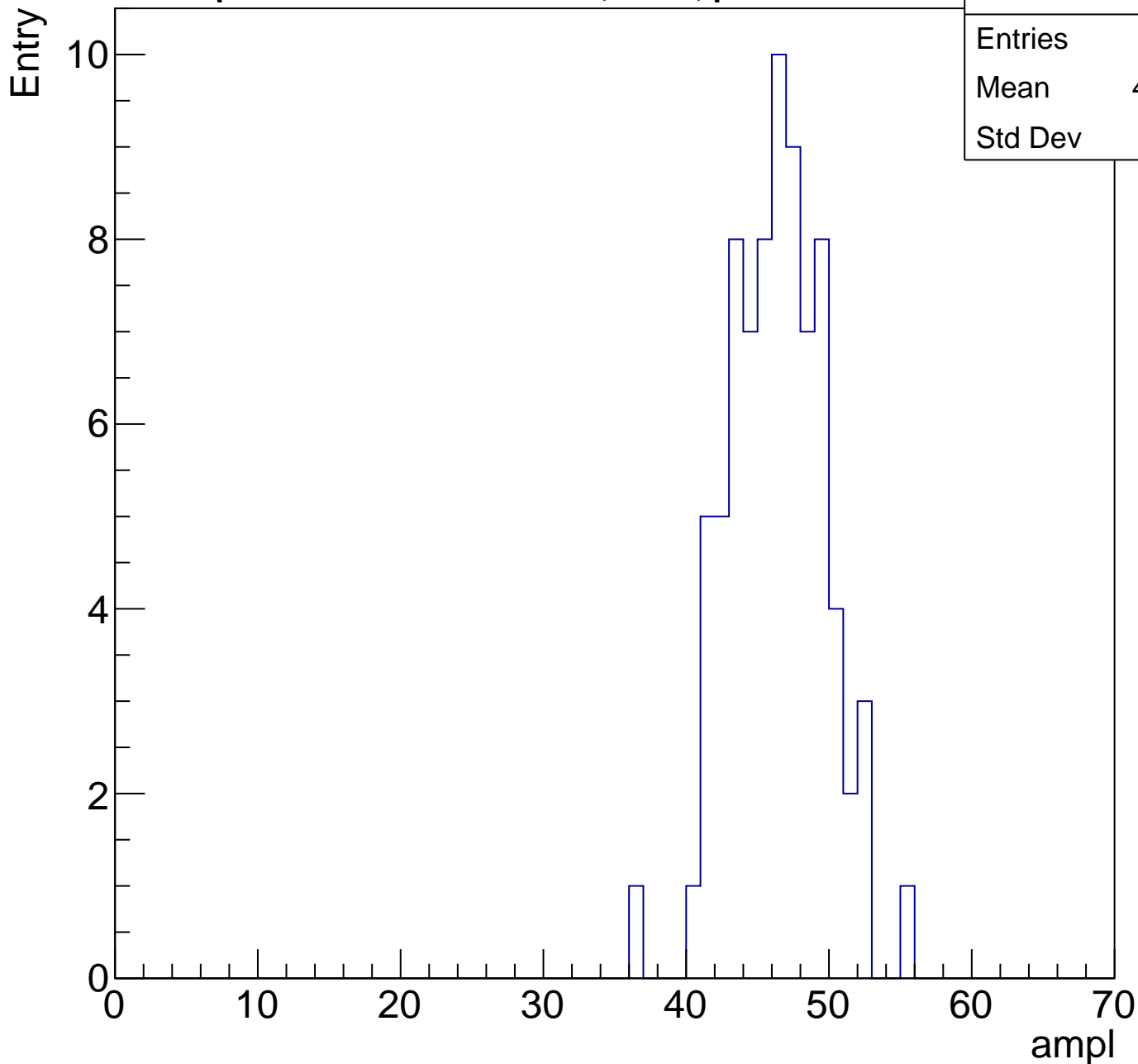
Entry



# B1L000S, U24-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	45.91
Std Dev	3.3

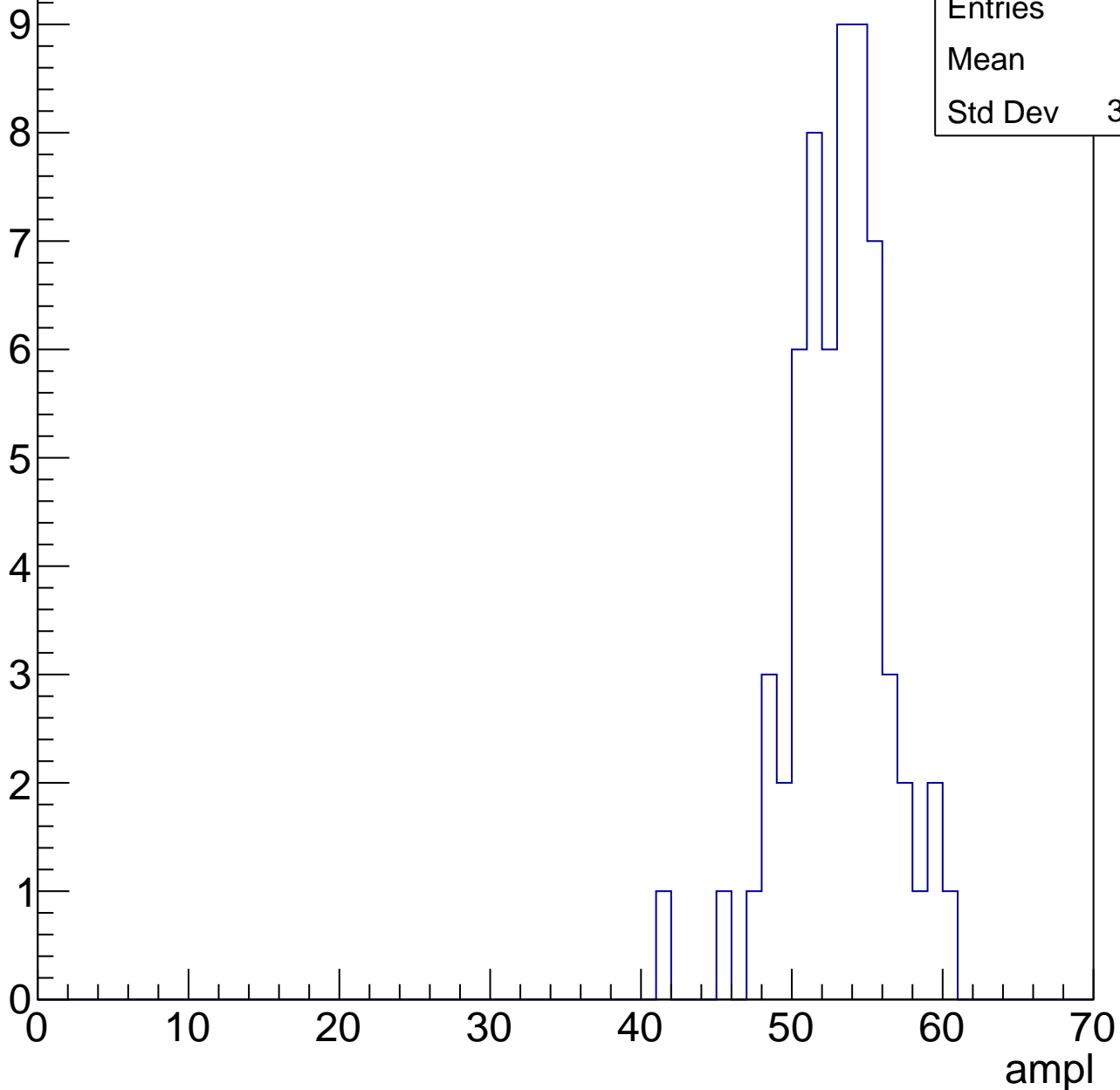


# B1L000S, U24-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	52.6
Std Dev	3.299

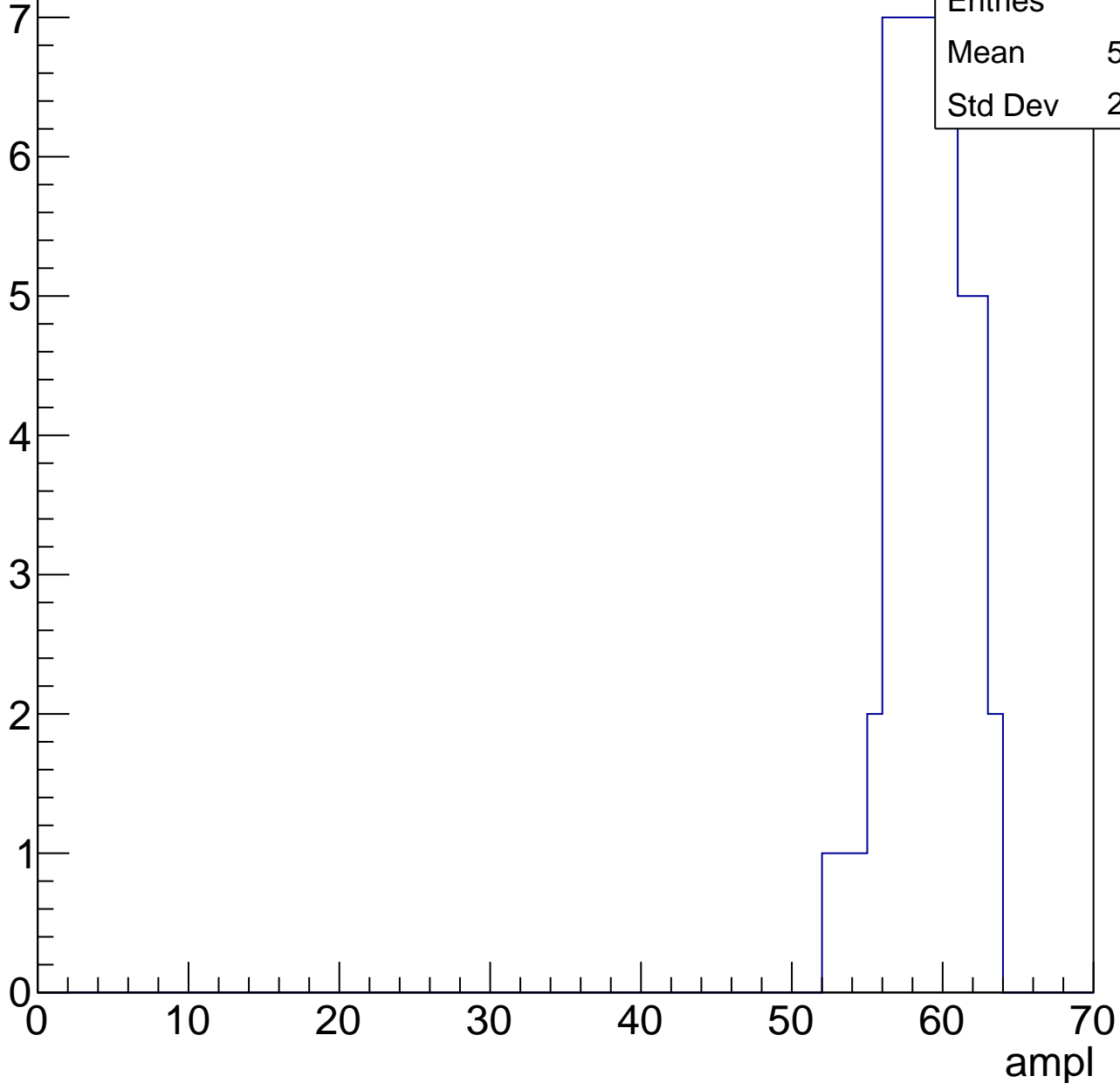


# B1L000S, U24-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	58.46
Std Dev	2.515

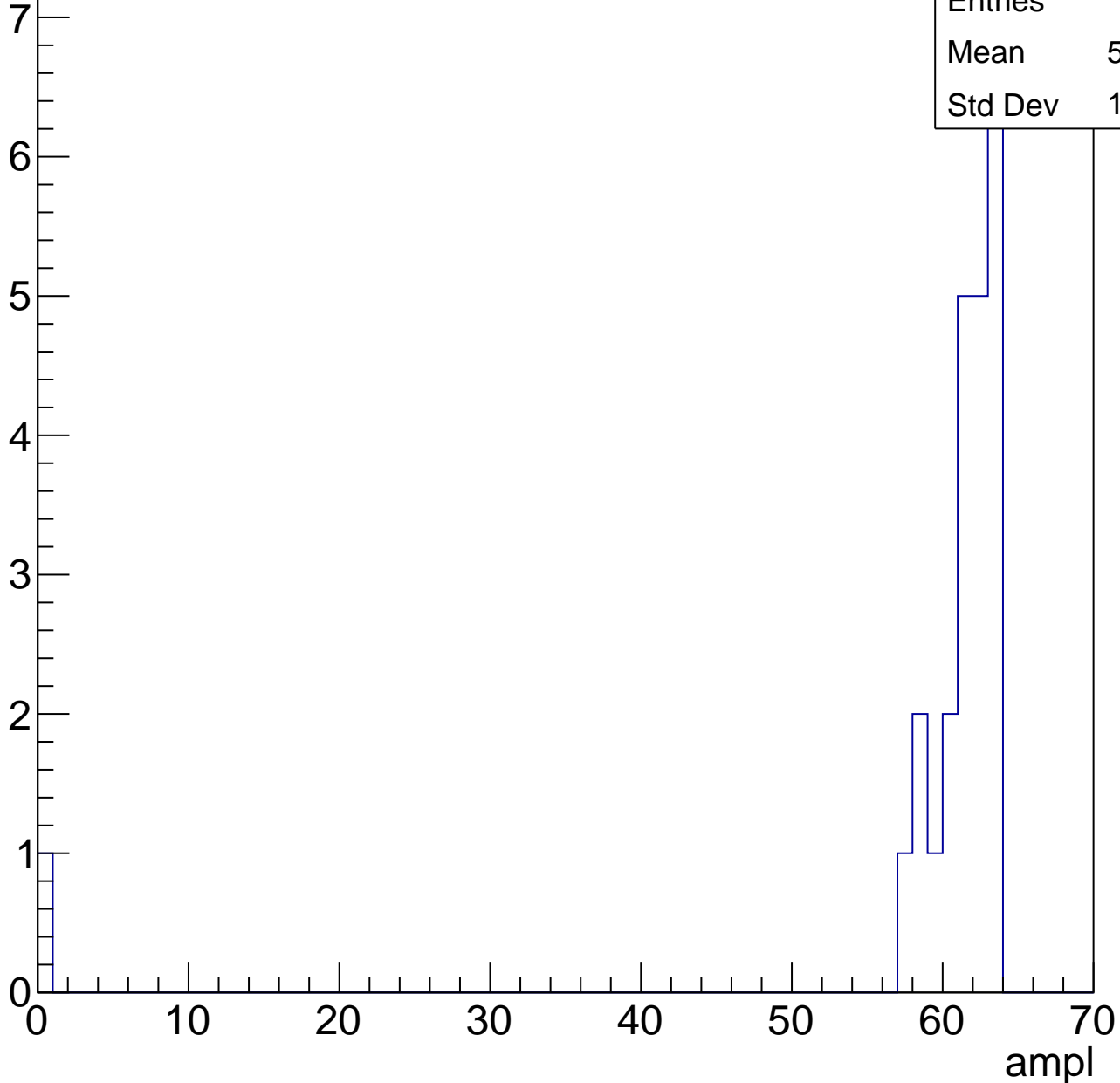


# B1L000S, U24-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.67
Std Dev	12.35





# B1L000S, U24-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch109, adc0

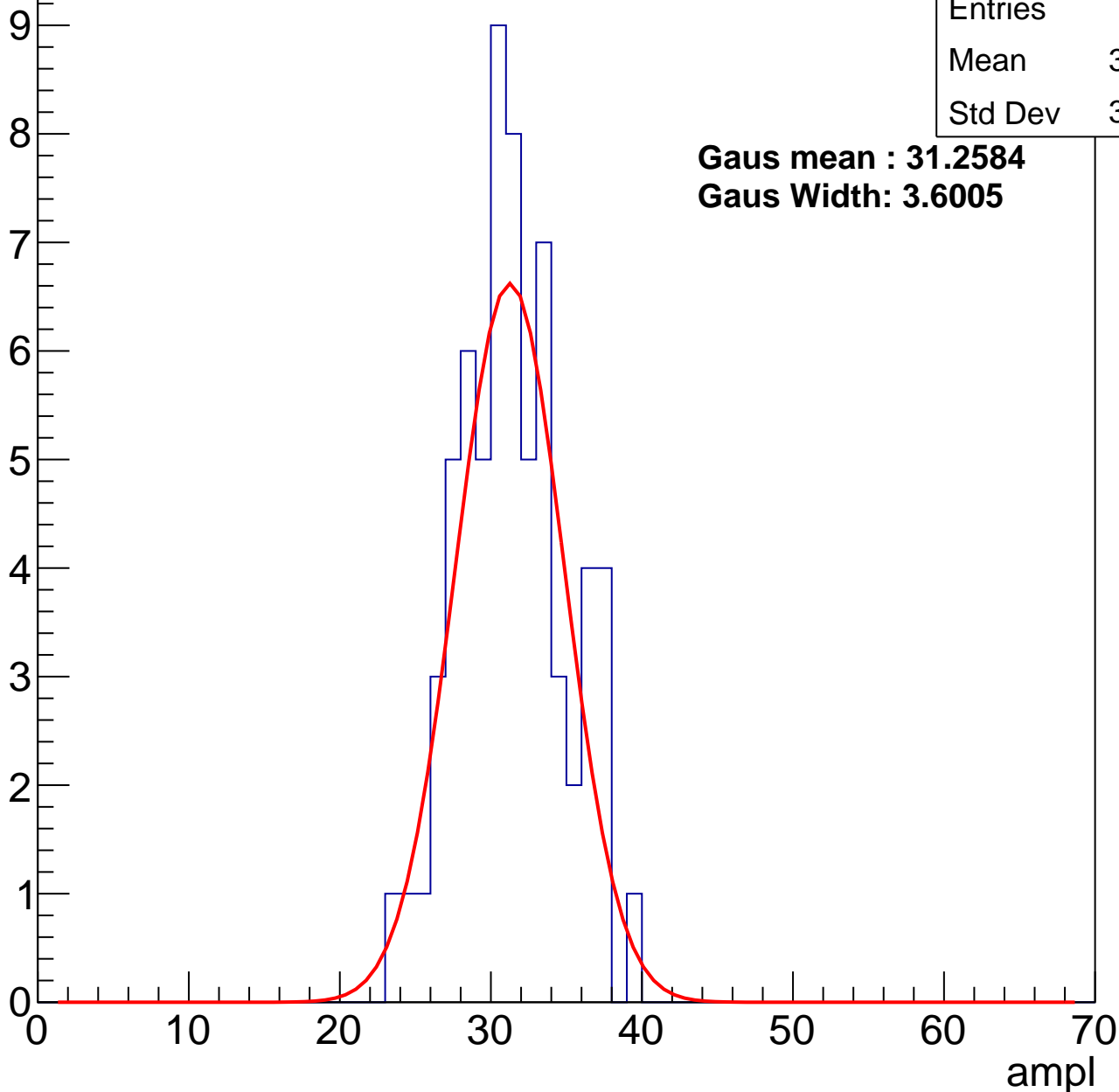
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	30.92
Std Dev	3.479

**Gaus mean : 31.2584**

**Gaus Width: 3.6005**



# B1L000S, U24-ch109, adc1

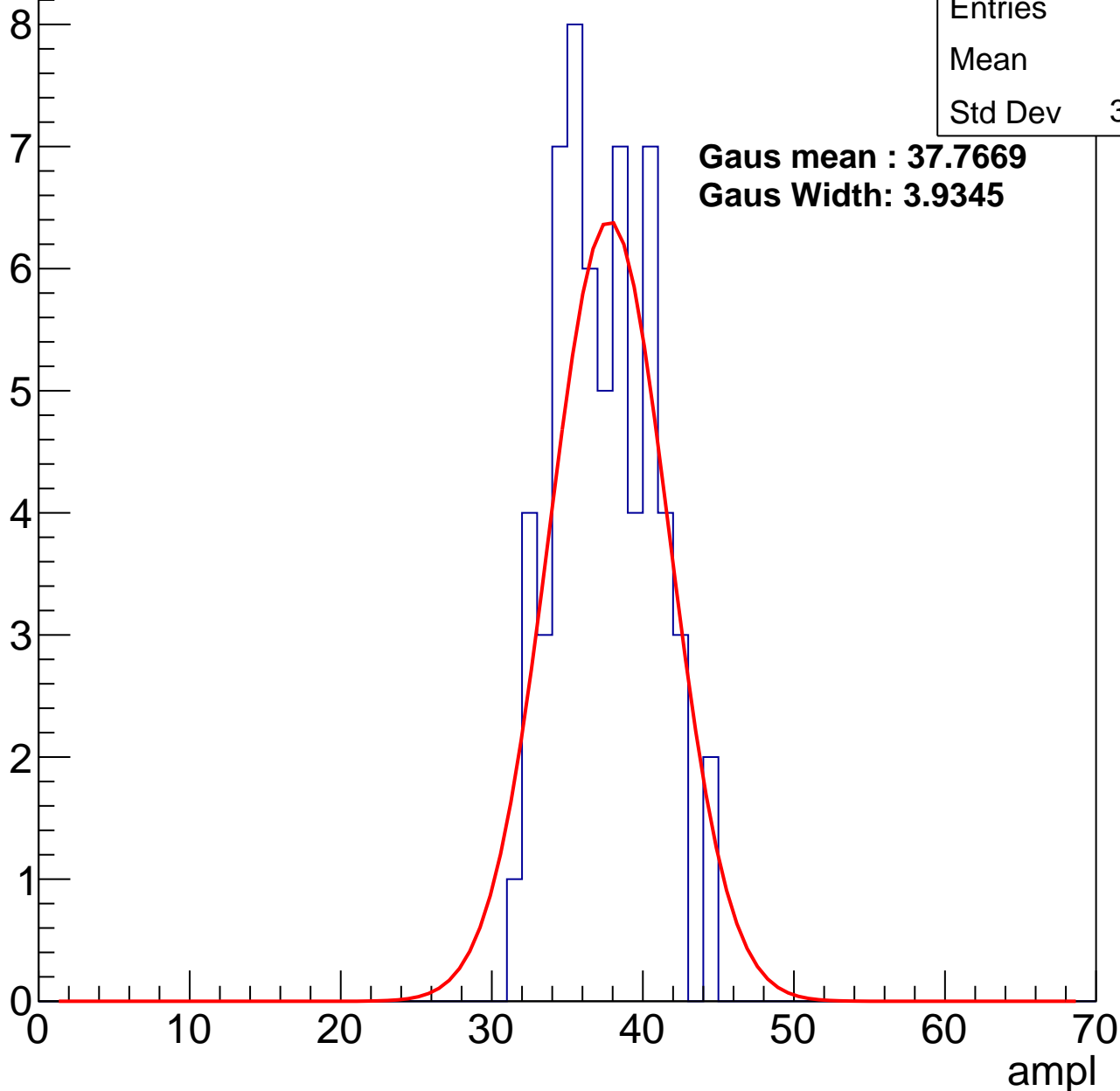
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	37
Std Dev	3.157

**Gaus mean : 37.7669**

**Gaus Width: 3.9345**



# B1L000S, U24-ch109, adc2

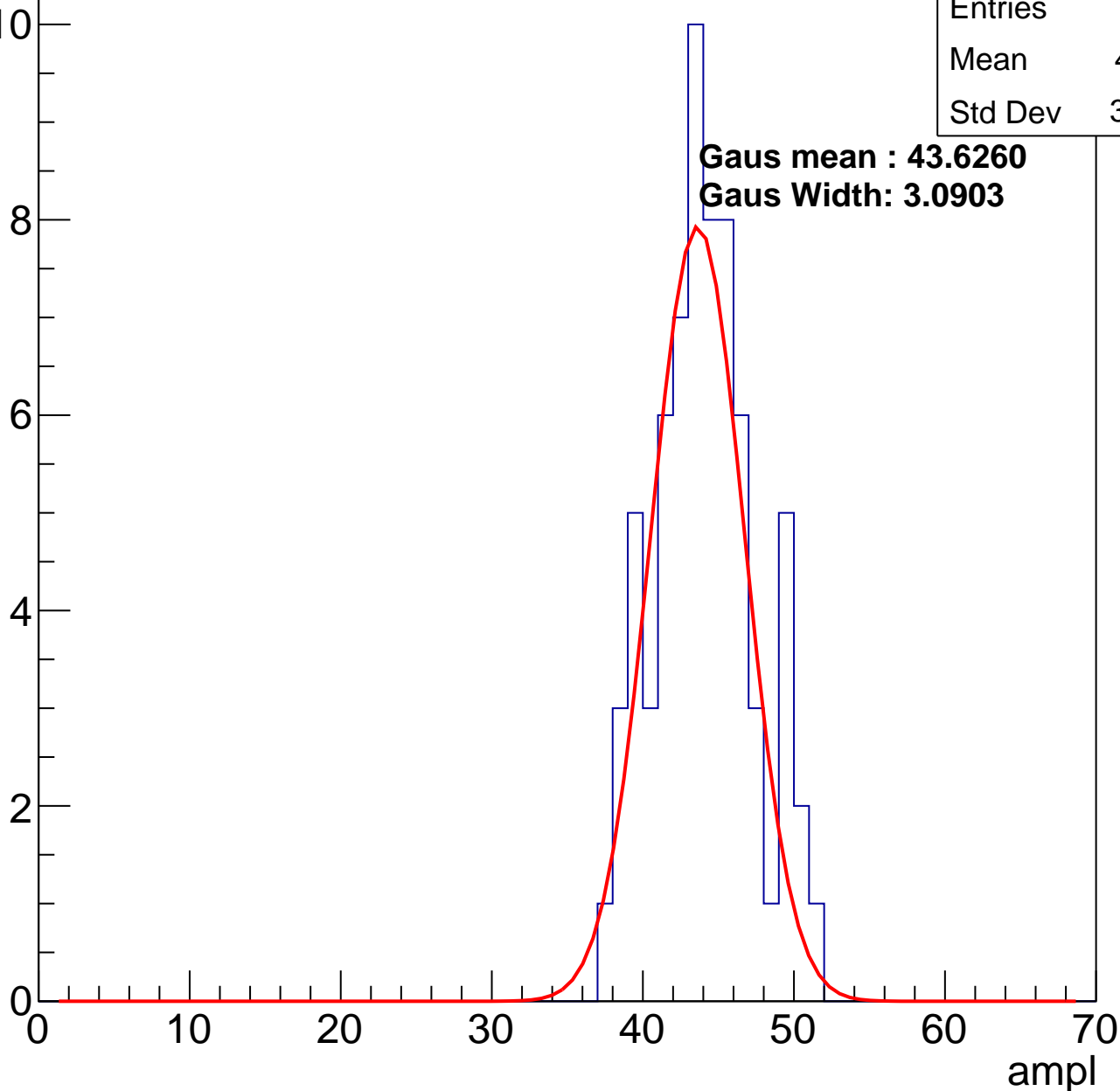
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	43.61
Std Dev	3.258

**Gaus mean : 43.6260**

**Gaus Width: 3.0903**

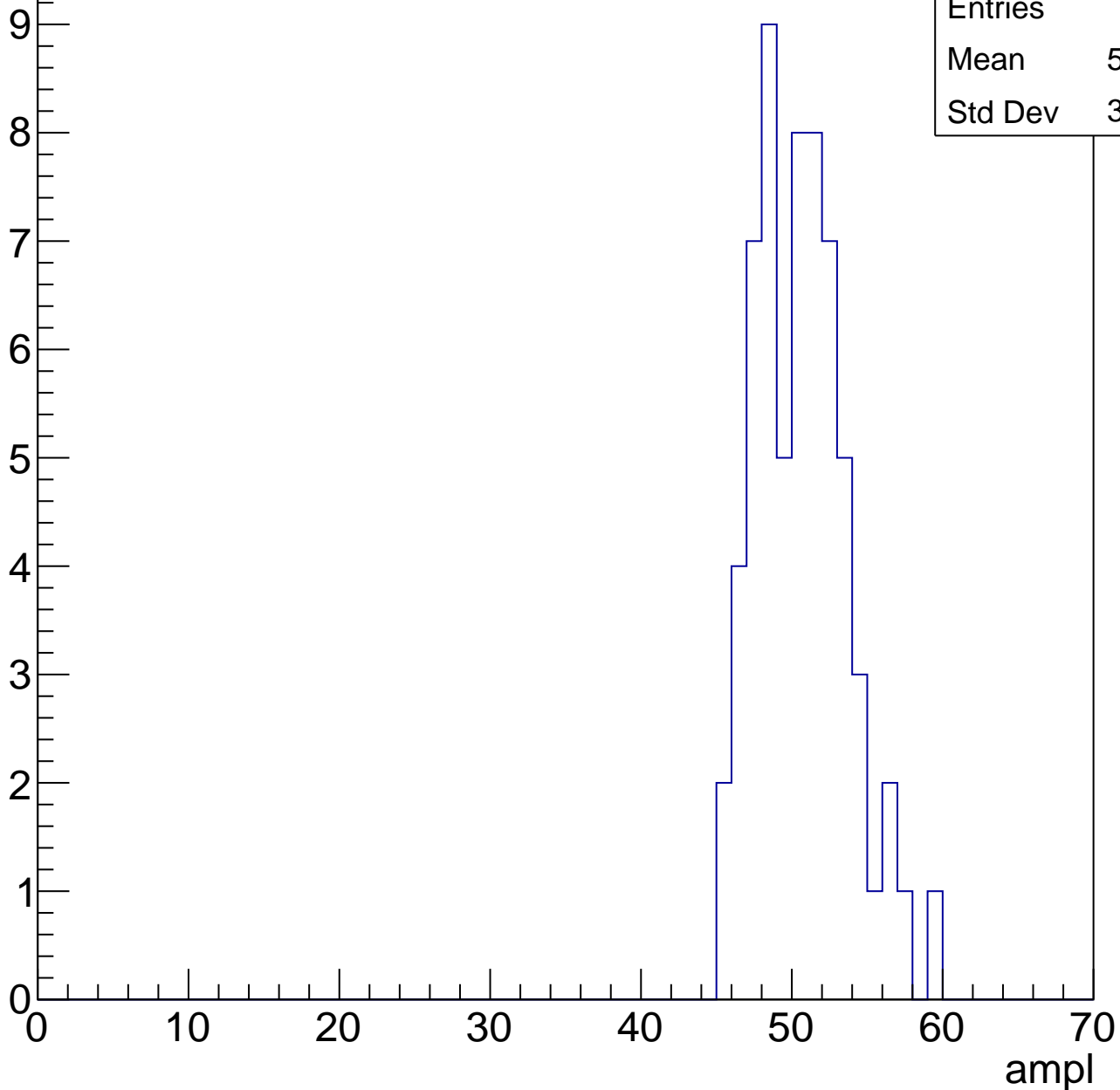


# B1L000S, U24-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	50.19
Std Dev	3.012

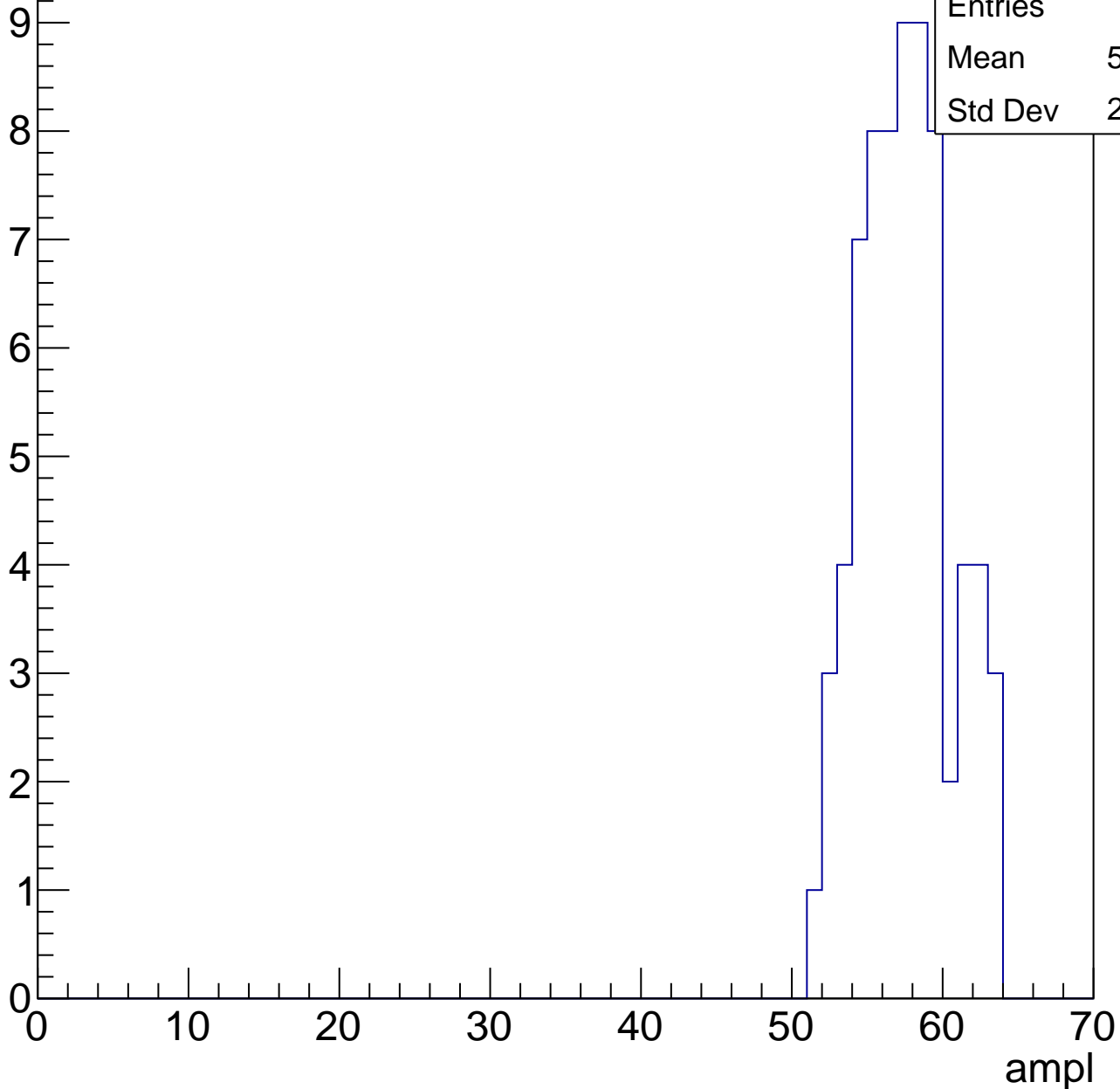


# B1L000S, U24-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	57.04
Std Dev	2.949

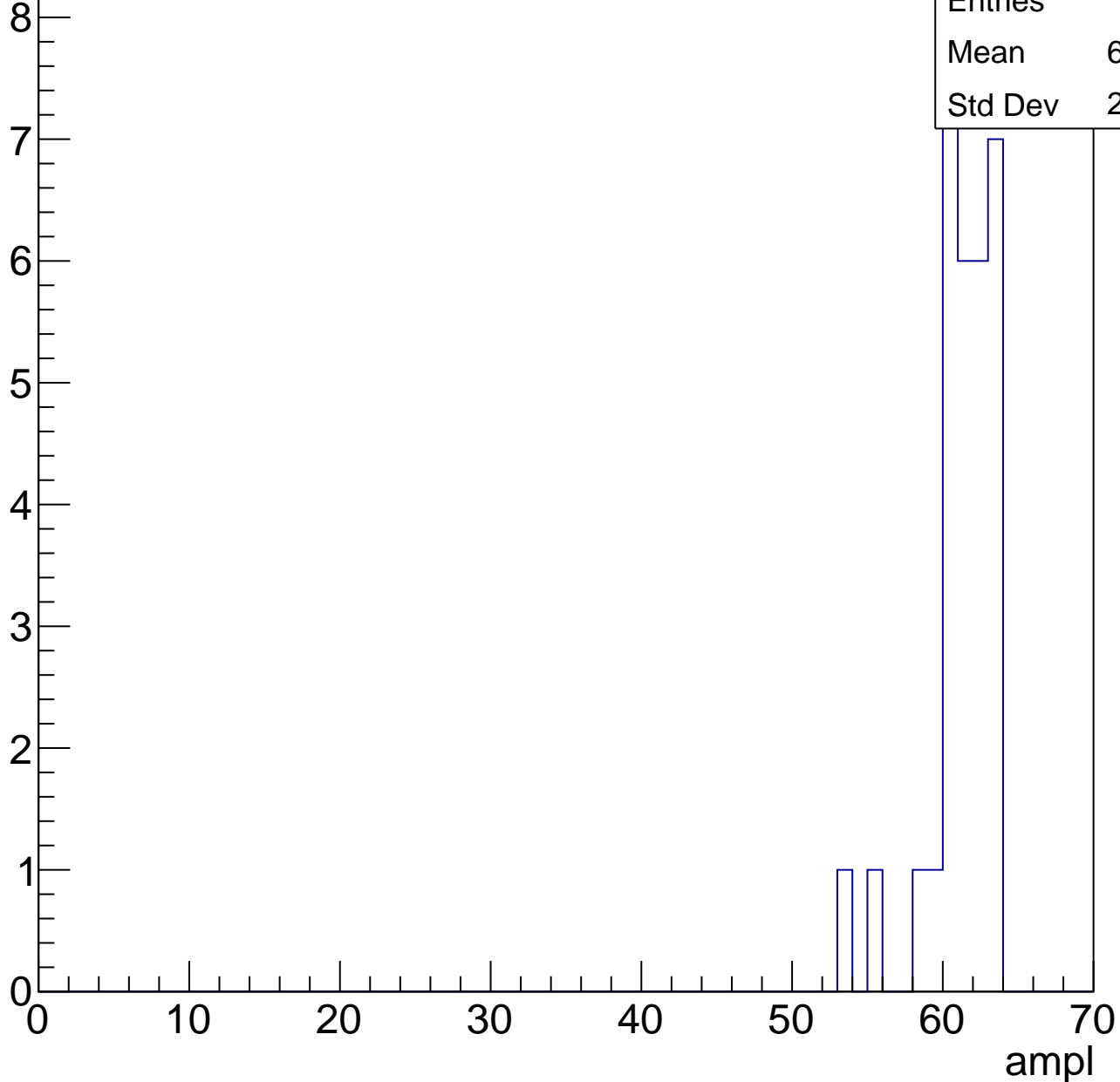


# B1L000S, U24-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	60.77
Std Dev	2.225



# B1L000S, U24-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0



# B1L000S, U24-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	29.21
Std Dev	3.549

**Gaus mean : 28.9364**

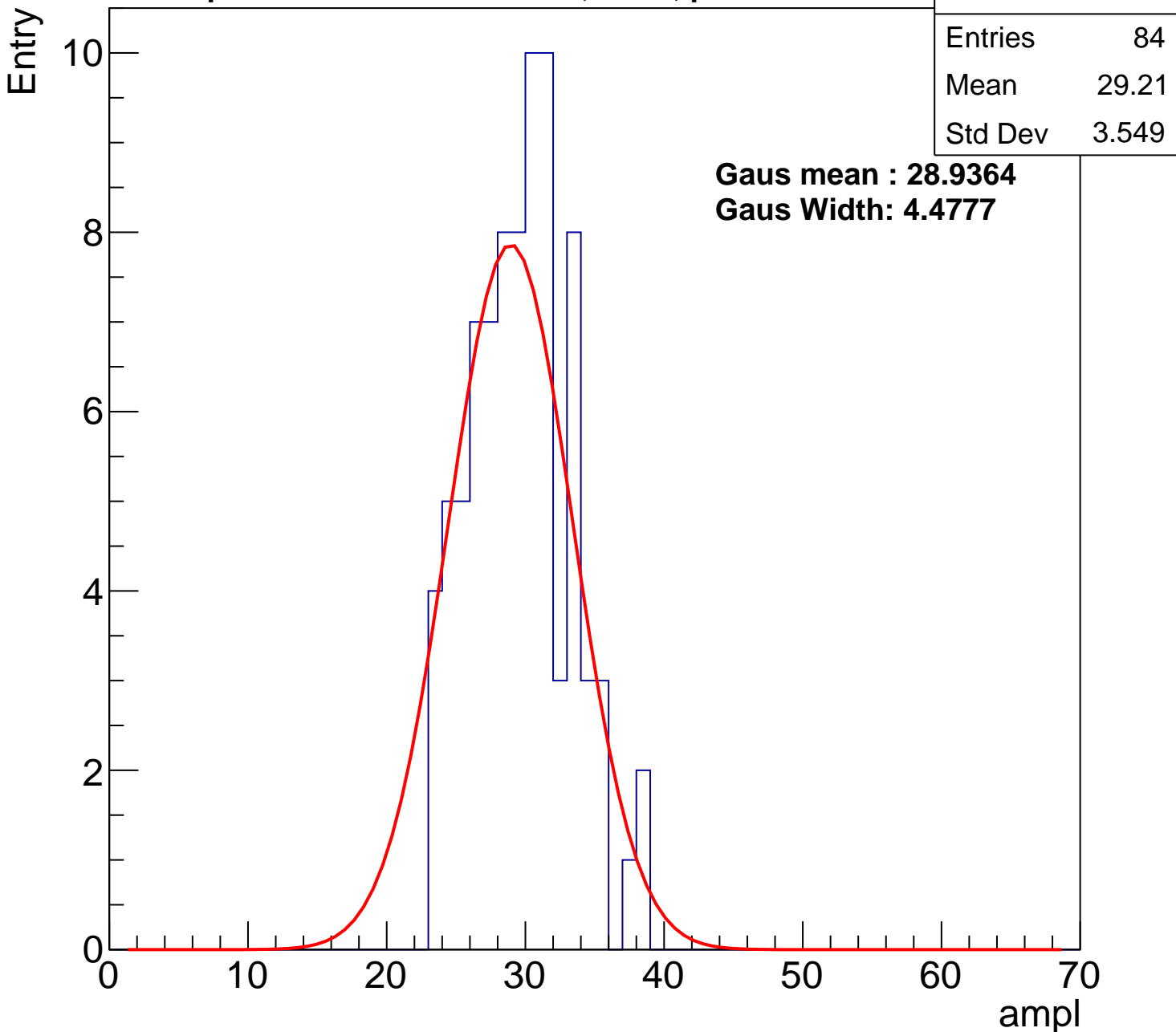
**Gaus Width: 4.4777**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch110, adc1

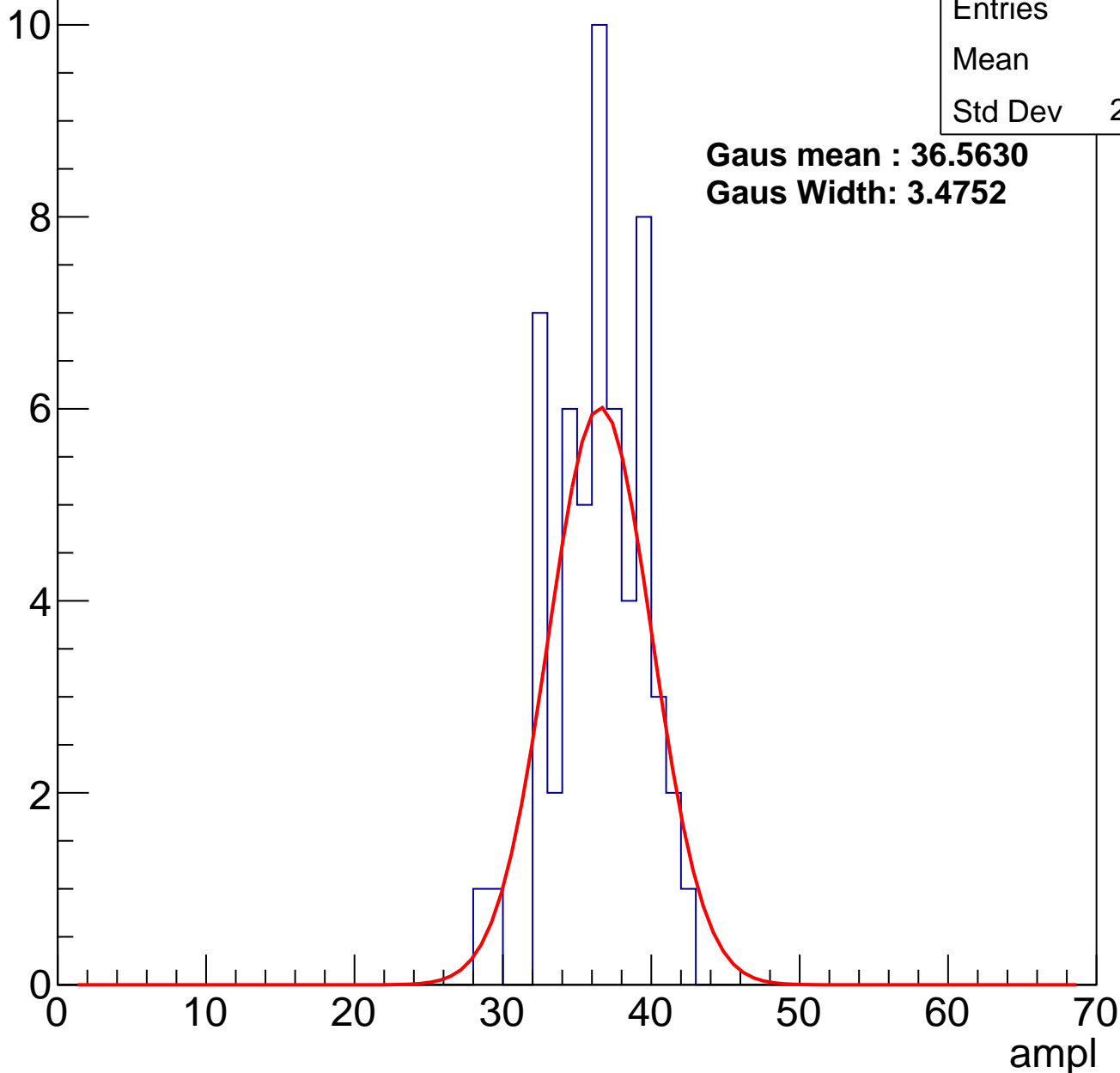
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	36
Std Dev	2.988

**Gaus mean : 36.5630**

**Gaus Width: 3.4752**

Entry



# B1L000S, U24-ch110, adc2

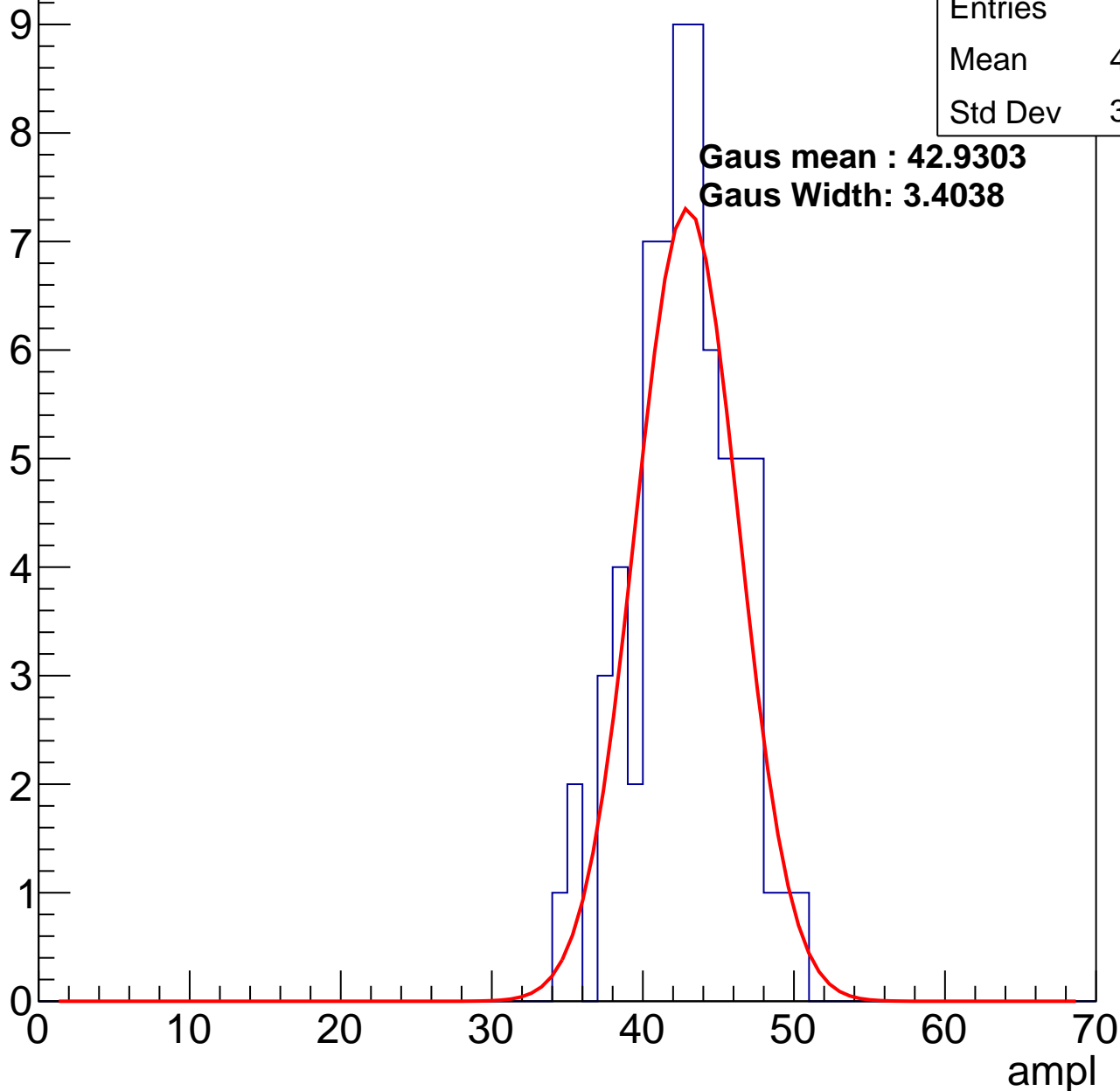
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.32
Std Dev	3.384

**Gaus mean : 42.9303**

**Gaus Width: 3.4038**

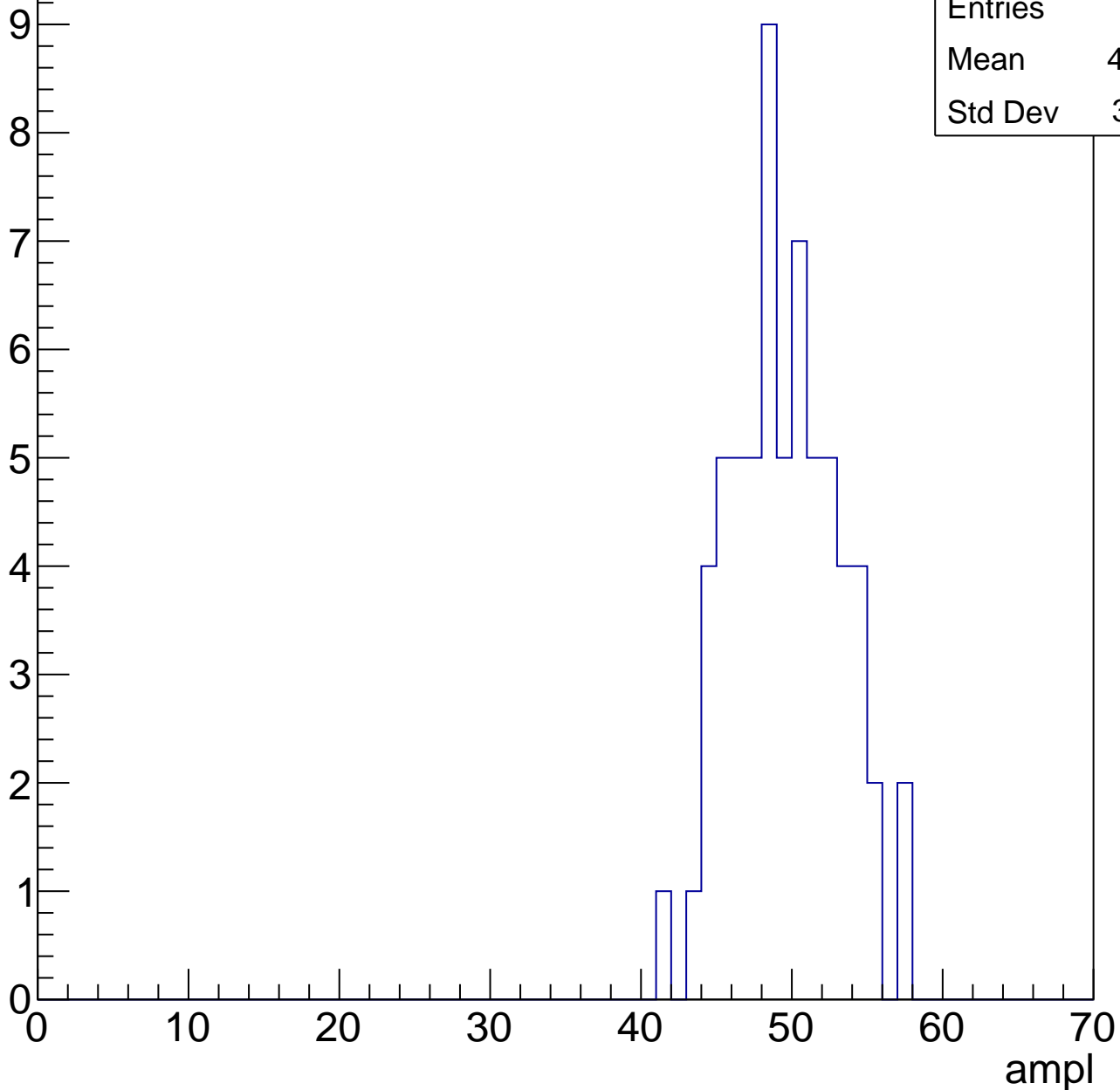


# B1L000S, U24-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	49.12
Std Dev	3.511

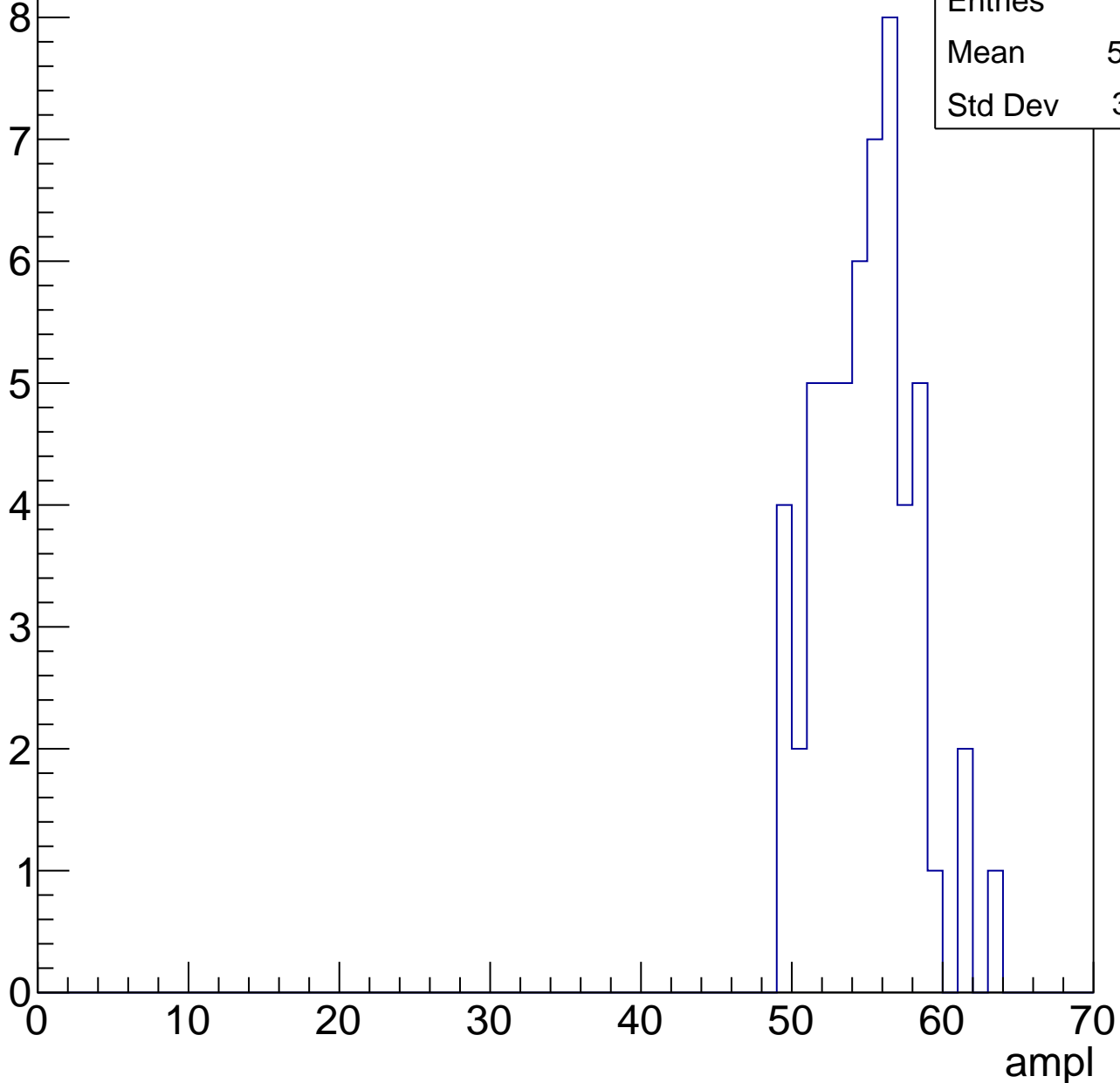


# B1L000S, U24-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	54.45
Std Dev	3.161

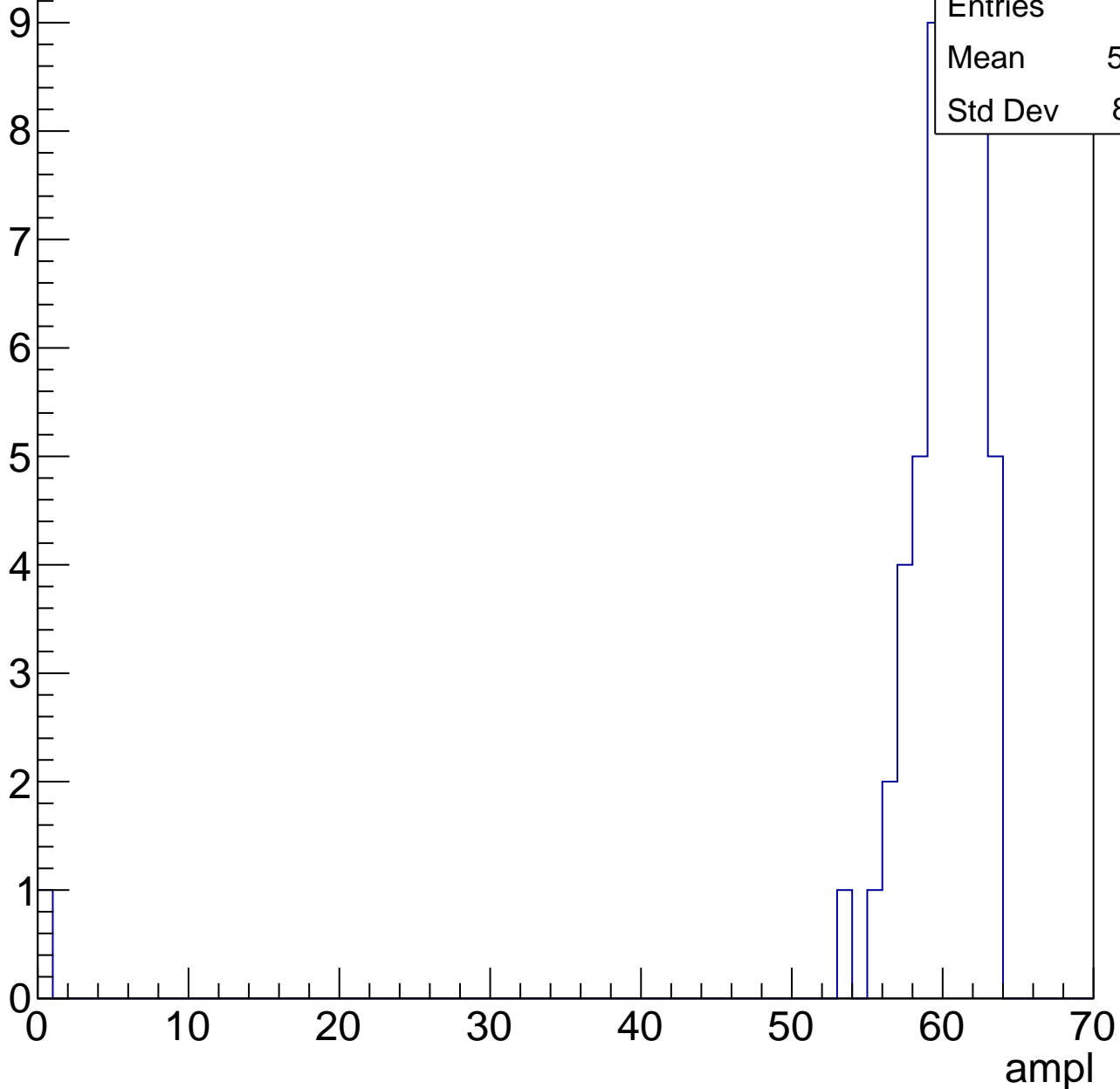


# B1L000S, U24-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

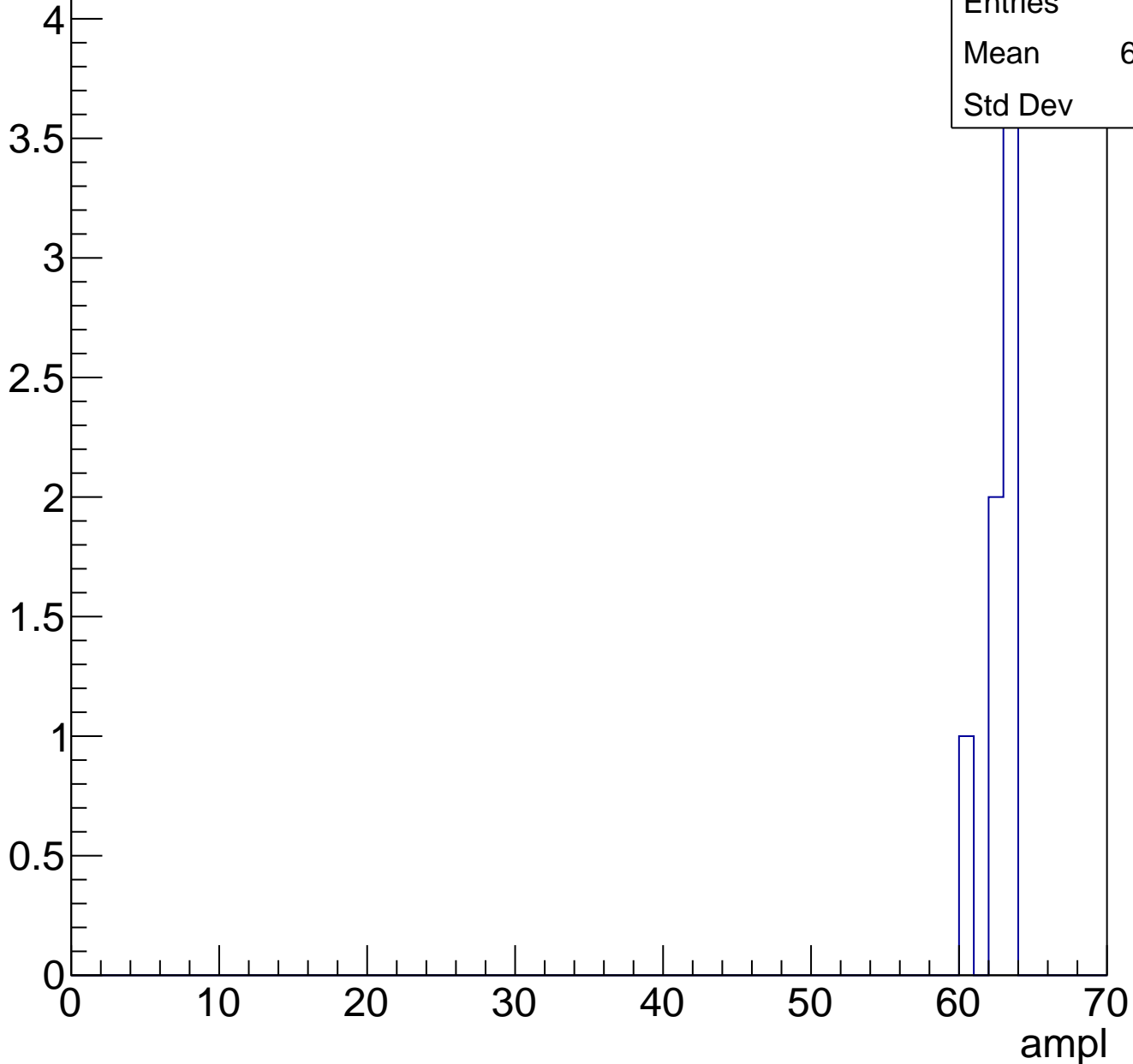
Entries	53
Mean	58.64
Std Dev	8.421



# B1L000S, U24-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U24-ch111, adc0

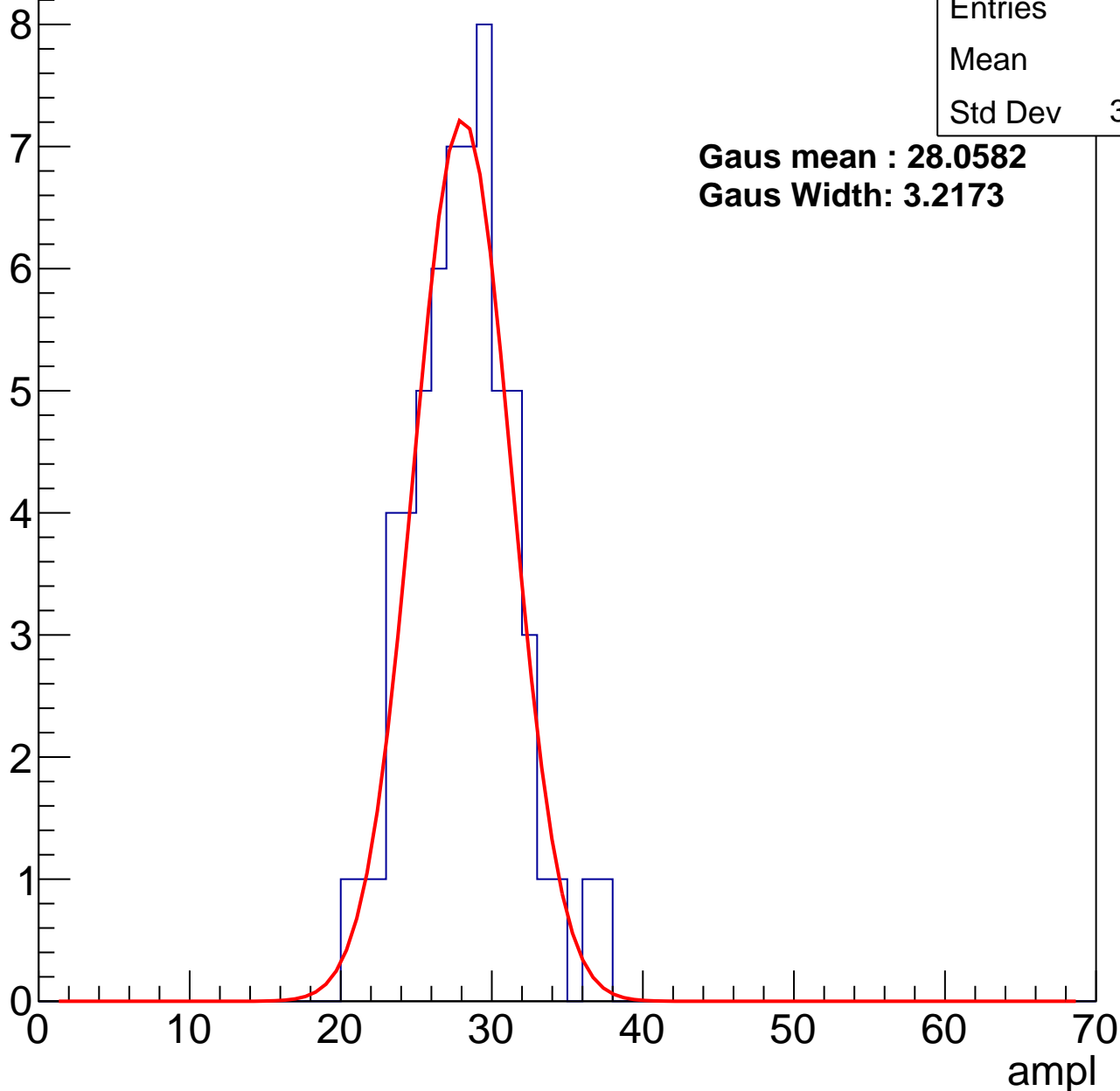
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.7
Std Dev	3.413

**Gaus mean : 28.0582**

**Gaus Width: 3.2173**



# B1L000S, U24-ch111, adc1

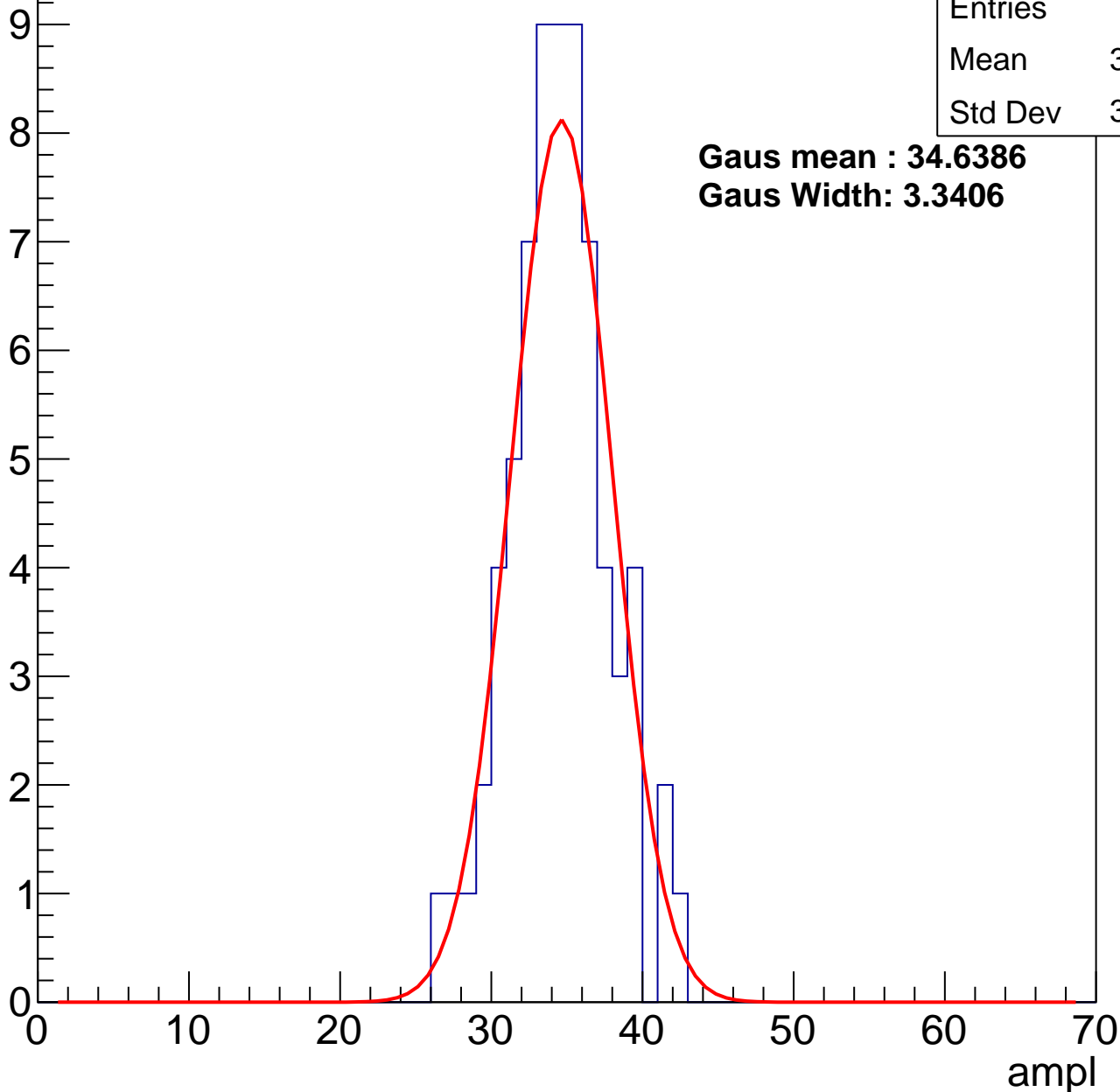
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.06
Std Dev	3.248

**Gaus mean : 34.6386**

**Gaus Width: 3.3406**



# B1L000S, U24-ch111, adc2

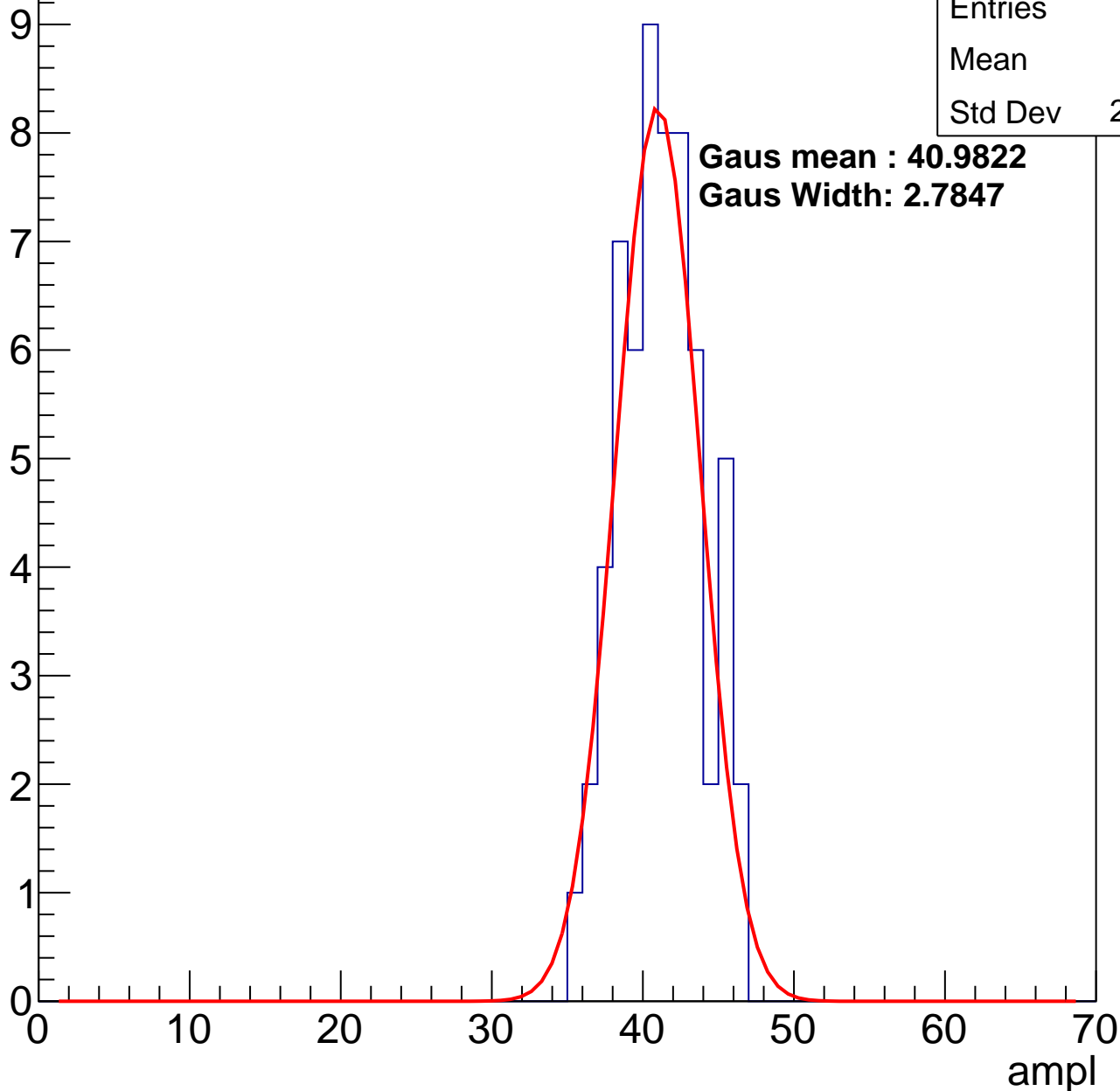
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.7
Std Dev	2.648

**Gaus mean : 40.9822**

**Gaus Width: 2.7847**



# B1L000S, U24-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	47.56
Std Dev	3.336

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

# B1L000S, U24-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

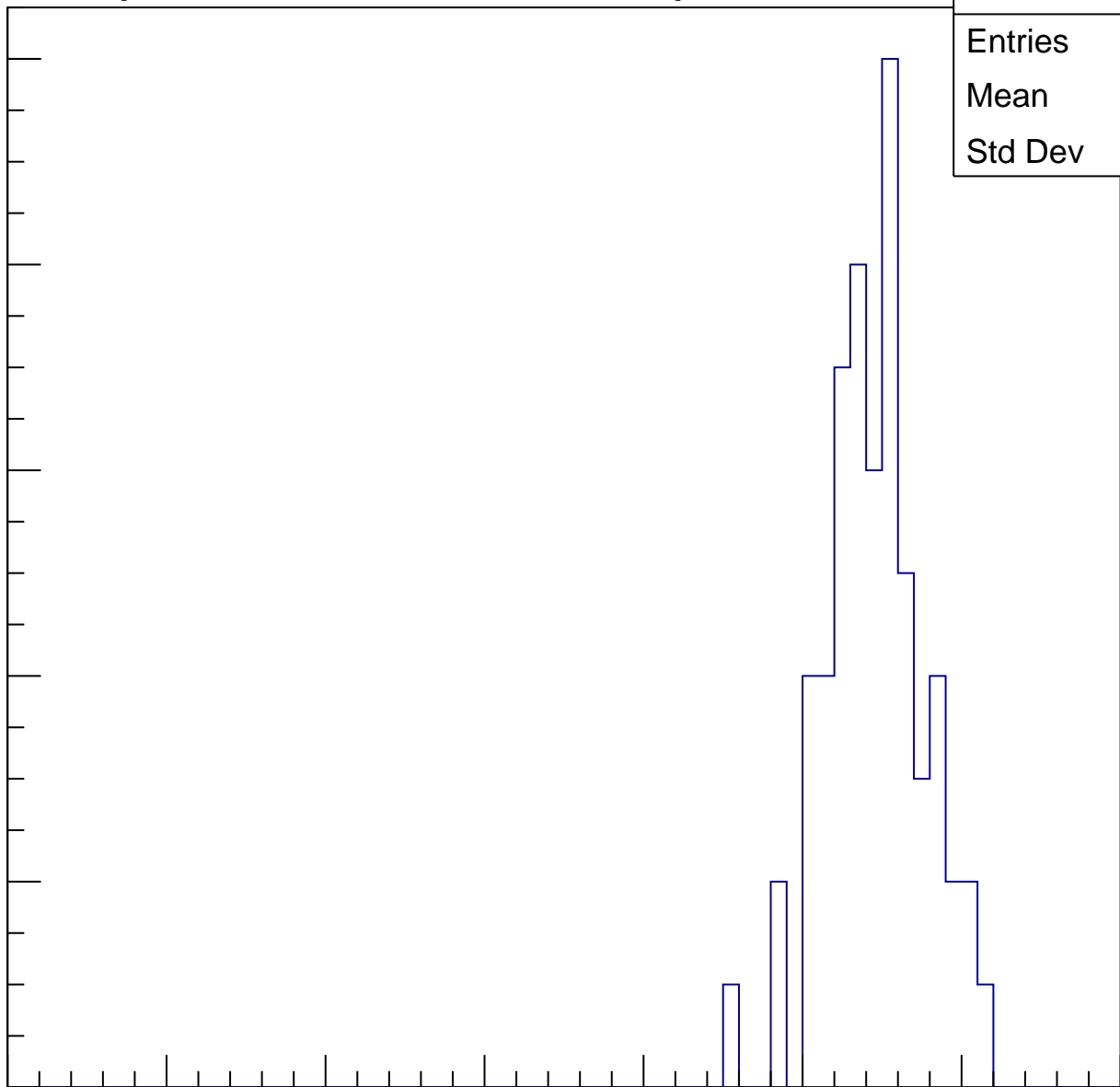
Entries	59
Mean	54.05
Std Dev	3.138

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

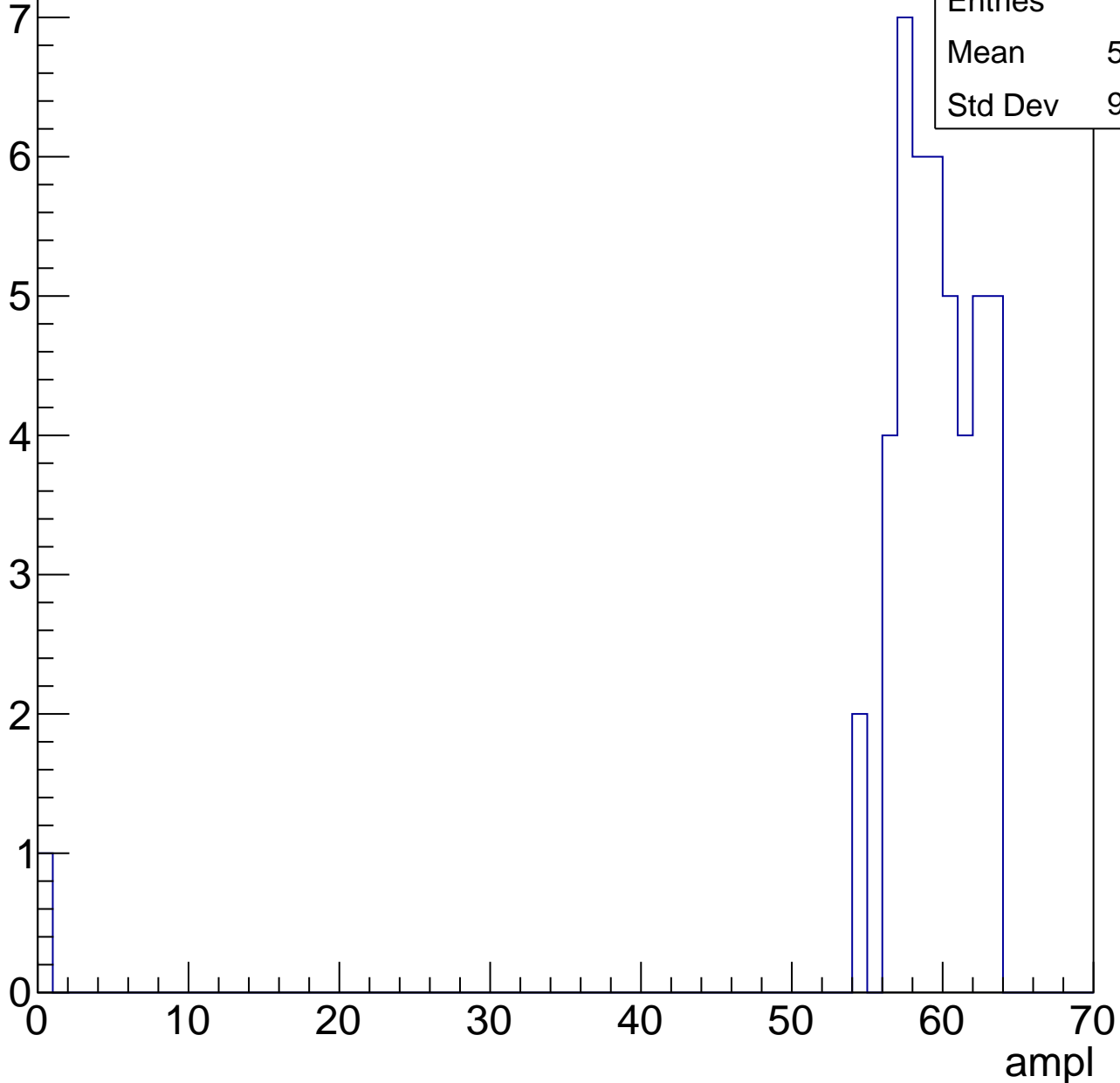


# B1L000S, U24-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	57.82
Std Dev	9.049

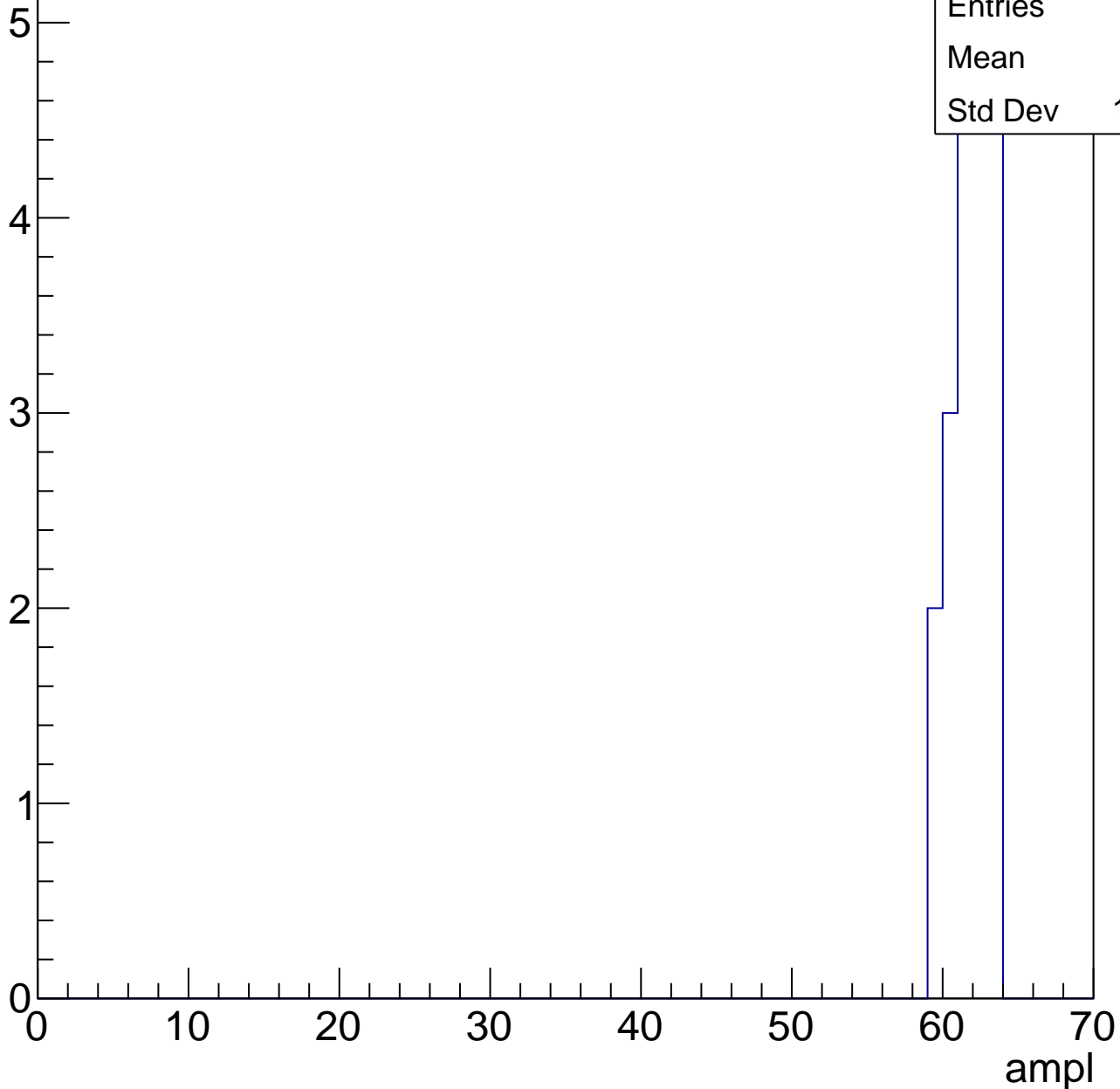


# B1L000S, U24-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.4
Std Dev	1.281





# B1L000S, U24-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U24-ch112, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	28.26
Std Dev	4.441

**Gaus mean : 28.9382**

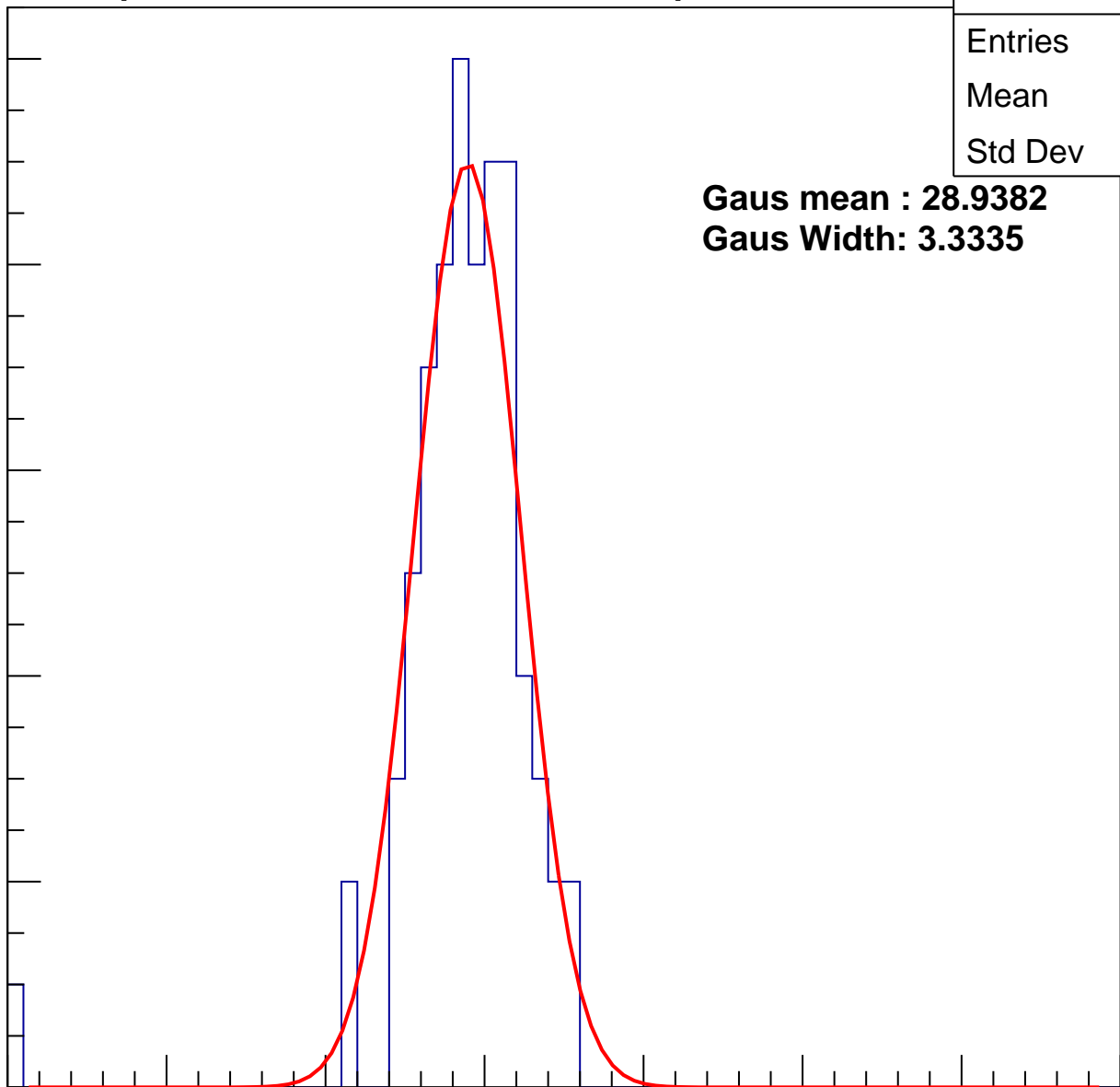
**Gaus Width: 3.3335**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch112, adc1

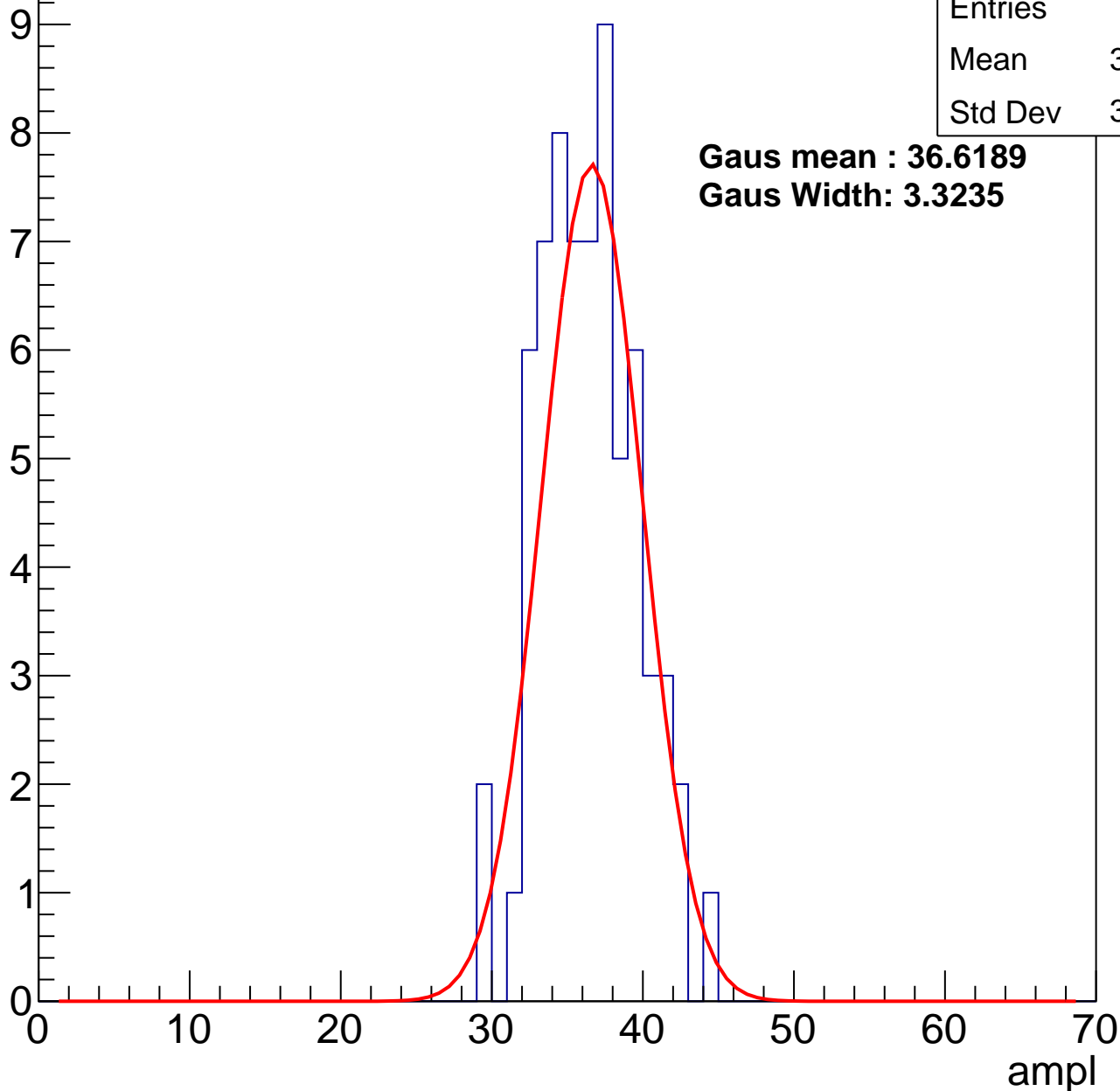
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.96
Std Dev	3.145

**Gaus mean : 36.6189**

**Gaus Width: 3.3235**



# B1L000S, U24-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	42.05
Std Dev	3.311

**Gaus mean : 42.5539**

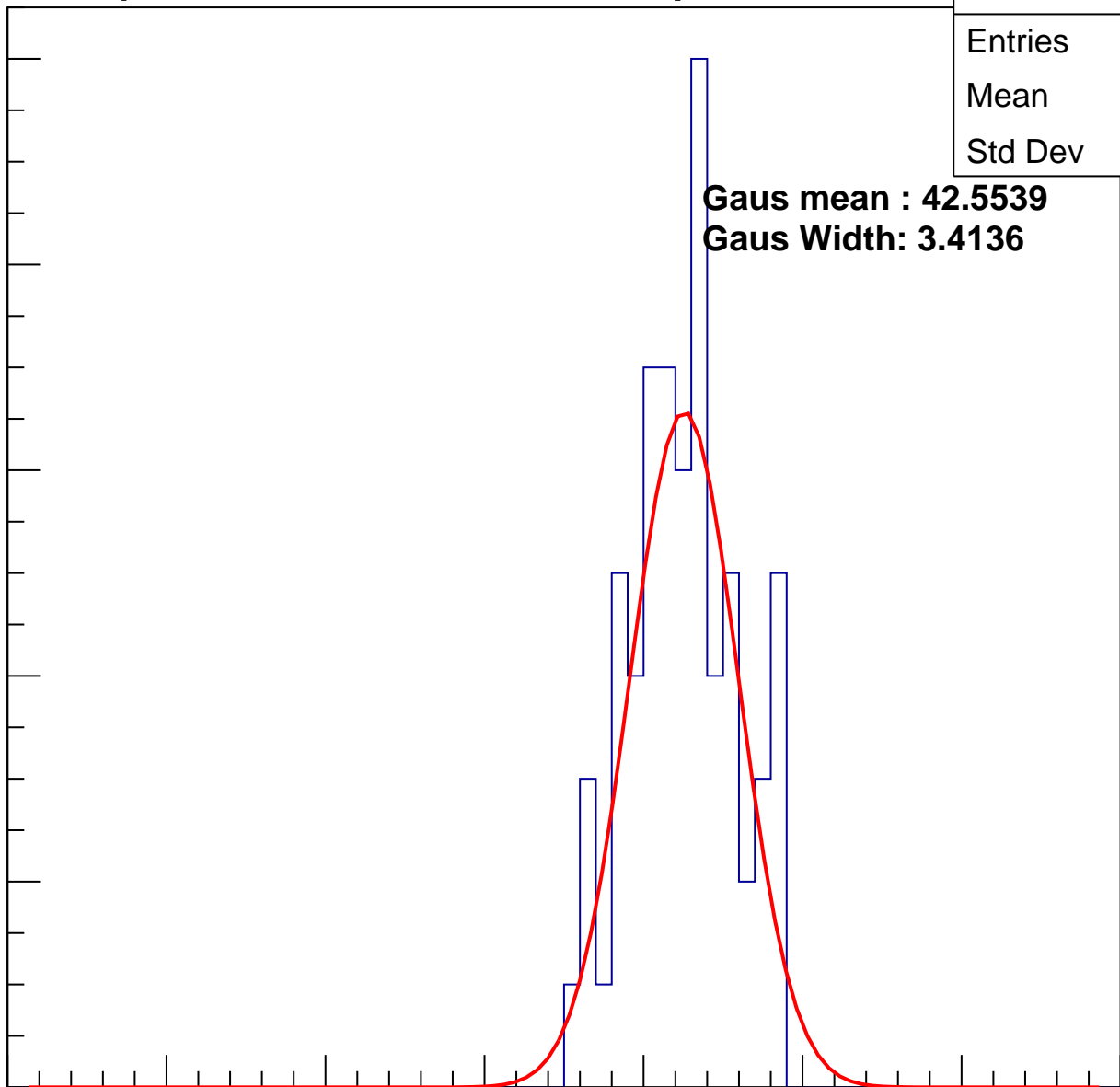
**Gaus Width: 3.4136**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

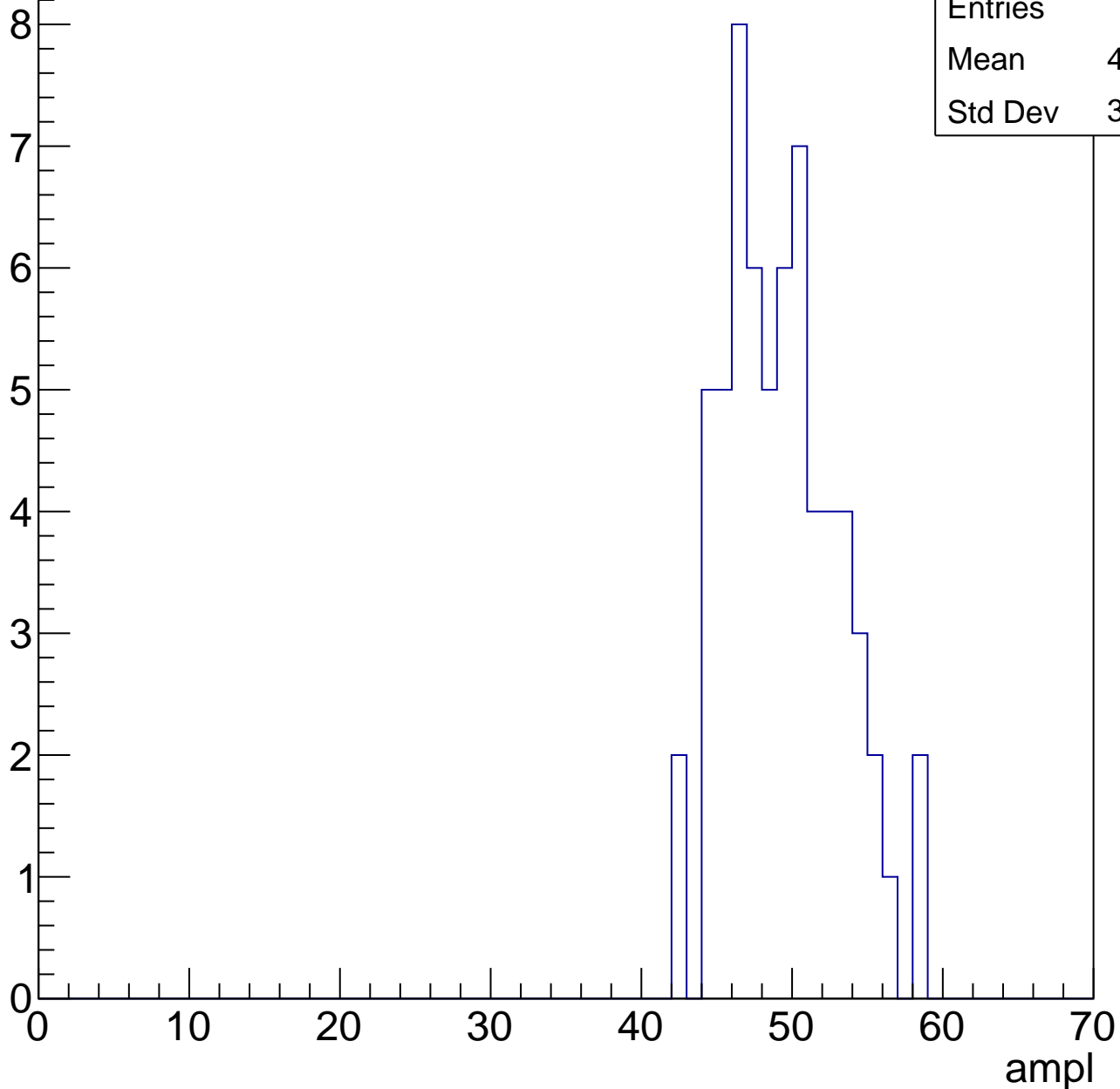


# B1L000S, U24-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.92
Std Dev	3.718

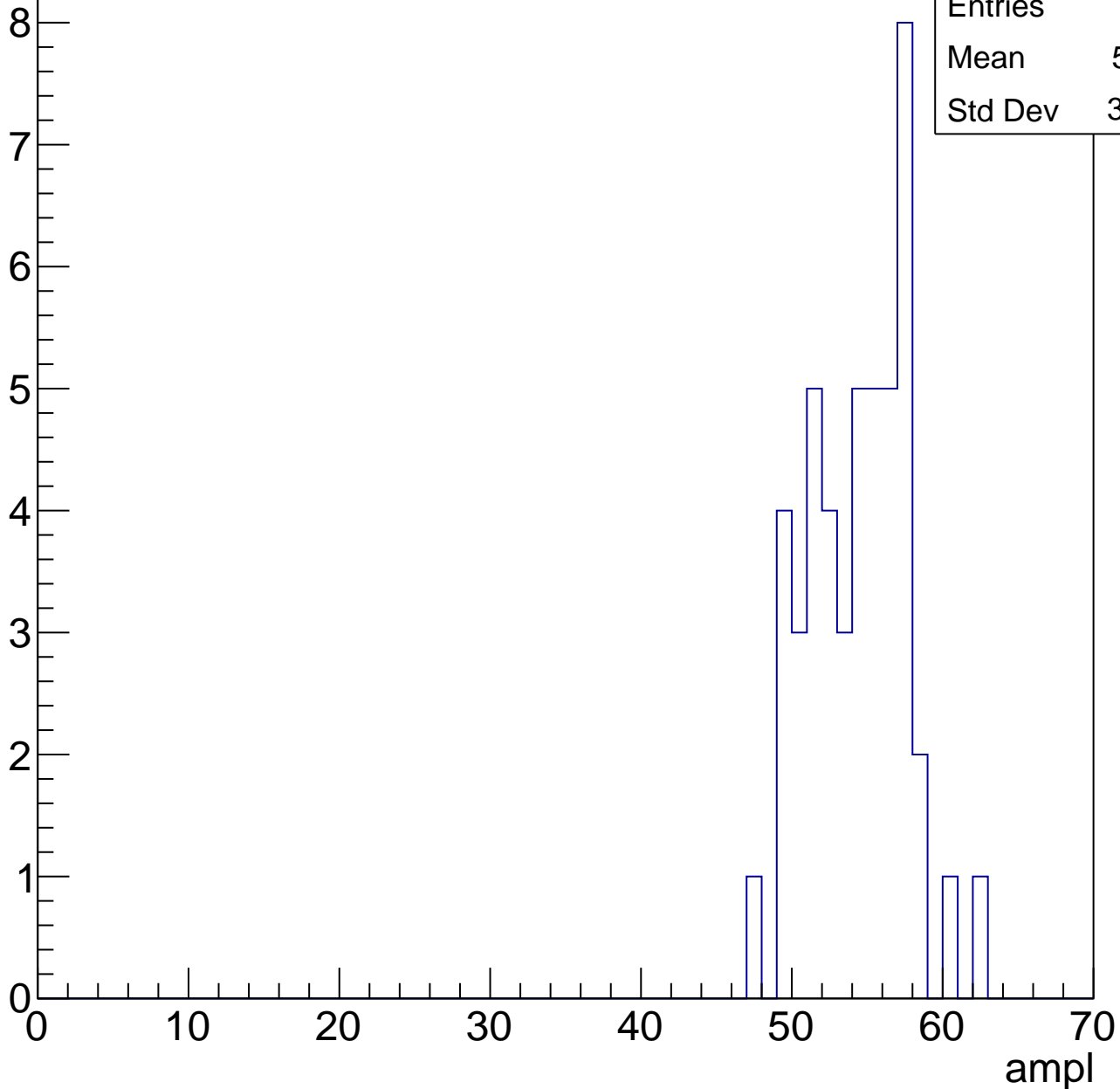


# B1L000S, U24-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	53.91
Std Dev	3.228

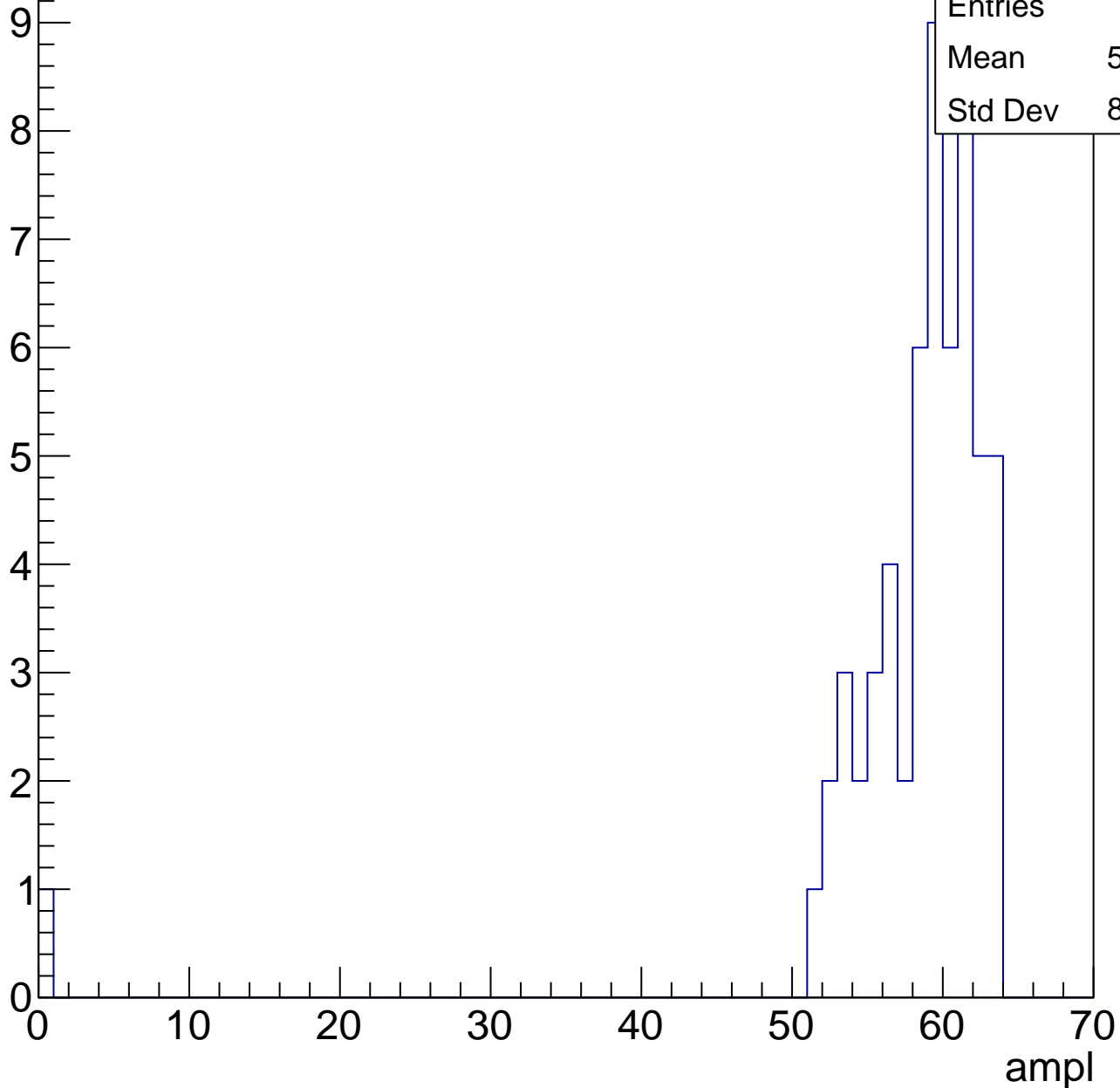


# B1L000S, U24-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	57.55
Std Dev	8.247

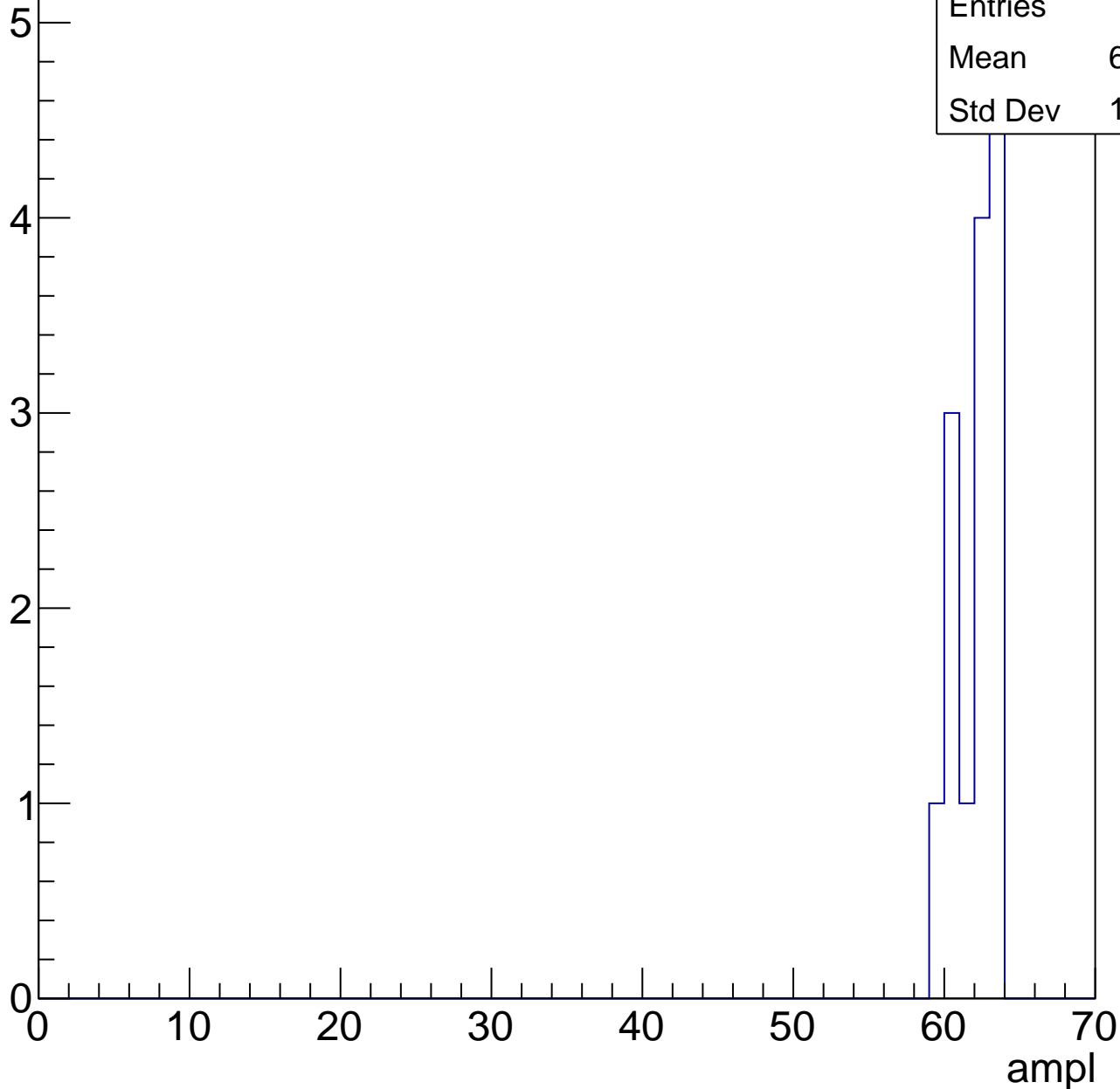


# B1L000S, U24-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.64
Std Dev	1.342





# B1L000S, U24-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch113, adc0

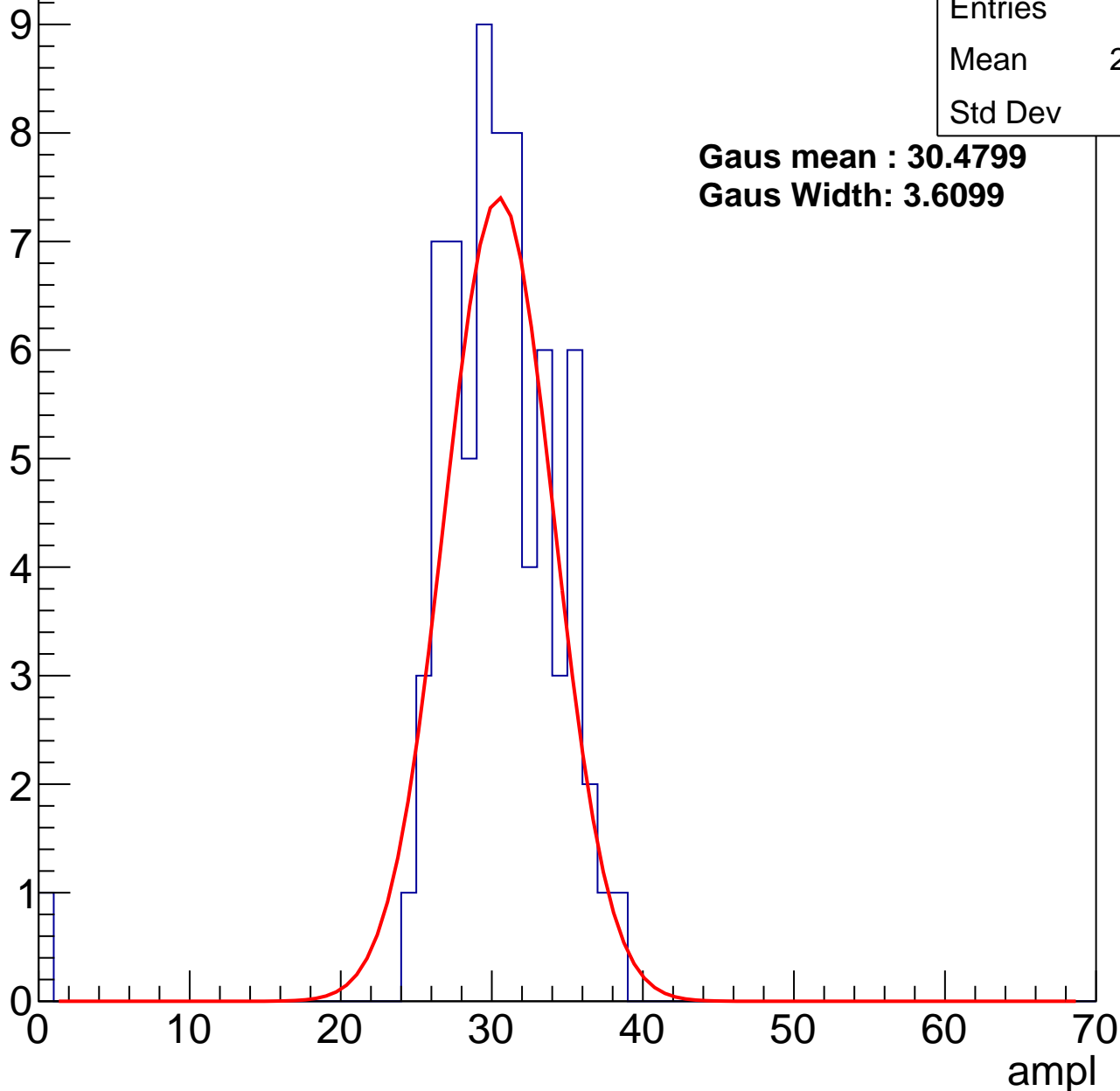
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	29.78
Std Dev	4.82

**Gaus mean : 30.4799**

**Gaus Width: 3.6099**



# B1L000S, U24-ch113, adc1

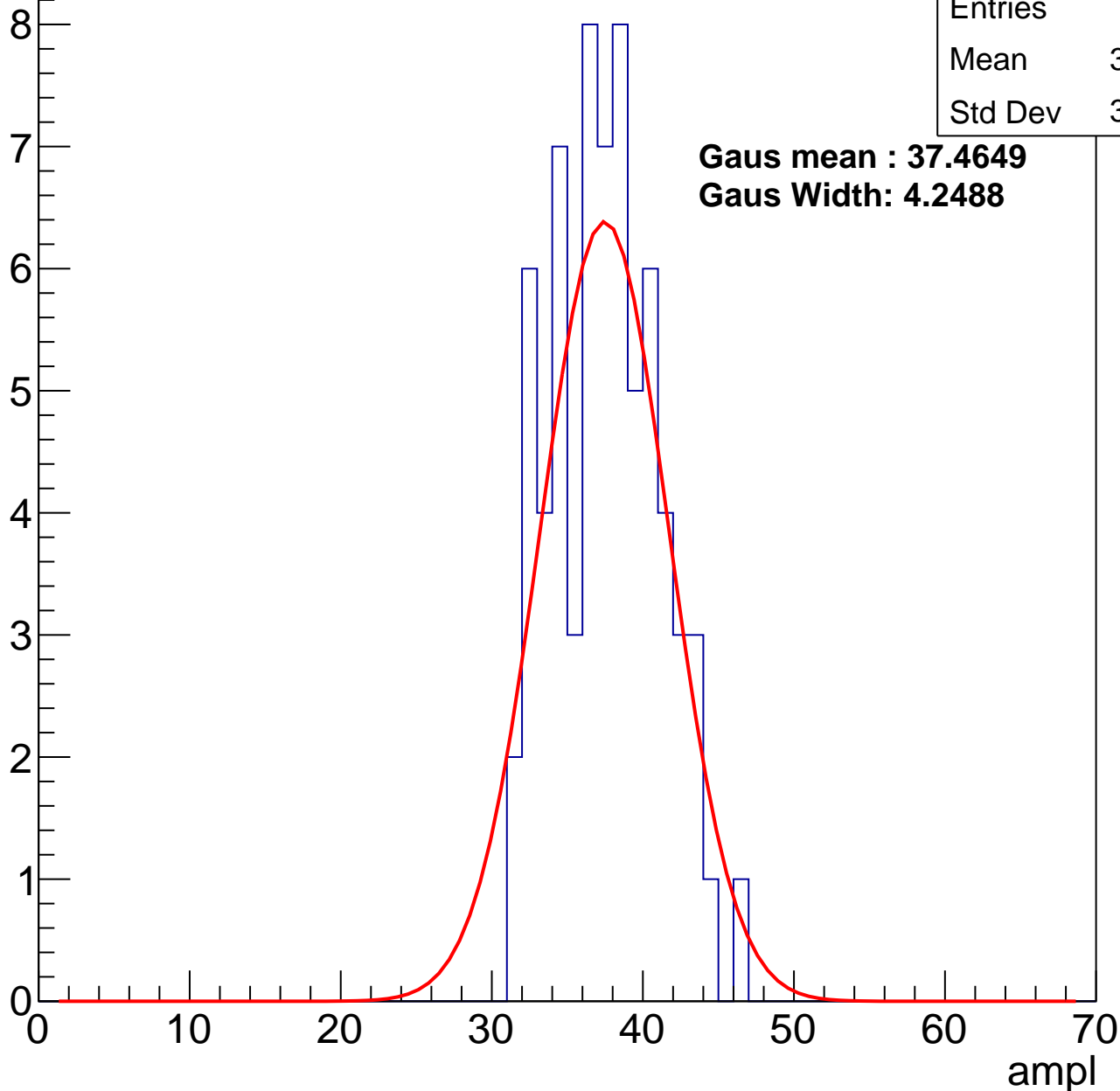
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	37.12
Std Dev	3.487

**Gaus mean : 37.4649**

**Gaus Width: 4.2488**



# B1L000S, U24-ch113, adc2

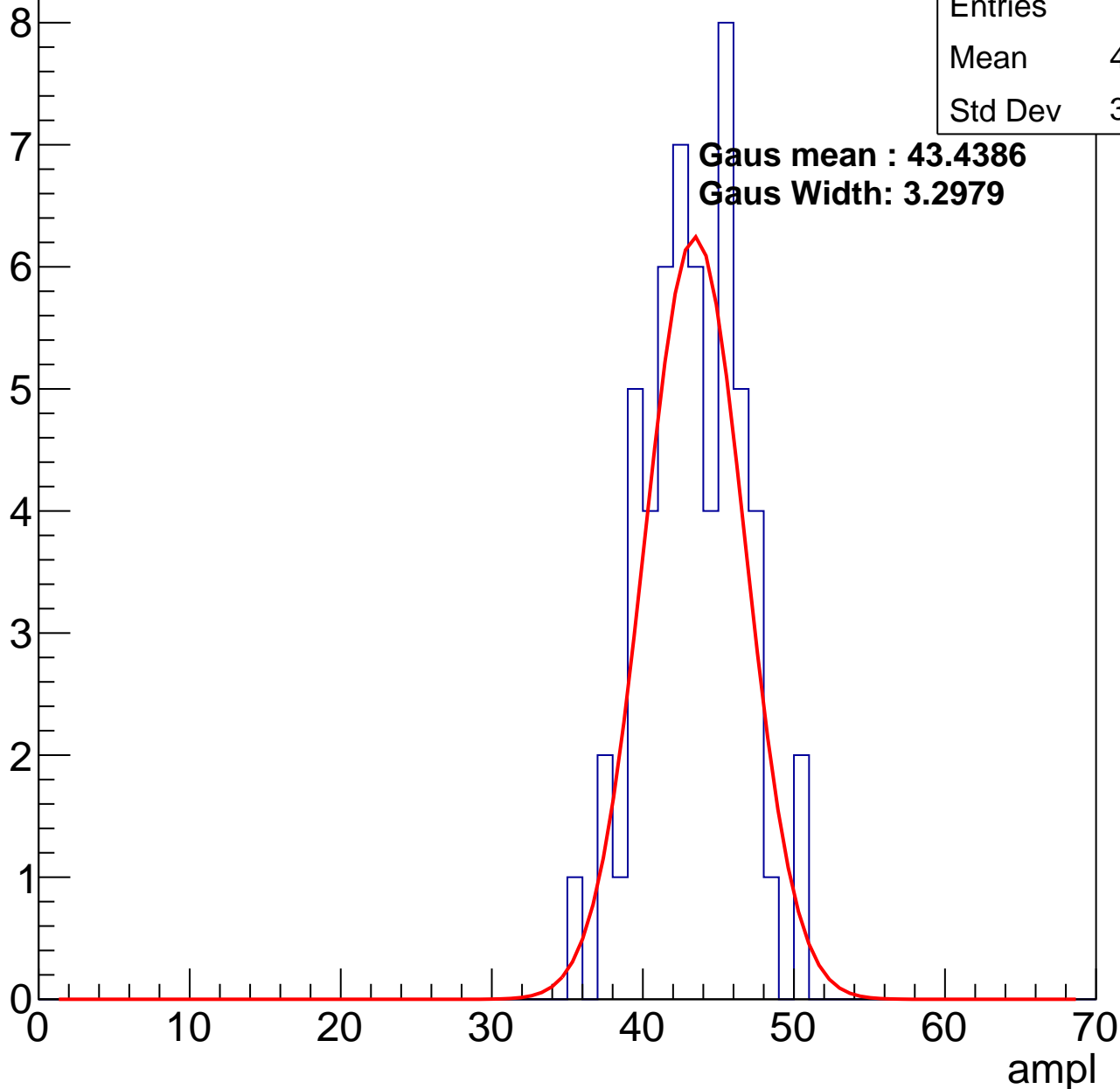
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.89
Std Dev	3.205

**Gaus mean : 43.4386**

**Gaus Width: 3.2979**

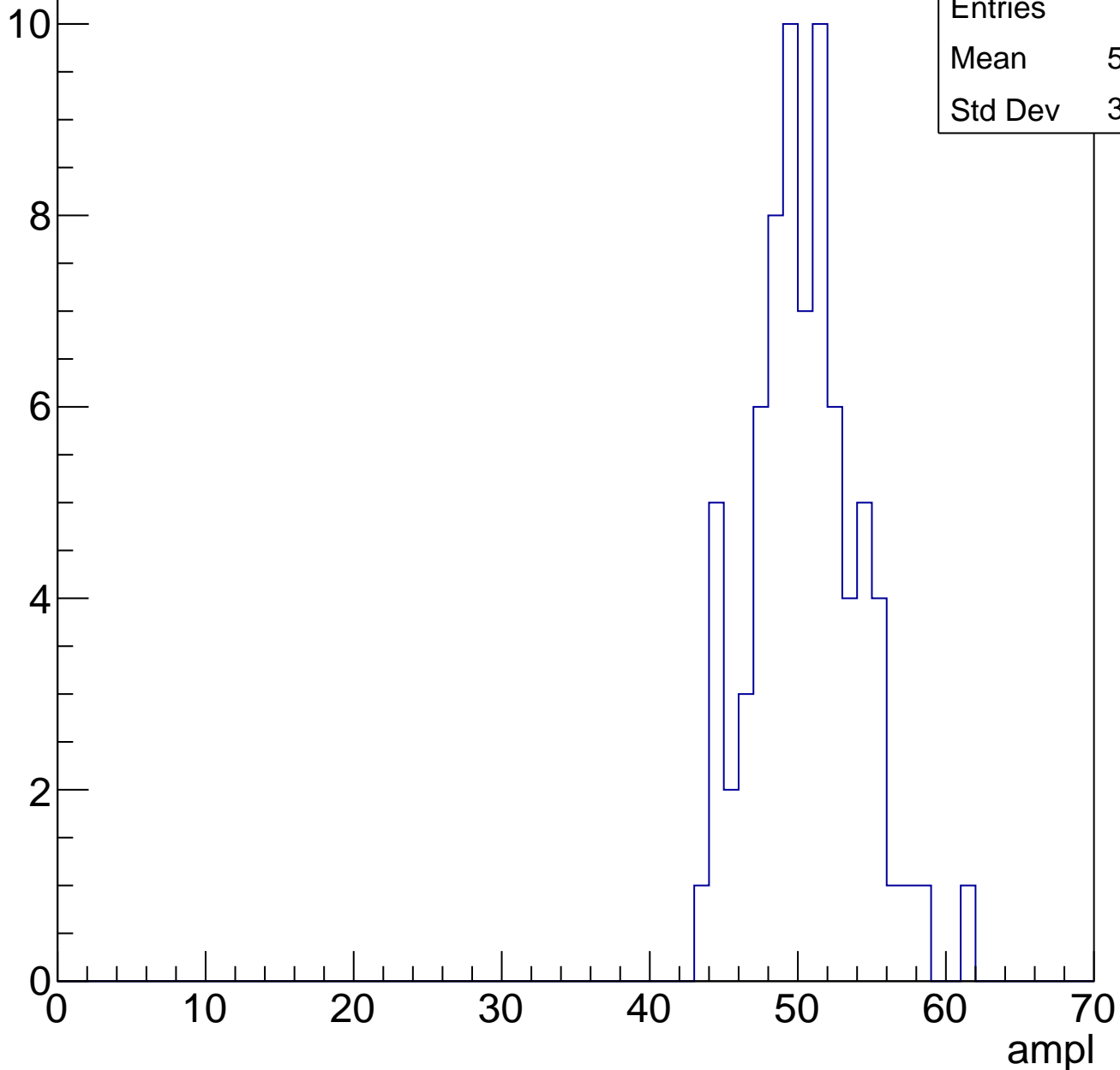


# B1L000S, U24-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	50.04
Std Dev	3.546

Entry

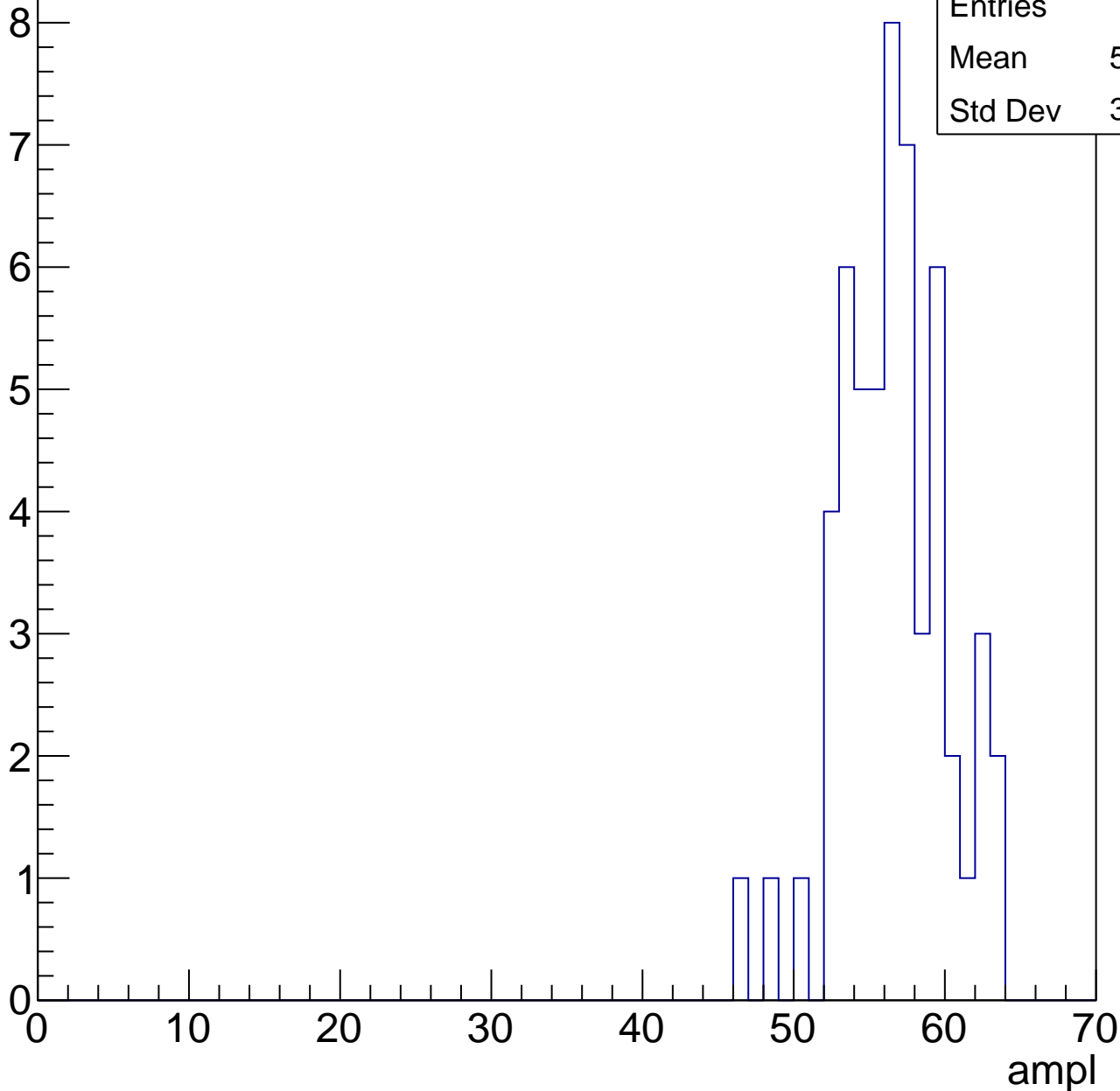


# B1L000S, U24-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

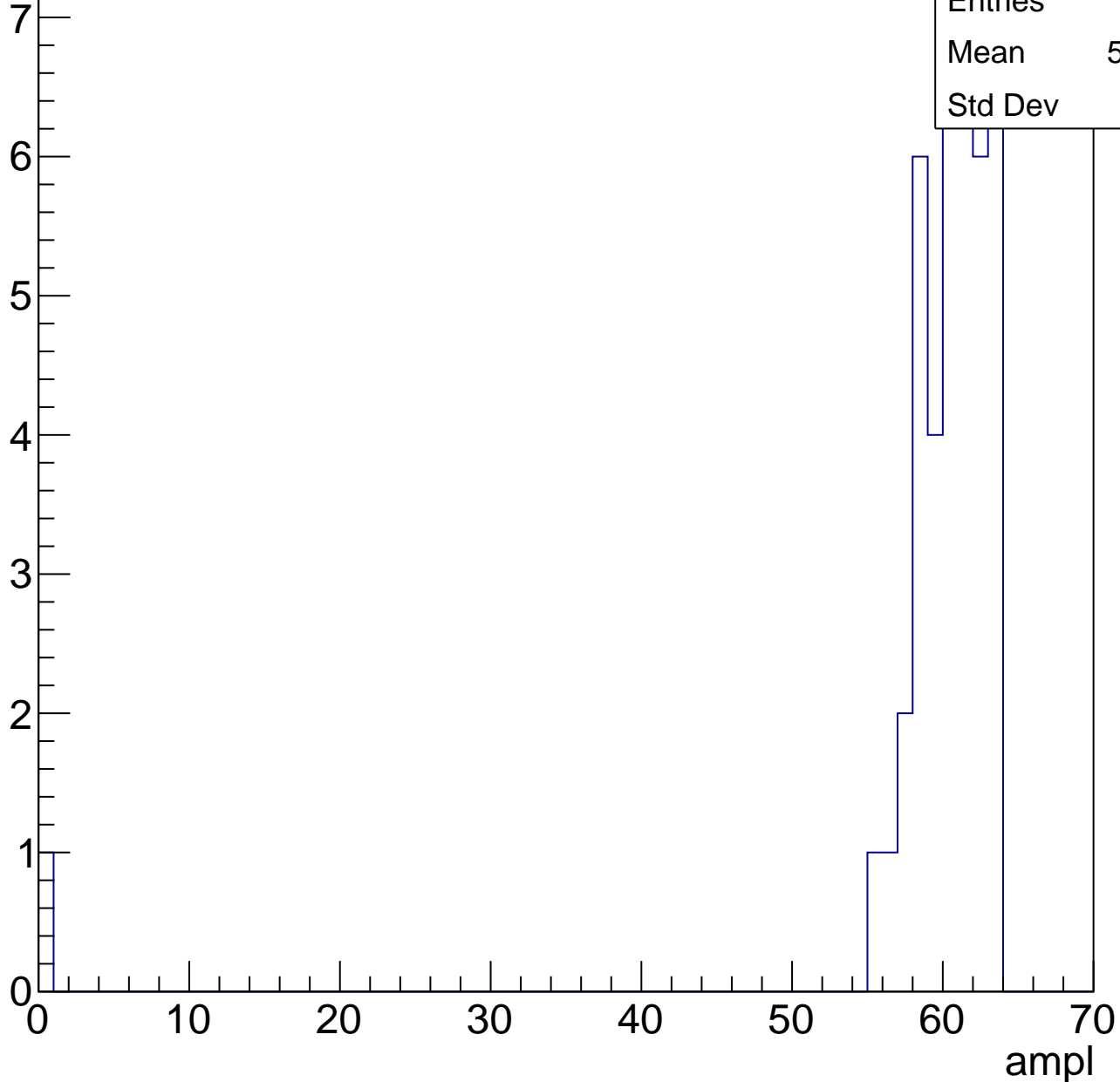
Entries	55
Mean	56.05
Std Dev	3.513



# B1L000S, U24-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U24-ch114, adc0

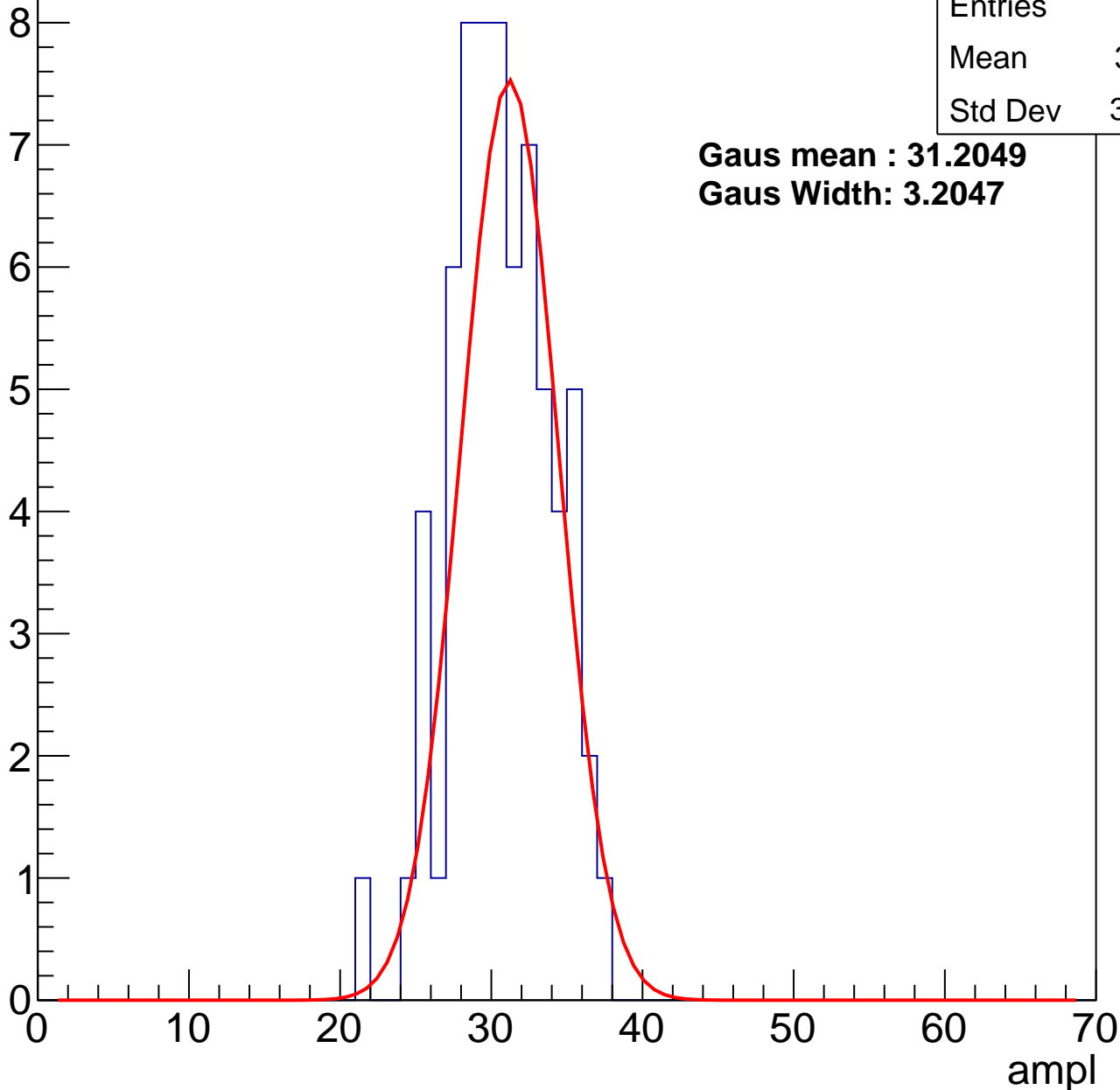
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	30.21
Std Dev	3.262

**Gaus mean : 31.2049**

**Gaus Width: 3.2047**



# B1L000S, U24-ch114, adc1

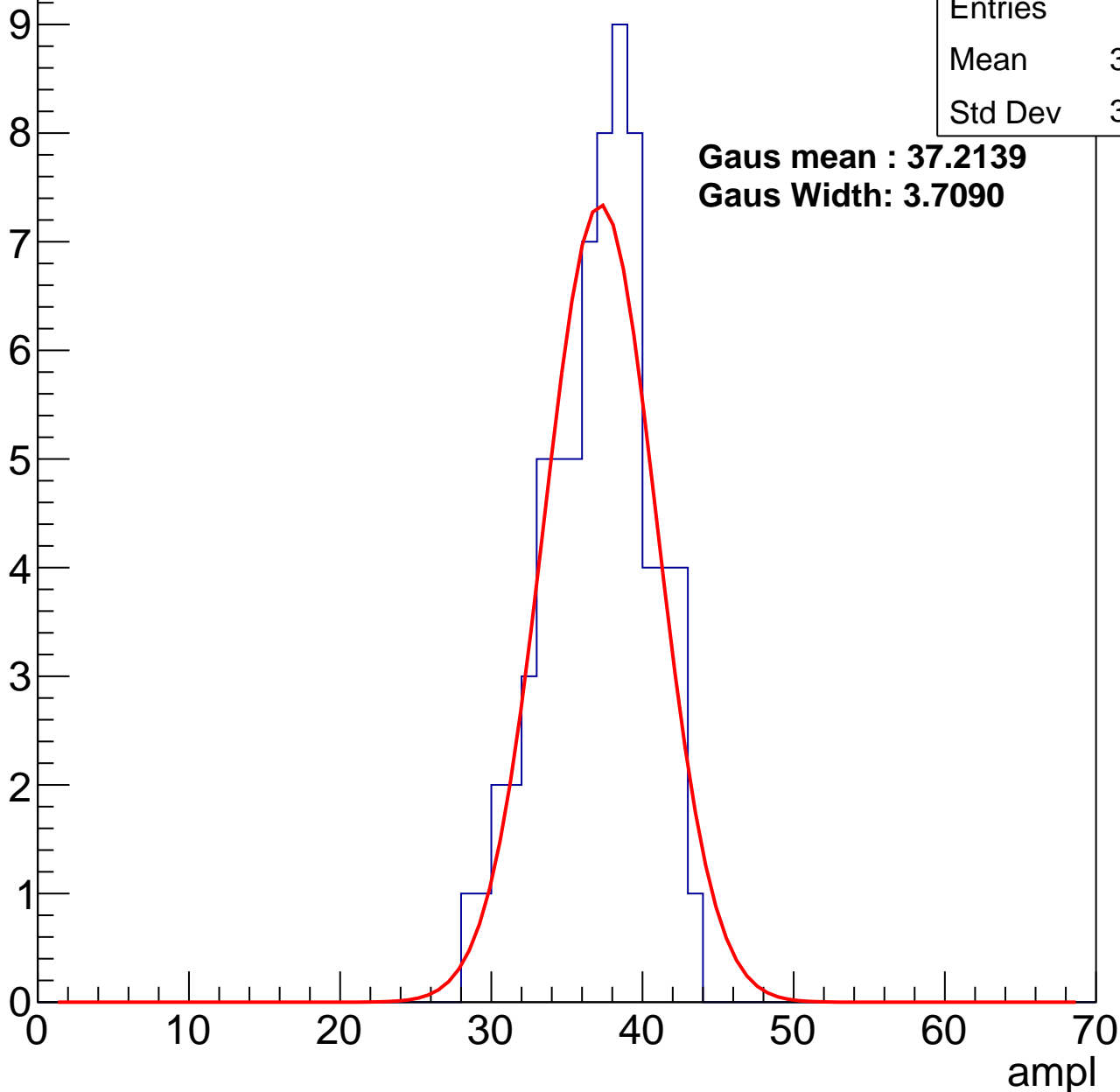
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	36.55
Std Dev	3.416

**Gaus mean : 37.2139**

**Gaus Width: 3.7090**



# B1L000S, U24-ch114, adc2

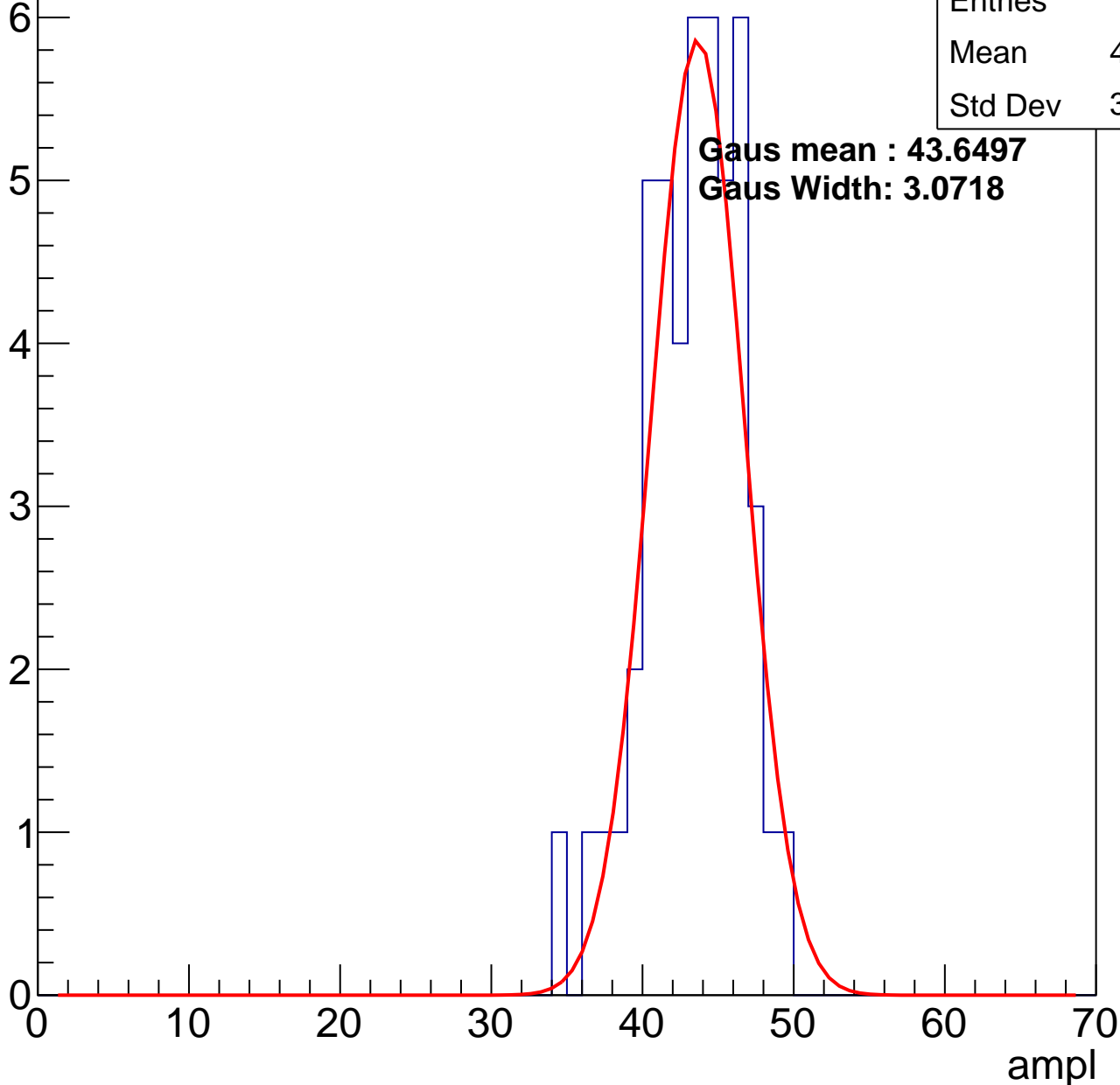
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	42.85
Std Dev	3.162

**Gaus mean : 43.6497**

**Gaus Width: 3.0718**

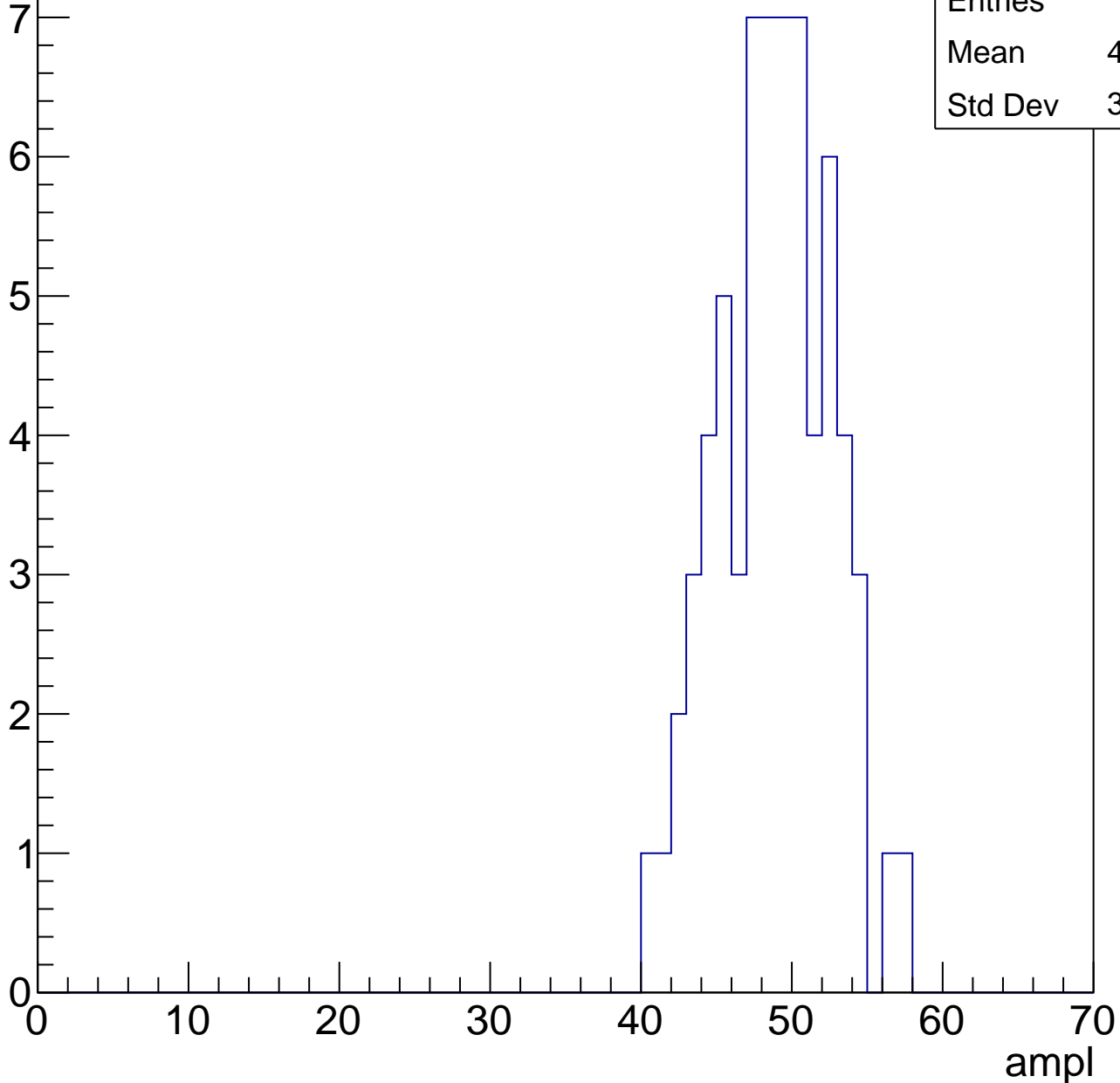


# B1L000S, U24-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	48.39
Std Dev	3.688

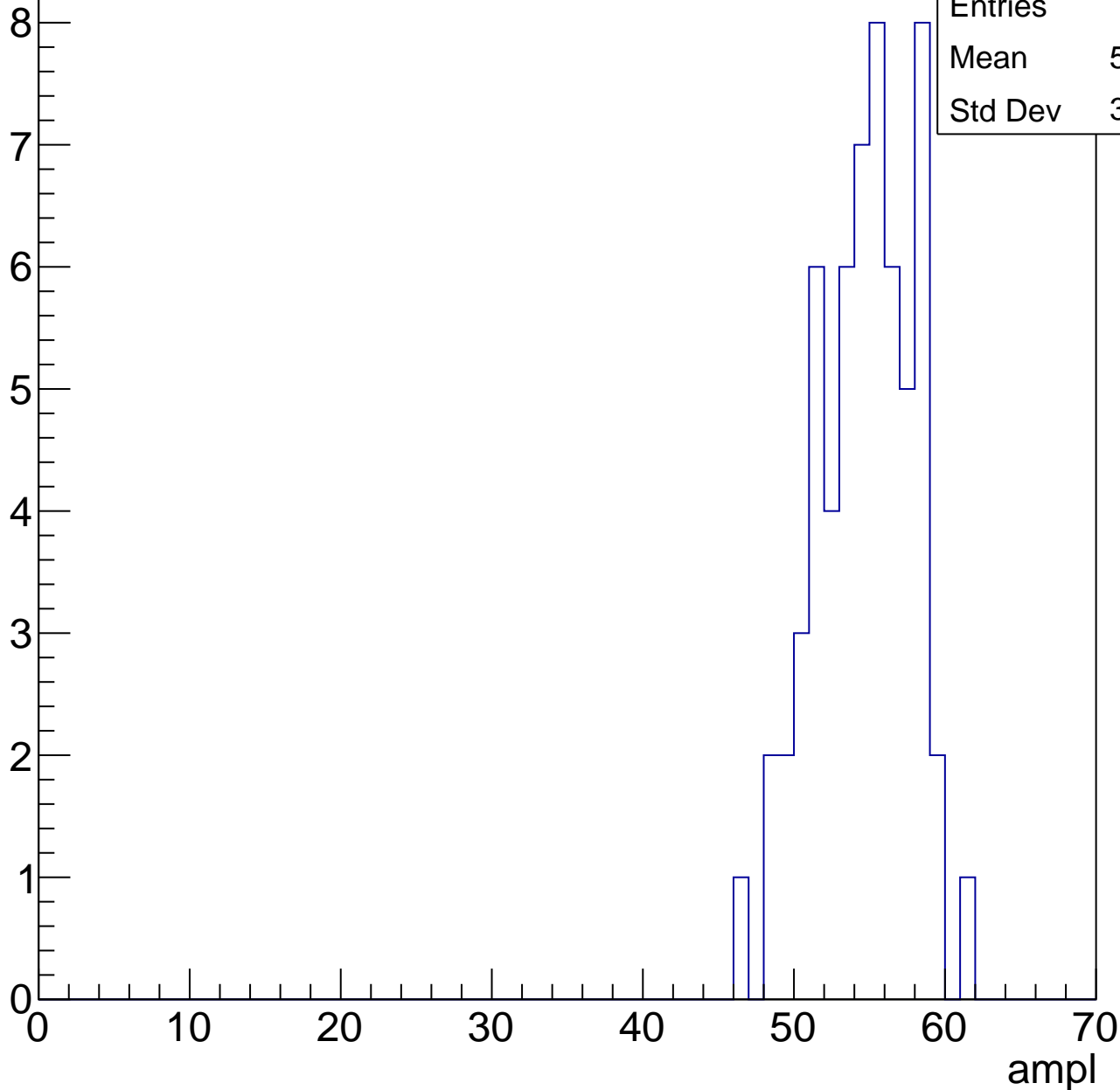


# B1L000S, U24-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	54.16
Std Dev	3.168

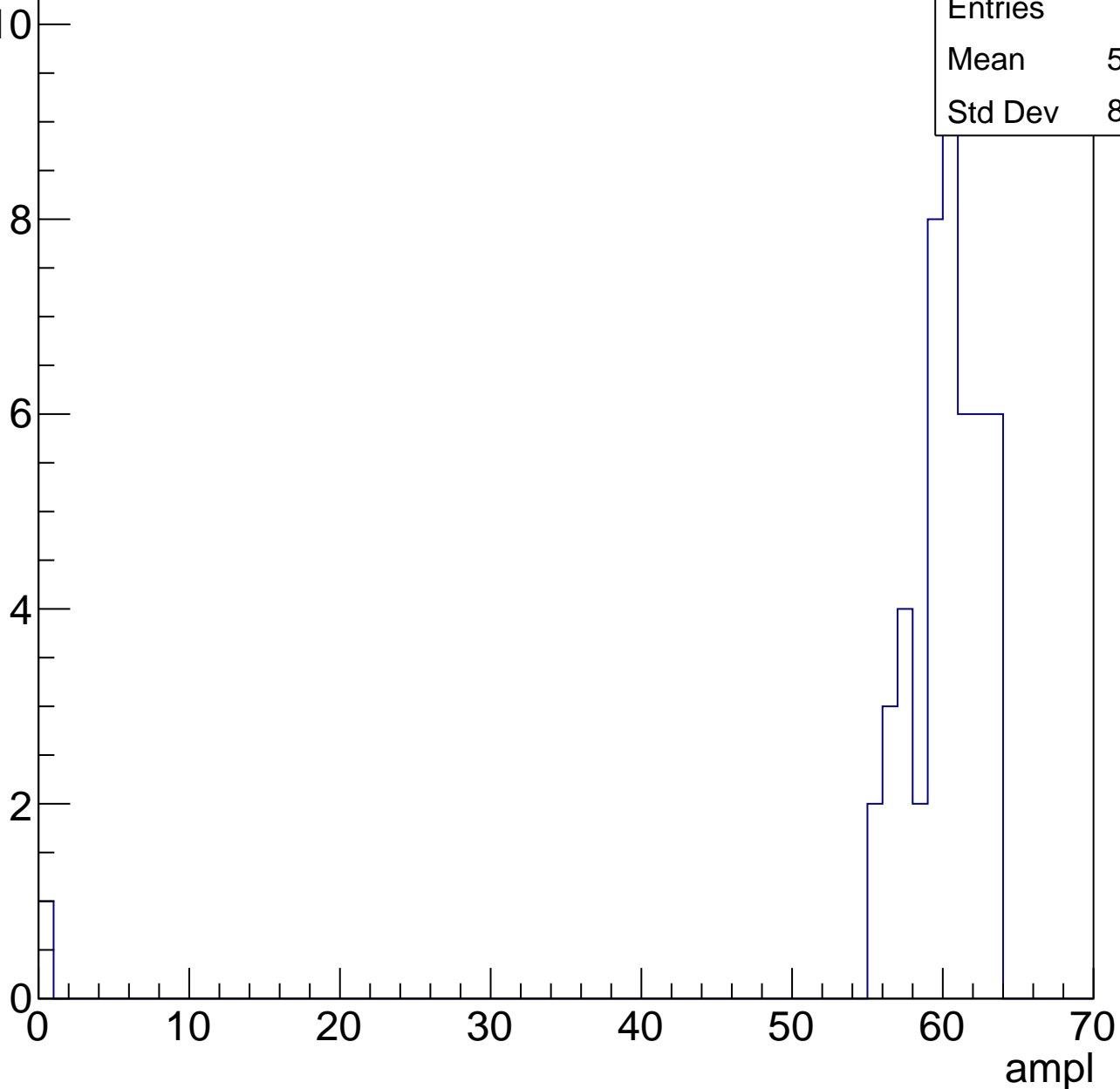


# B1L000S, U24-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

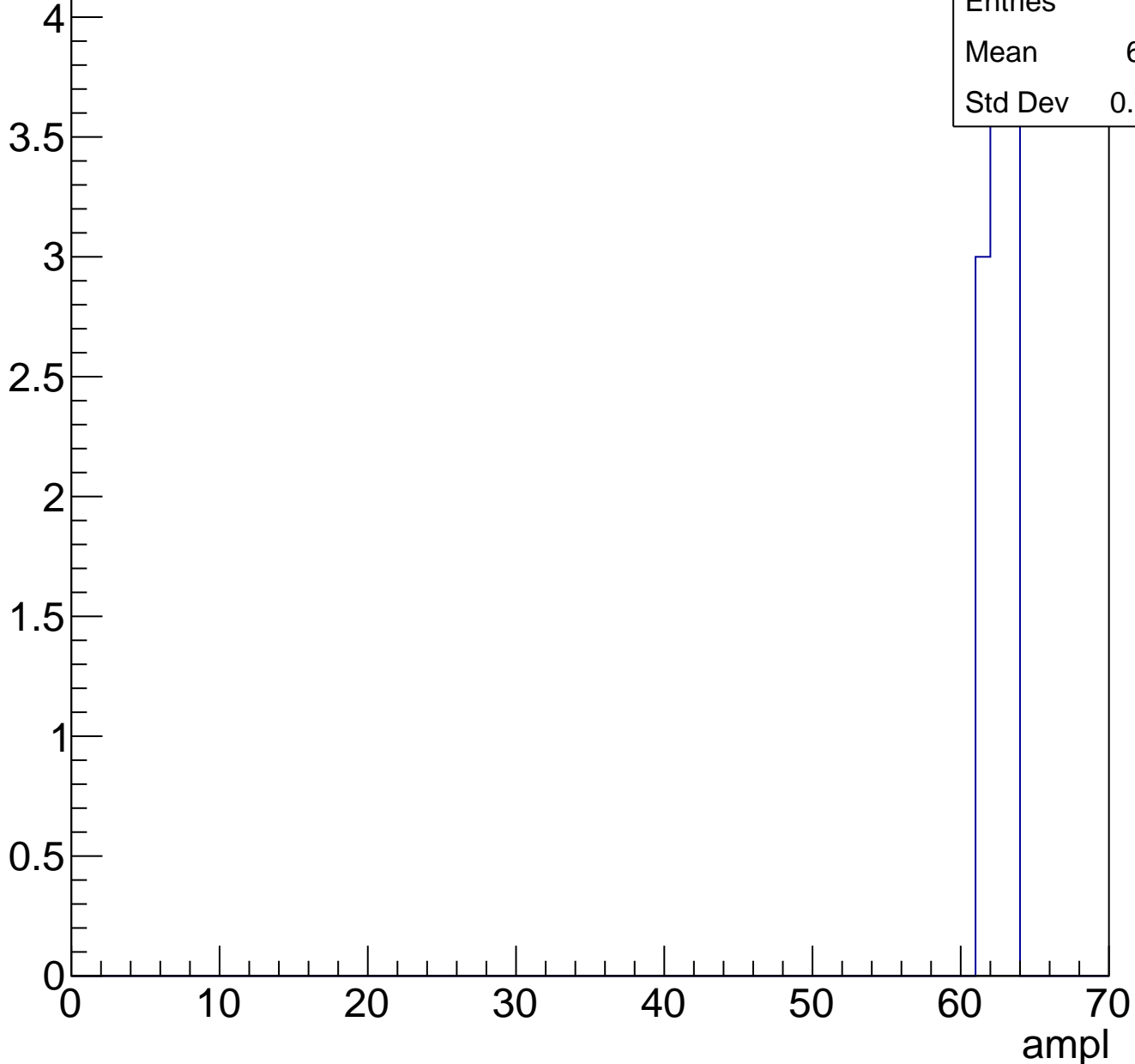
Entries	48
Mean	58.54
Std Dev	8.817



# B1L000S, U24-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	11
Mean	62.09
Std Dev	0.7925



# B1L000S, U24-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch115, adc0

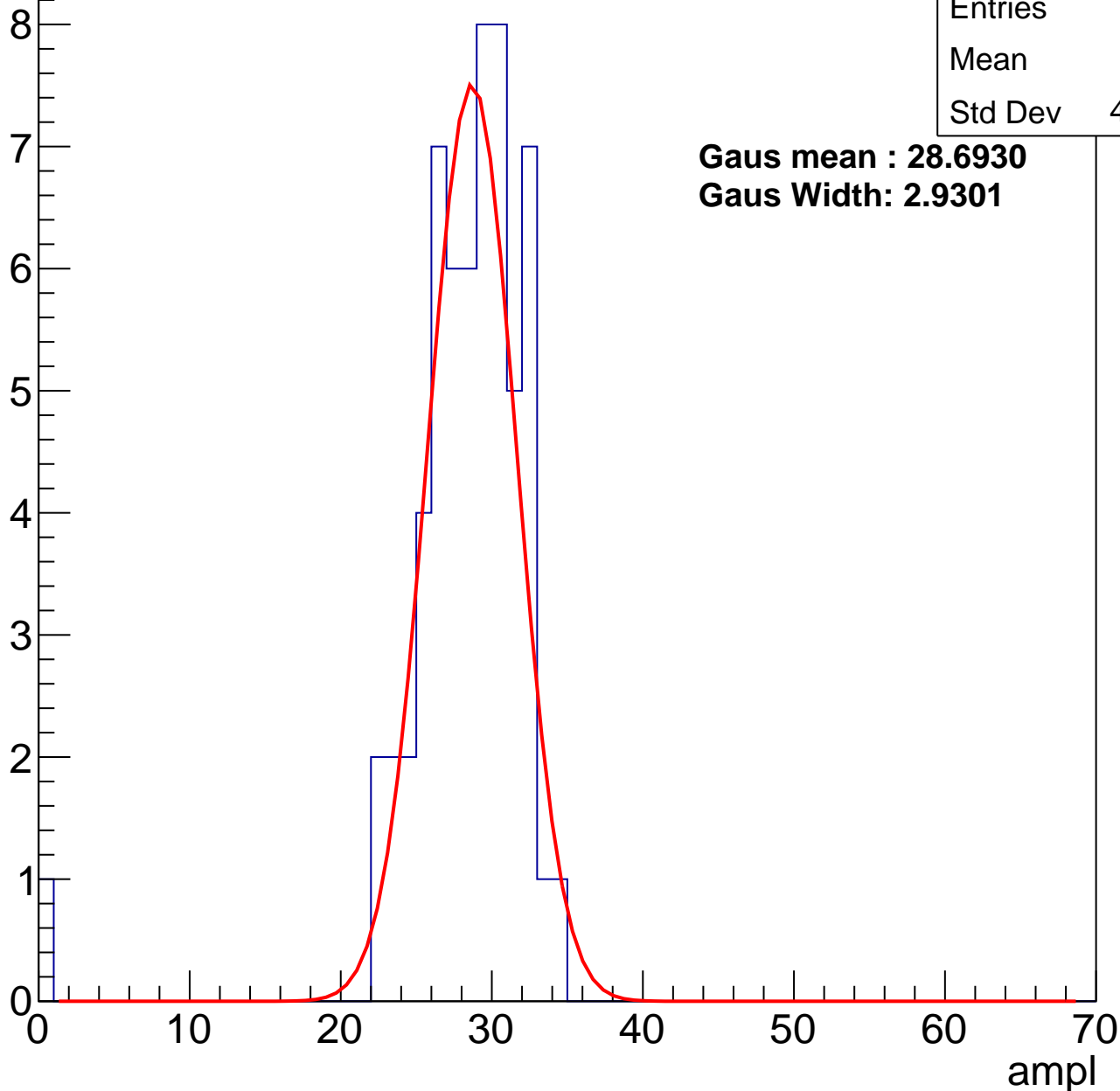
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.8
Std Dev	4.589

**Gaus mean : 28.6930**

**Gaus Width: 2.9301**



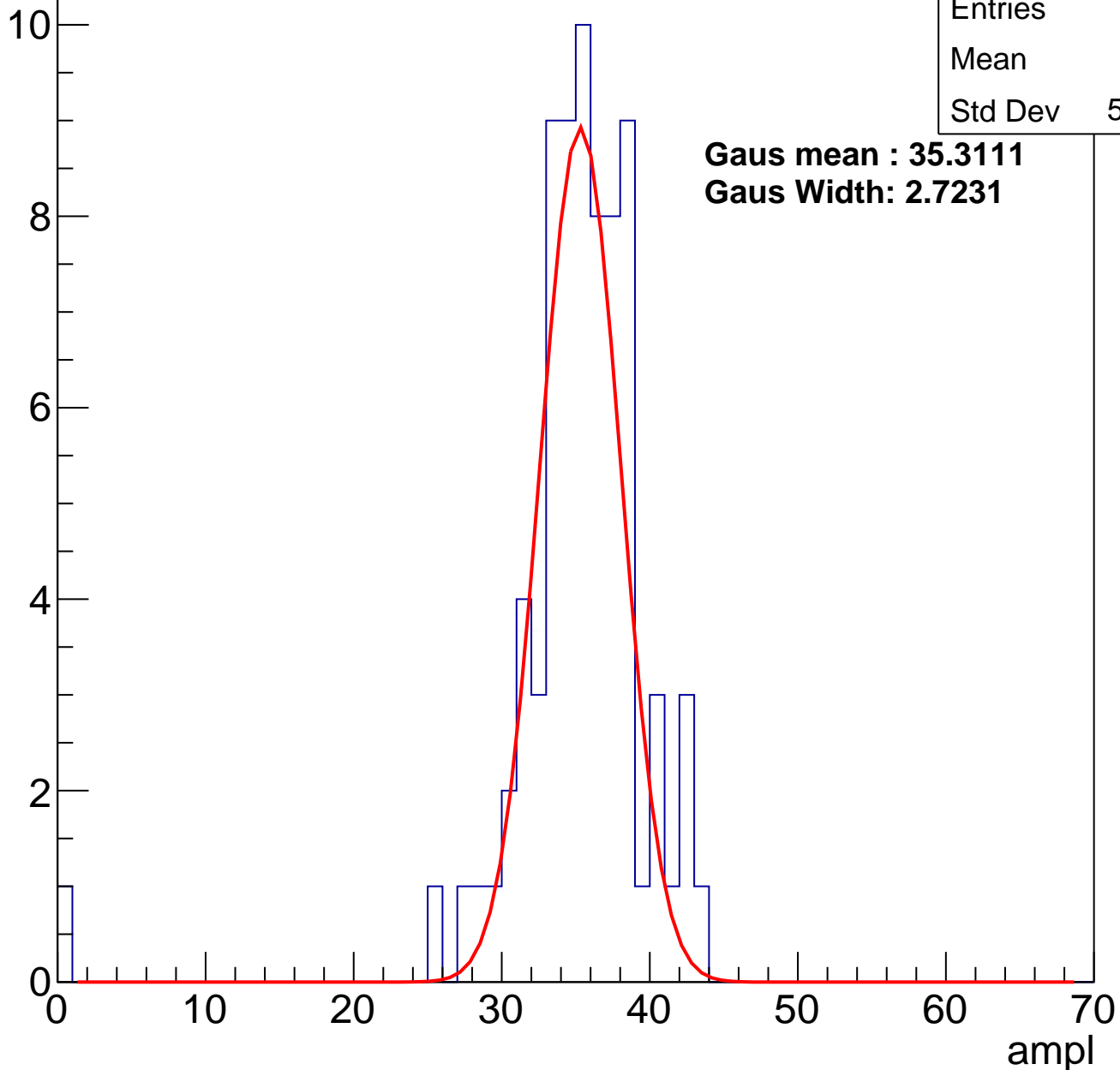
# B1L000S, U24-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	34.7
Std Dev	5.272

**Gaus mean : 35.3111**  
**Gaus Width: 2.7231**

Entry



# B1L000S, U24-ch115, adc2

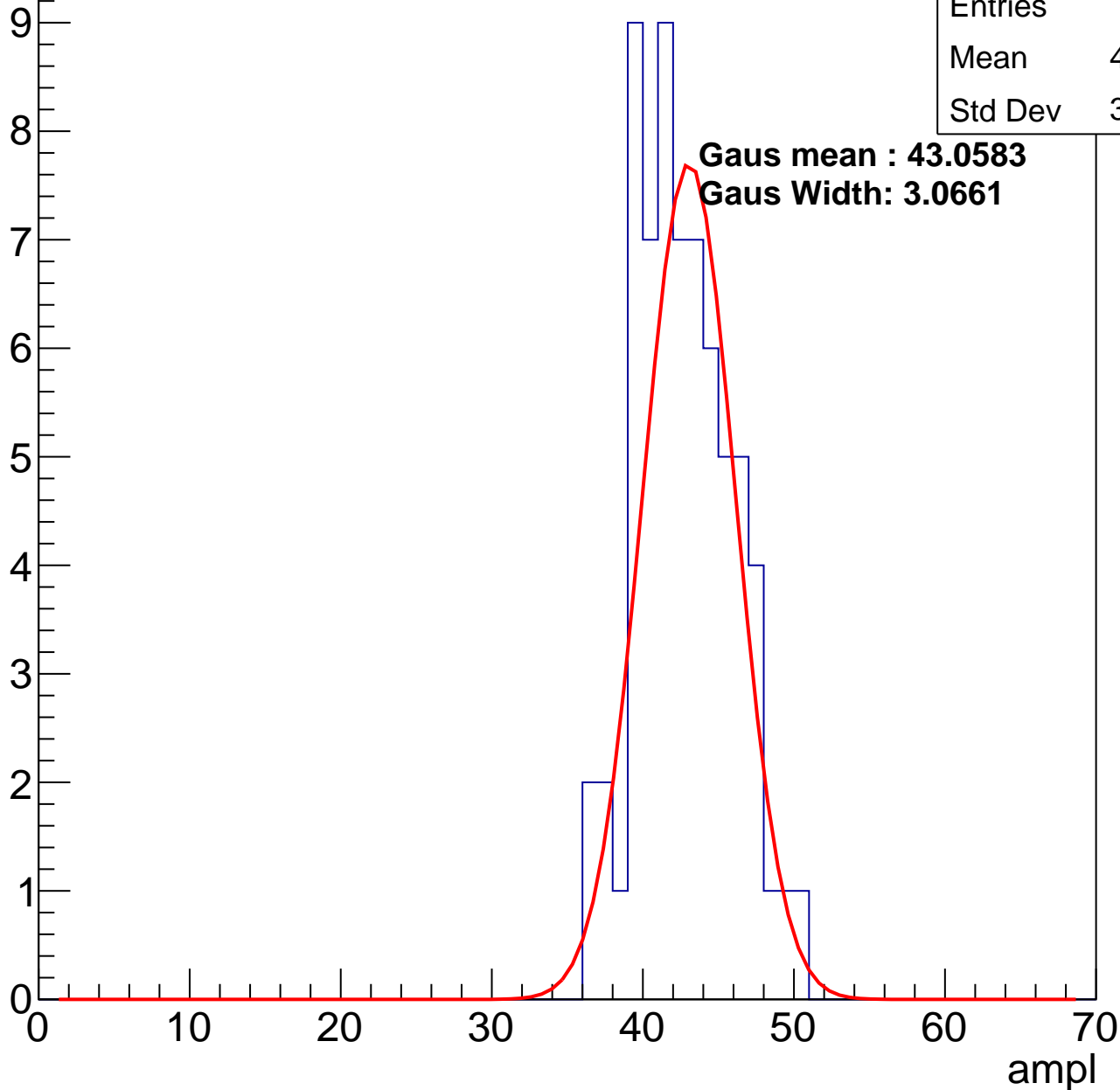
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.28
Std Dev	3.128

**Gaus mean : 43.0583**

**Gaus Width: 3.0661**

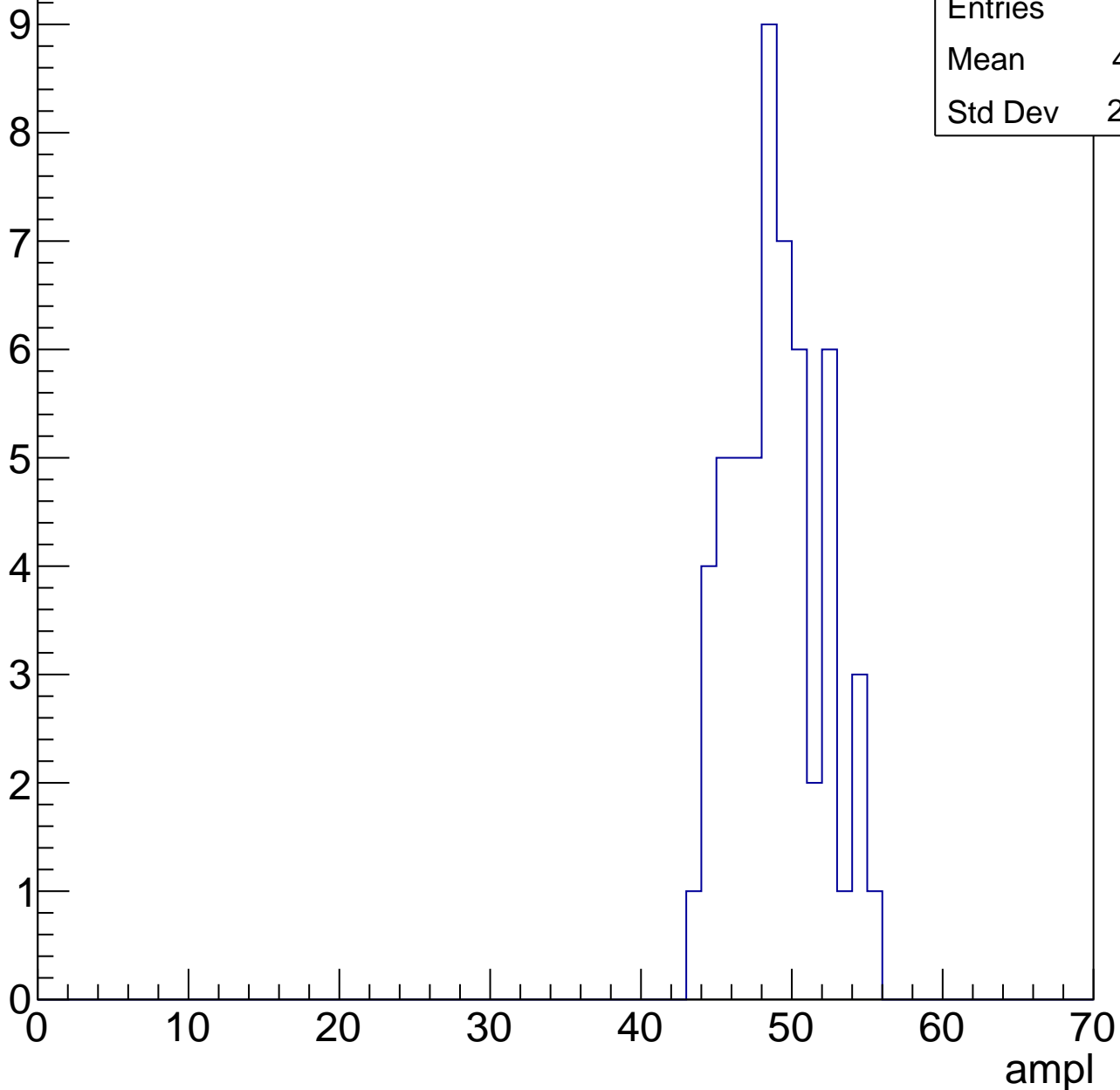


# B1L000S, U24-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	48.51
Std Dev	2.929

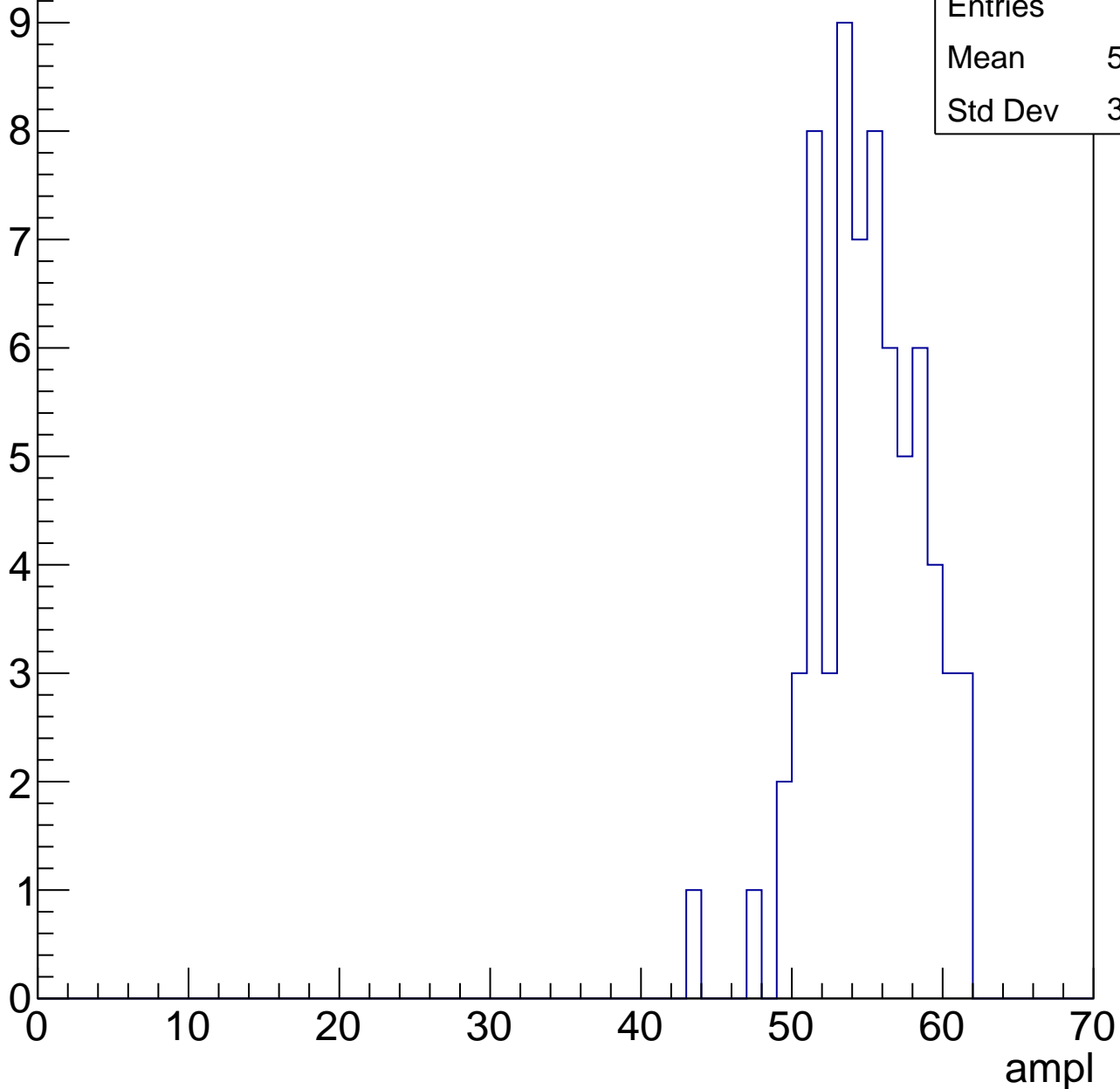


# B1L000S, U24-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	54.57
Std Dev	3.545

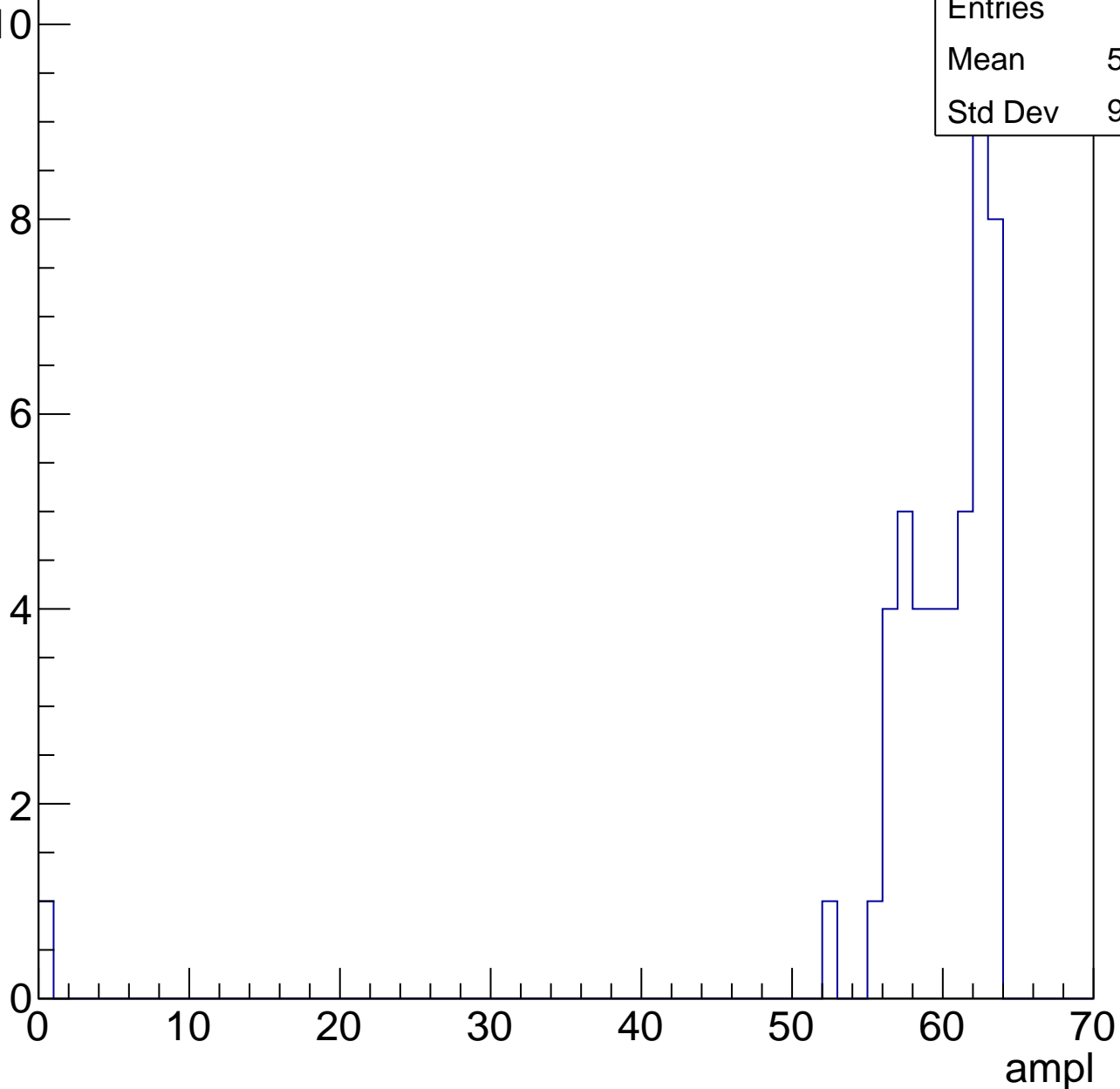


# B1L000S, U24-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

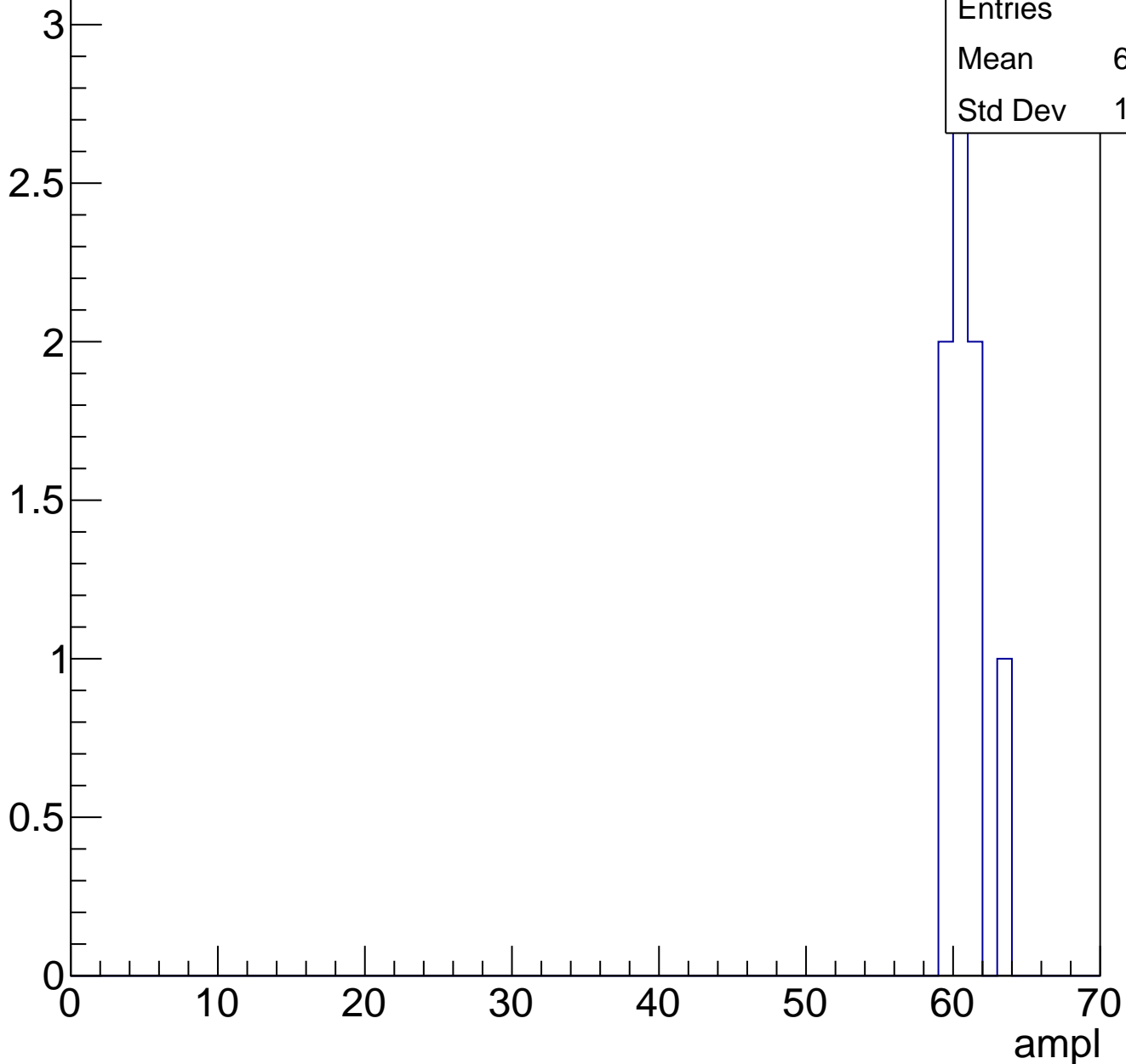
Entries	47
Mean	58.57
Std Dev	9.038



# B1L000S, U24-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

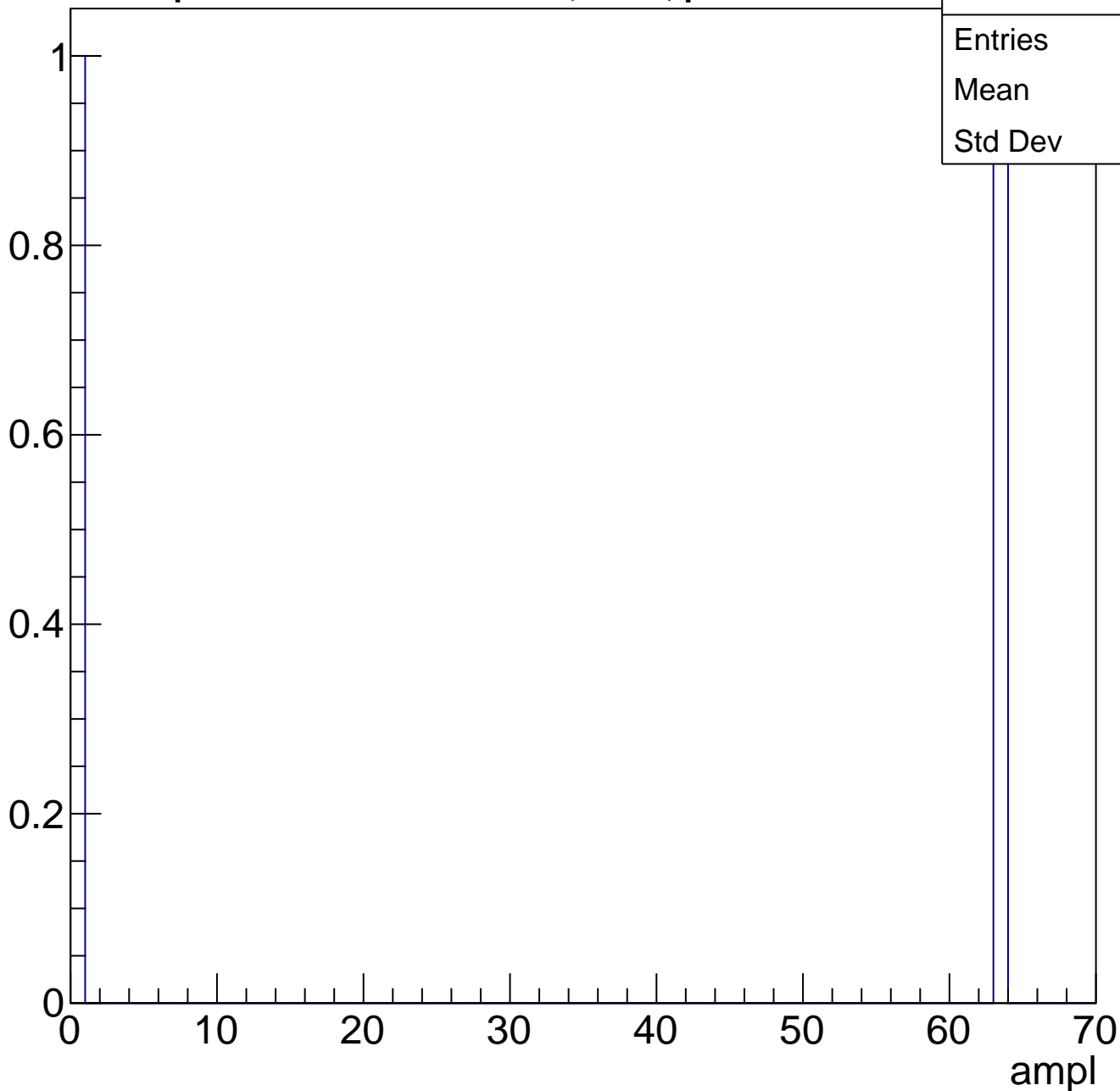




# B1L000S, U24-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch116, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	28.08
Std Dev	3.584

**Gaus mean : 28.8347**

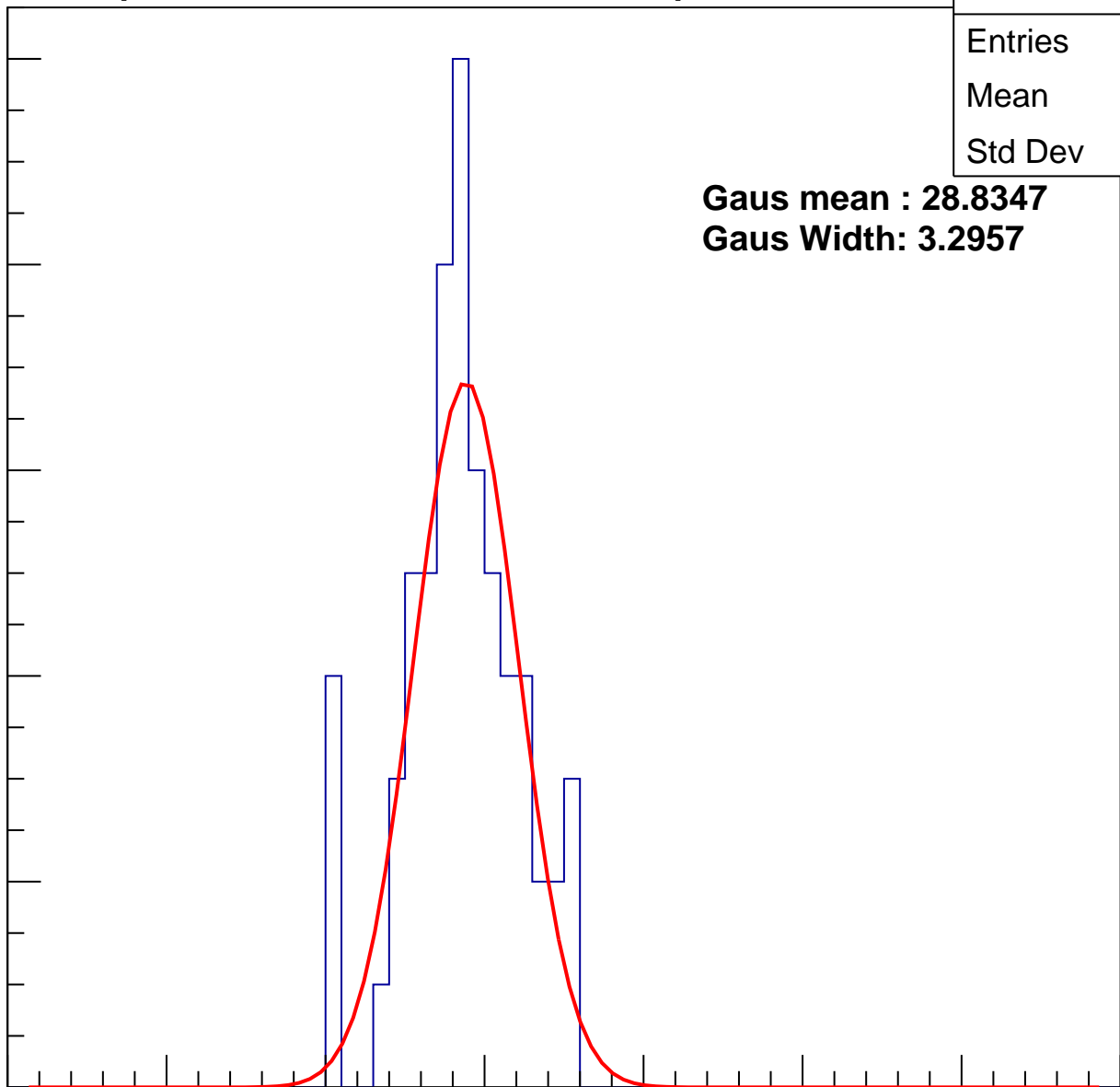
**Gaus Width: 3.2957**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch116, adc1

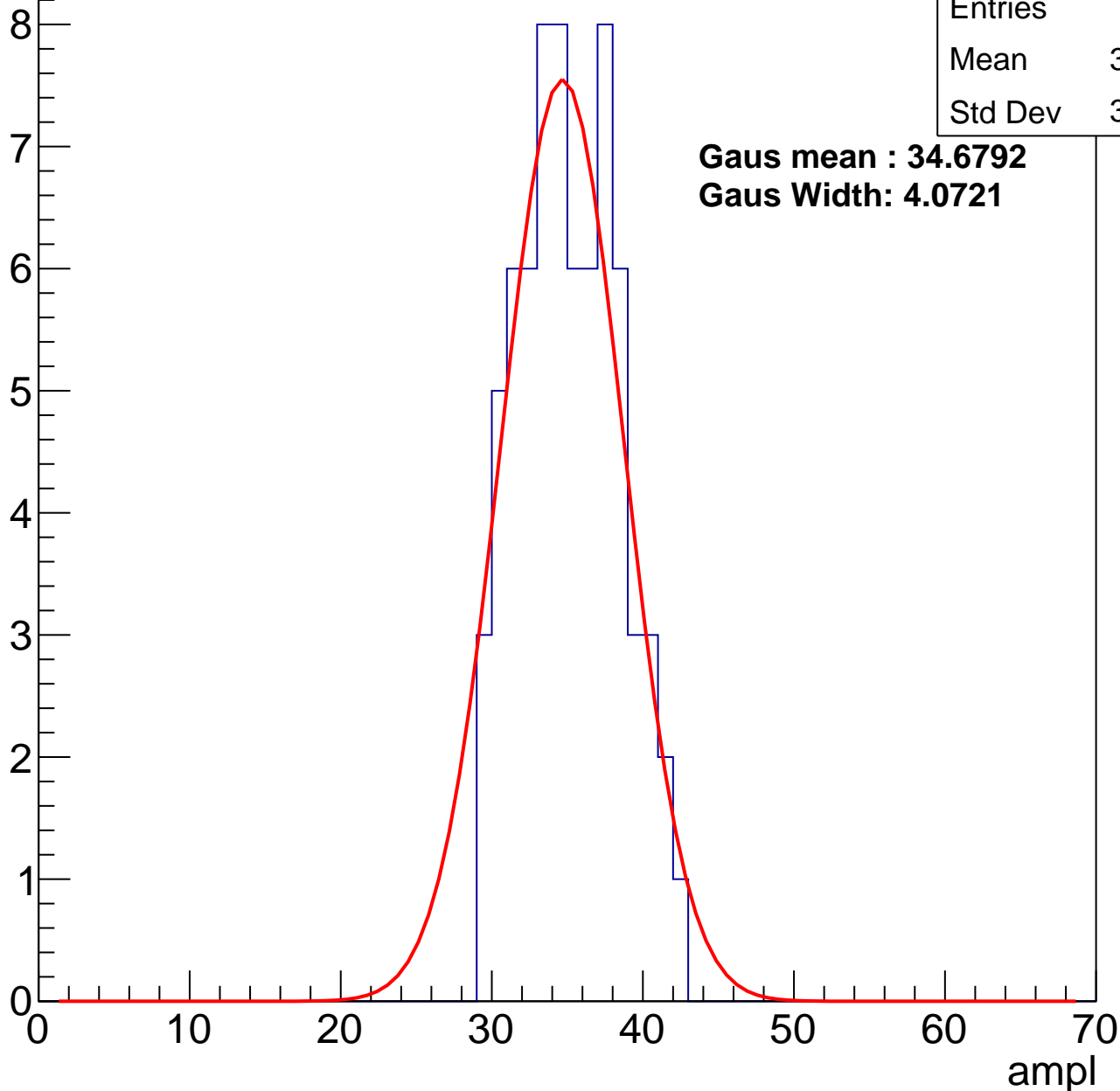
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.68
Std Dev	3.253

**Gaus mean : 34.6792**

**Gaus Width: 4.0721**



# B1L000S, U24-ch116, adc2

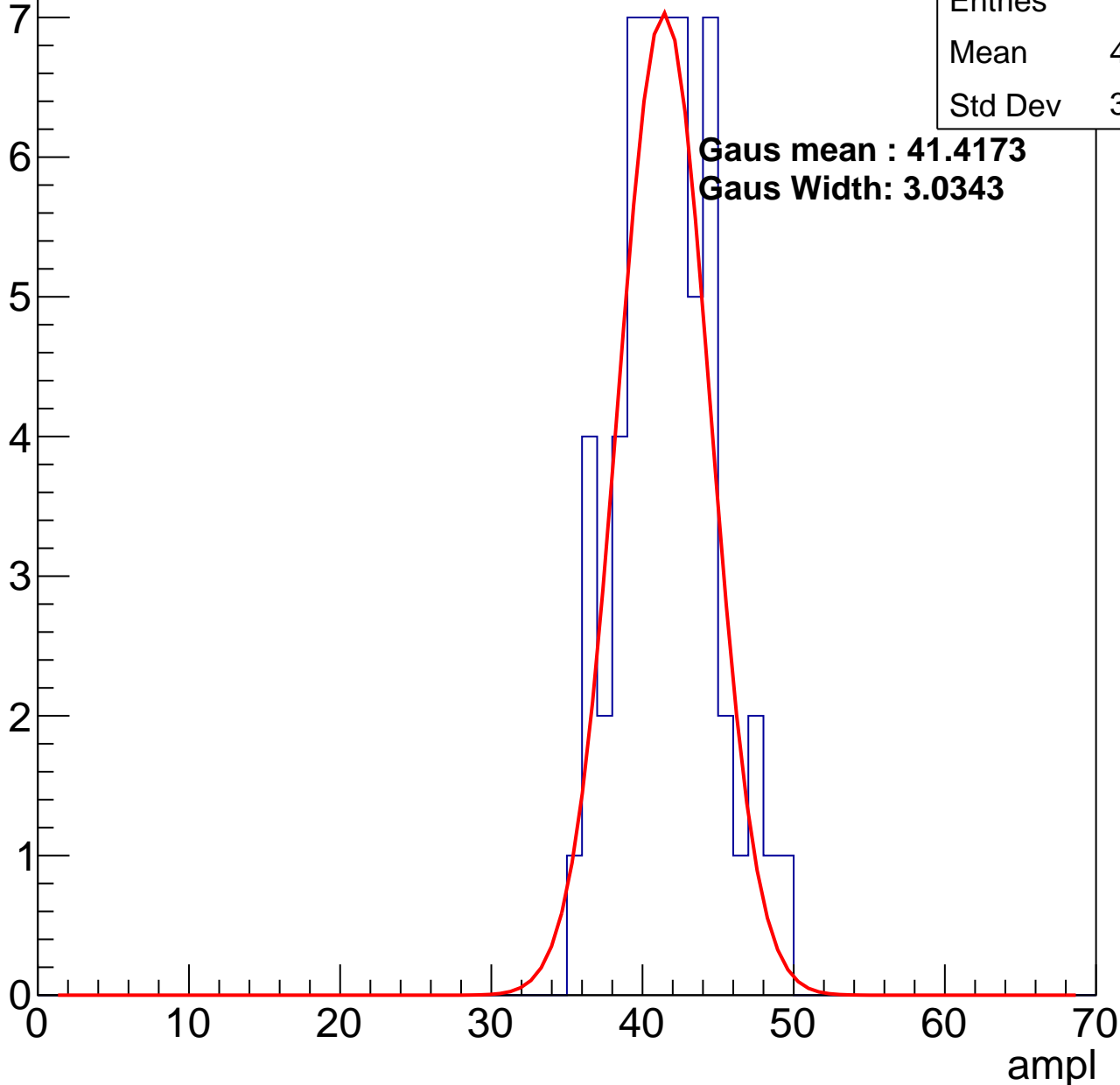
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.19
Std Dev	3.132

**Gaus mean : 41.4173**

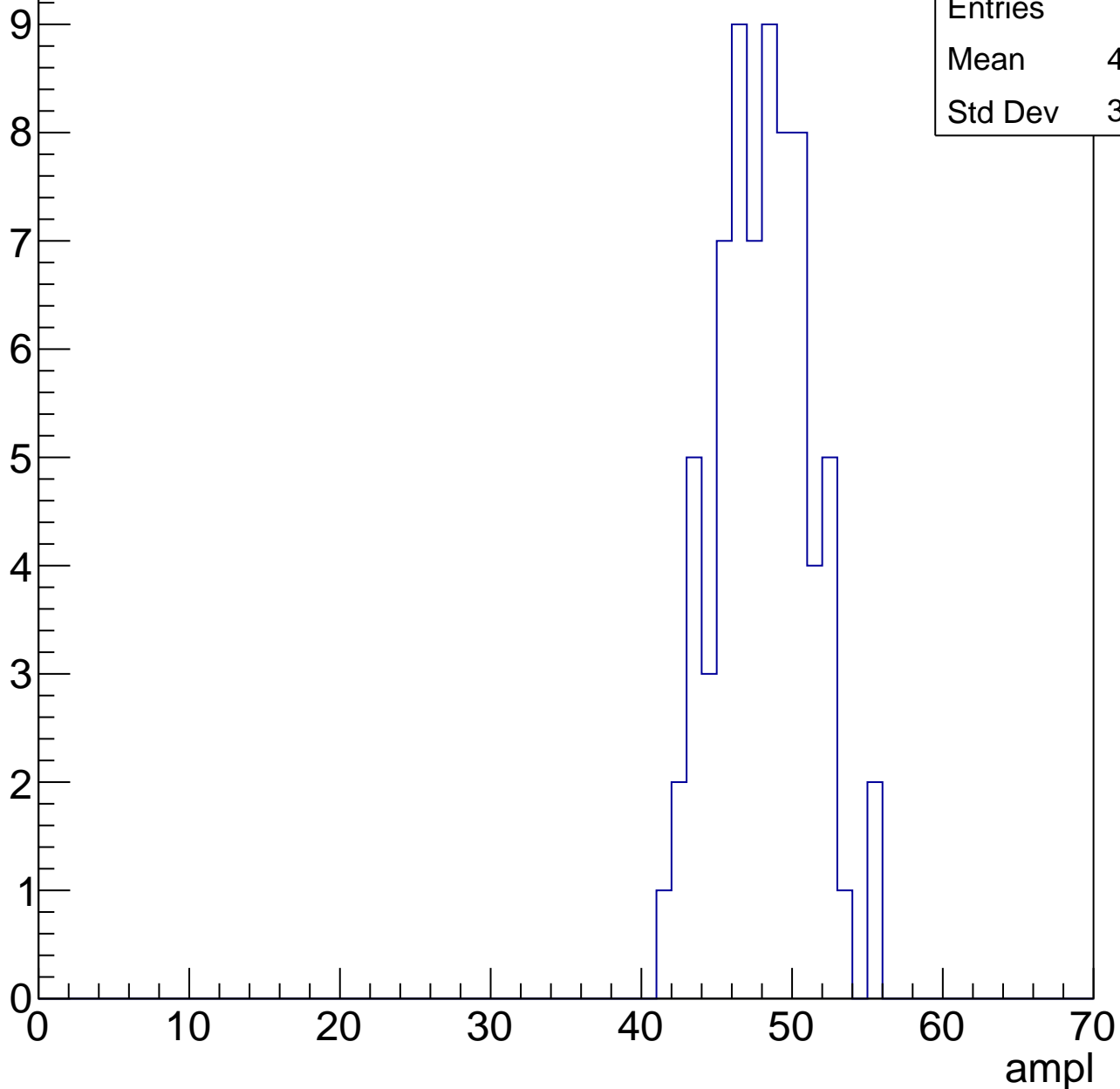
**Gaus Width: 3.0343**



# B1L000S, U24-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



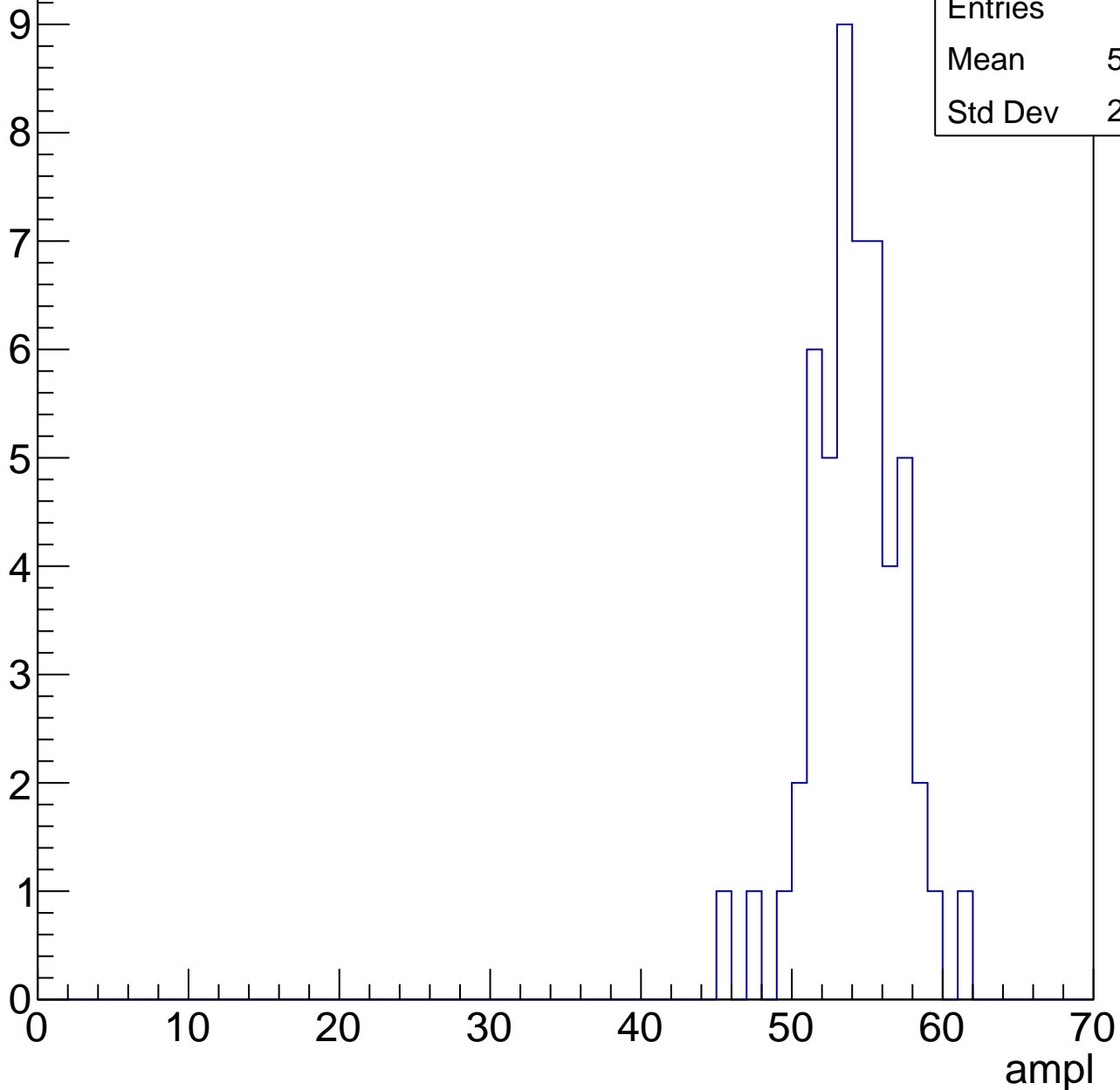
Entries	71
Mean	47.62
Std Dev	3.069

# B1L000S, U24-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

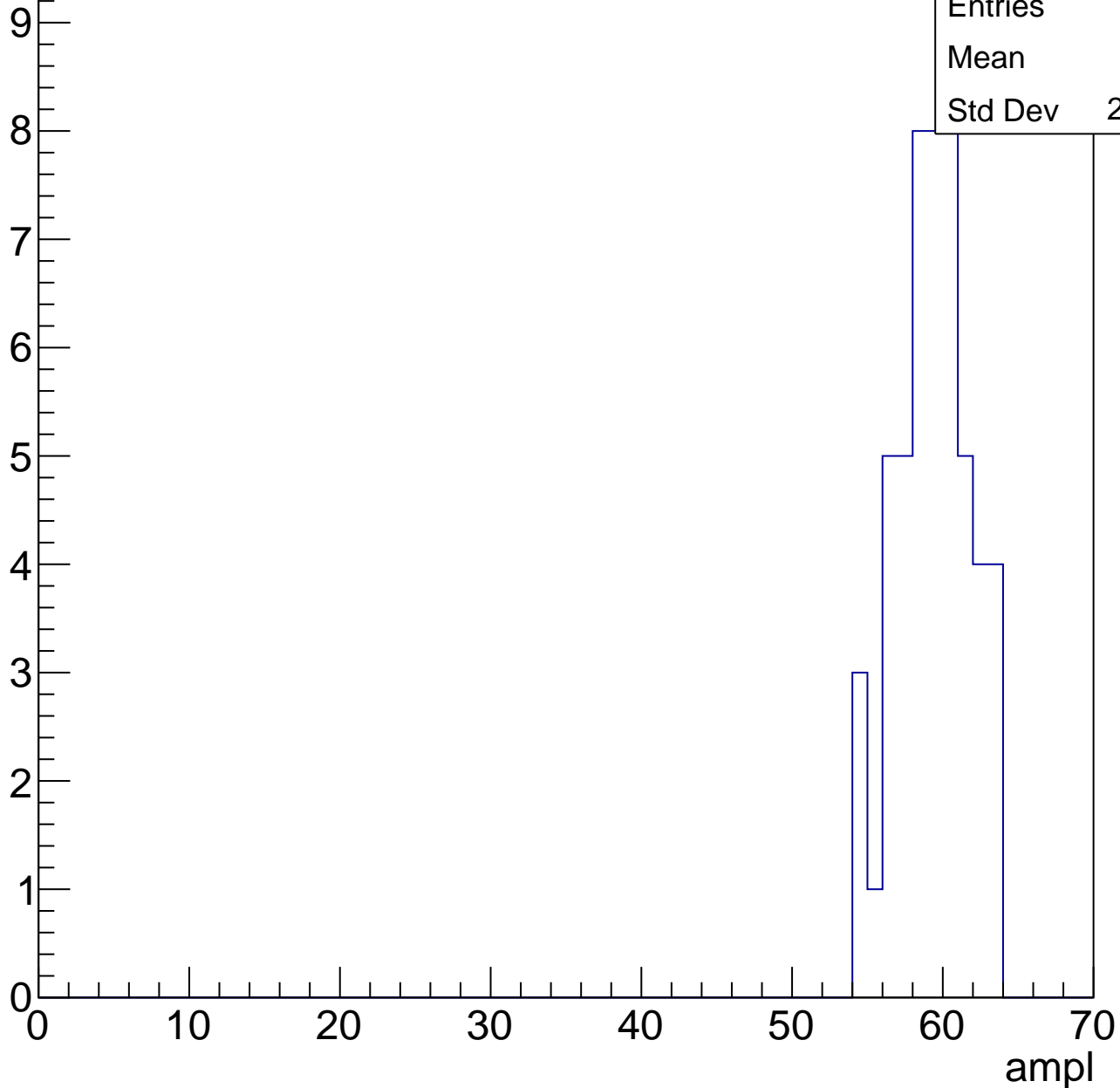
Entries	52
Mean	53.69
Std Dev	2.906



# B1L000S, U24-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

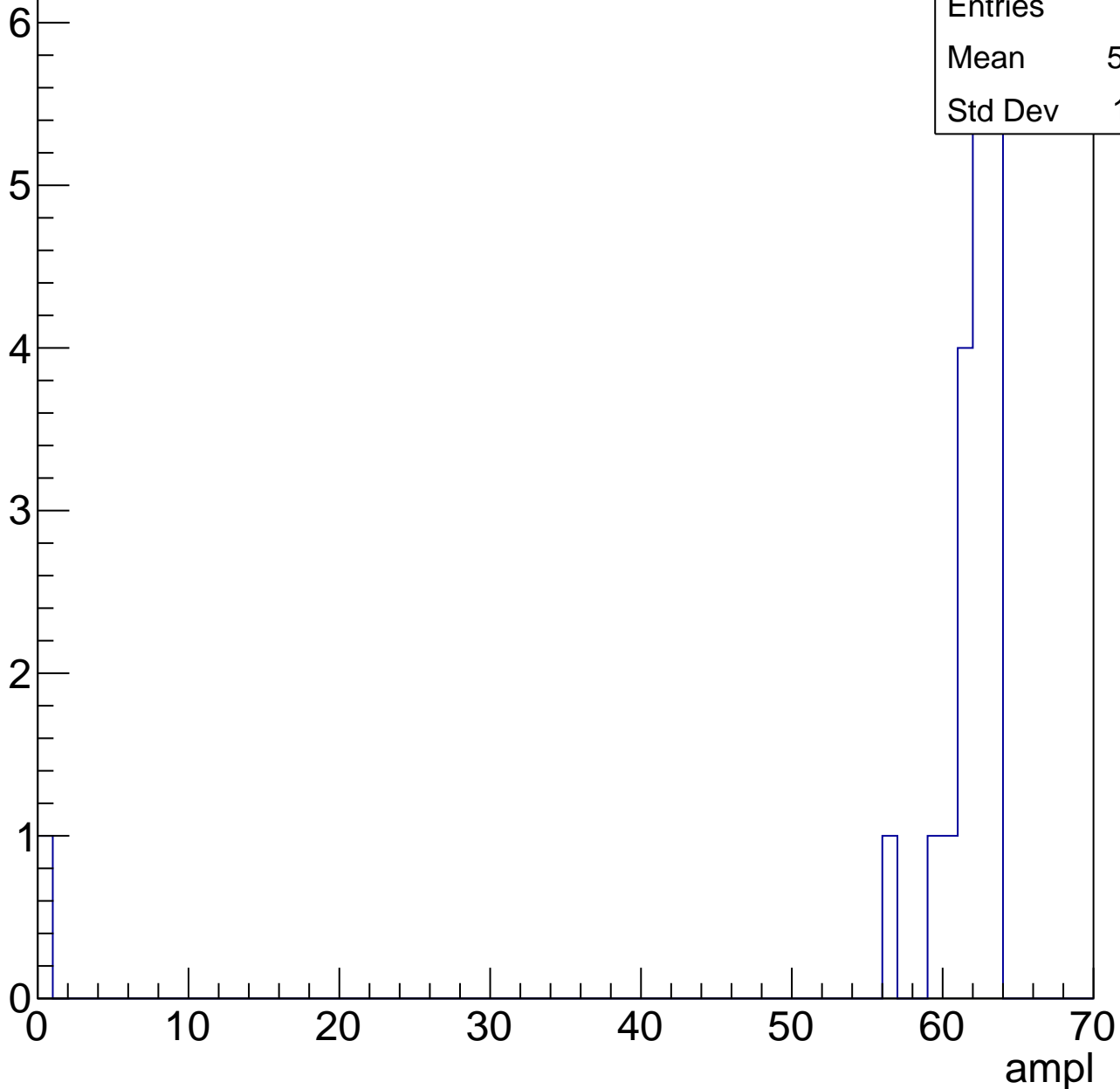


# B1L000S, U24-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.51





# B1L000S, U24-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch117, adc0

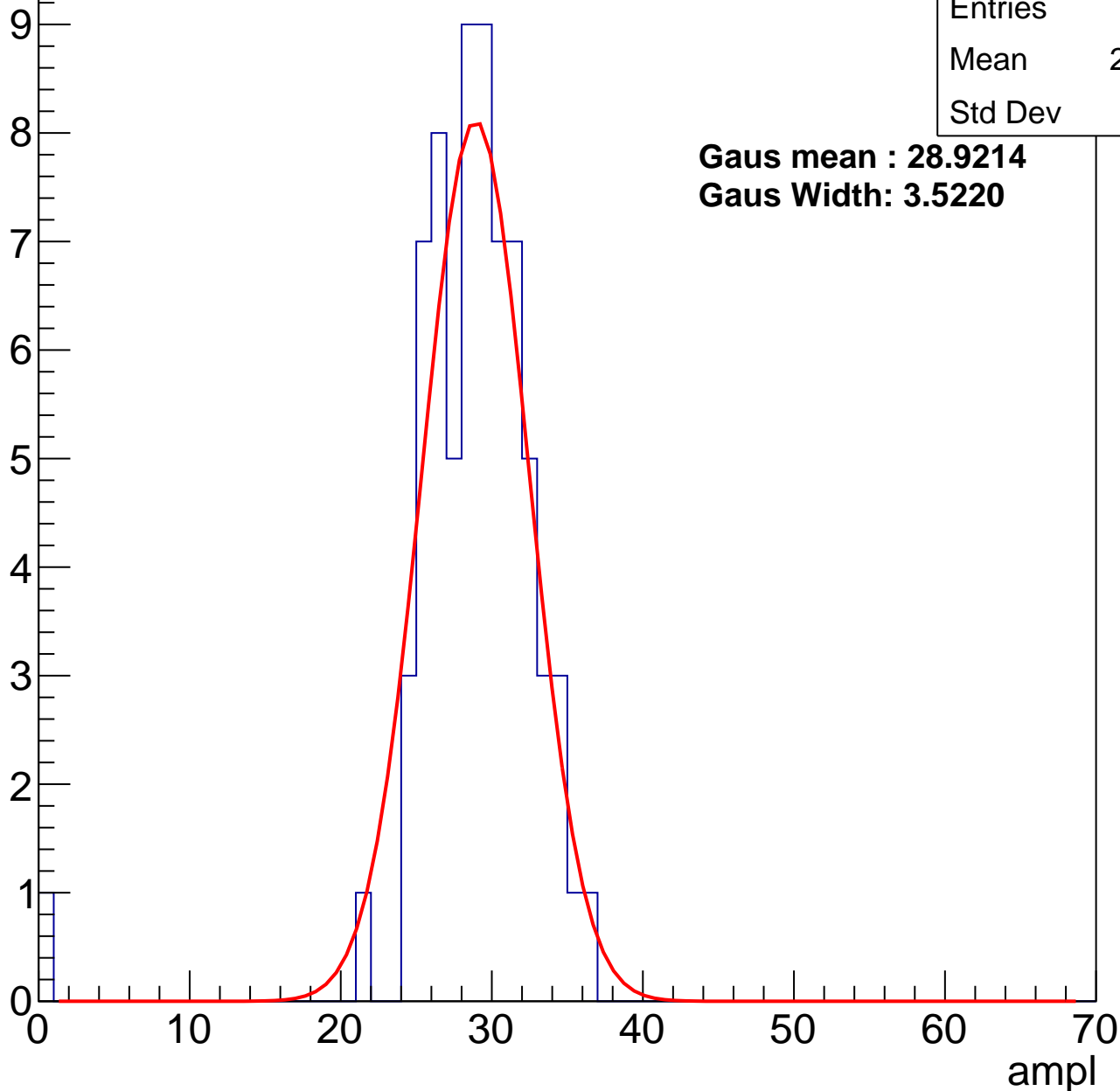
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.33
Std Dev	4.55

**Gaus mean : 28.9214**

**Gaus Width: 3.5220**



# B1L000S, U24-ch117, adc1

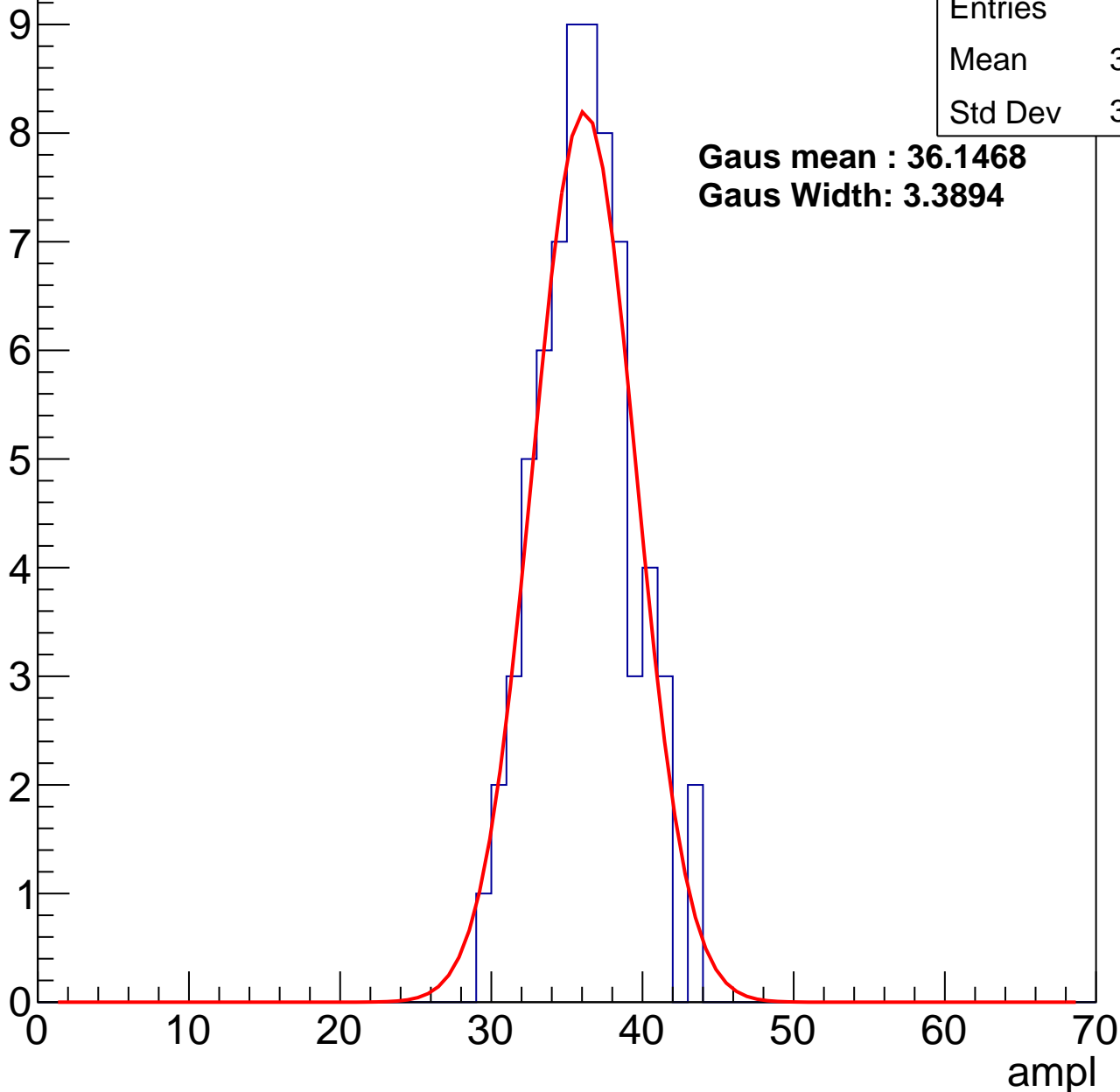
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.72
Std Dev	3.097

**Gaus mean : 36.1468**

**Gaus Width: 3.3894**

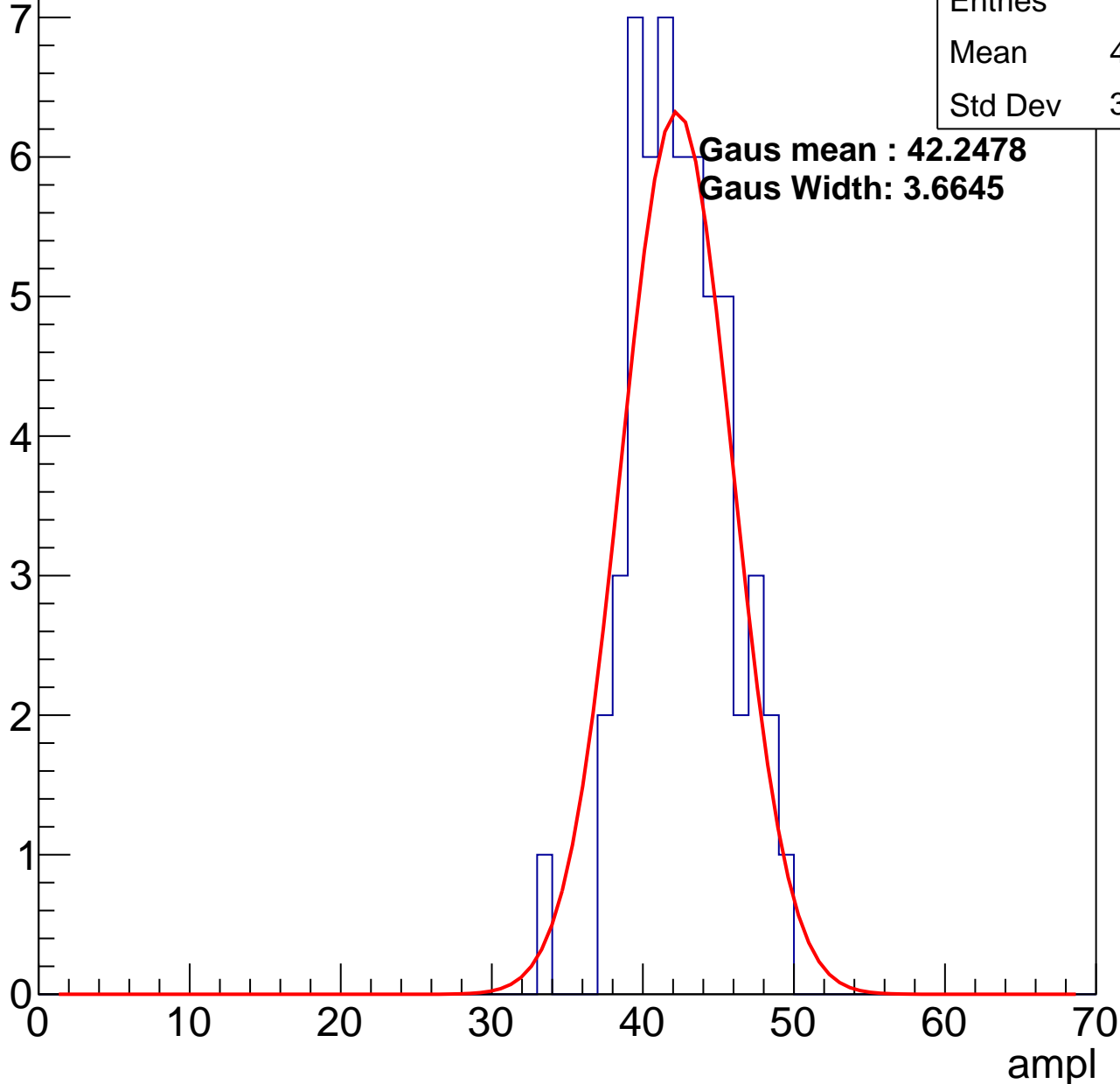


# B1L000S, U24-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

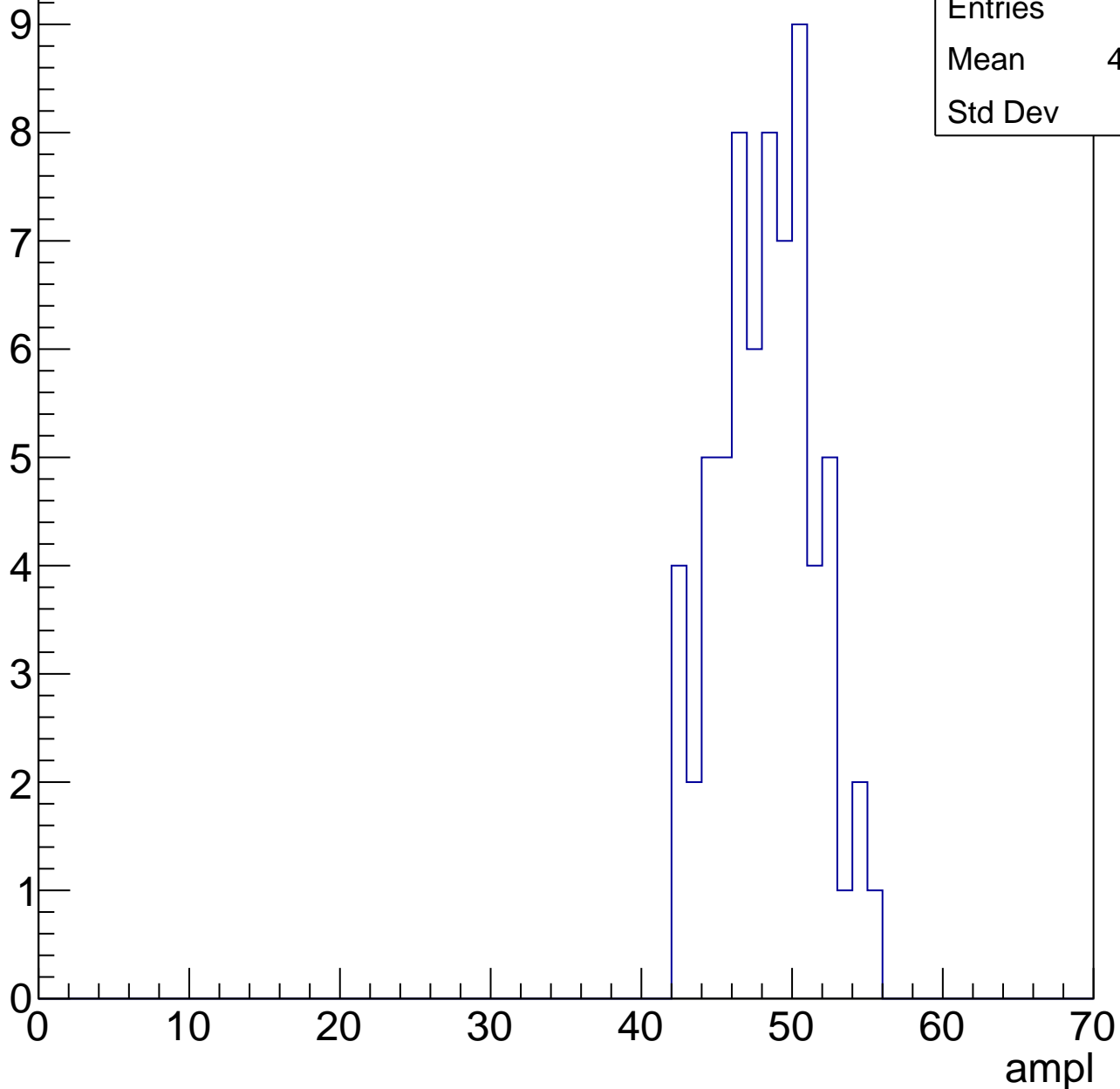
Entries	56
Mean	42.04
Std Dev	3.196



# B1L000S, U24-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



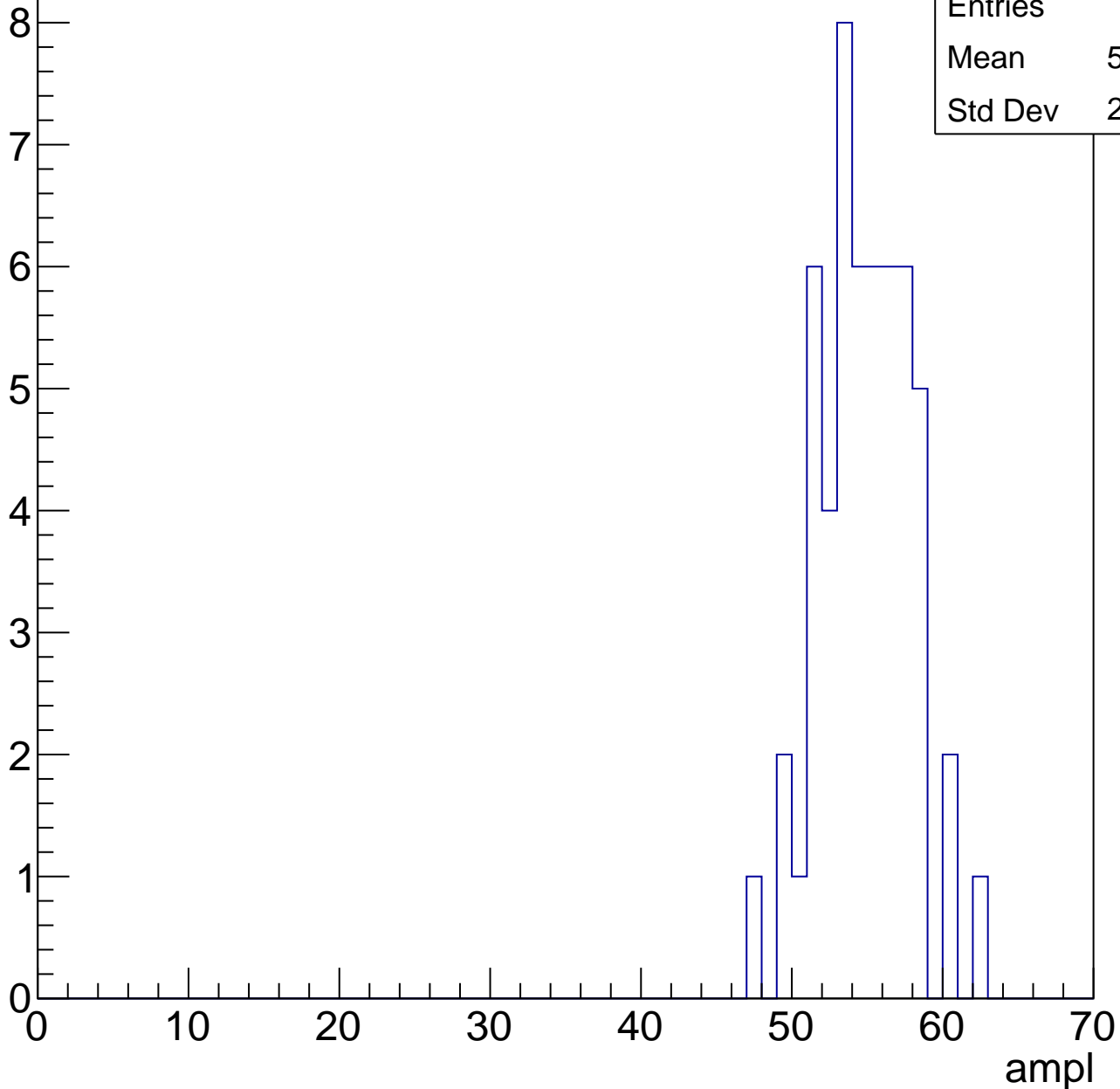
Entries	67
Mean	47.85
Std Dev	3.14

# B1L000S, U24-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

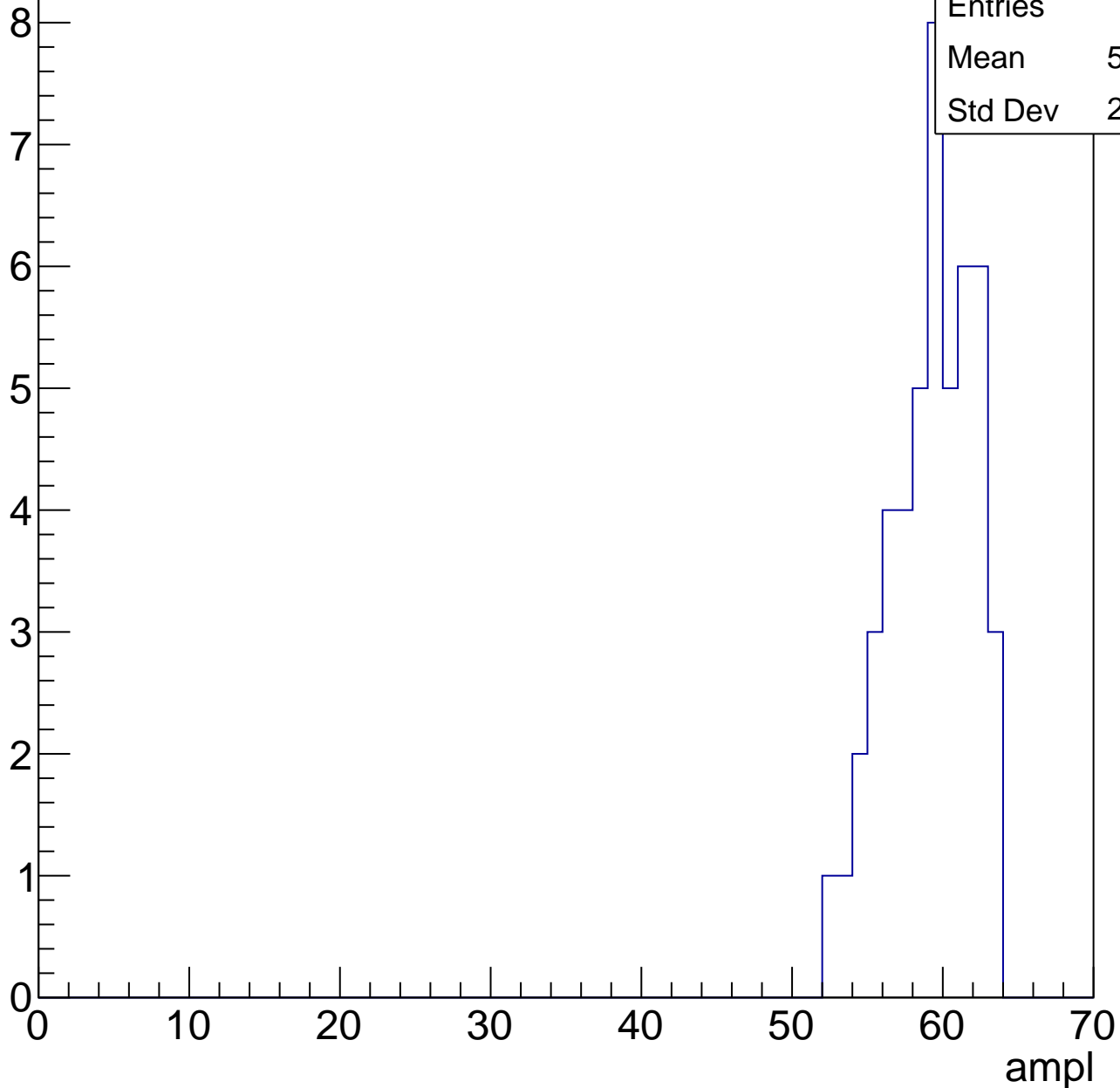
Entries	54
Mean	54.39
Std Dev	2.996



# B1L000S, U24-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

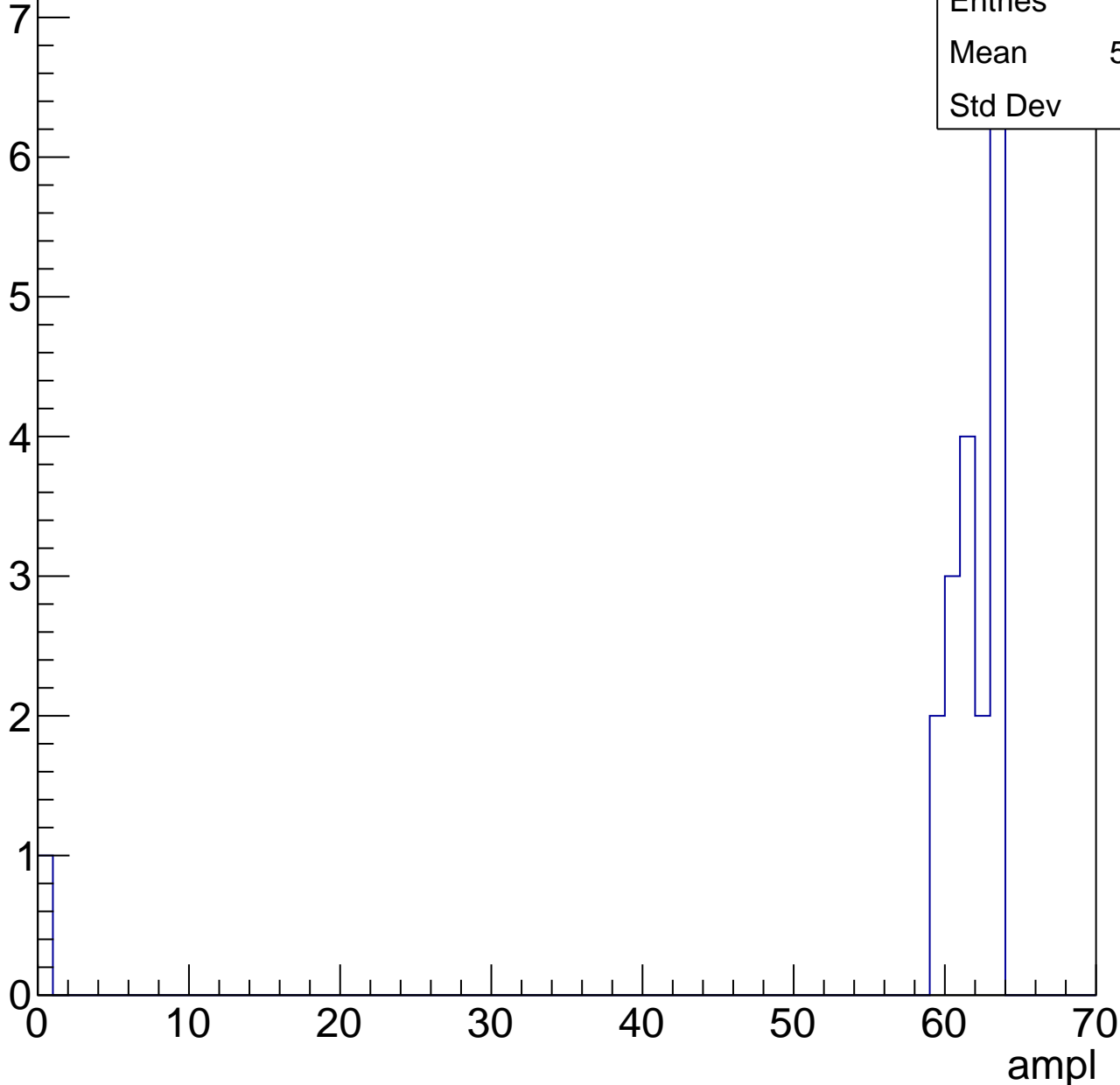


# B1L000S, U24-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.26
Std Dev	13.8

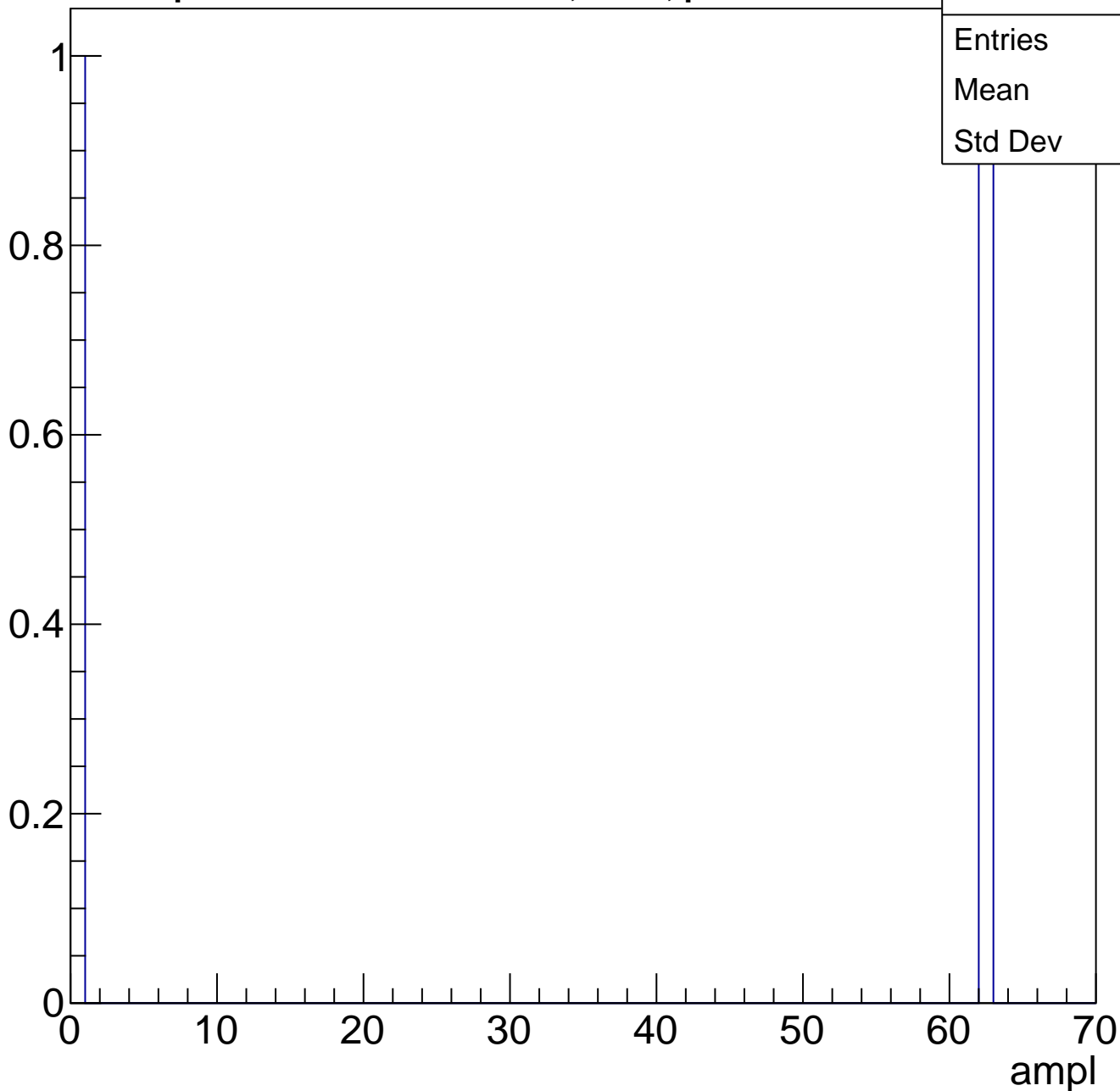




# B1L000S, U24-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch118, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	27.85
Std Dev	5.562

**Gaus mean : 29.5951**

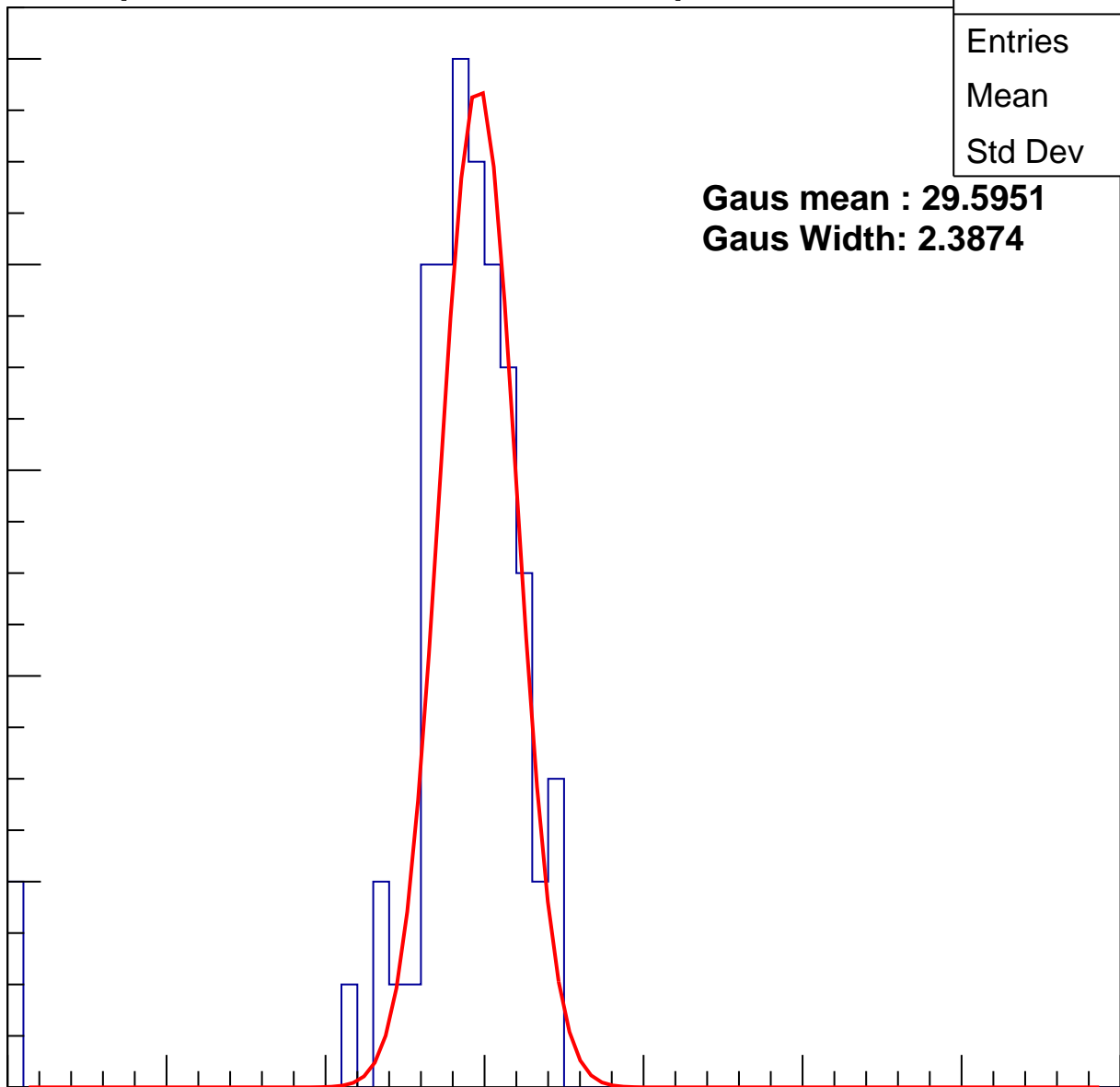
**Gaus Width: 2.3874**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch118, adc1

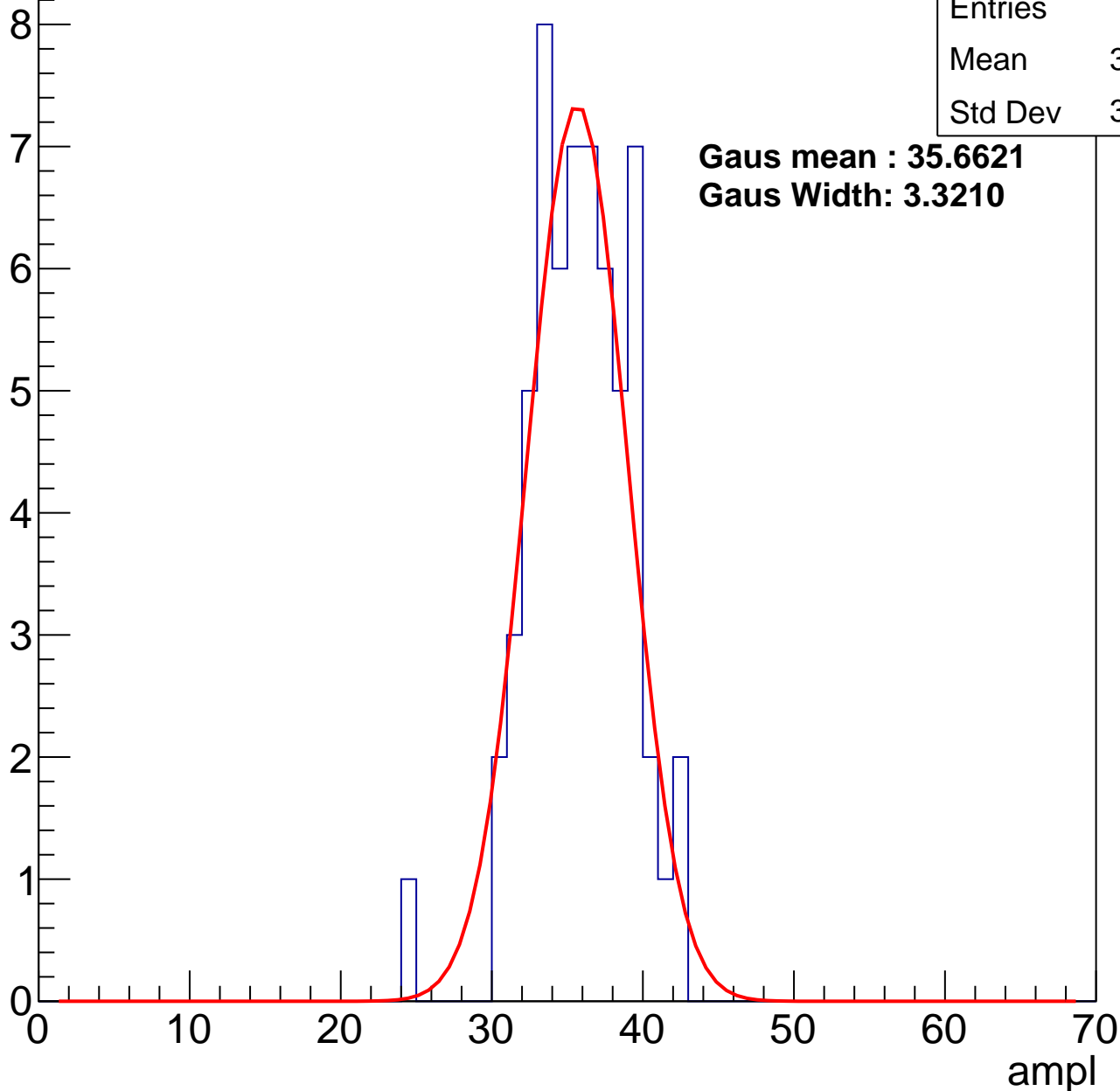
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.35
Std Dev	3.278

**Gaus mean : 35.6621**

**Gaus Width: 3.3210**



# B1L000S, U24-ch118, adc2

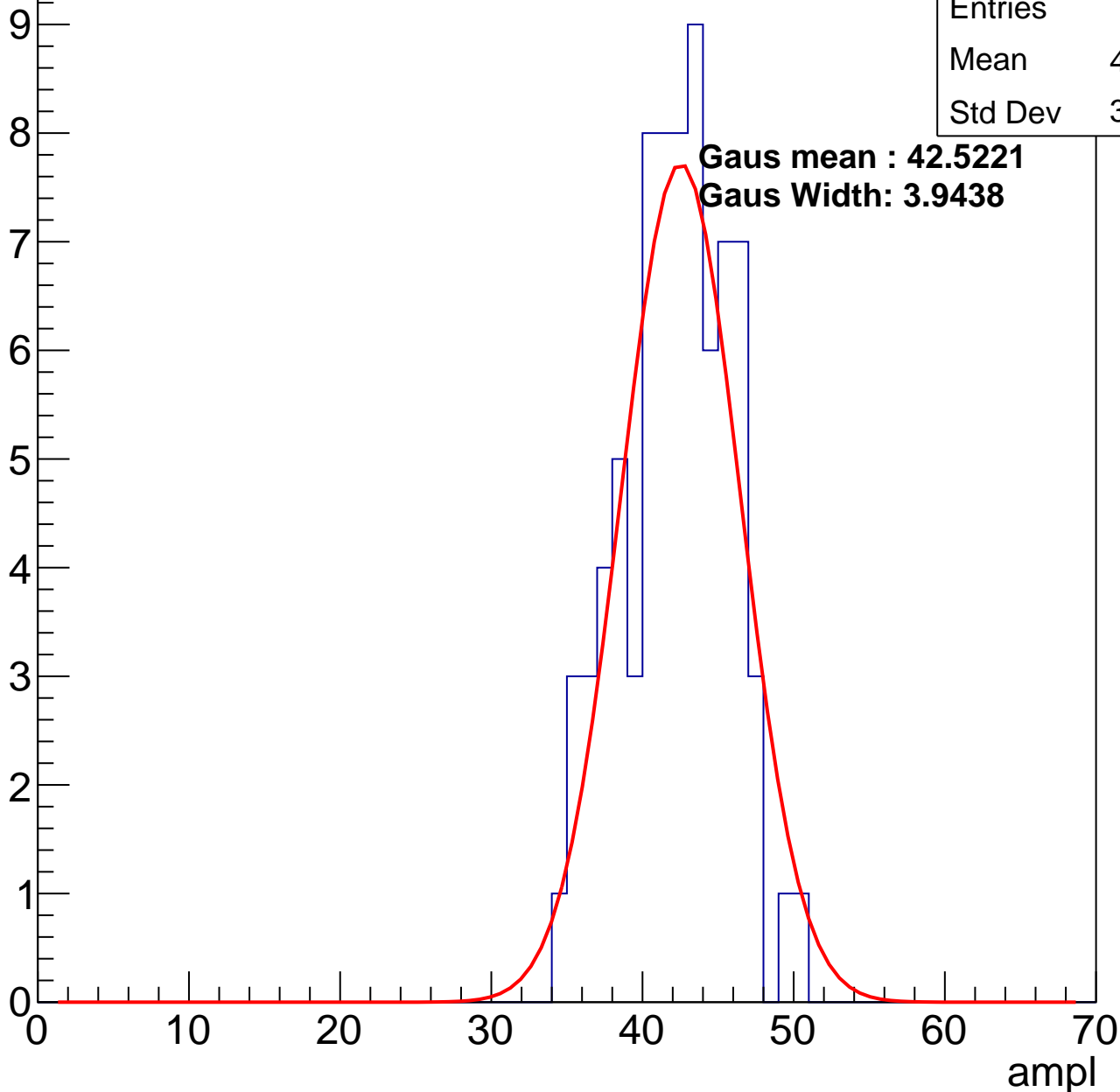
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	41.74
Std Dev	3.525

**Gaus mean : 42.5221**

**Gaus Width: 3.9438**

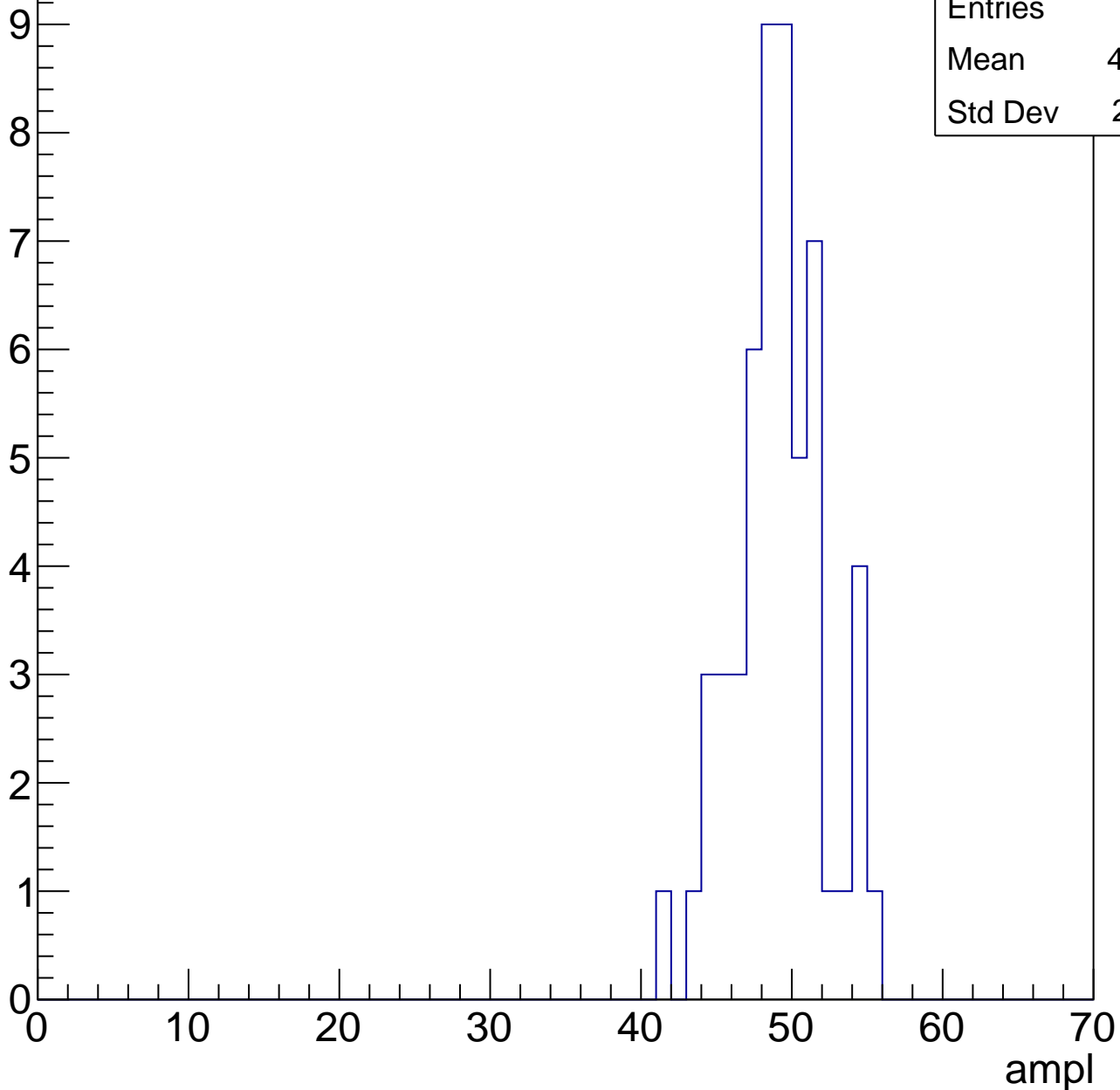


# B1L000S, U24-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	48.65
Std Dev	2.951

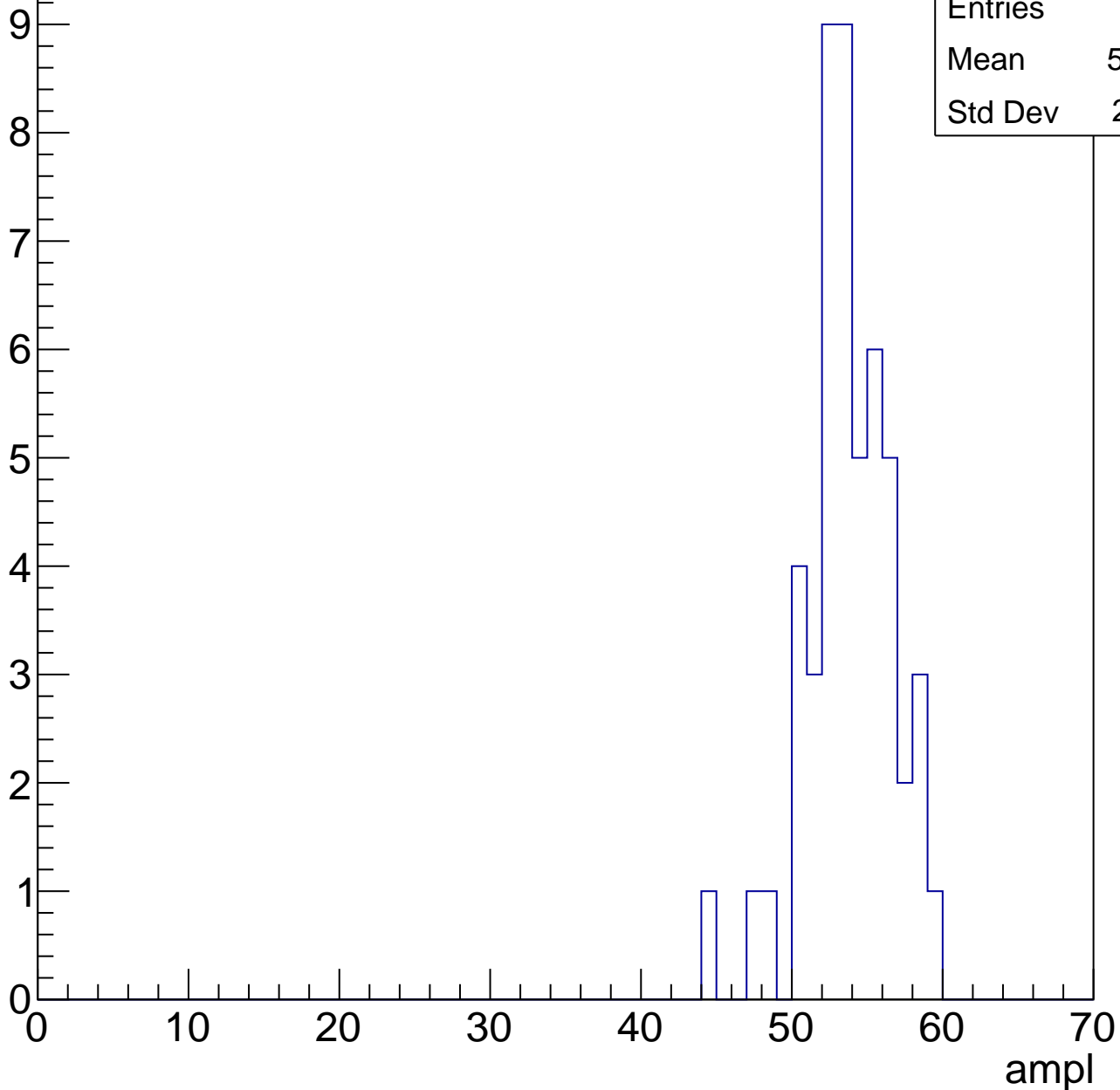


# B1L000S, U24-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.28
Std Dev	2.871

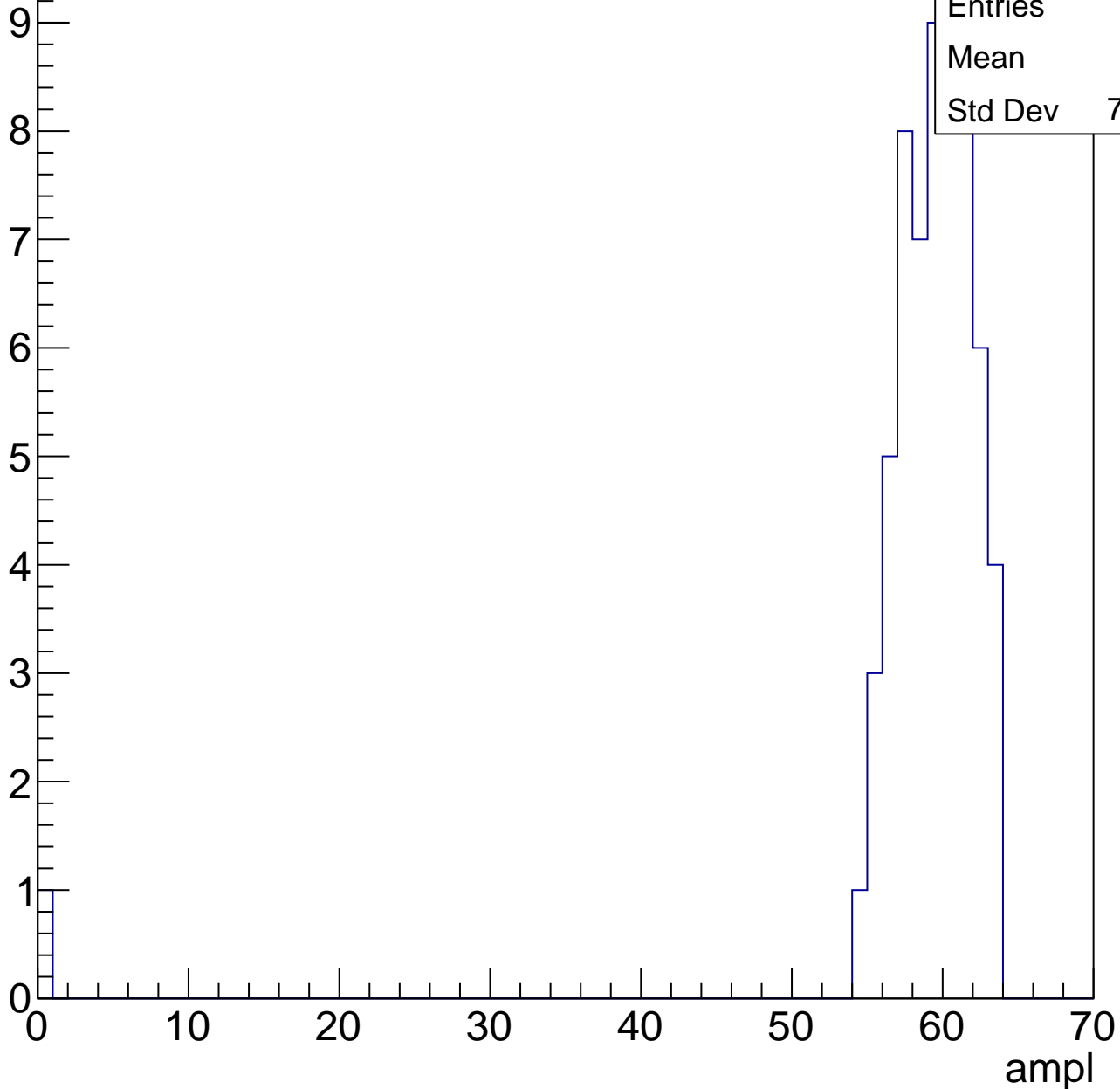


# B1L000S, U24-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

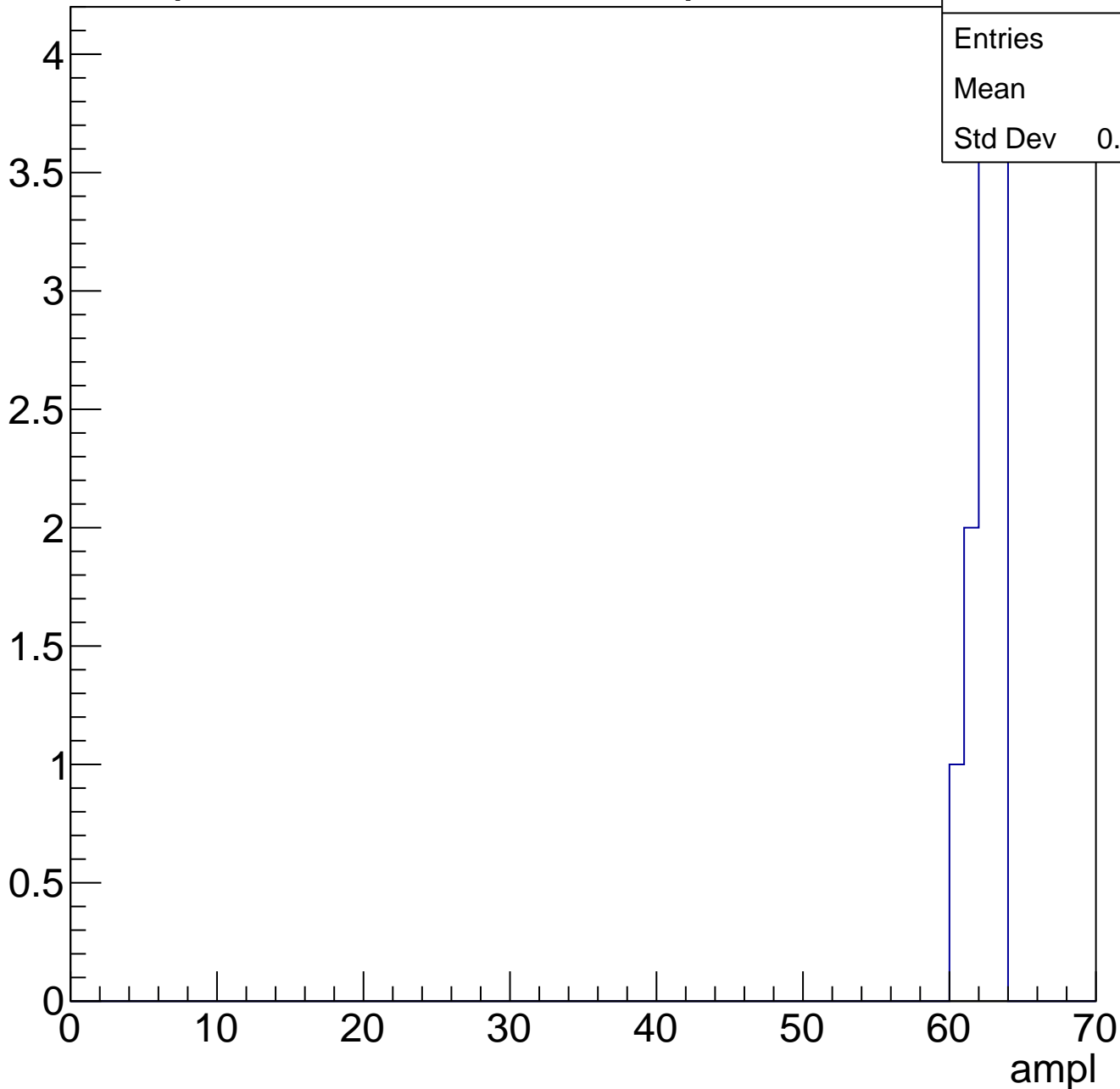
Entries	61
Mean	58.1
Std Dev	7.838



# B1L000S, U24-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	11
Mean	62
Std Dev	0.9535



# B1L000S, U24-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	63
Std Dev	0

# B1L000S, U24-ch119, adc0

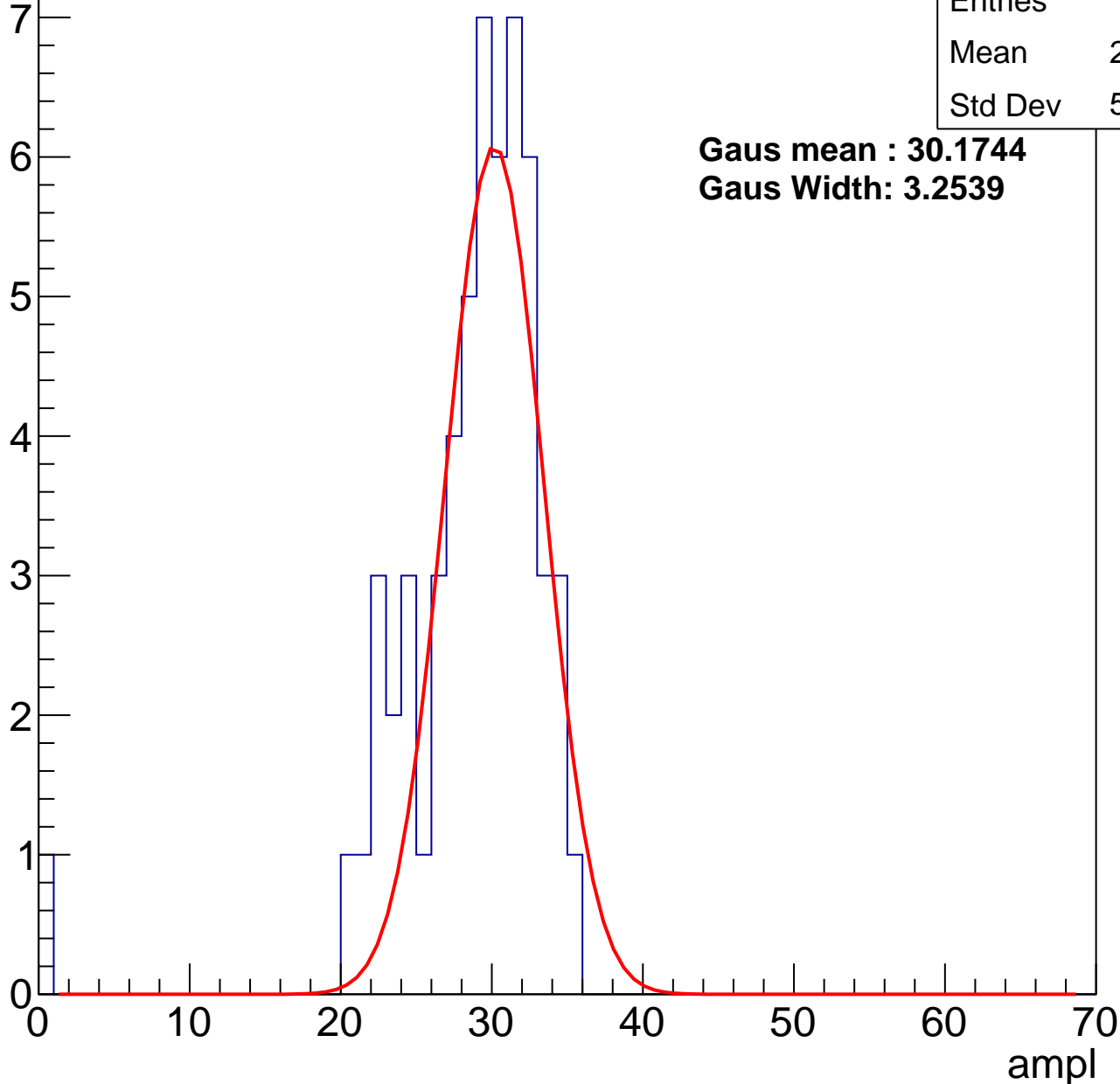
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.14
Std Dev	5.213

**Gaus mean : 30.1744**

**Gaus Width: 3.2539**



# B1L000S, U24-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

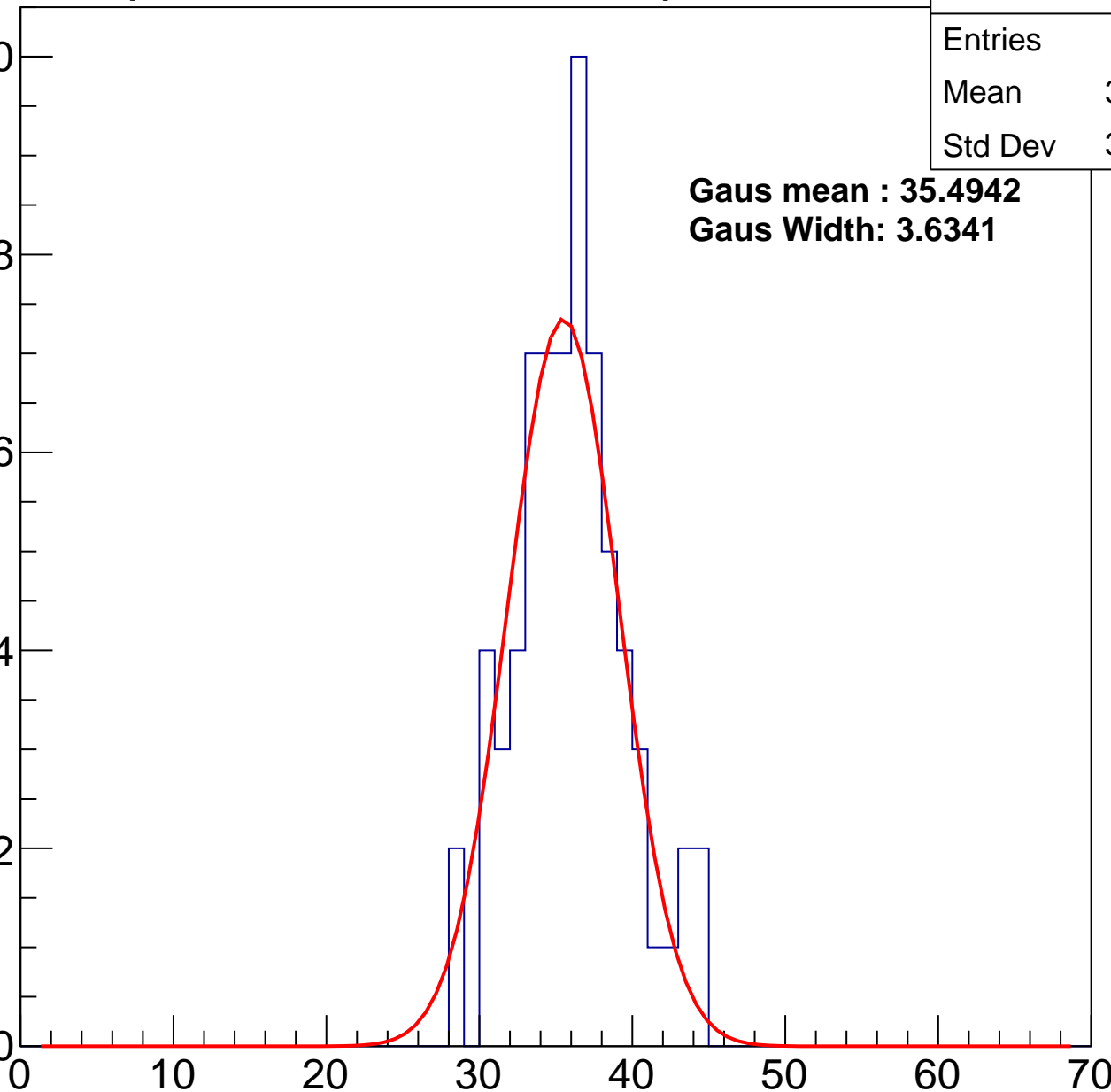
Entries	69
Mean	35.55
Std Dev	3.598

**Gaus mean : 35.4942**

**Gaus Width: 3.6341**

10  
8  
6  
4  
2  
0

ampl



# B1L000S, U24-ch119, adc2

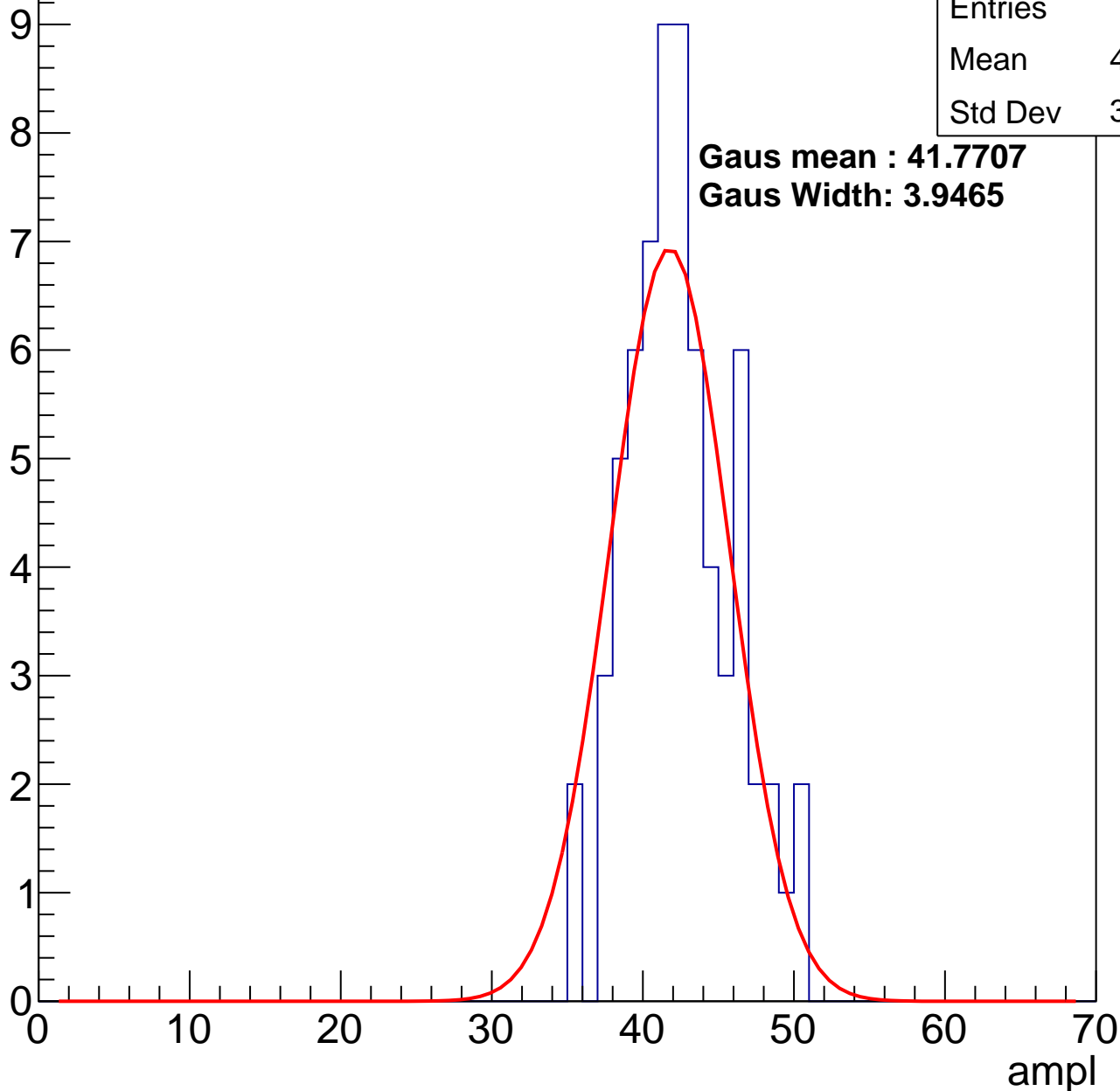
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.03
Std Dev	3.429

**Gaus mean : 41.7707**

**Gaus Width: 3.9465**

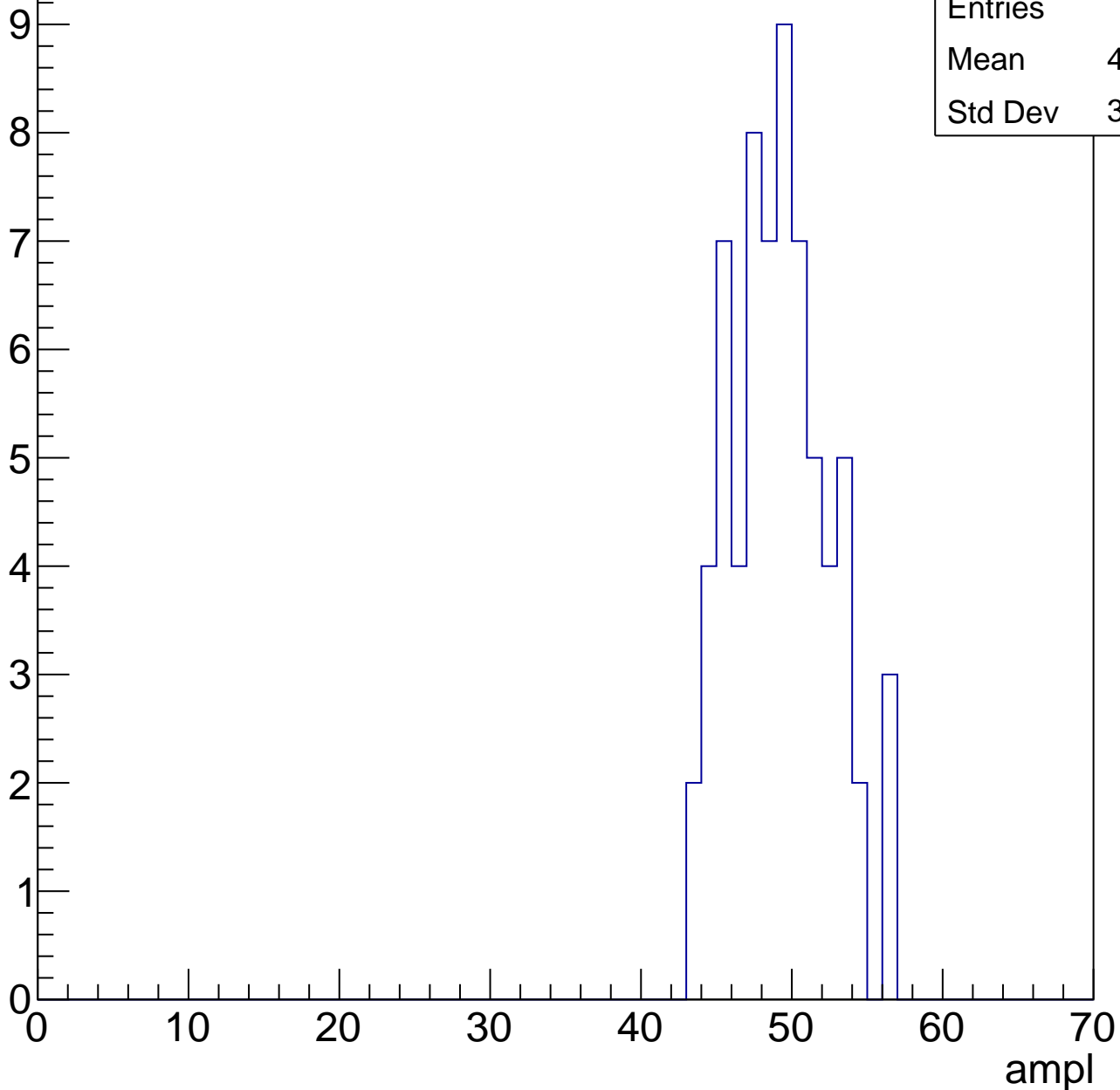


# B1L000S, U24-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	48.78
Std Dev	3.222

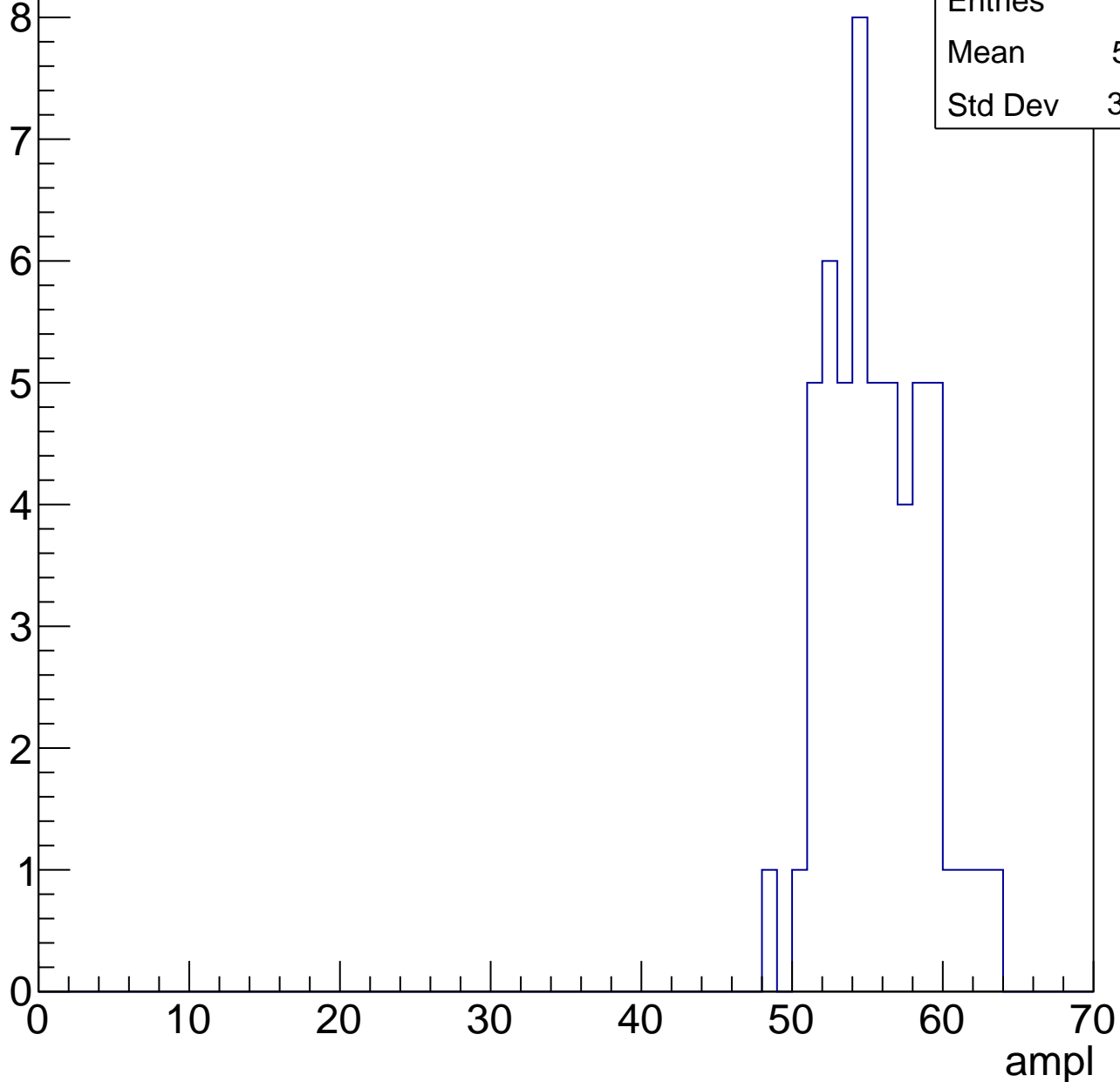


# B1L000S, U24-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	55.11
Std Dev	3.207

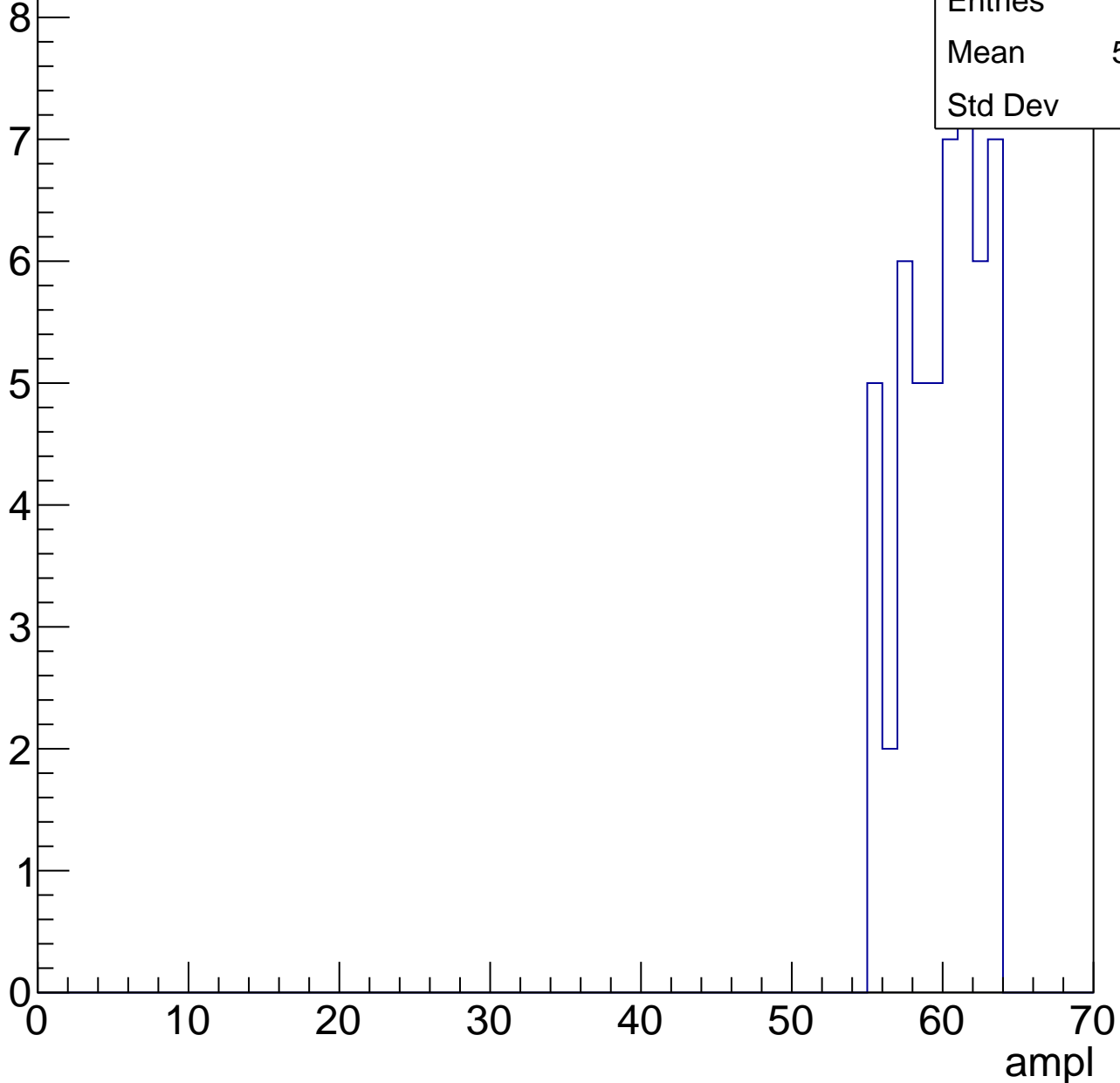


# B1L000S, U24-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

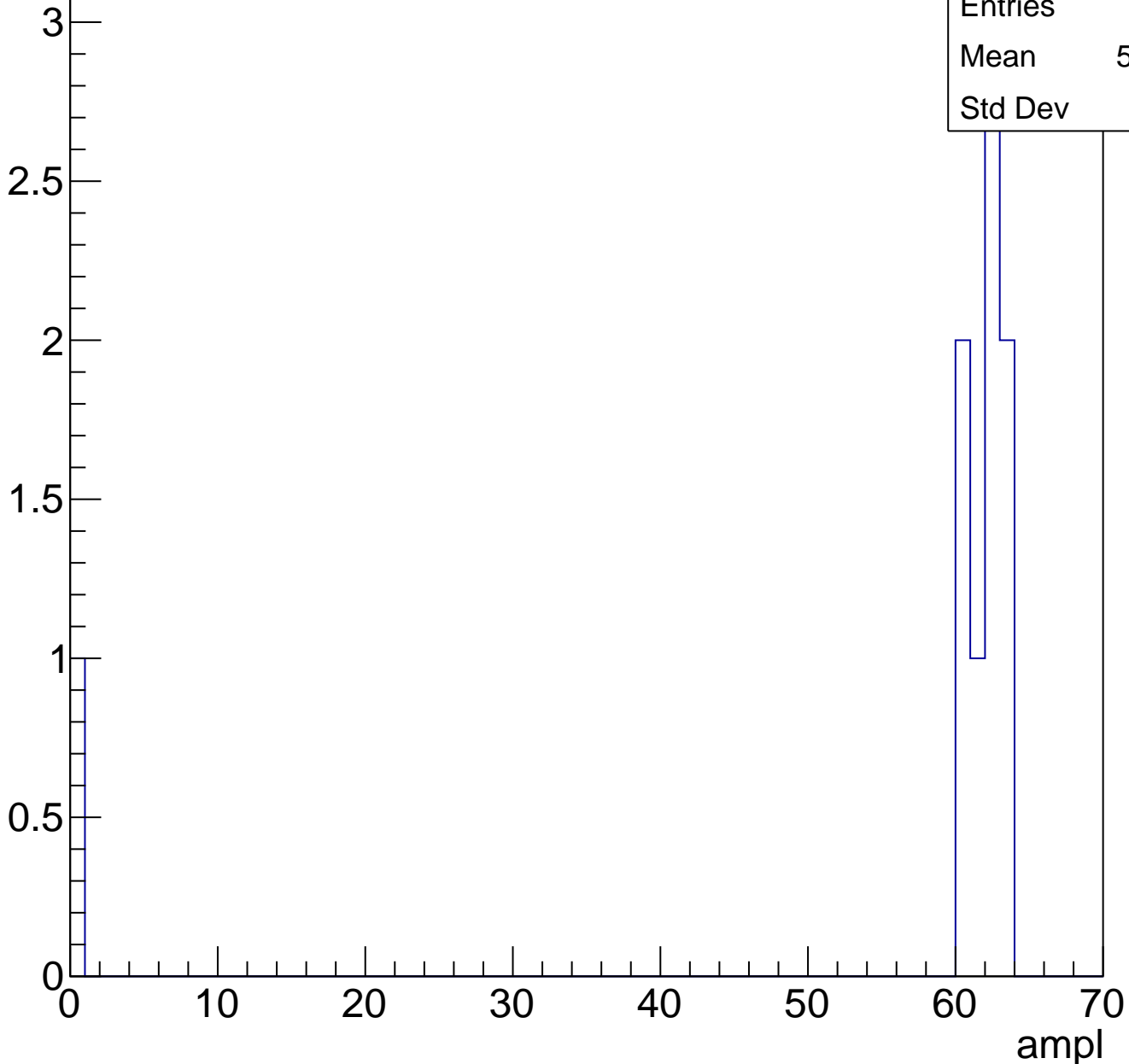
Entries	51
Mean	59.51
Std Dev	2.5



# B1L000S, U24-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	89
Mean	28.12
Std Dev	4.728

**Gaus mean : 28.5249**

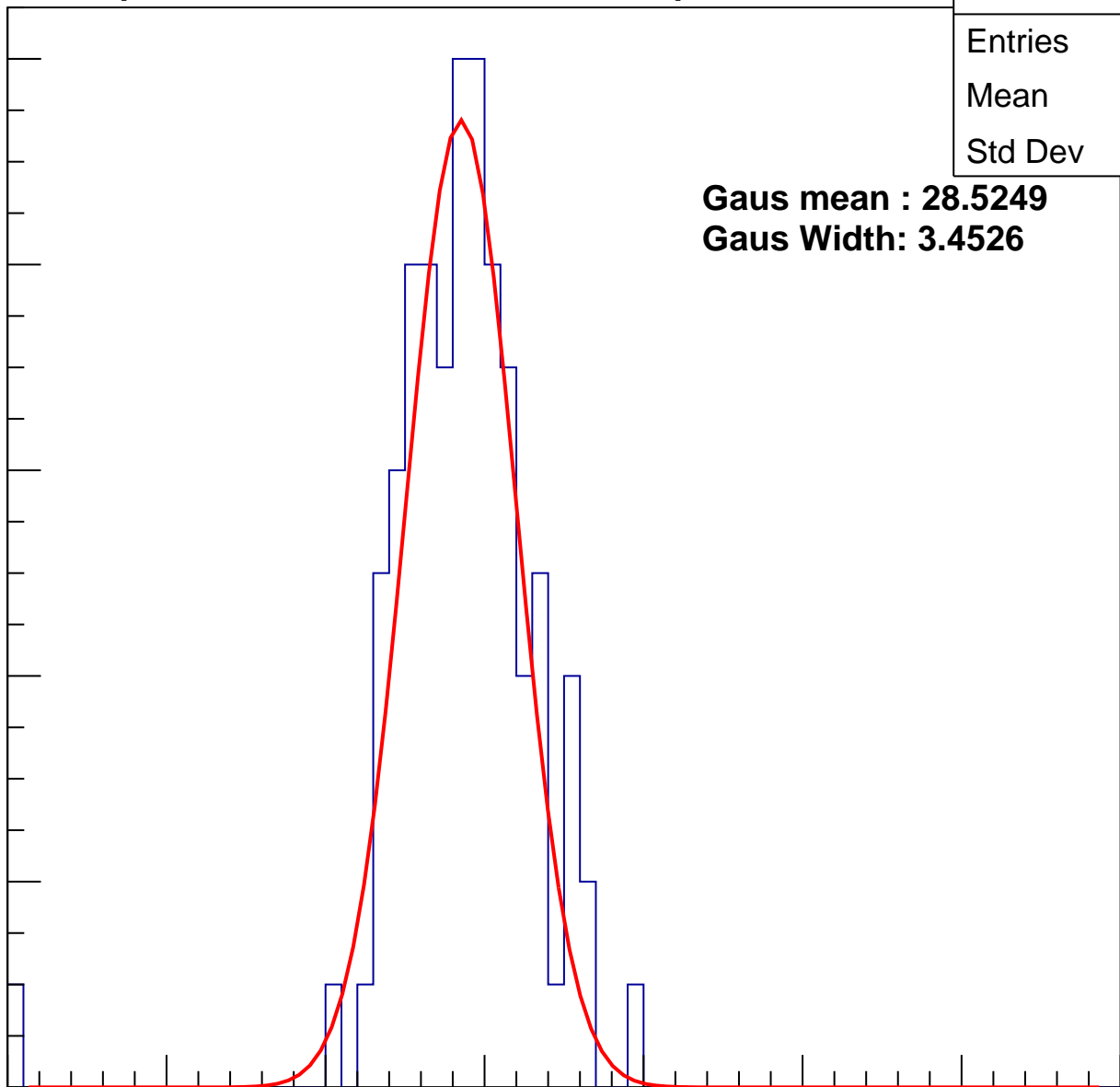
**Gaus Width: 3.4526**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch120, adc1

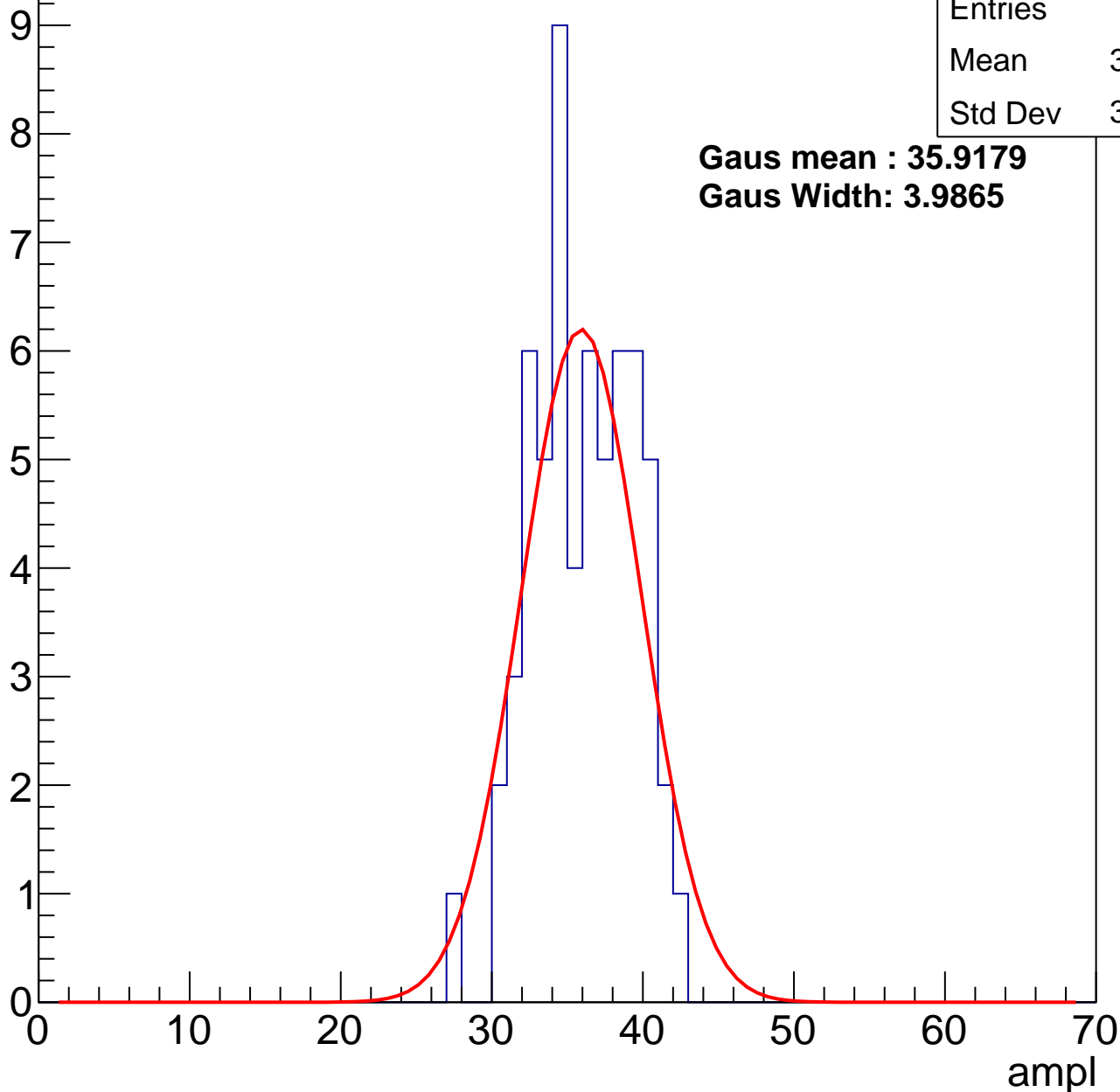
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.57
Std Dev	3.257

**Gaus mean : 35.9179**

**Gaus Width: 3.9865**



# B1L000S, U24-ch120, adc2

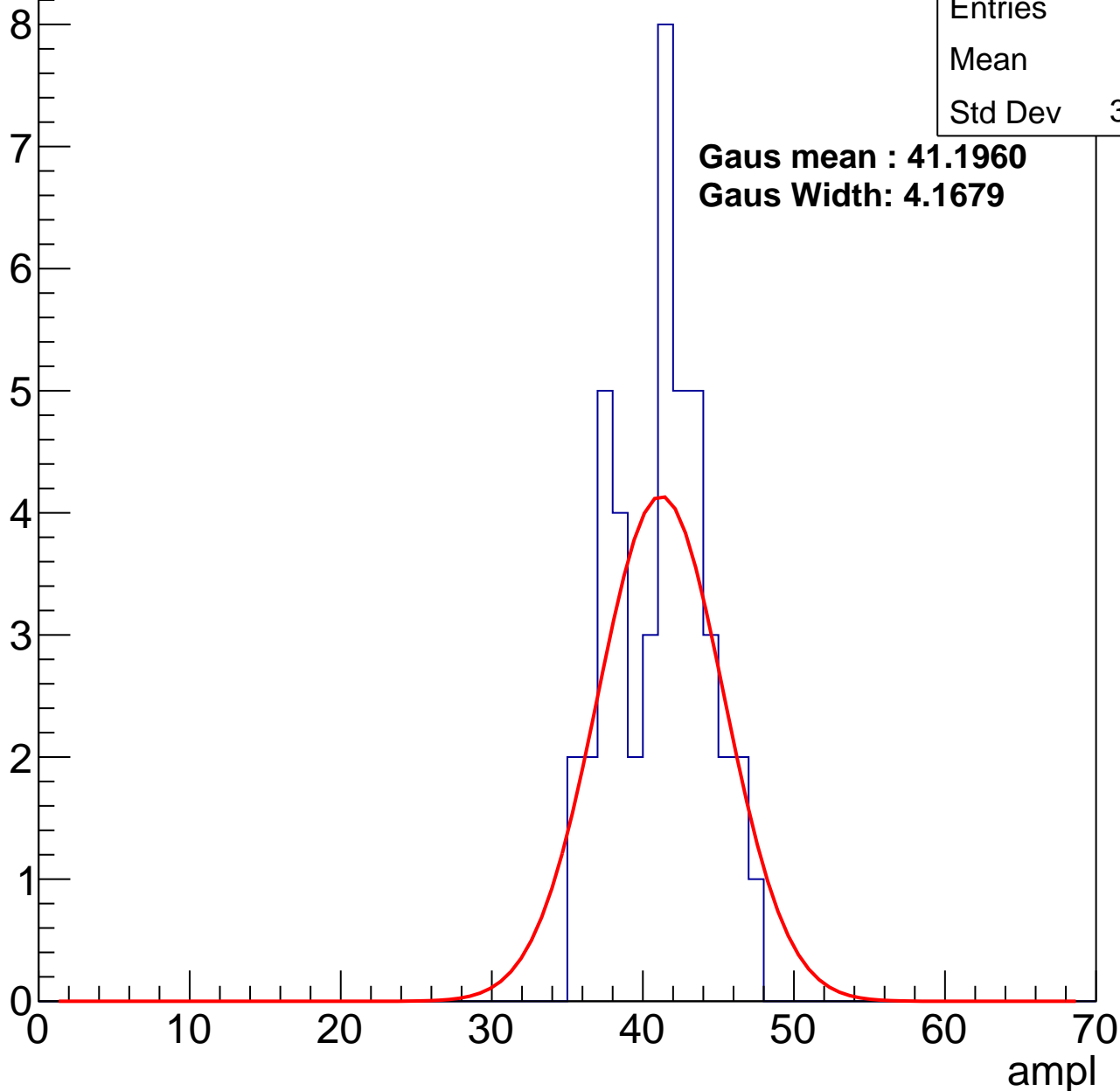
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	40.7
Std Dev	3.072

**Gaus mean : 41.1960**

**Gaus Width: 4.1679**

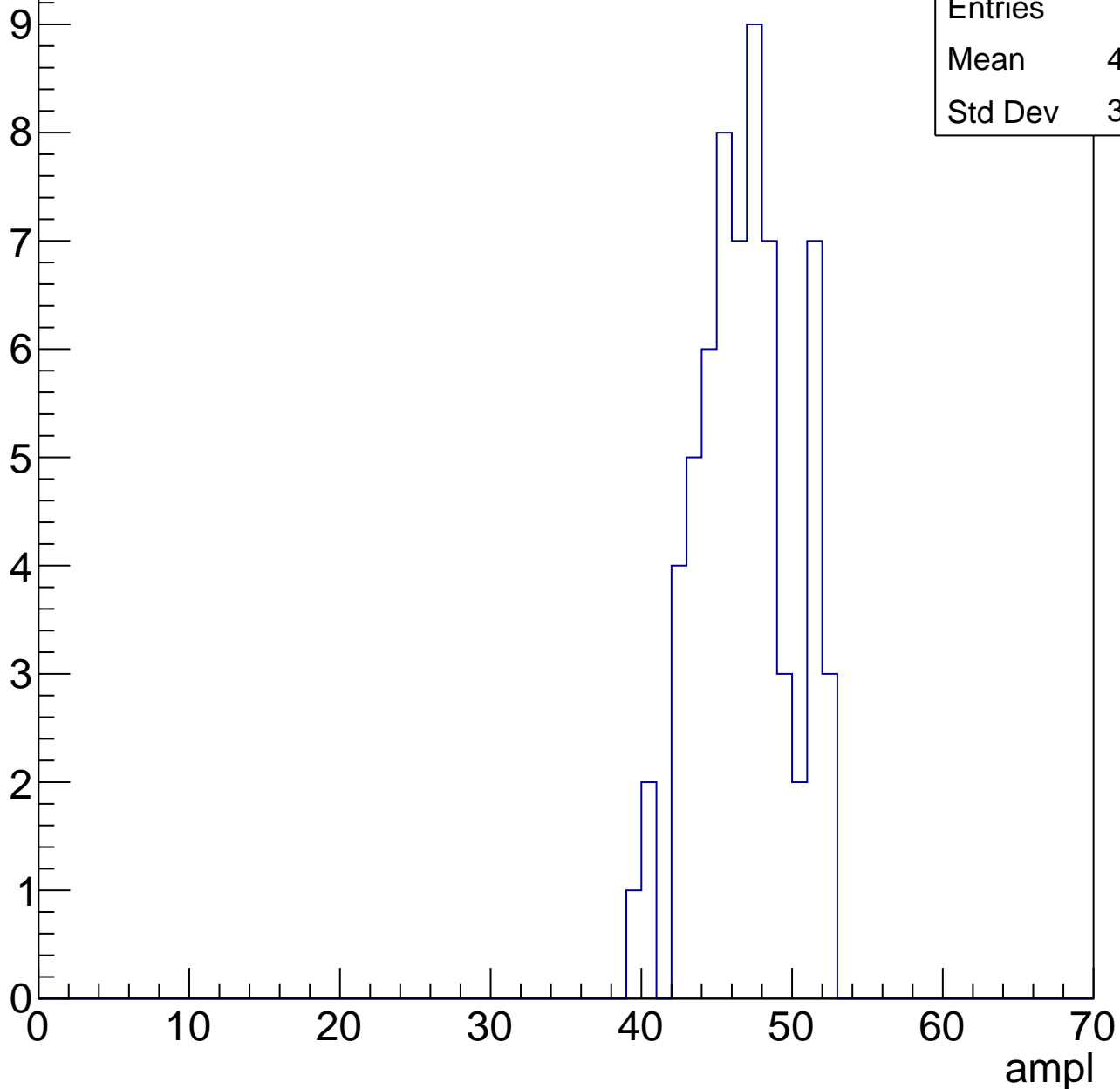


# B1L000S, U24-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.36
Std Dev	3.144

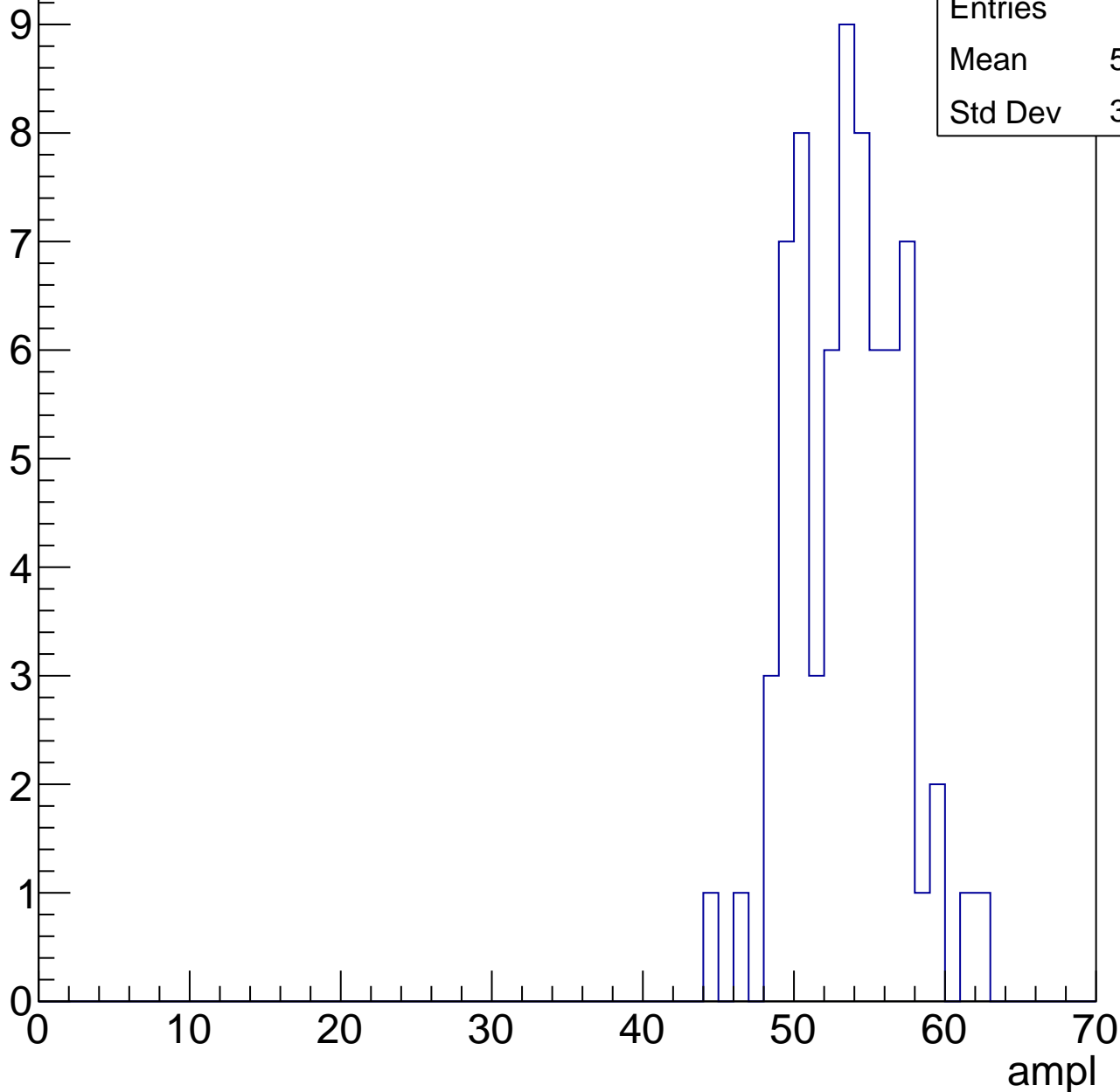


# B1L000S, U24-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	53.07
Std Dev	3.478

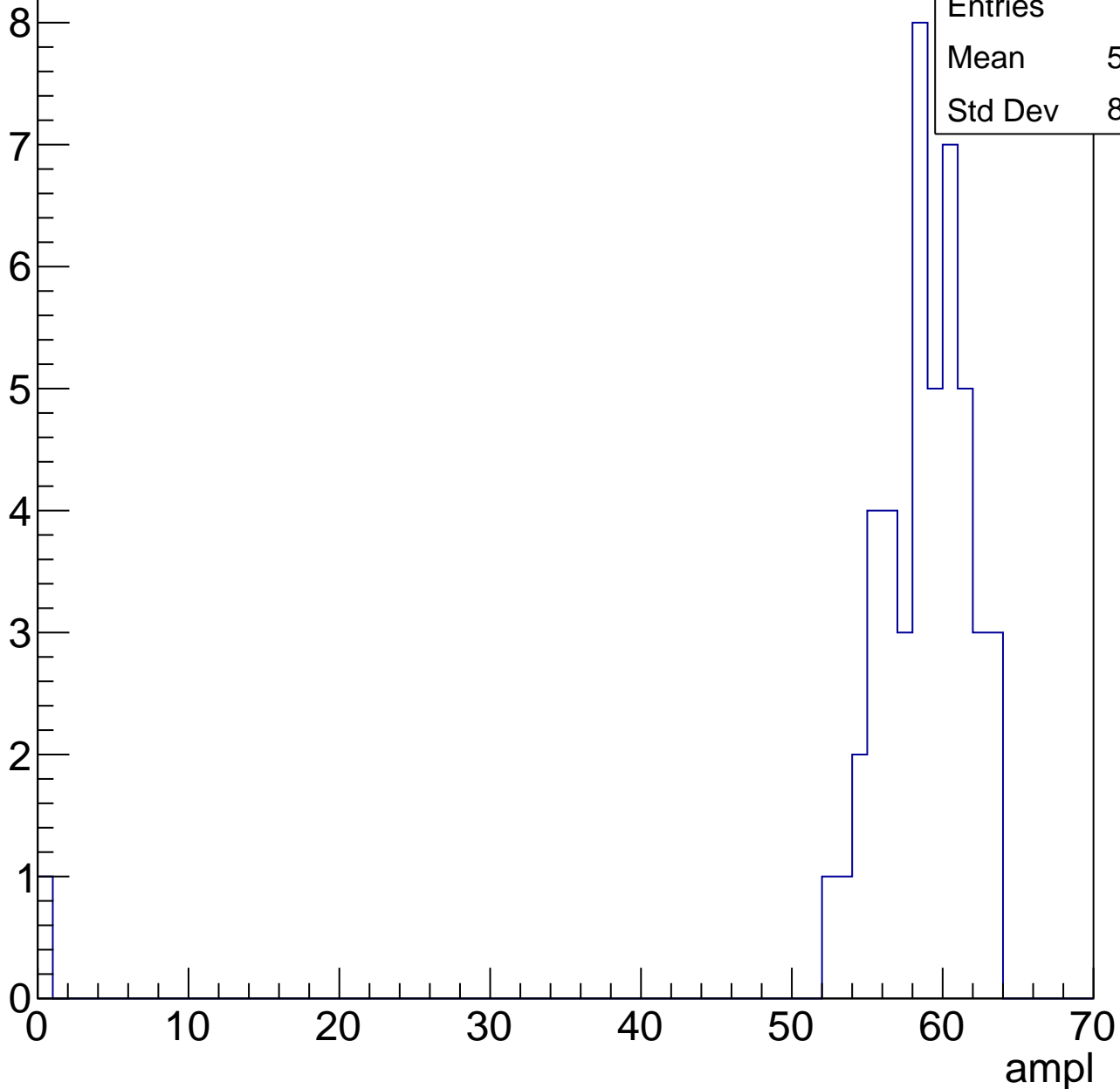


# B1L000S, U24-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

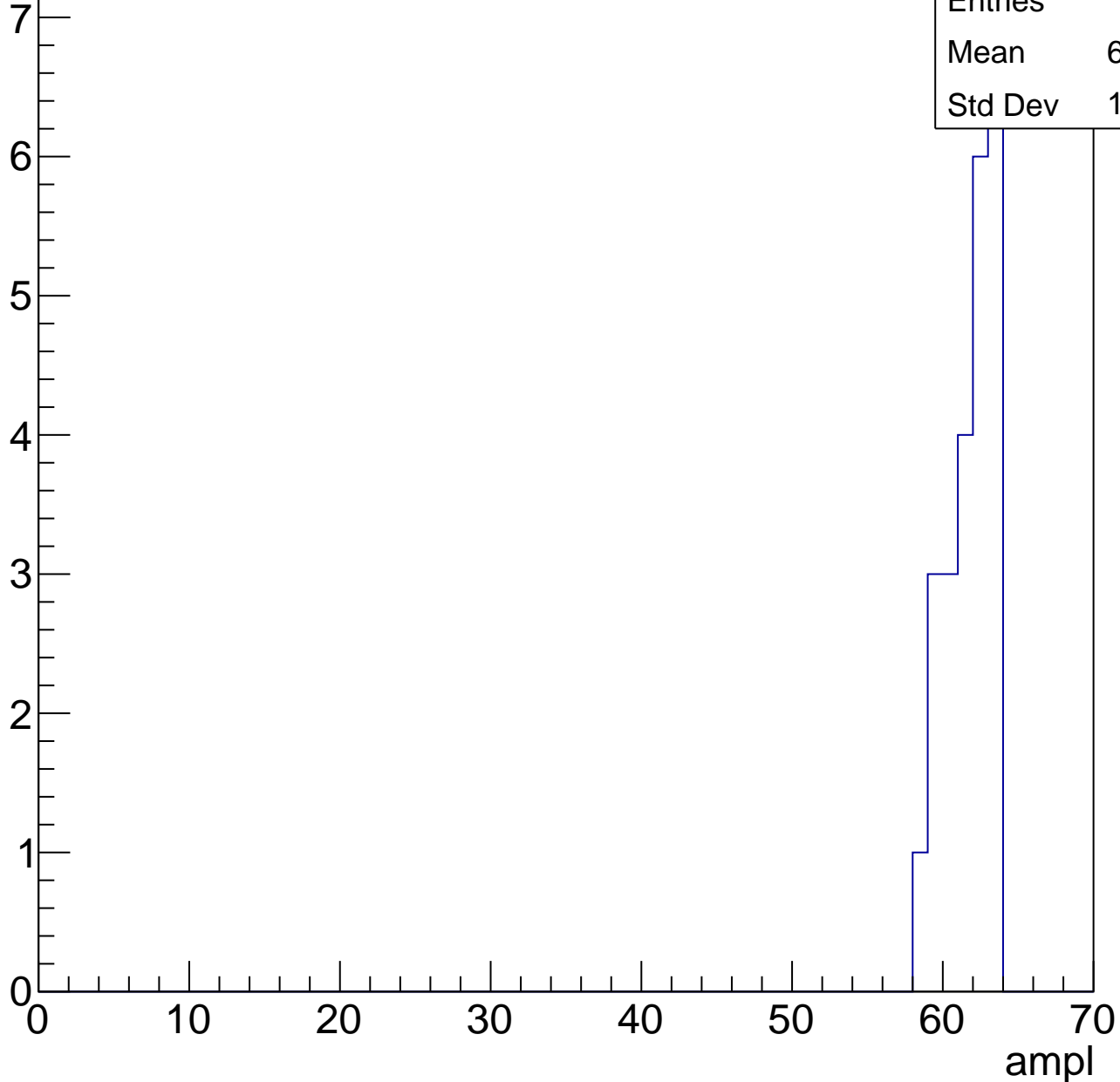
Entries	47
Mean	57.17
Std Dev	8.849



# B1L000S, U24-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U24-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	28.02
Std Dev	3.212

**Gaus mean : 28.4973**

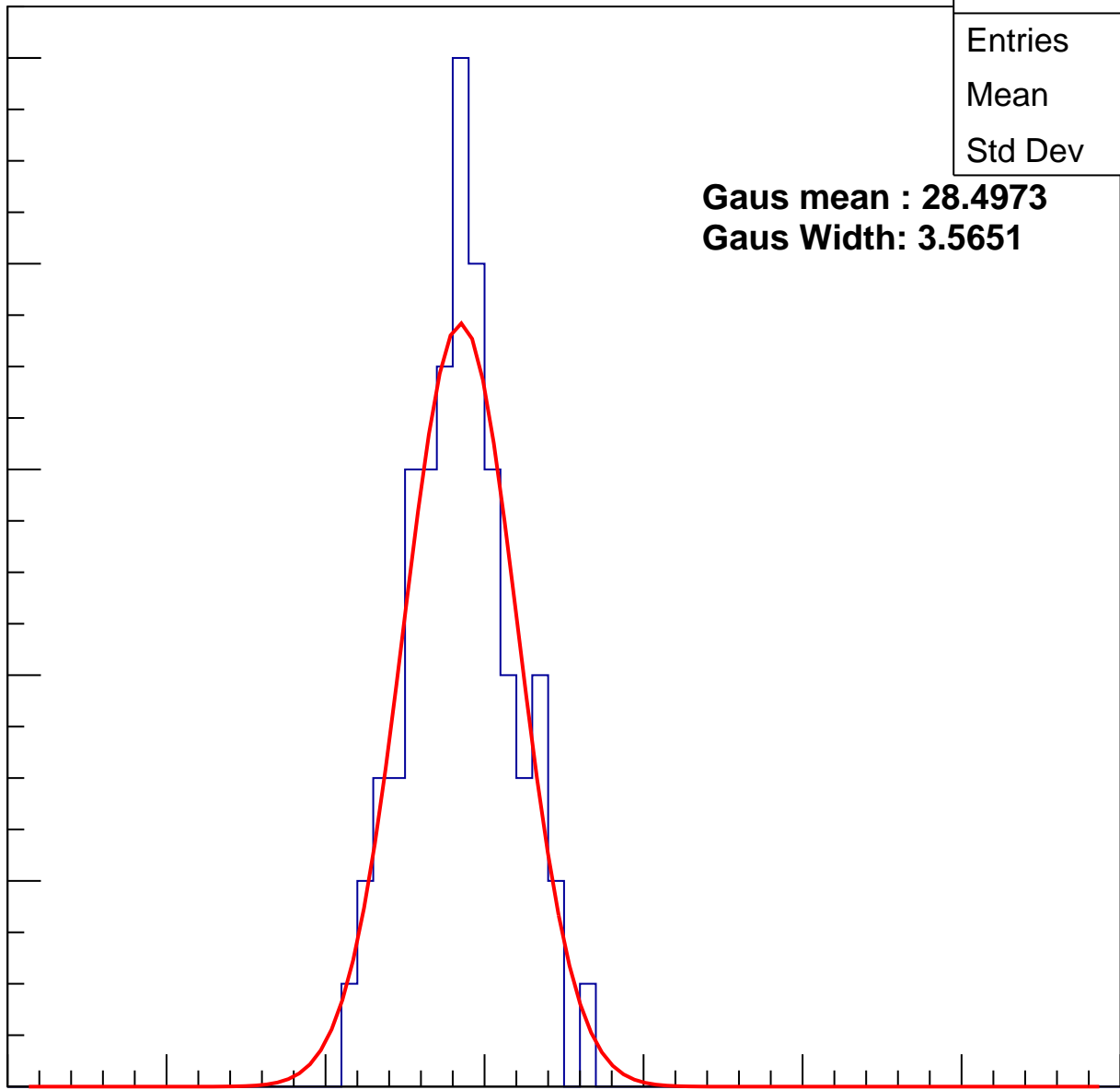
**Gaus Width: 3.5651**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U24-ch121, adc1

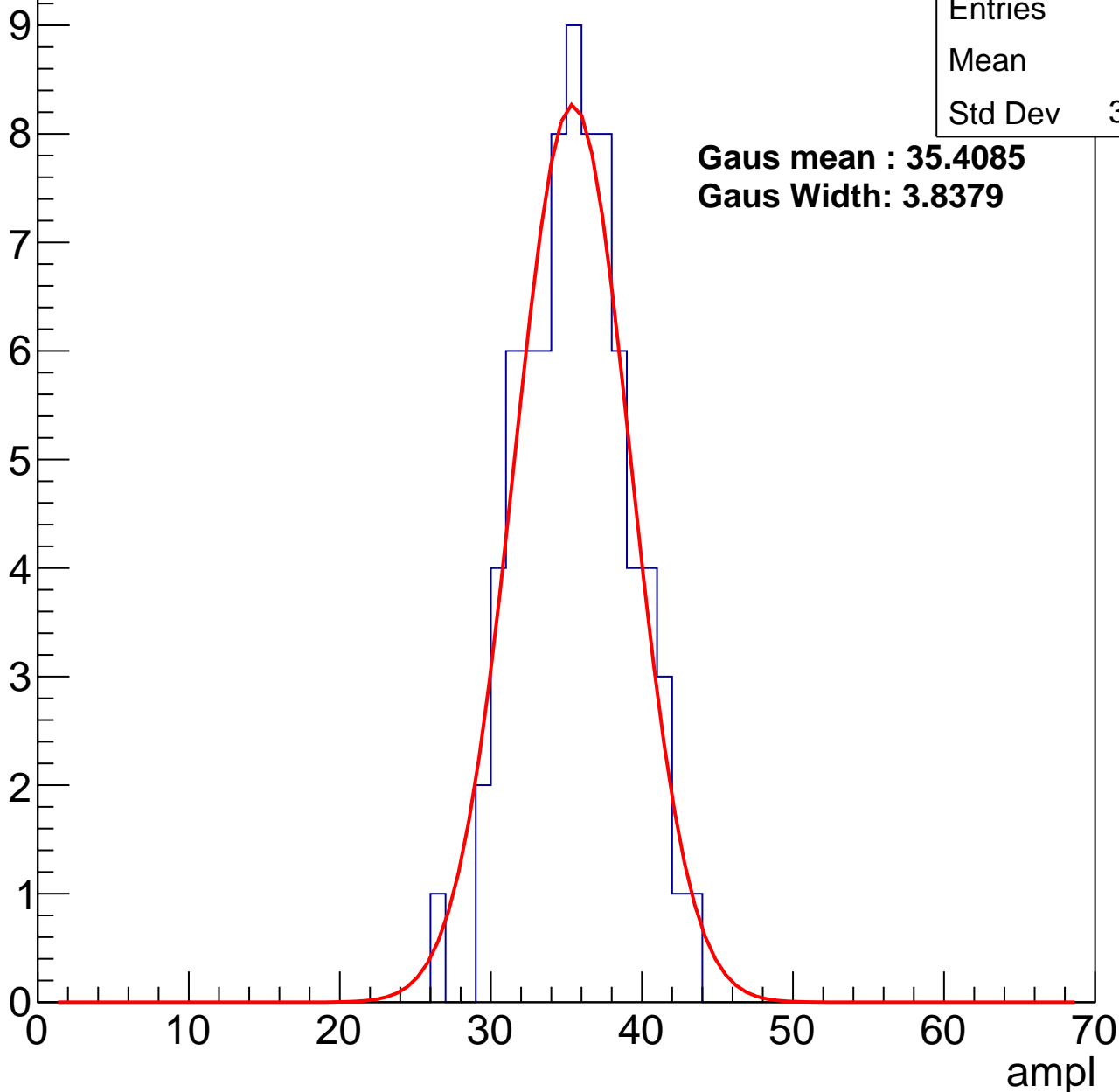
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	35.1
Std Dev	3.444

**Gaus mean : 35.4085**

**Gaus Width: 3.8379**



# B1L000S, U24-ch121, adc2

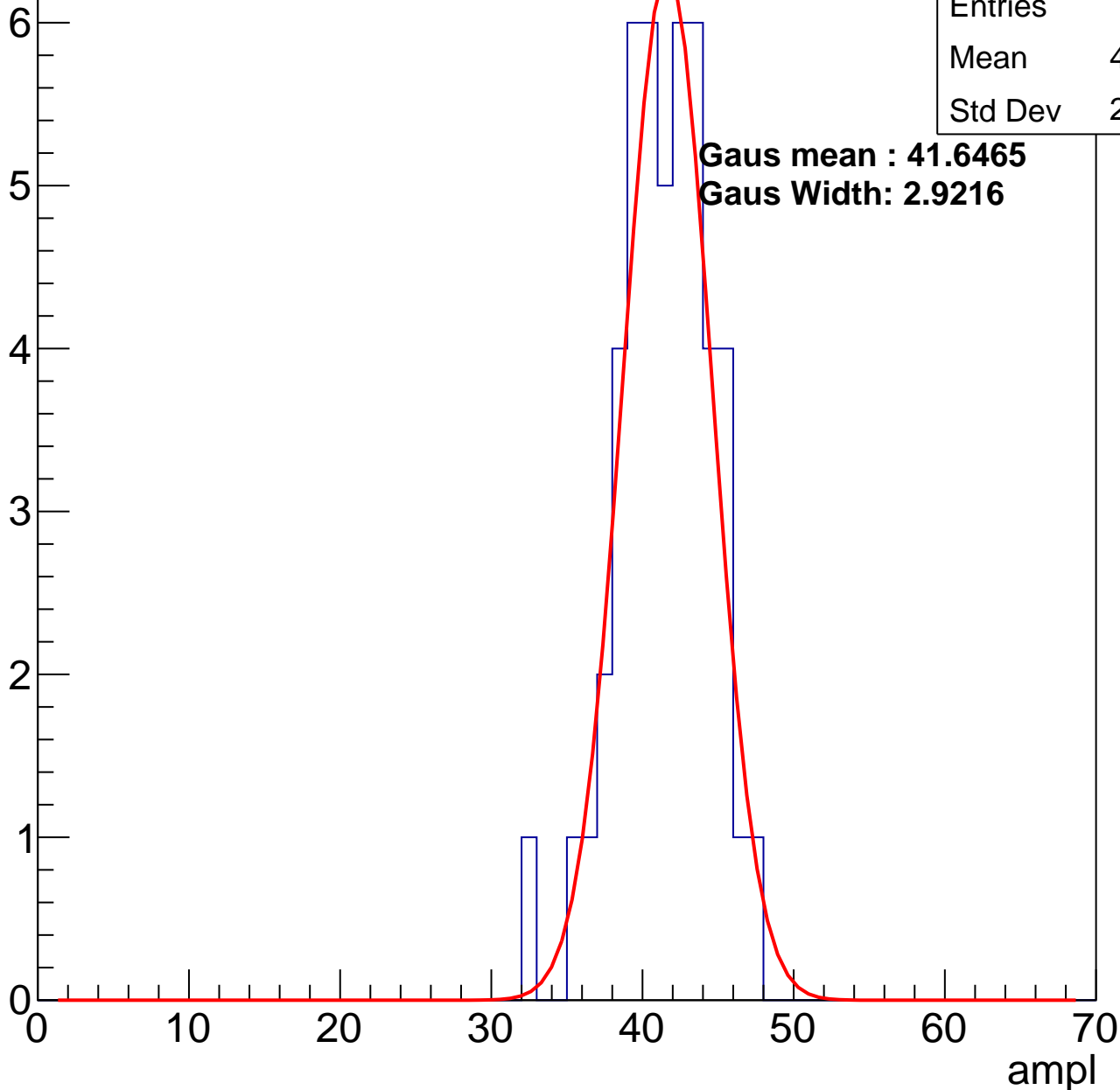
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	40.98
Std Dev	2.996

**Gaus mean : 41.6465**

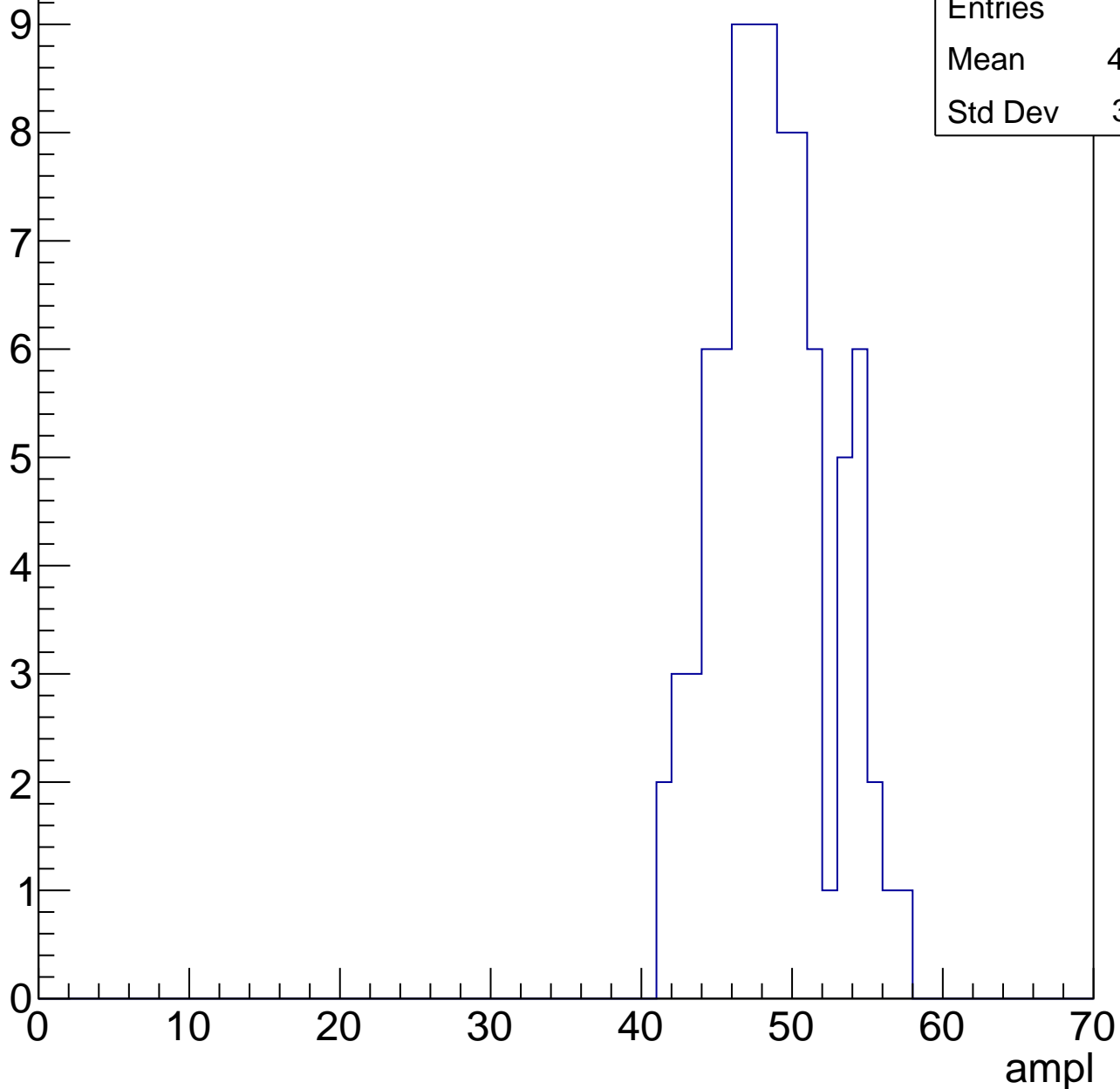
**Gaus Width: 2.9216**



# B1L000S, U24-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



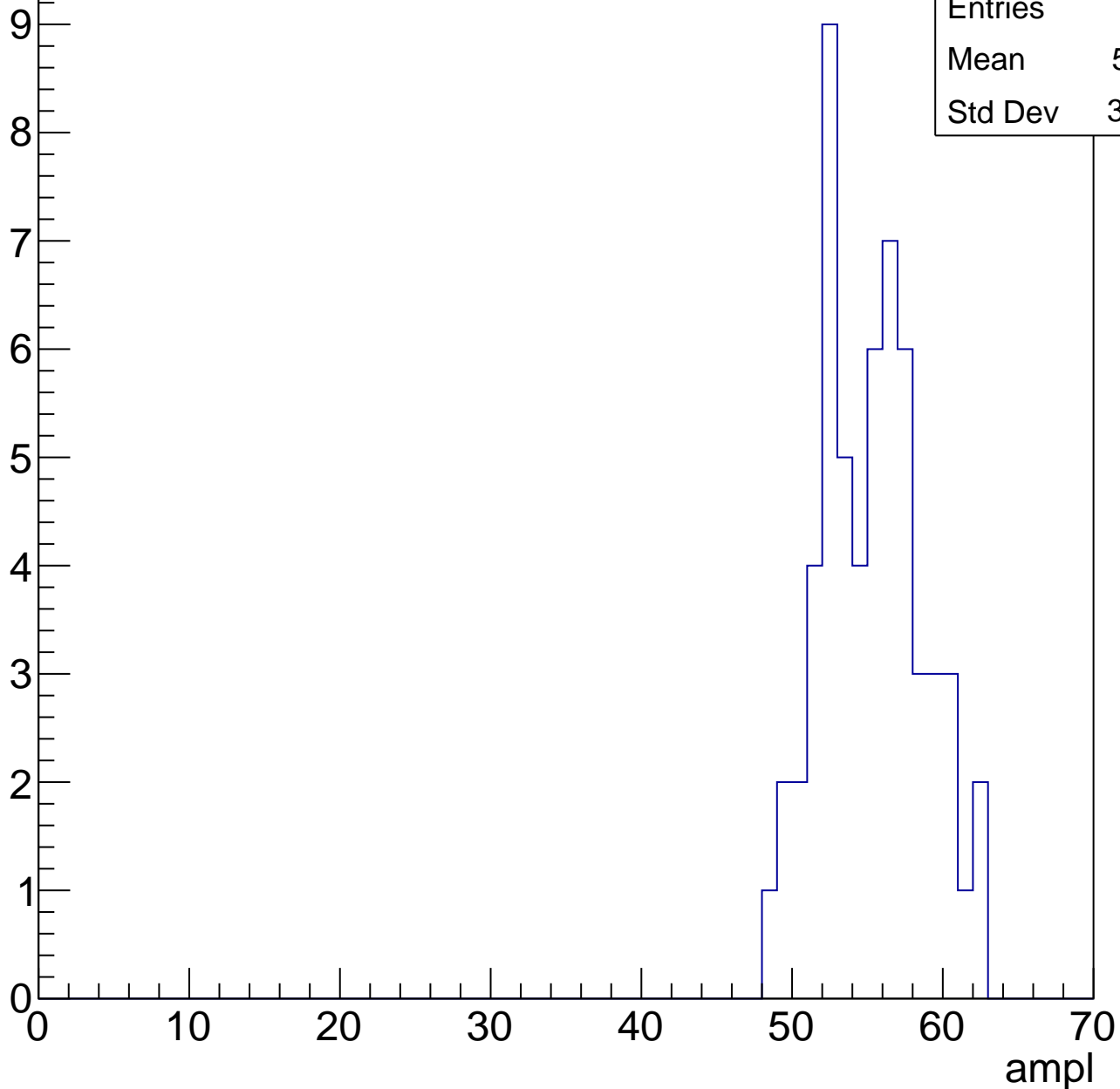
Entries	85
Mean	48.26
Std Dev	3.701

# B1L000S, U24-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	54.81
Std Dev	3.355

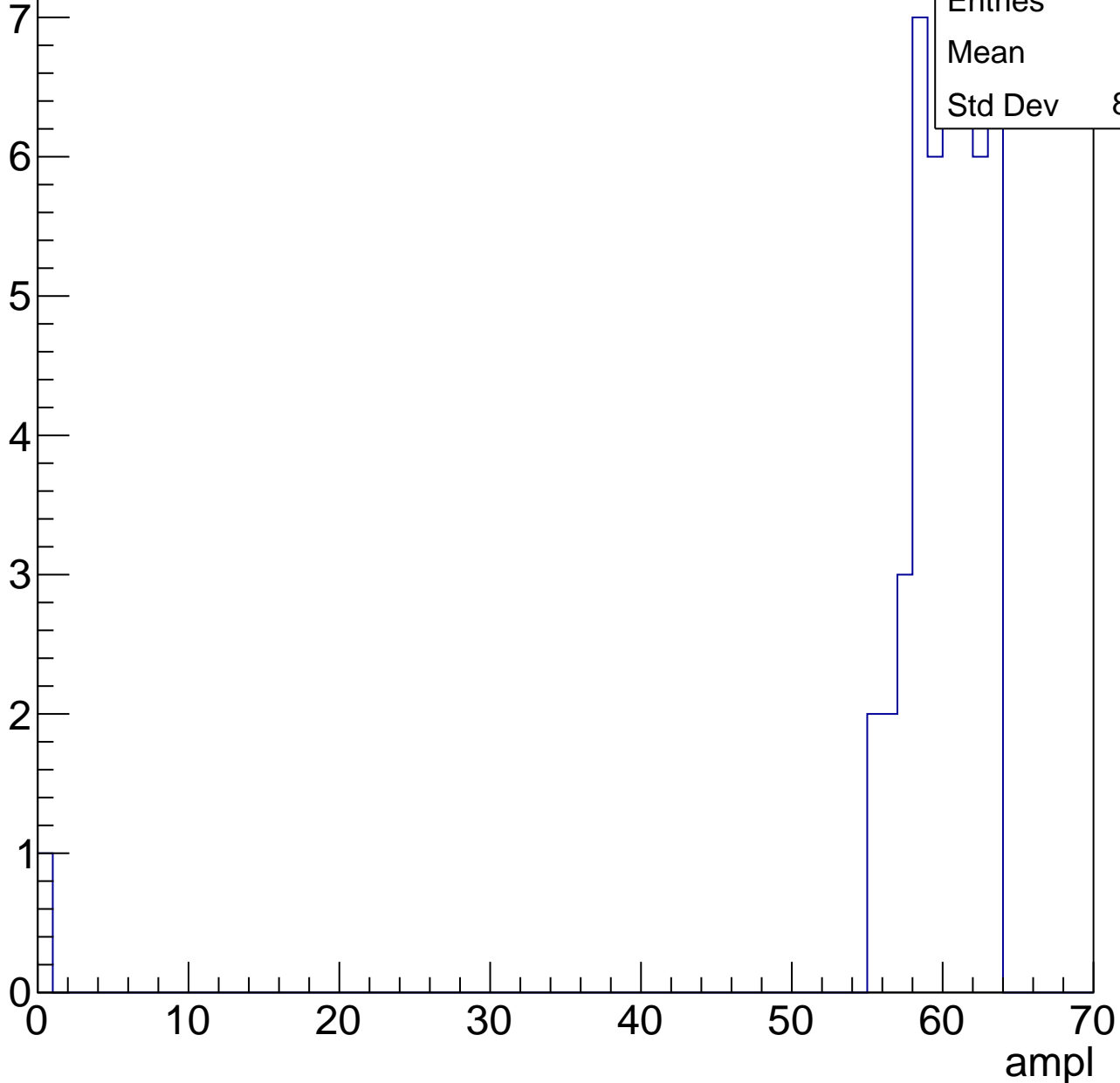


# B1L000S, U24-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

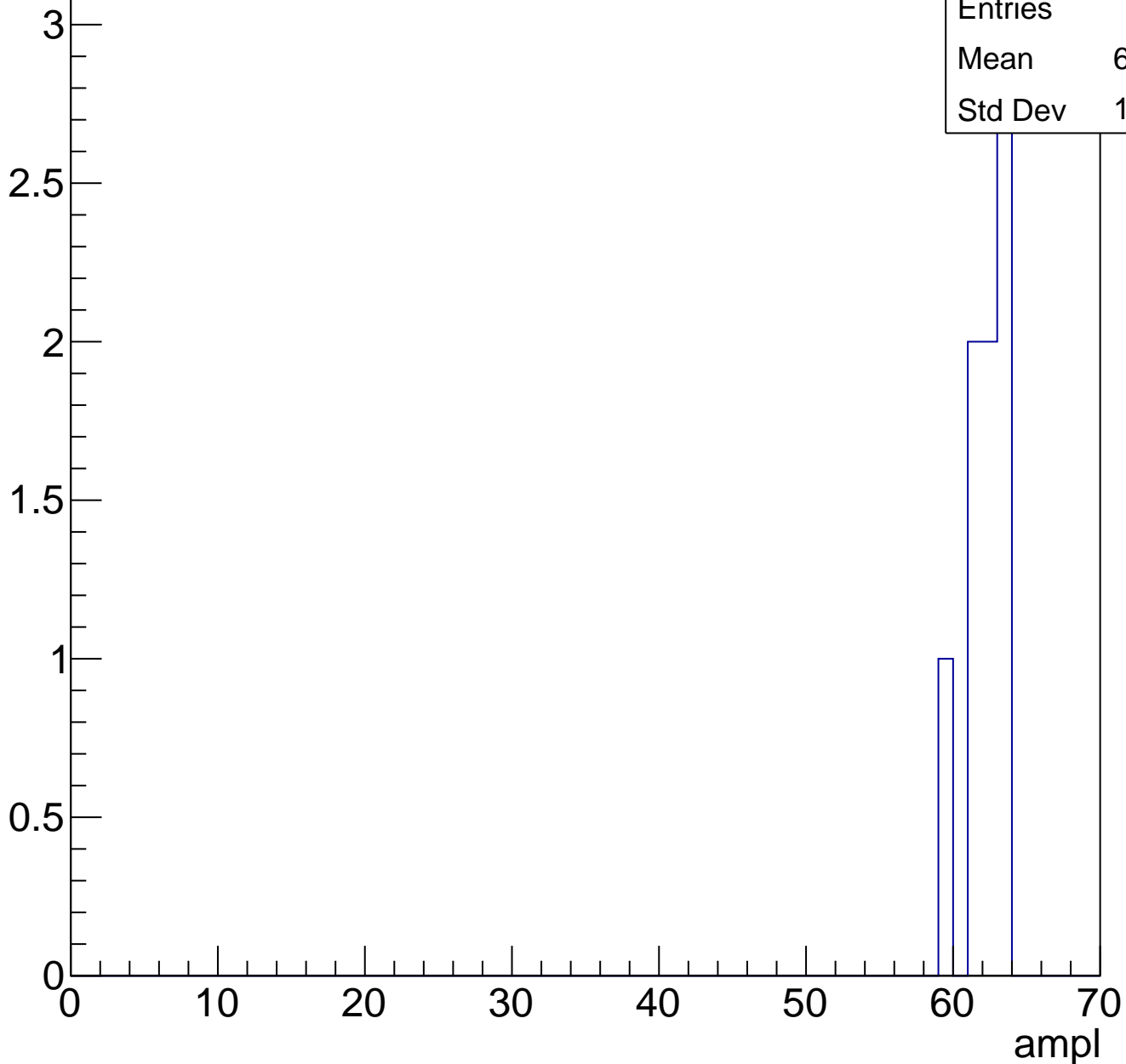
Entries	48
Mean	58.6
Std Dev	8.831



# B1L000S, U24-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch122, adc0

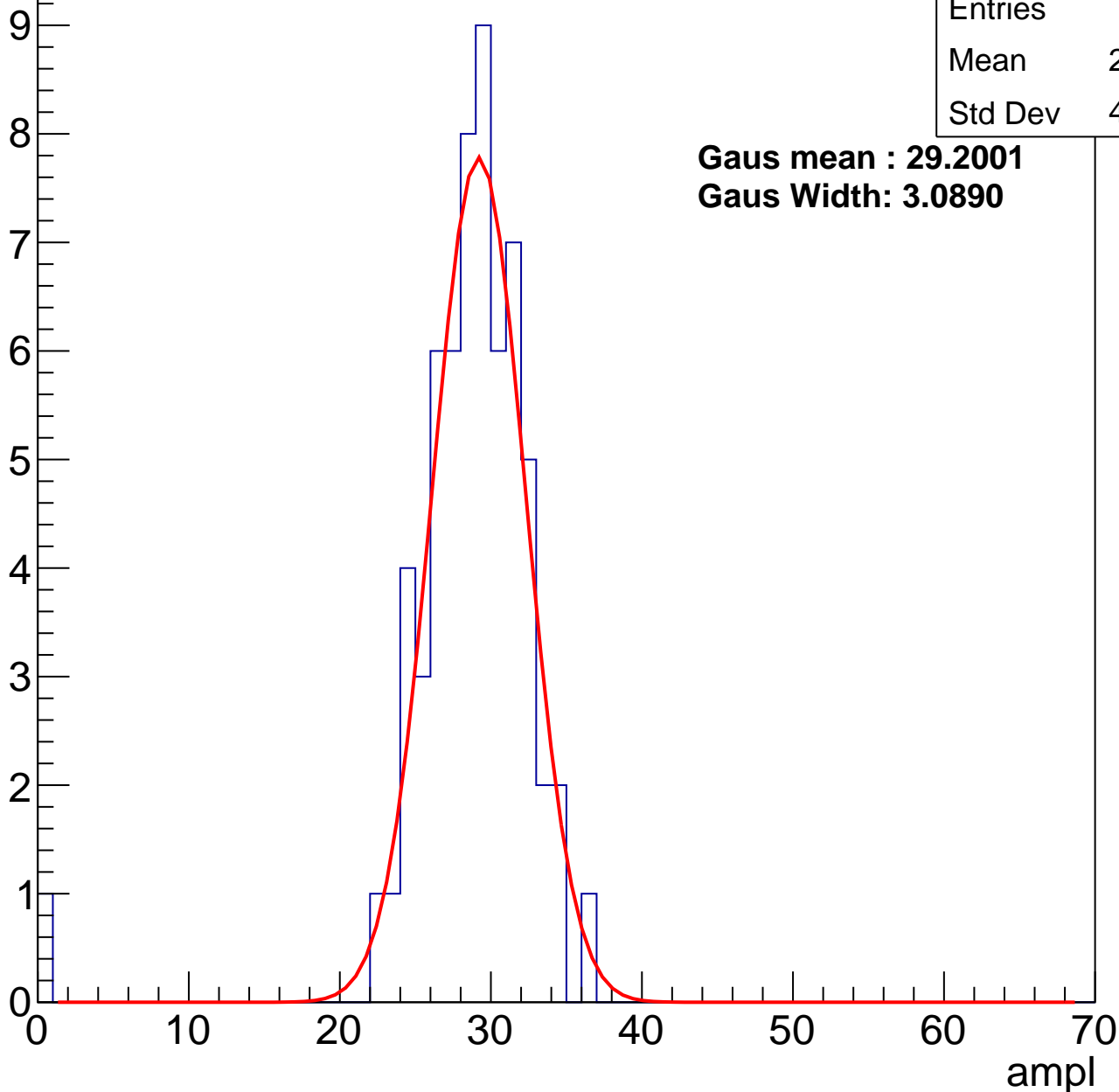
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.16
Std Dev	4.625

**Gaus mean : 29.2001**

**Gaus Width: 3.0890**



# B1L000S, U24-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	35.41
Std Dev	2.981

**Gaus mean : 36.0379**

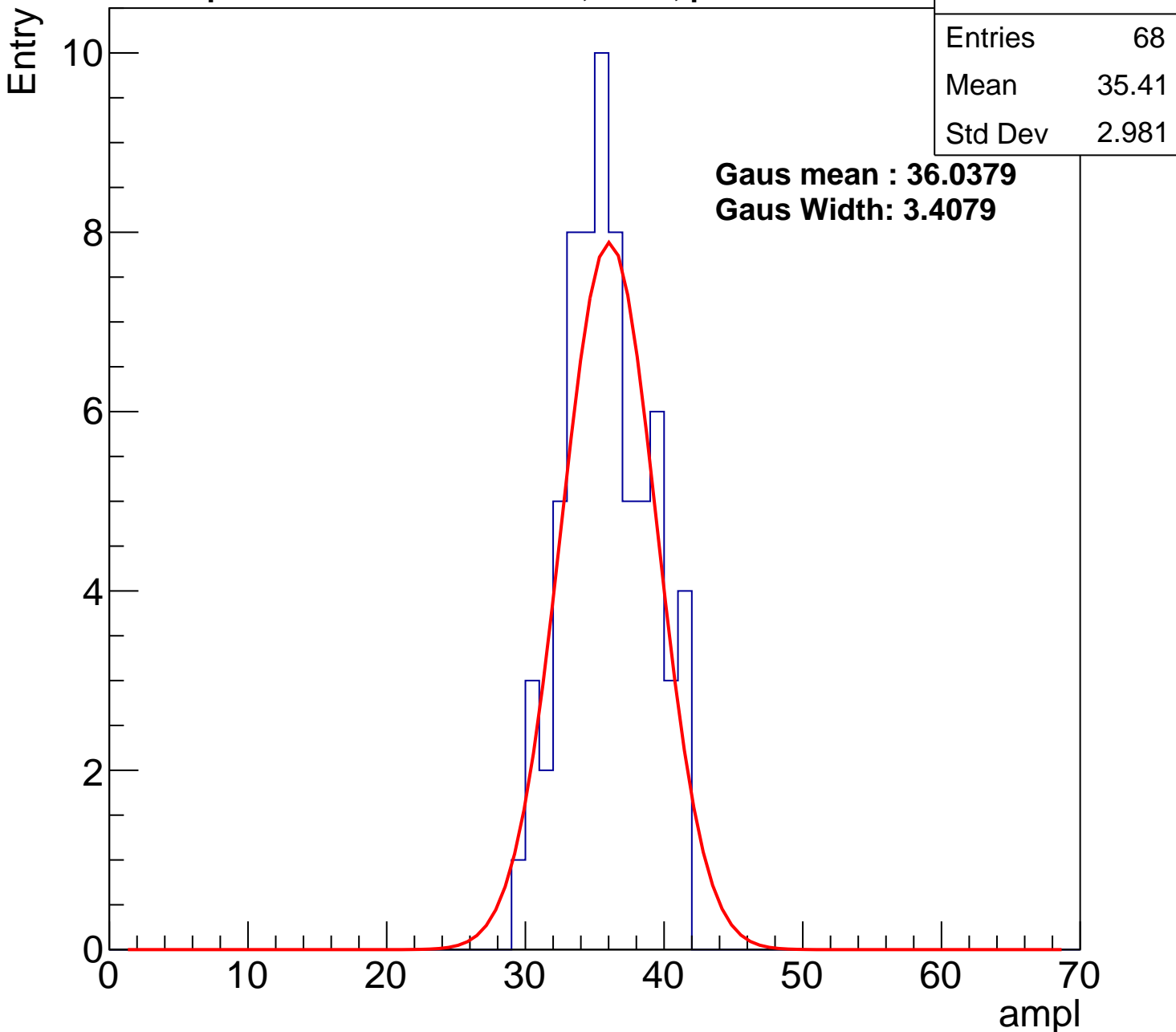
**Gaus Width: 3.4079**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U24-ch122, adc2

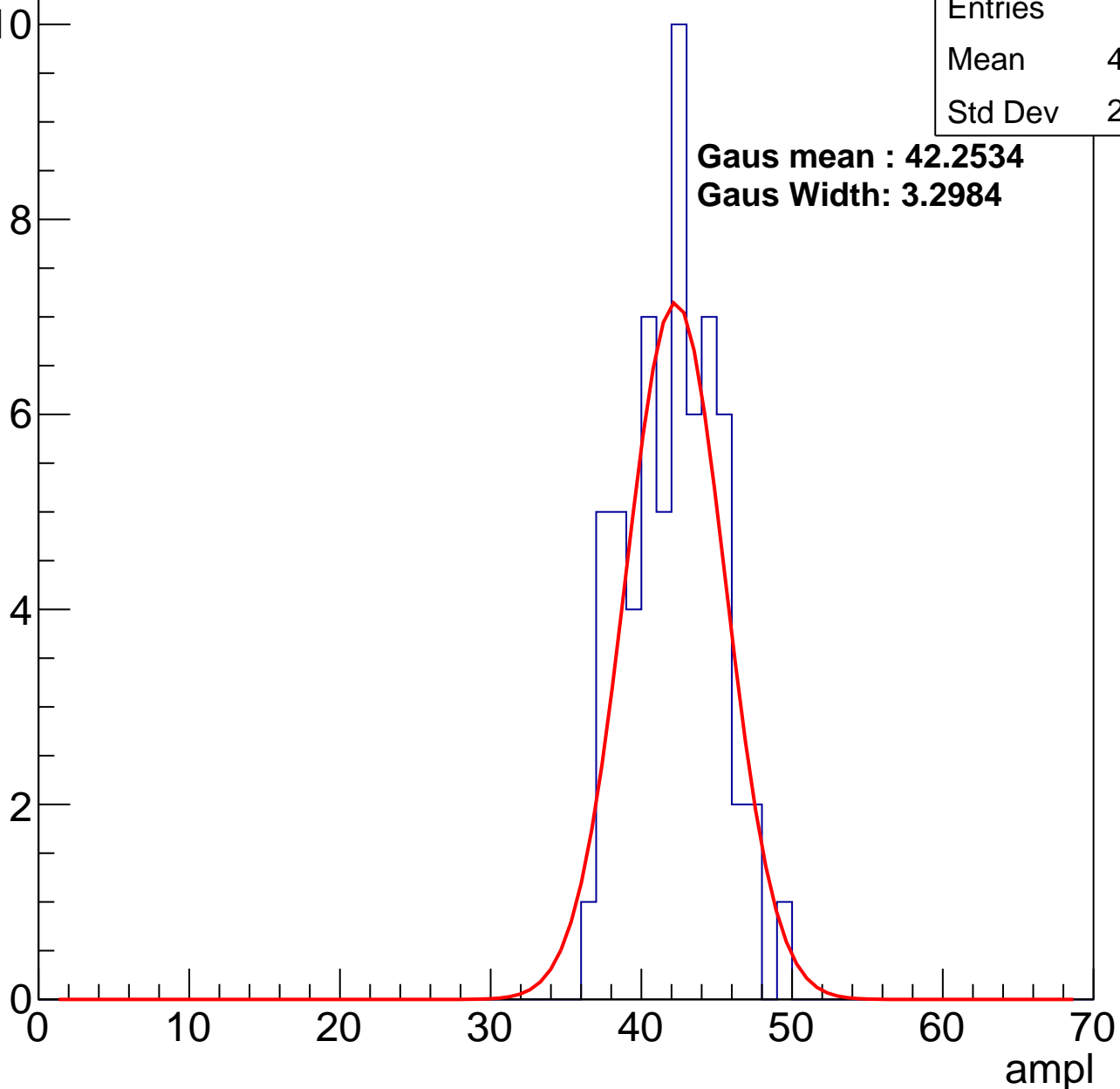
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.69
Std Dev	2.928

**Gaus mean : 42.2534**

**Gaus Width: 3.2984**

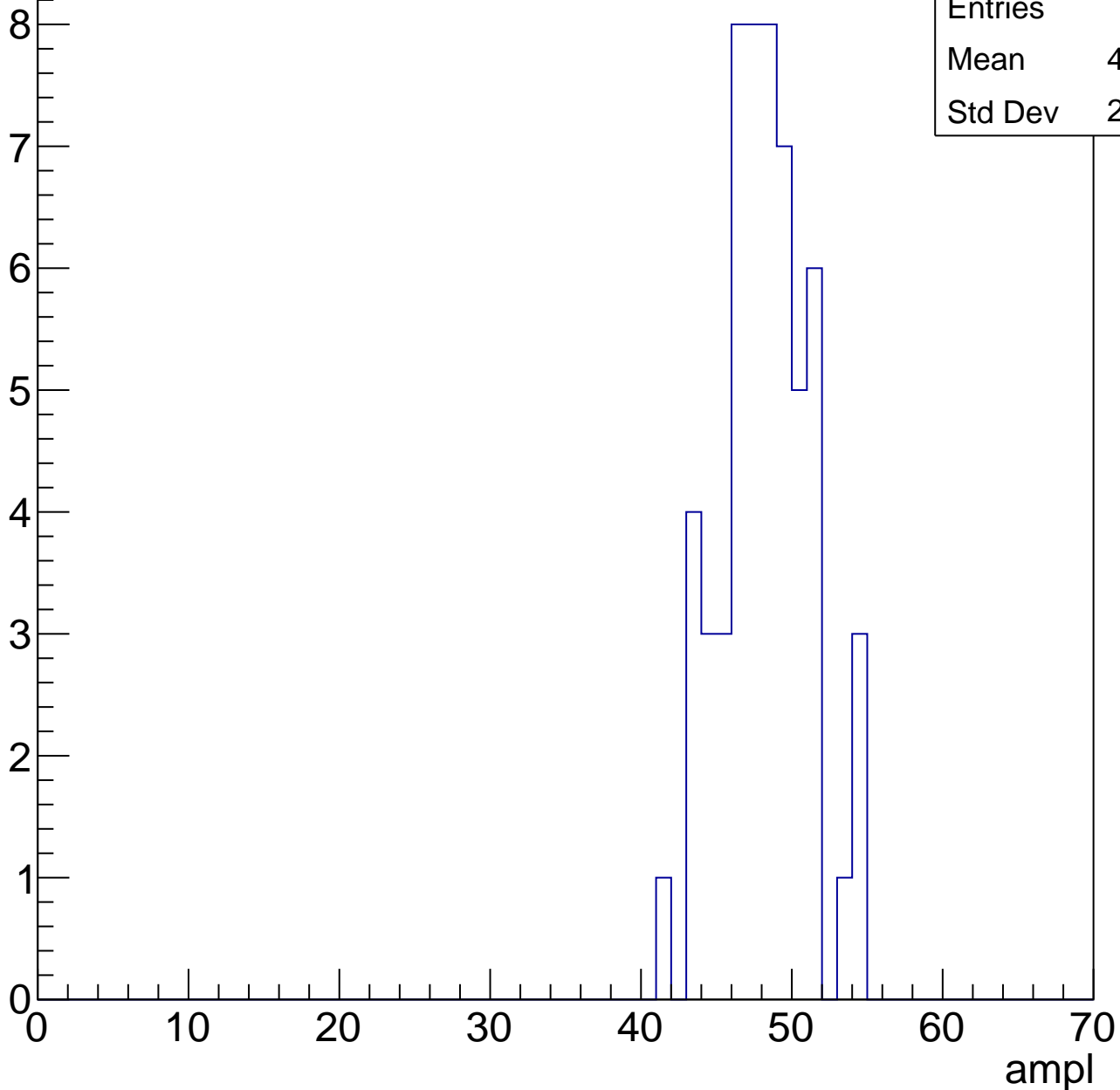


# B1L000S, U24-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.75
Std Dev	2.885

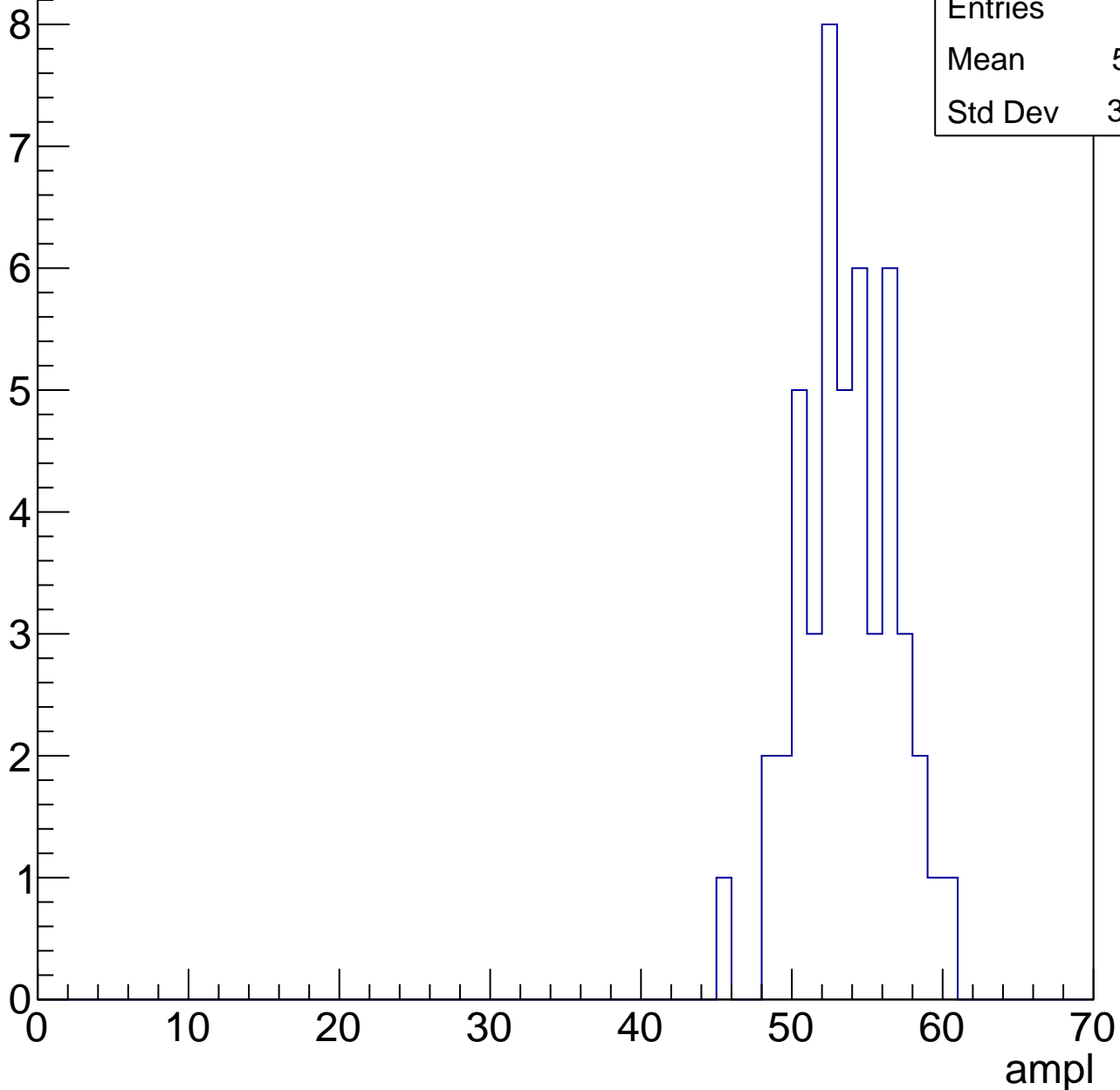


# B1L000S, U24-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	53.21
Std Dev	3.109

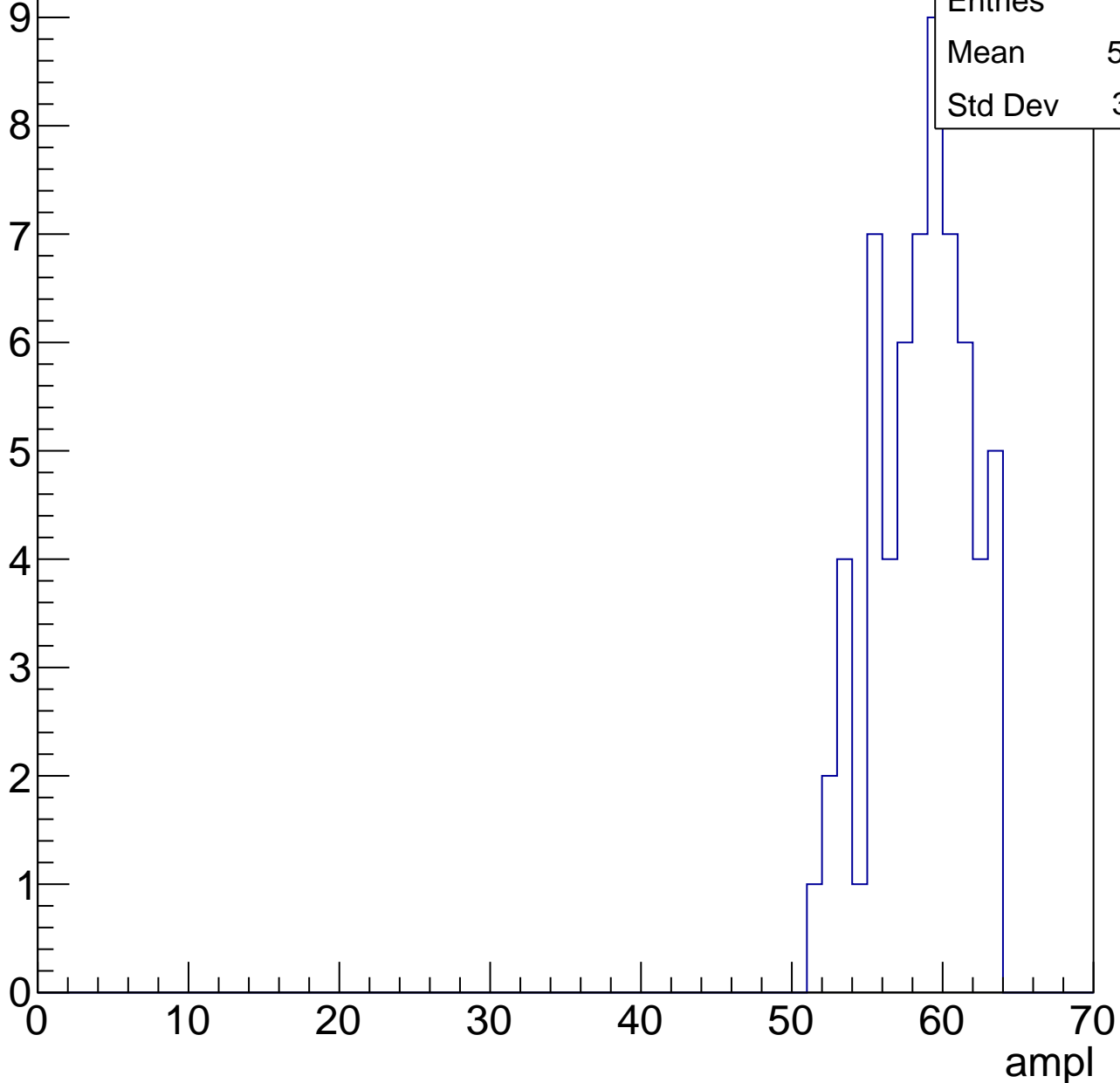


# B1L000S, U24-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	58.06
Std Dev	3.091

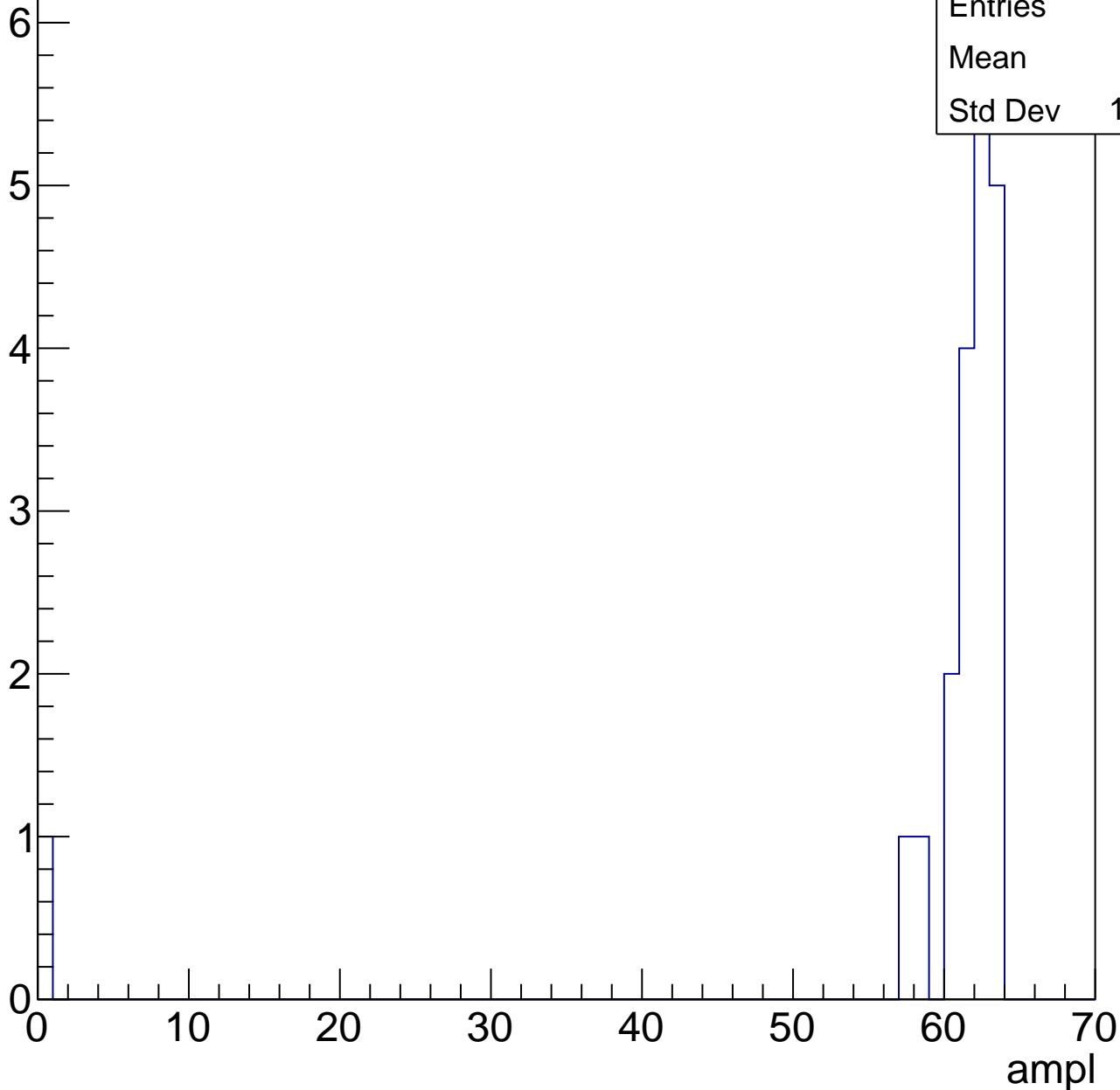


# B1L000S, U24-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.3
Std Dev	13.47





# B1L000S, U24-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U24-ch123, adc0

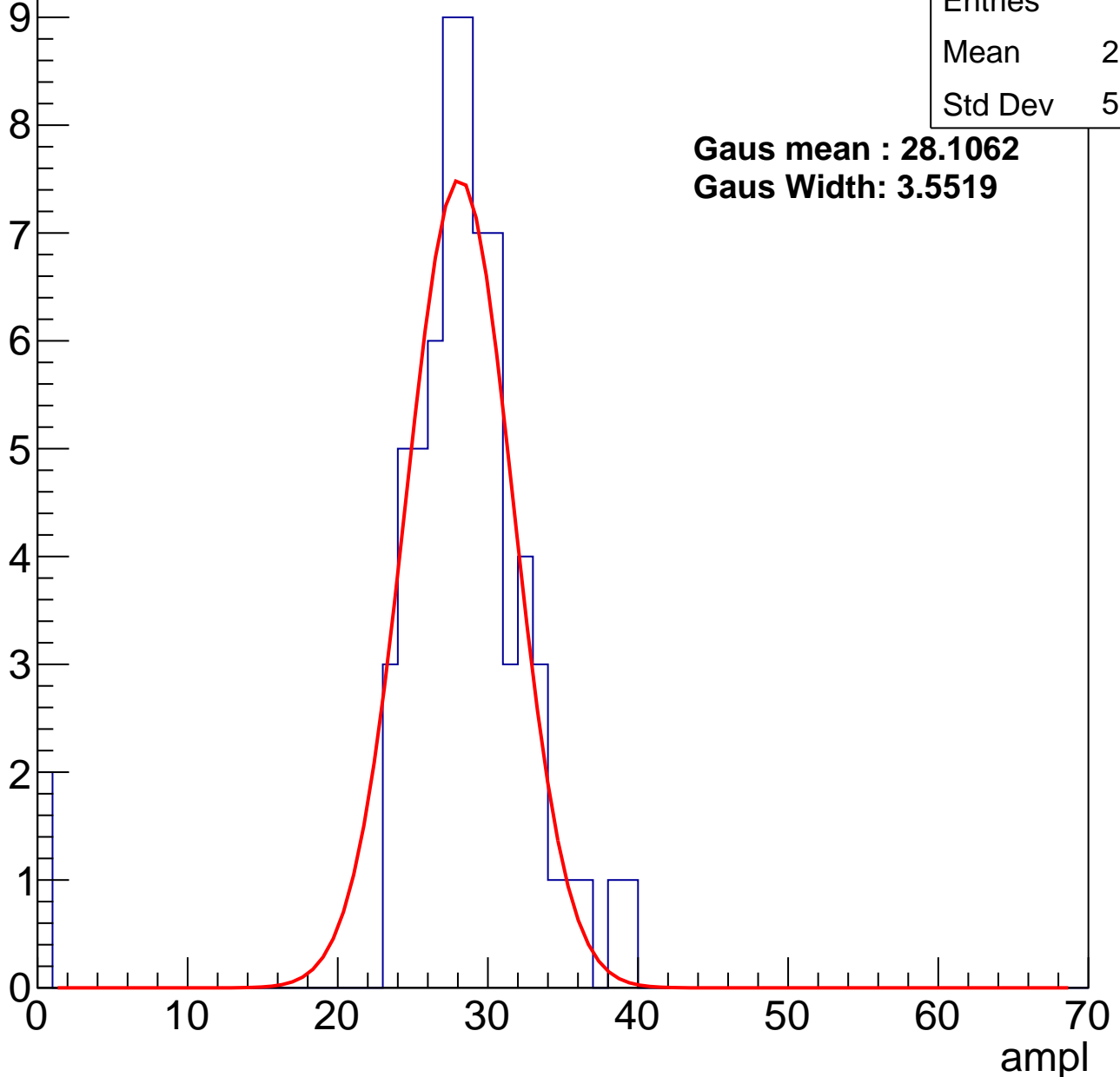
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.65
Std Dev	5.896

**Gaus mean : 28.1062**

**Gaus Width: 3.5519**



# B1L000S, U24-ch123, adc1

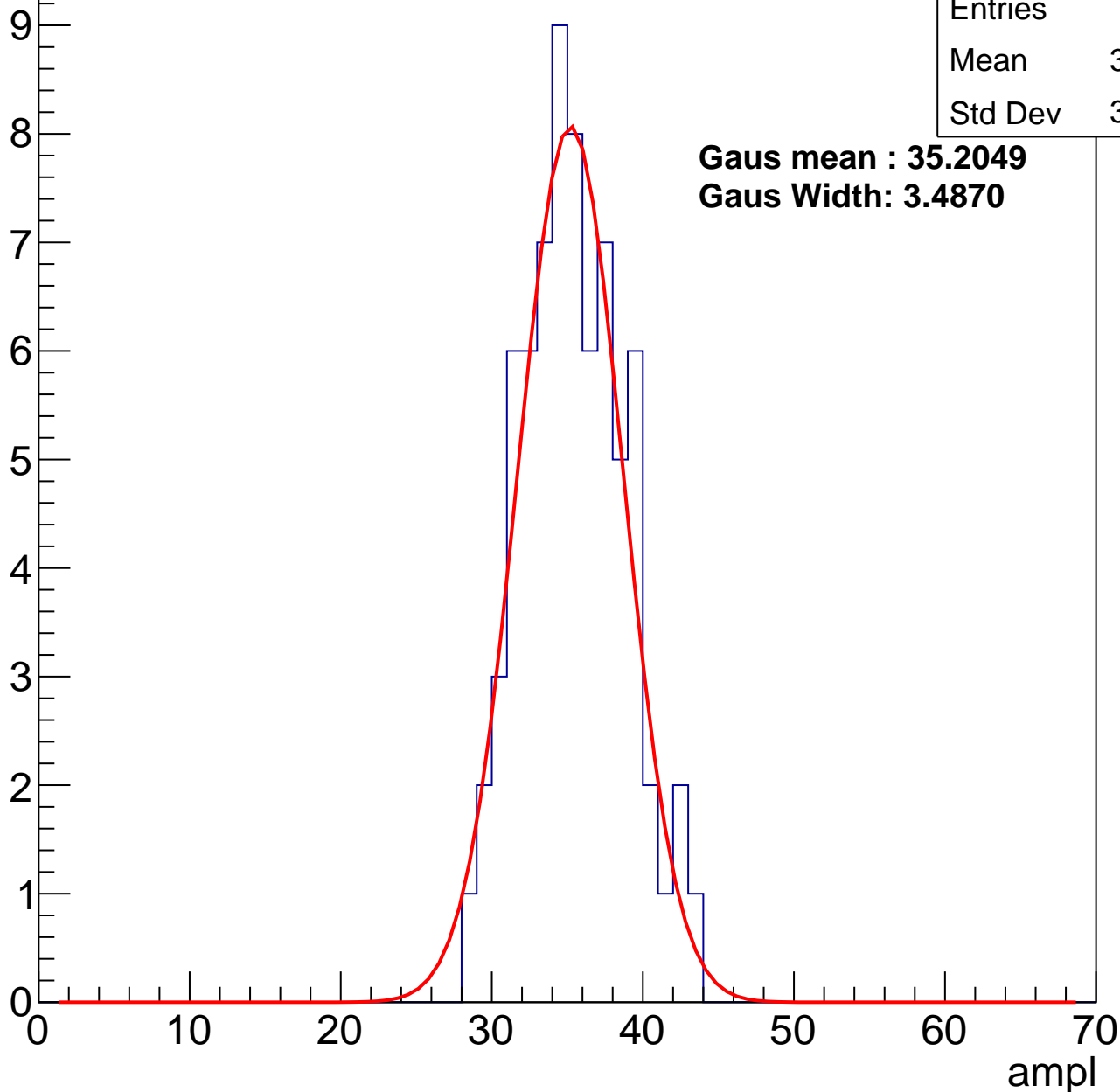
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.97
Std Dev	3.346

**Gaus mean : 35.2049**

**Gaus Width: 3.4870**



# B1L000S, U24-ch123, adc2

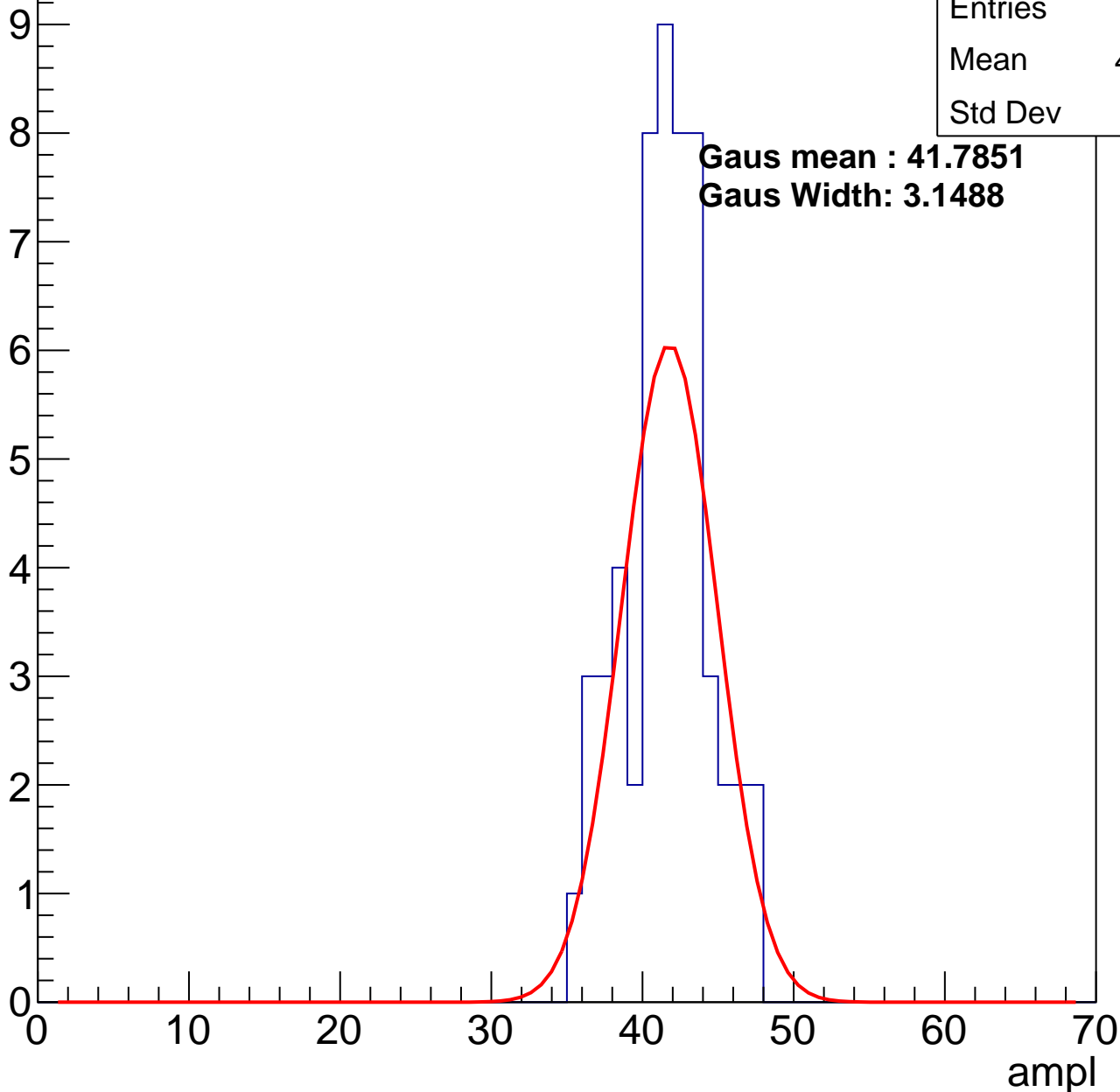
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.11
Std Dev	2.8

**Gaus mean : 41.7851**

**Gaus Width: 3.1488**

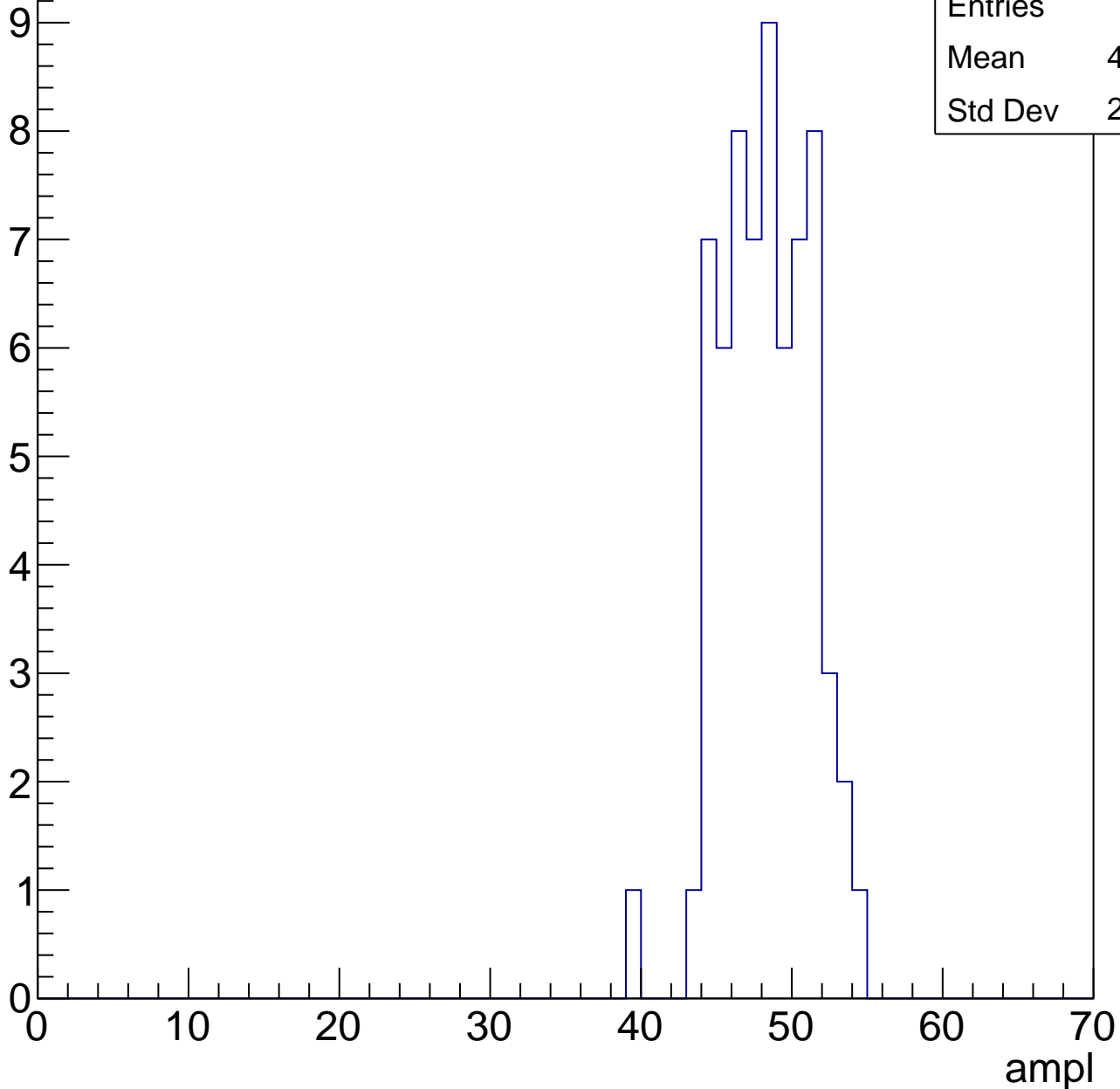


# B1L000S, U24-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

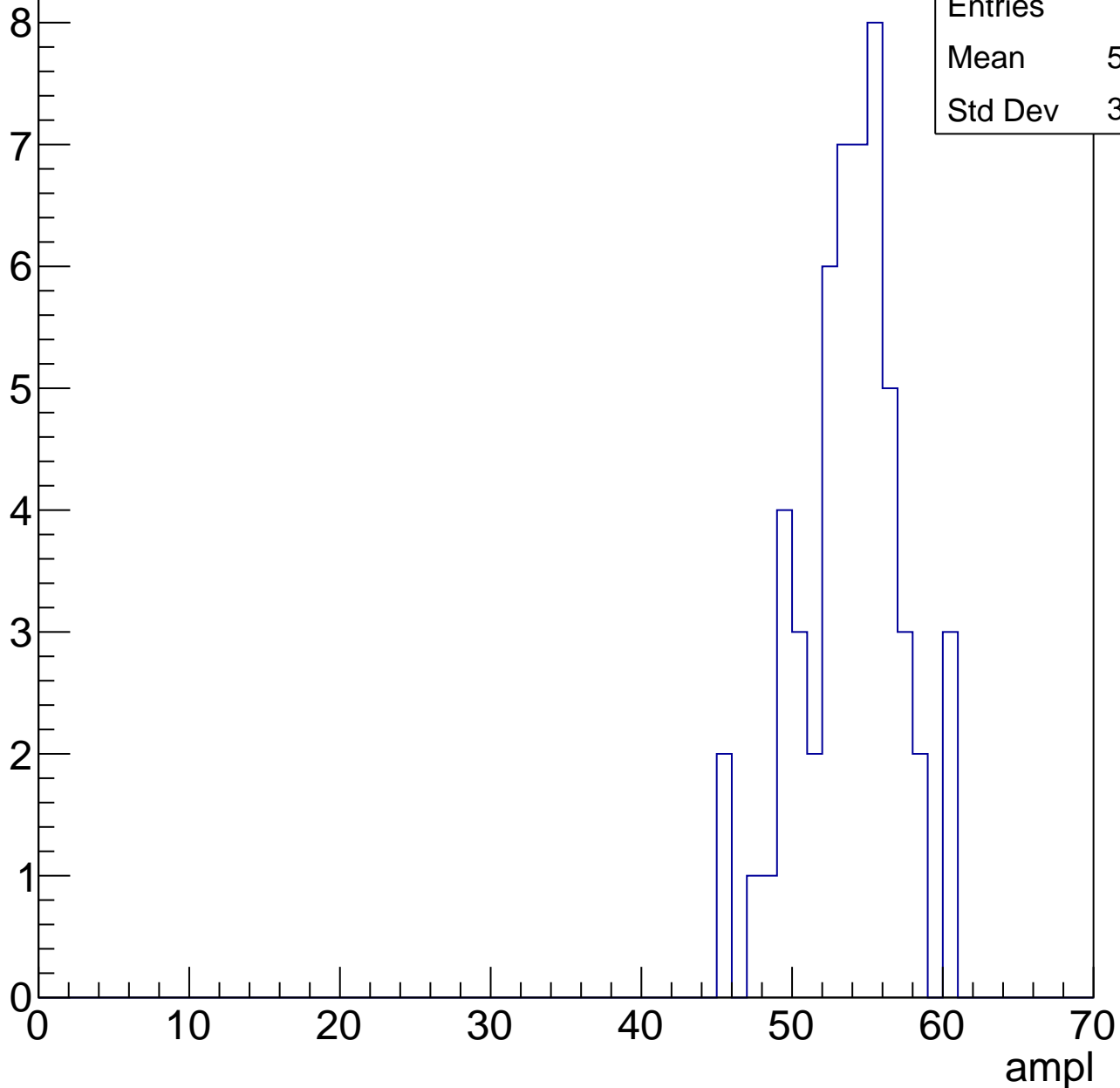
Entries	66
Mean	47.83
Std Dev	2.885



# B1L000S, U24-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

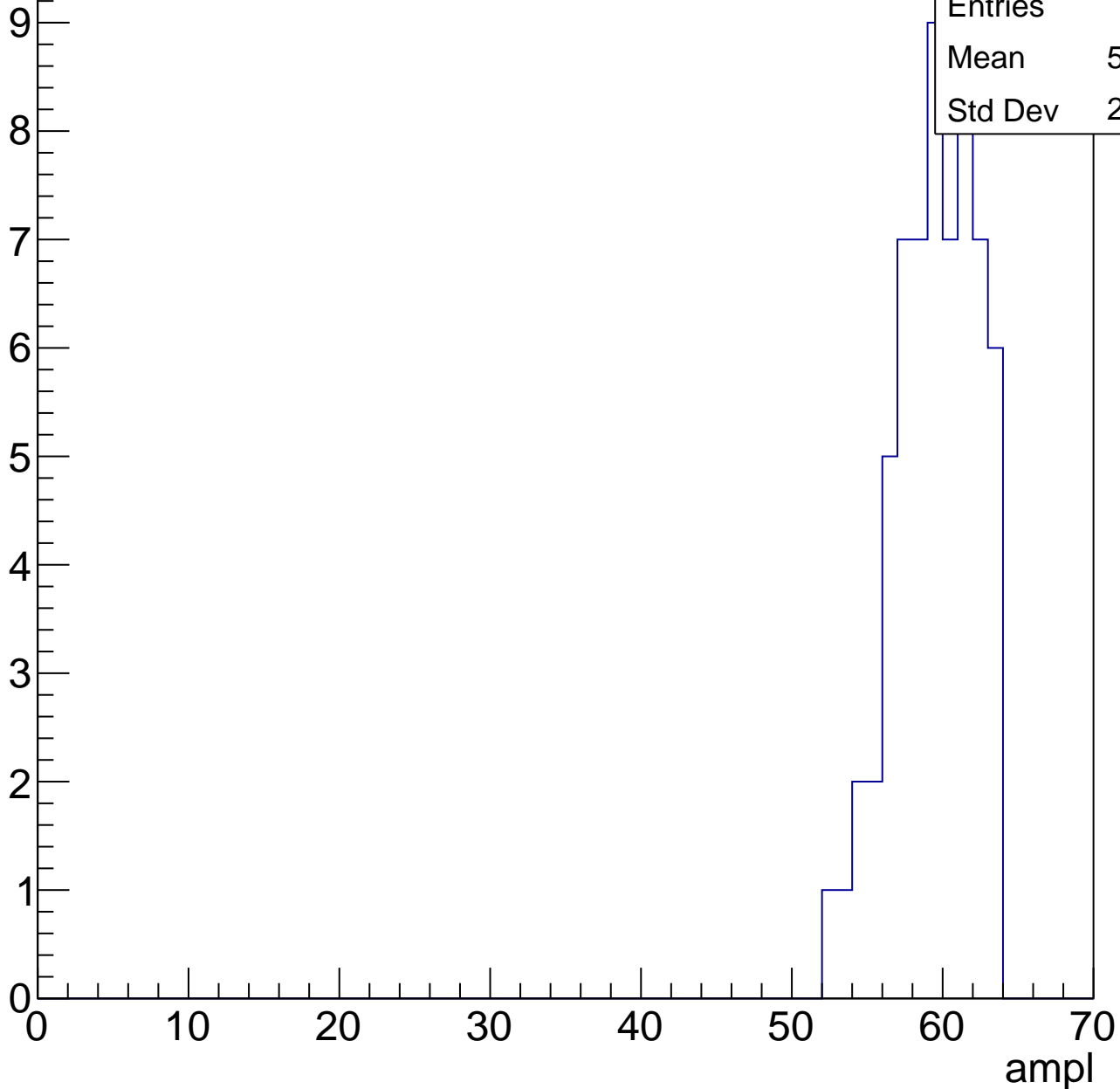


# B1L000S, U24-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

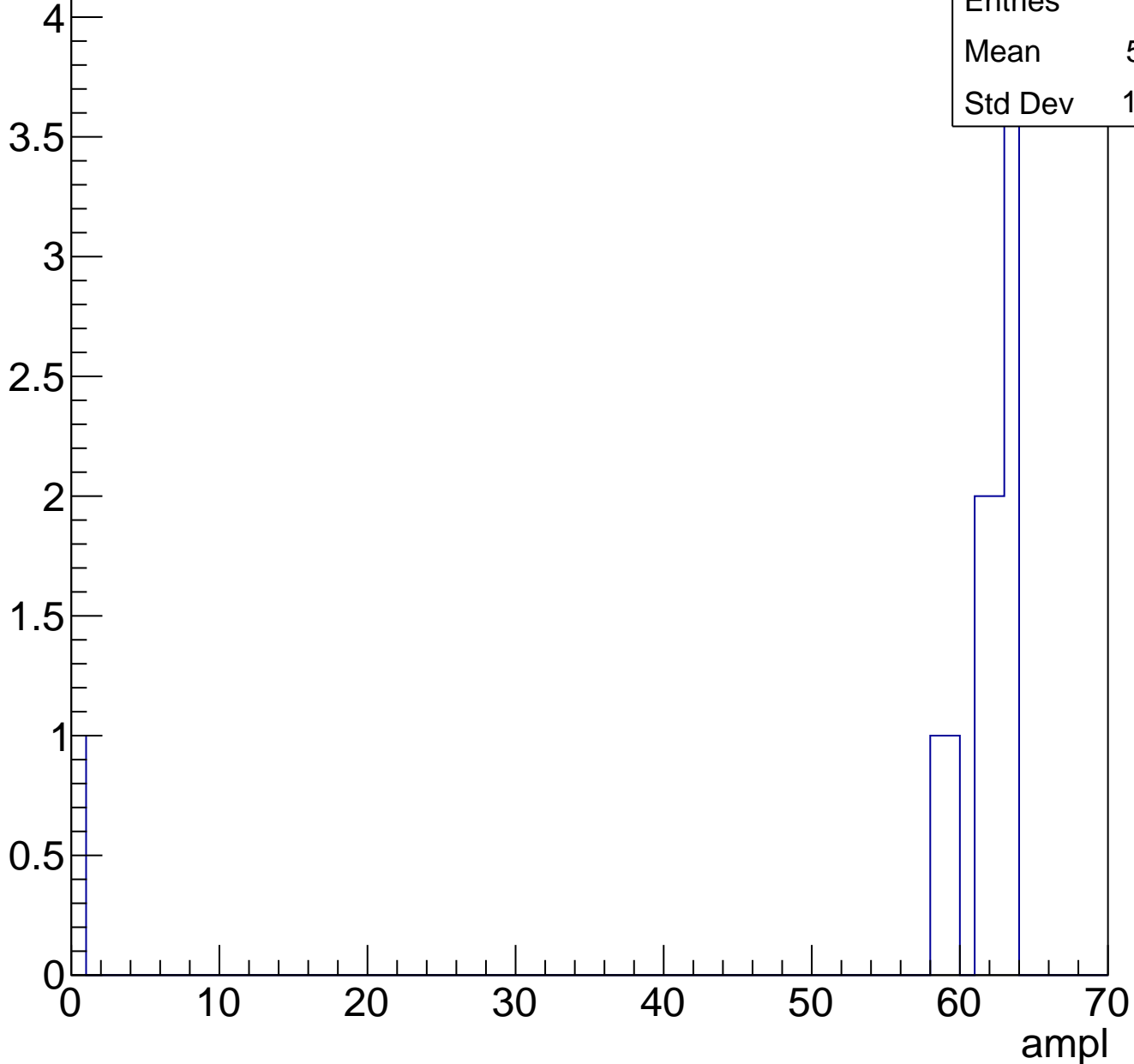
Entries	62
Mean	59.02
Std Dev	2.679



# B1L000S, U24-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

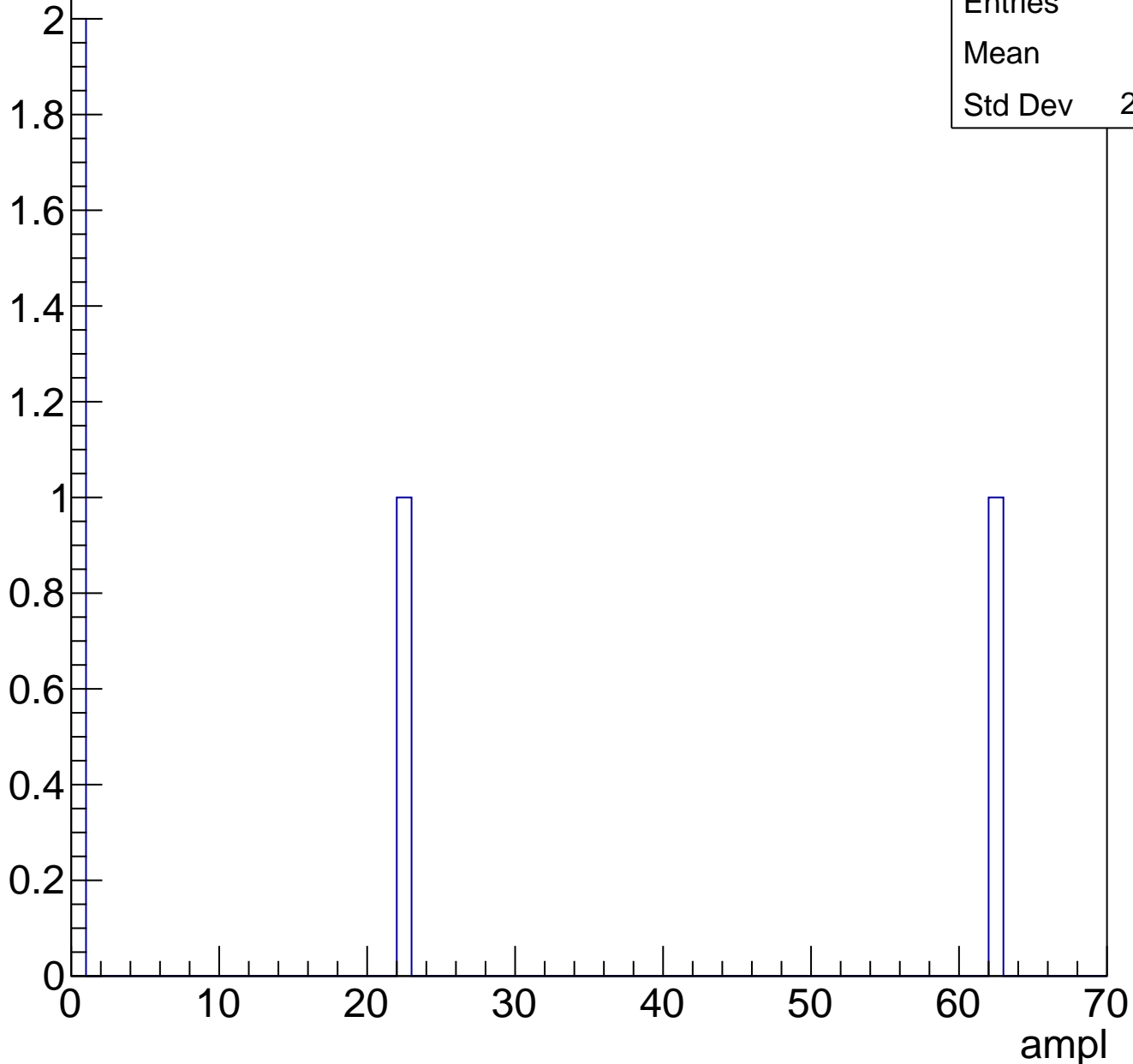




# B1L000S, U24-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21
Std Dev	25.32

# B1L000S, U24-ch124, adc0

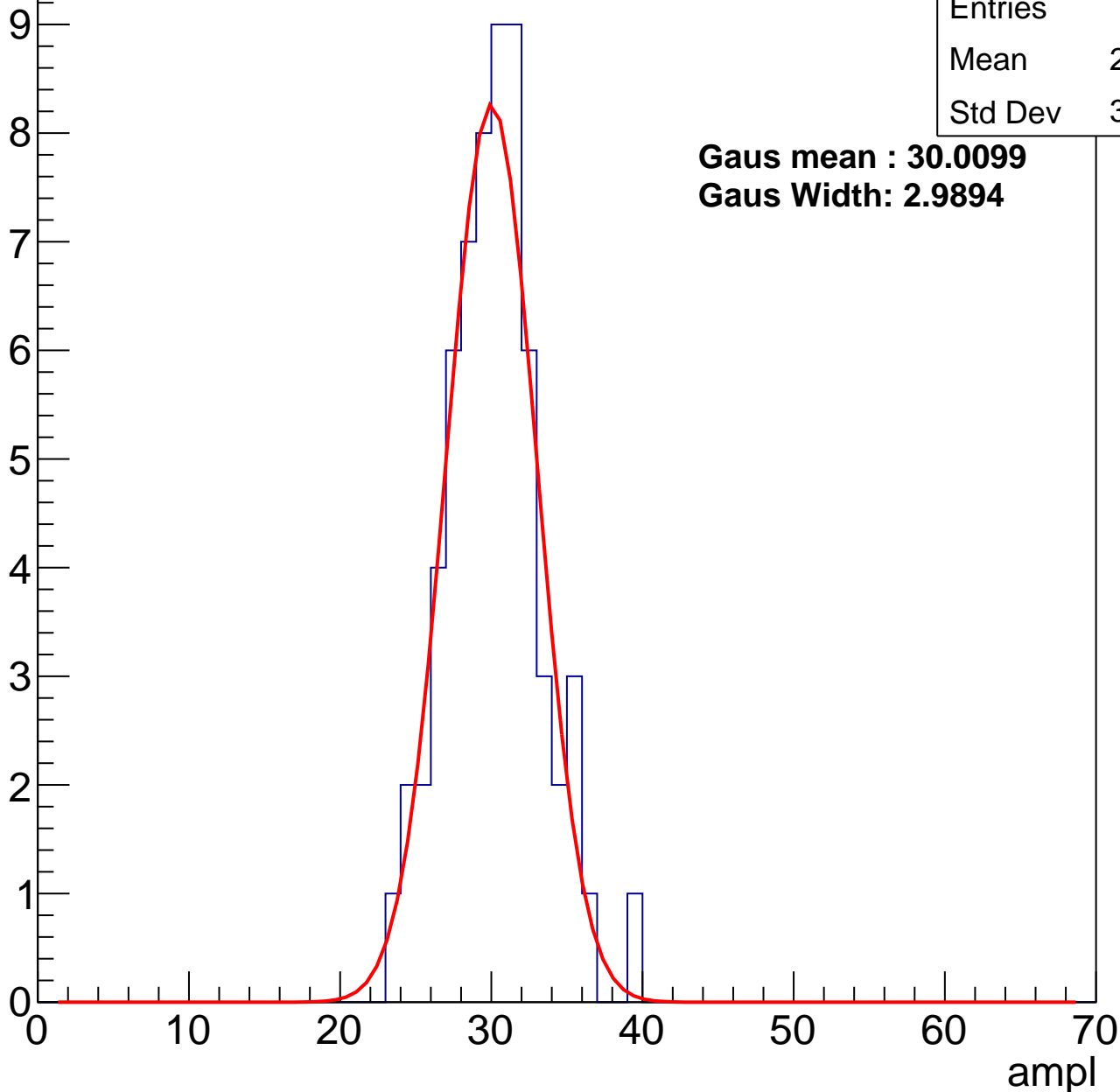
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.73
Std Dev	3.073

**Gaus mean : 30.0099**

**Gaus Width: 2.9894**



# B1L000S, U24-ch124, adc1

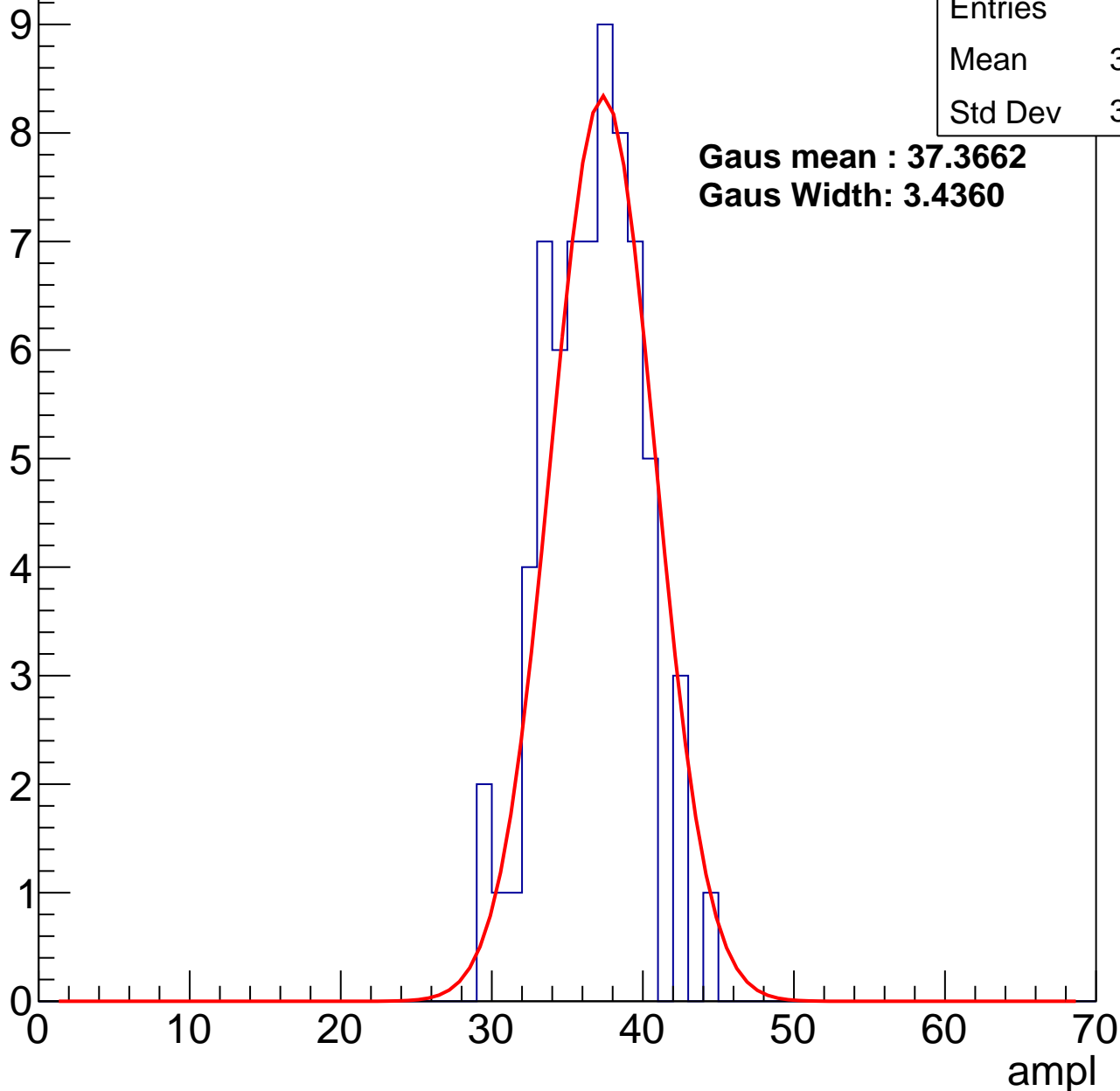
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.16
Std Dev	3.142

**Gaus mean : 37.3662**

**Gaus Width: 3.4360**



# B1L000S, U24-ch124, adc2

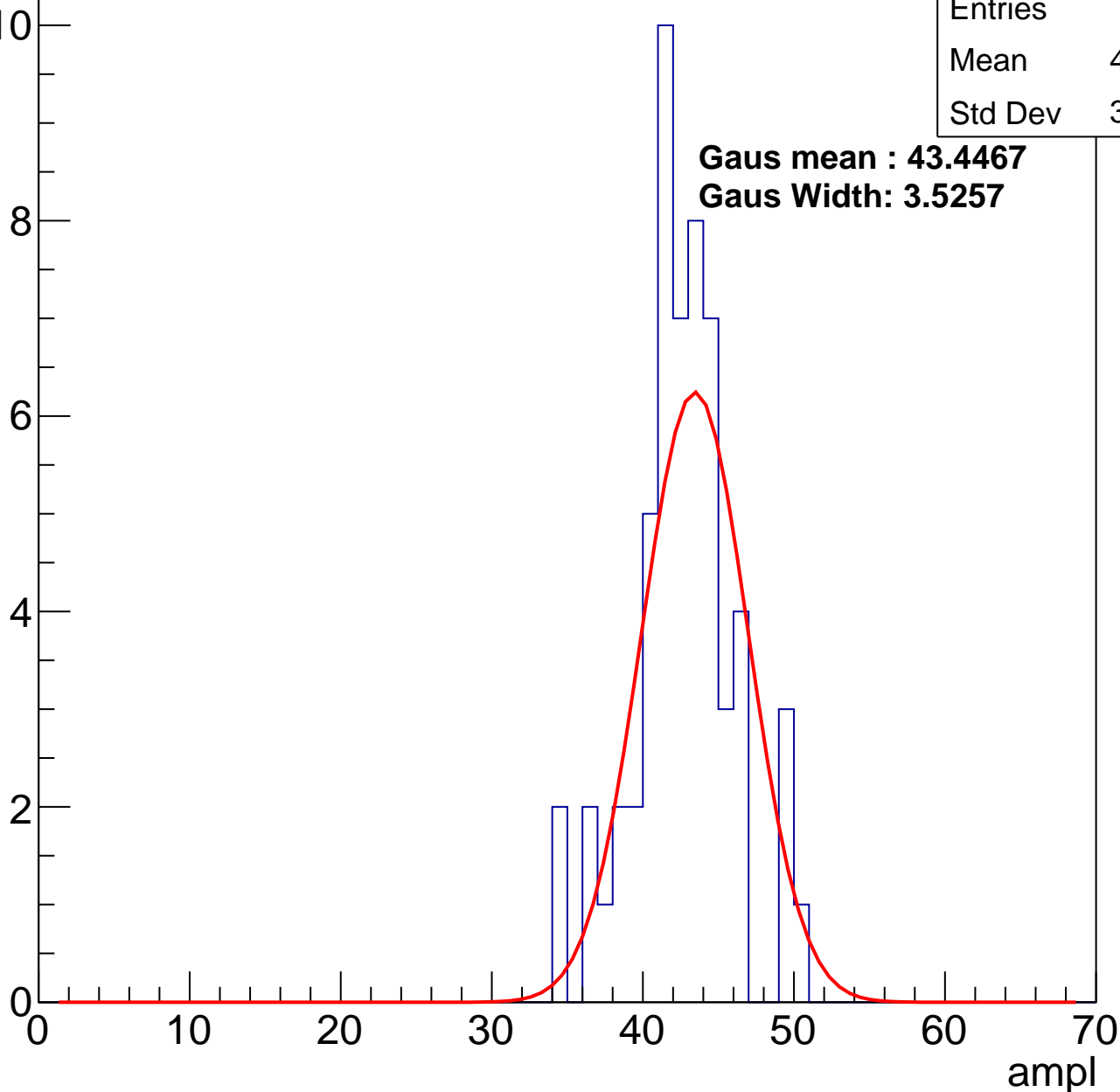
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42.16
Std Dev	3.355

**Gaus mean : 43.4467**

**Gaus Width: 3.5257**

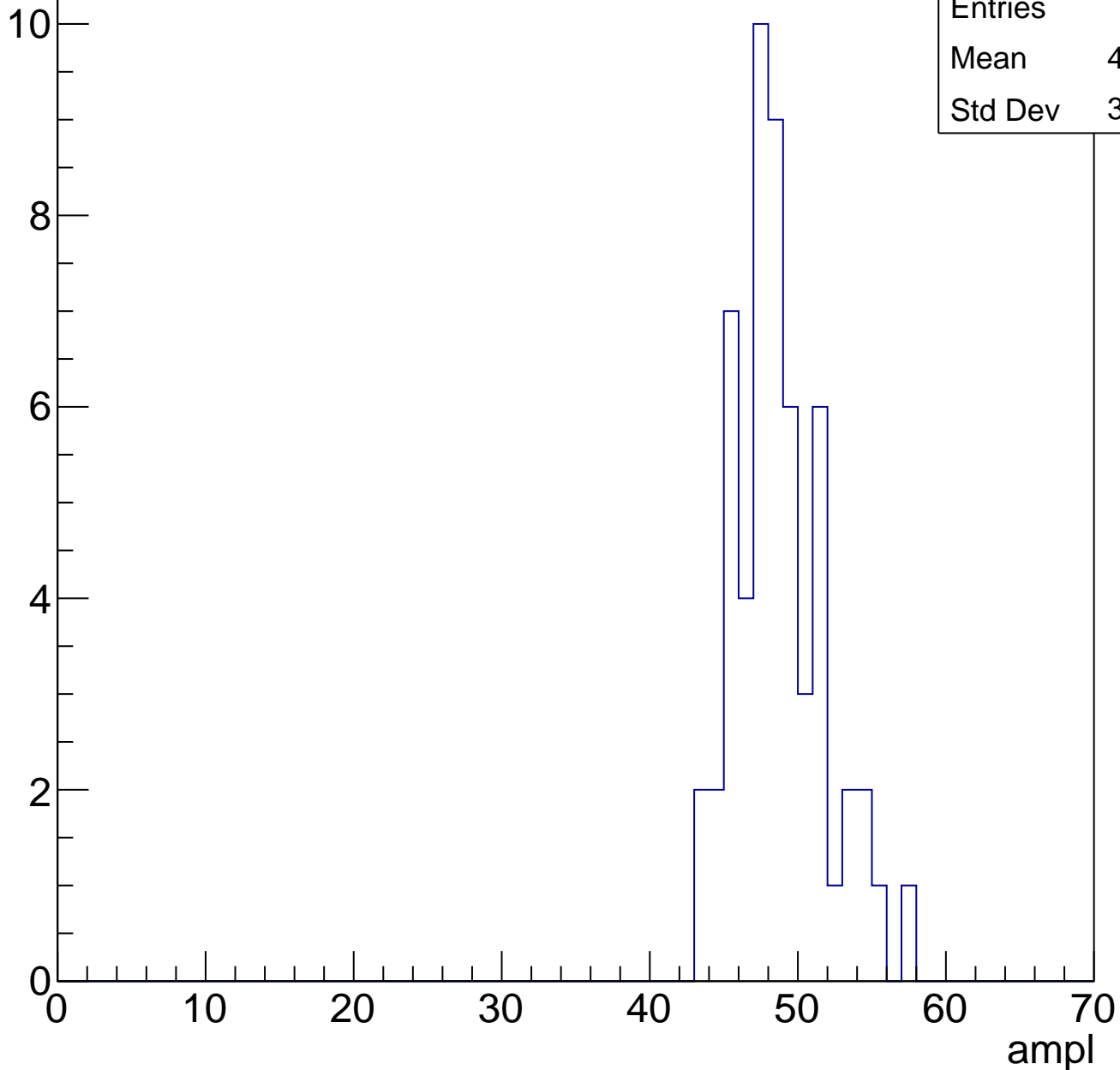


# B1L000S, U24-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	48.27
Std Dev	3.009

Entry

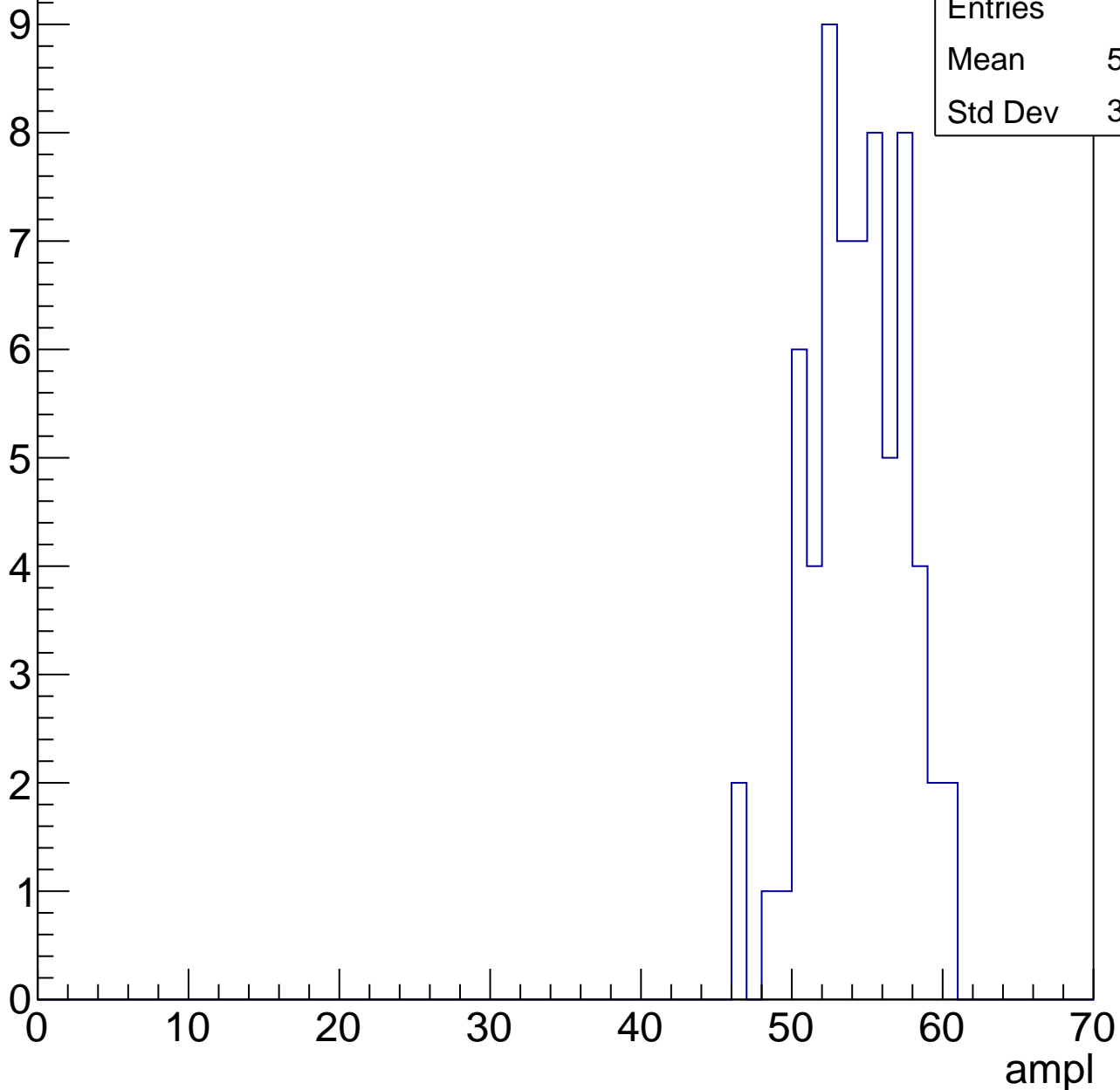


# B1L000S, U24-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	53.88
Std Dev	3.136

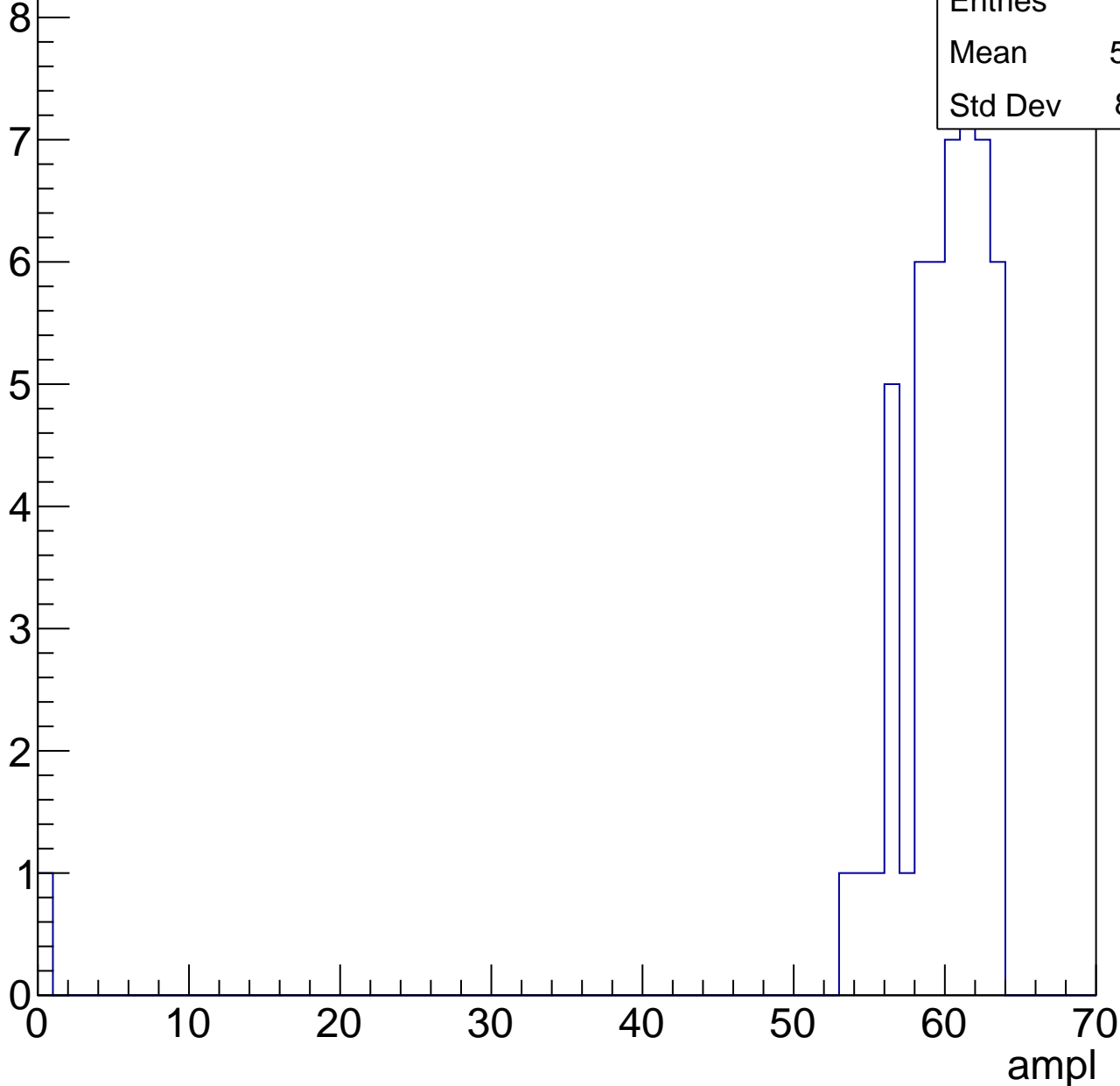


# B1L000S, U24-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

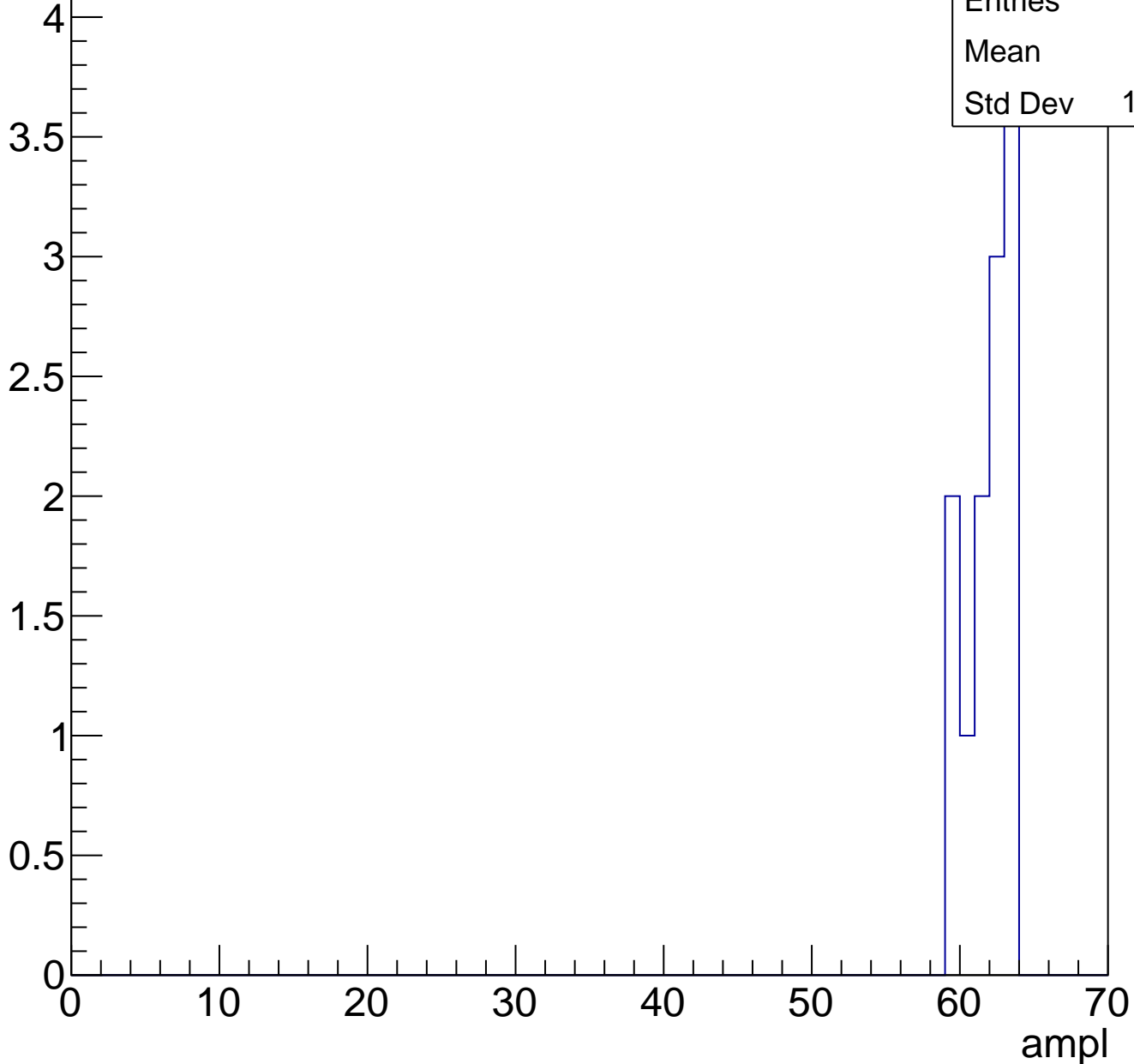
Entries	50
Mean	58.42
Std Dev	8.711



# B1L000S, U24-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U24-ch125, adc0

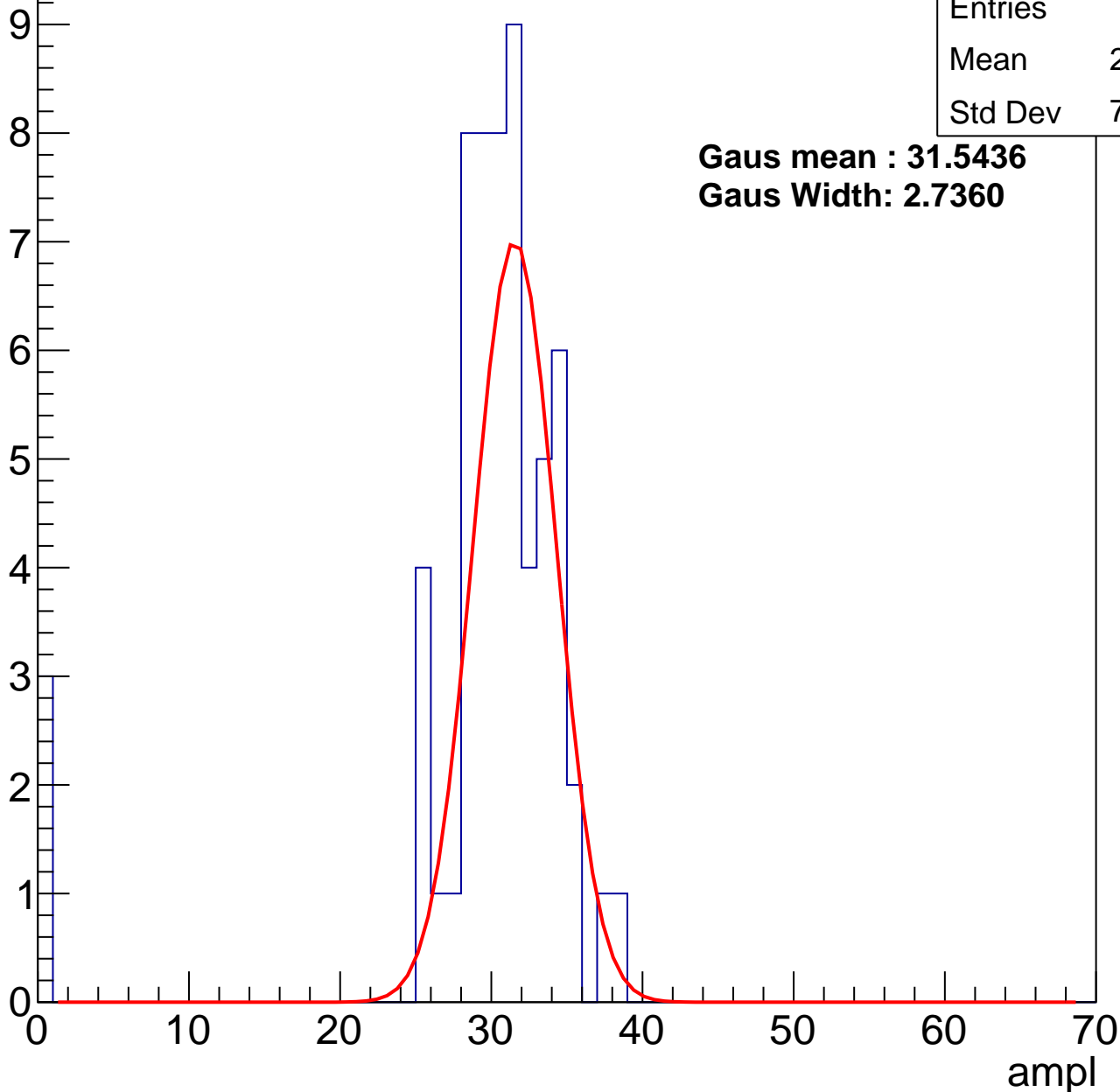
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	29.02
Std Dev	7.169

**Gaus mean : 31.5436**

**Gaus Width: 2.7360**



# B1L000S, U24-ch125, adc1

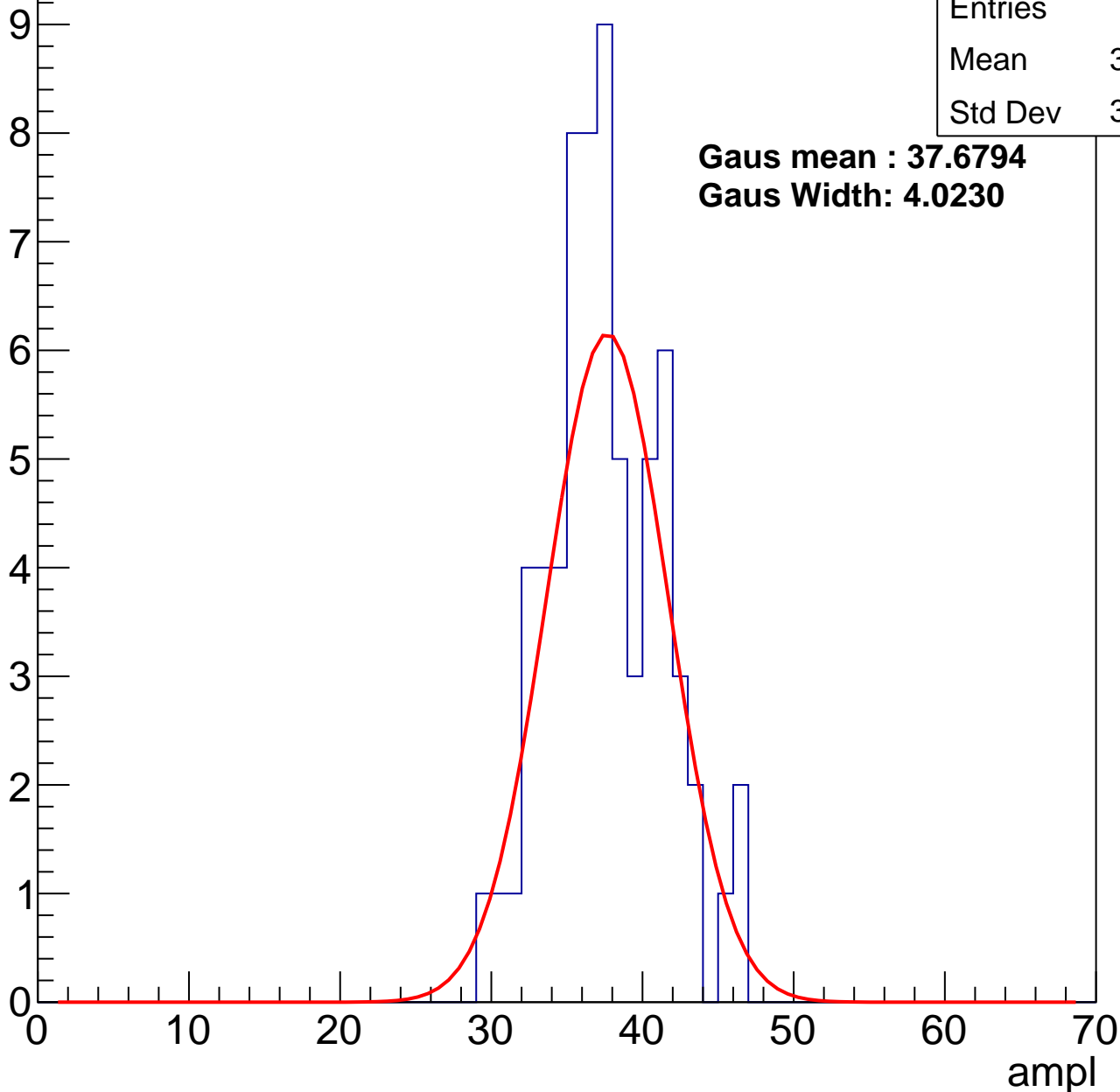
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	37.15
Std Dev	3.703

**Gaus mean : 37.6794**

**Gaus Width: 4.0230**



# B1L000S, U24-ch125, adc2

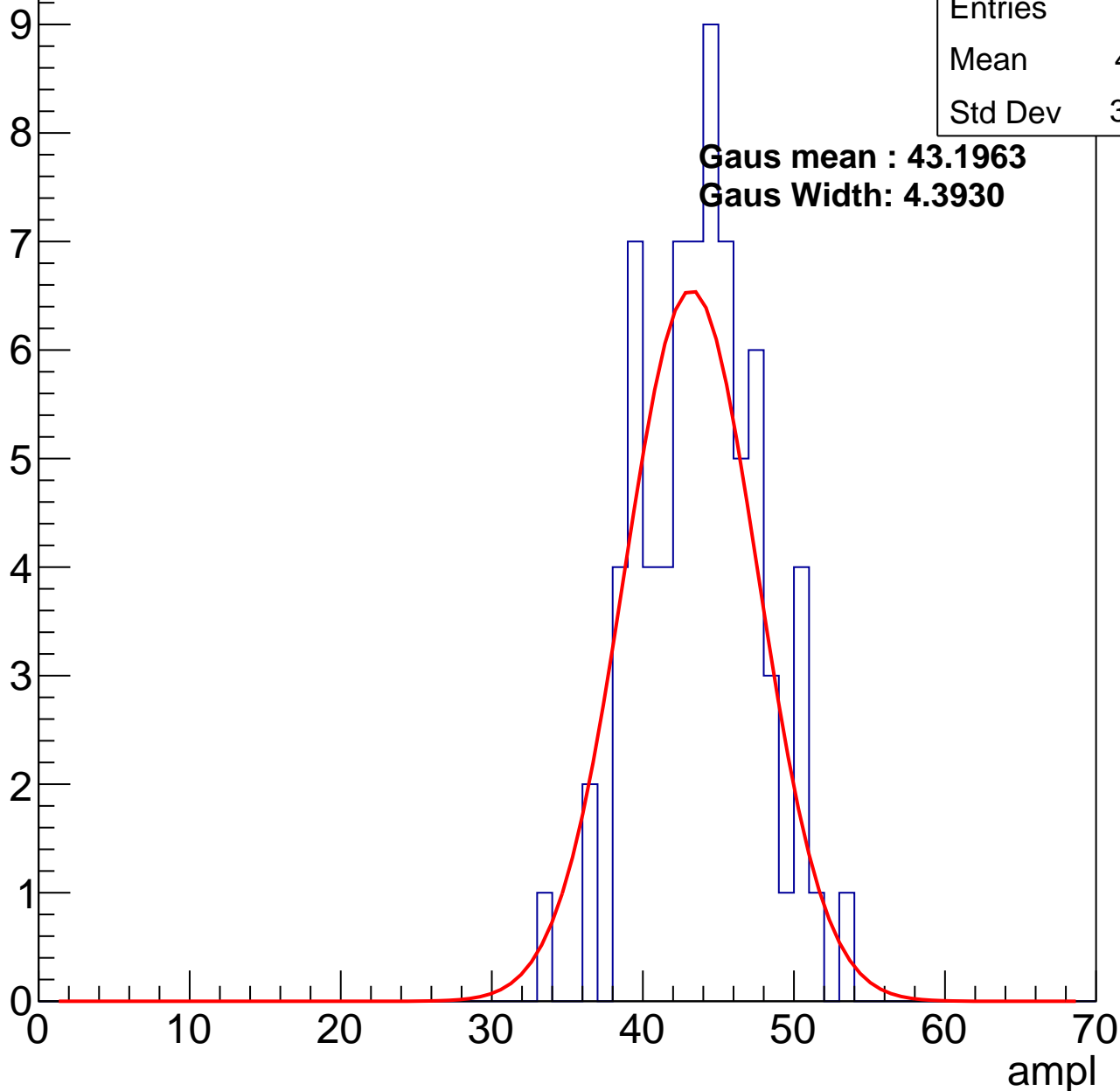
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	43.41
Std Dev	3.892

**Gaus mean : 43.1963**

**Gaus Width: 4.3930**

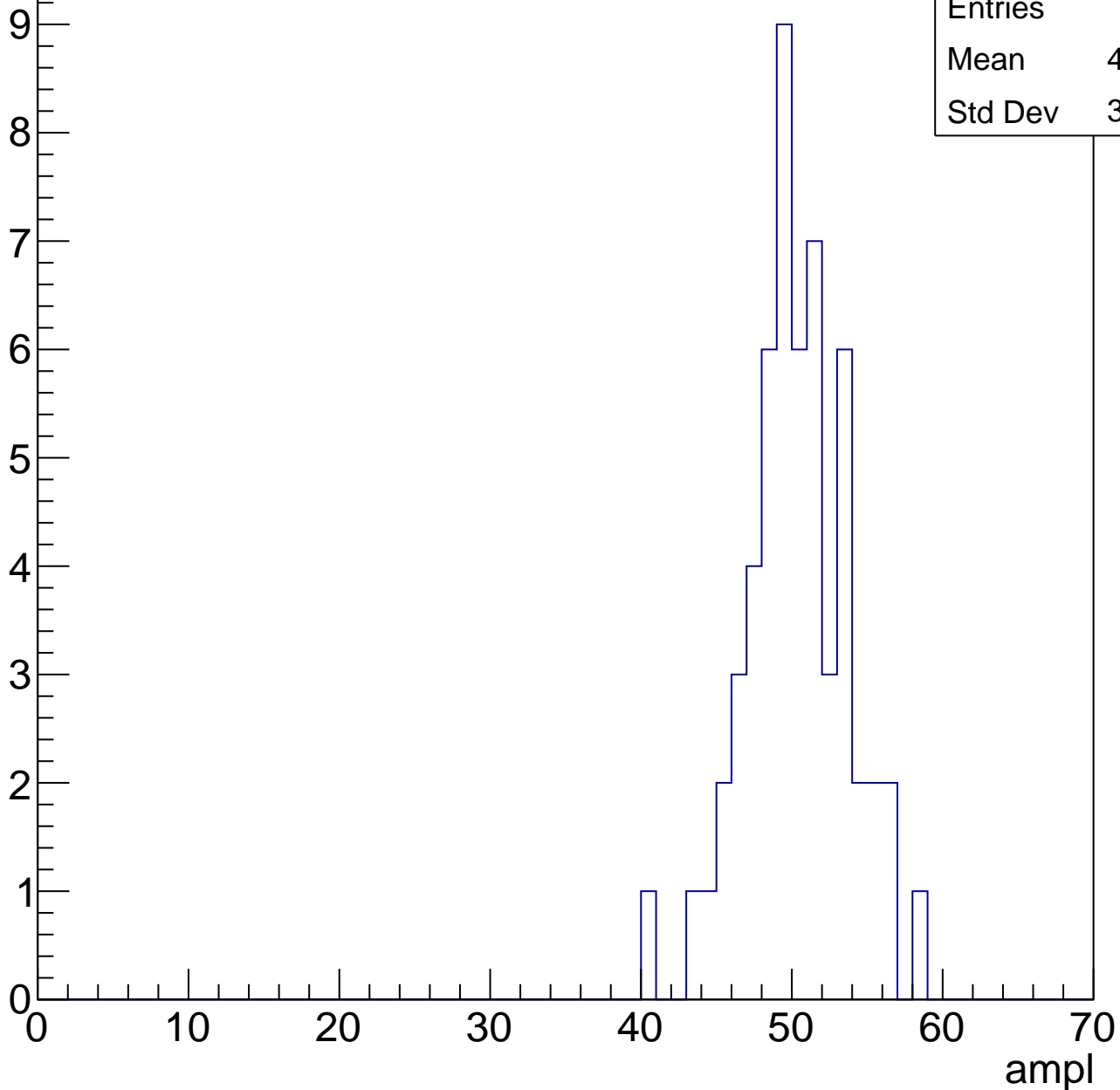


# B1L000S, U24-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	49.84
Std Dev	3.385

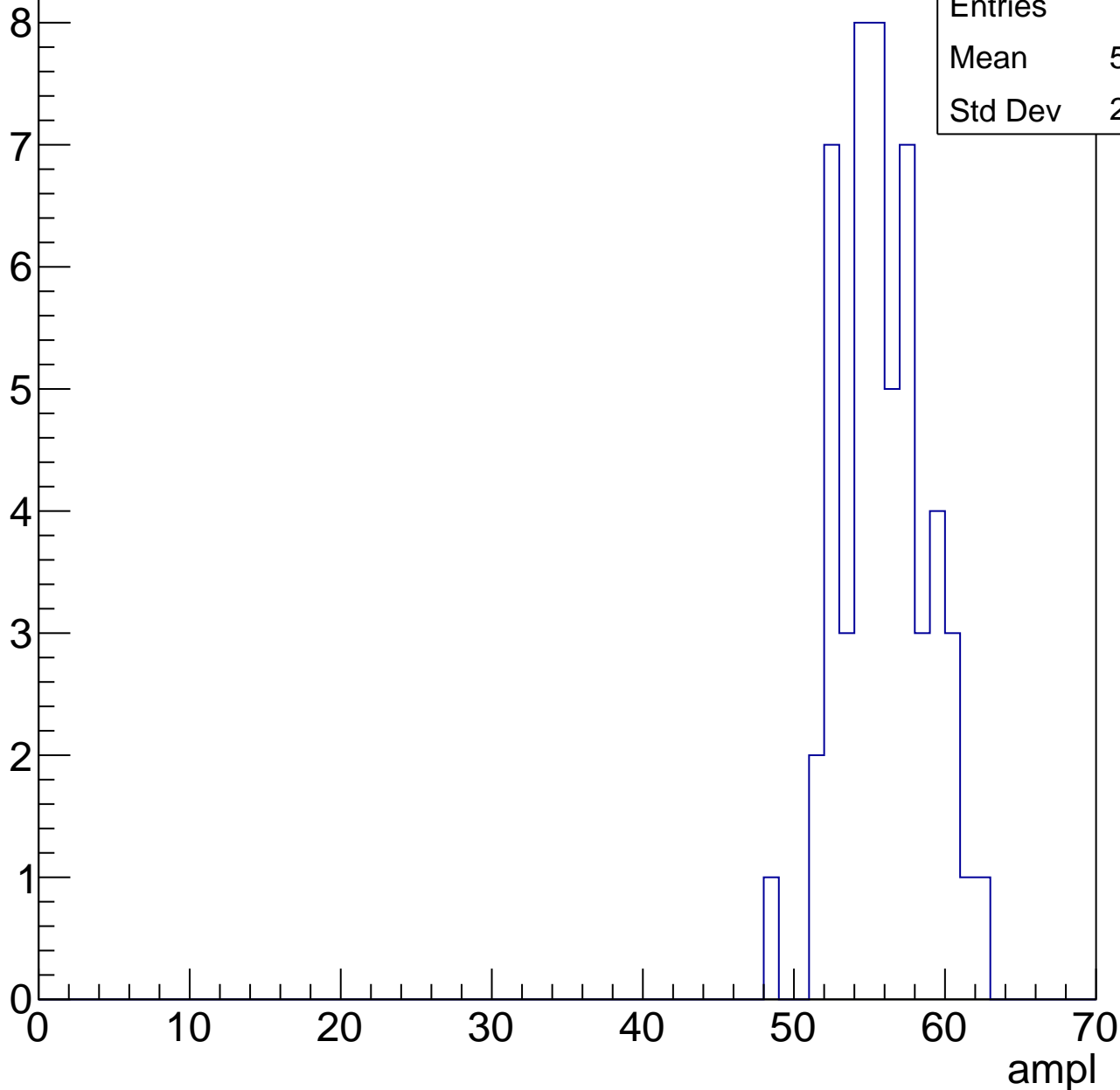


# B1L000S, U24-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	55.42
Std Dev	2.878

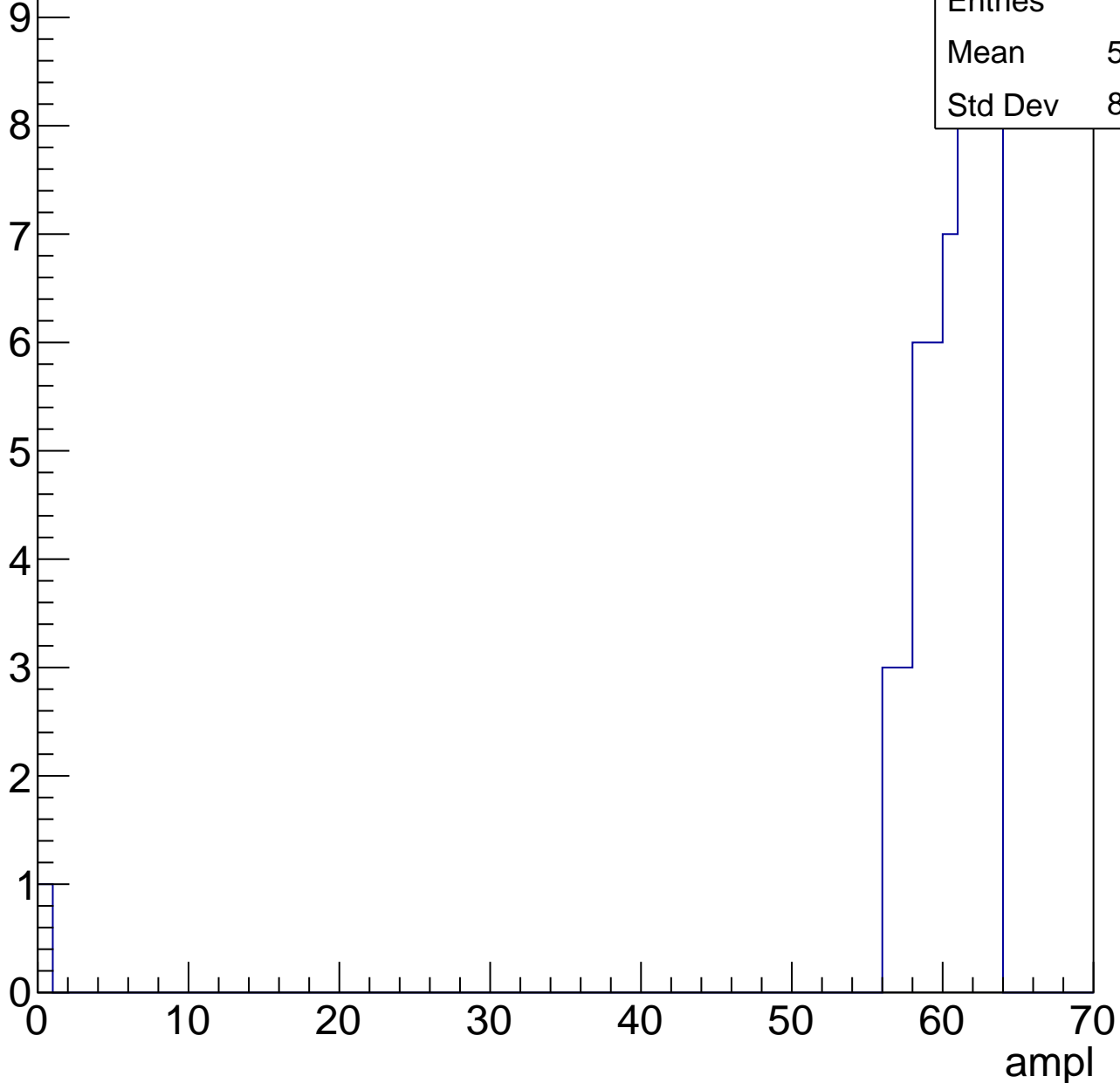


# B1L000S, U24-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	59.04
Std Dev	8.602



# B1L000S, U24-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B1L000S, U24-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch126, adc0

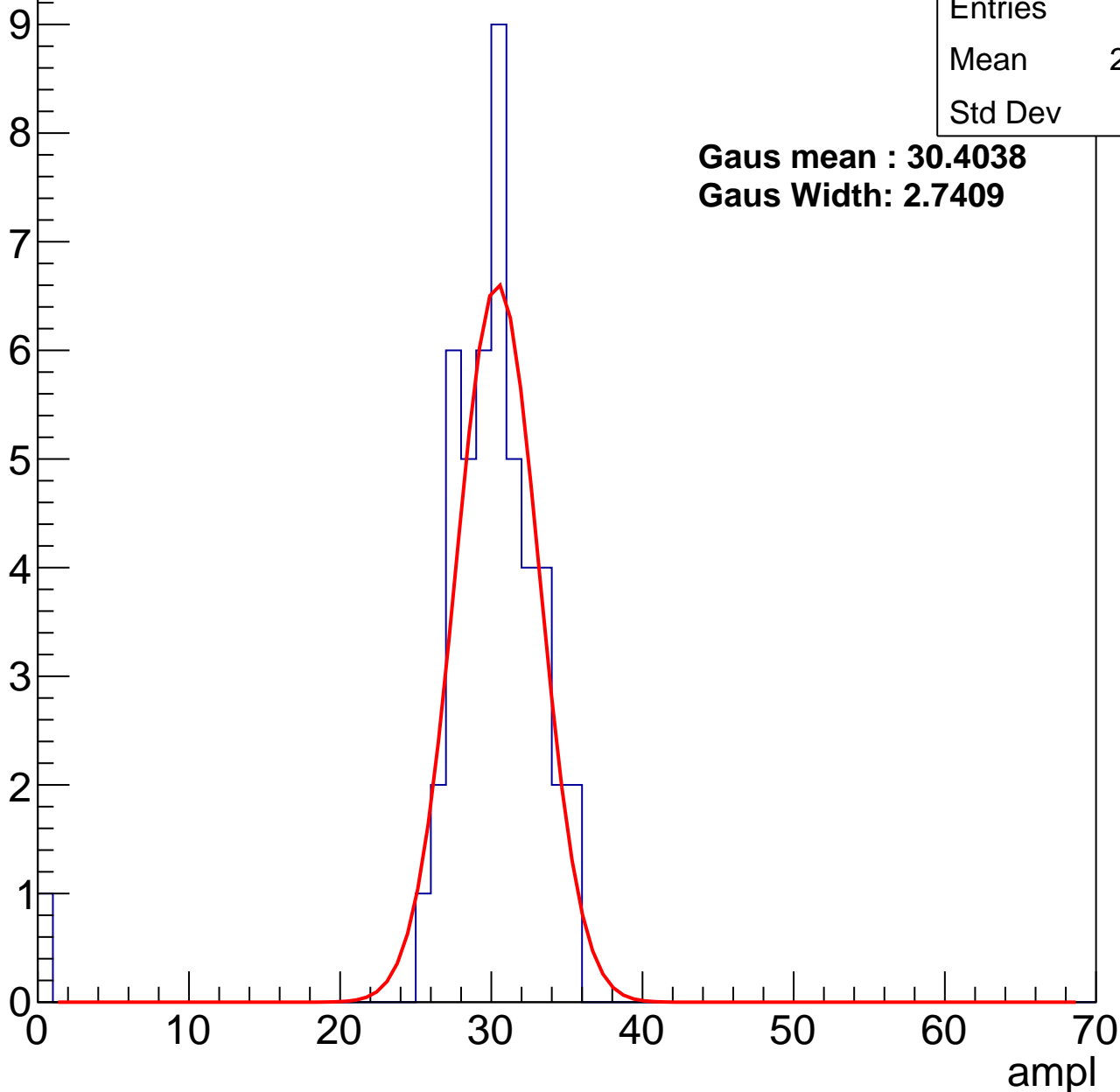
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	29.28
Std Dev	4.95

**Gaus mean : 30.4038**

**Gaus Width: 2.7409**



# B1L000S, U24-ch126, adc1

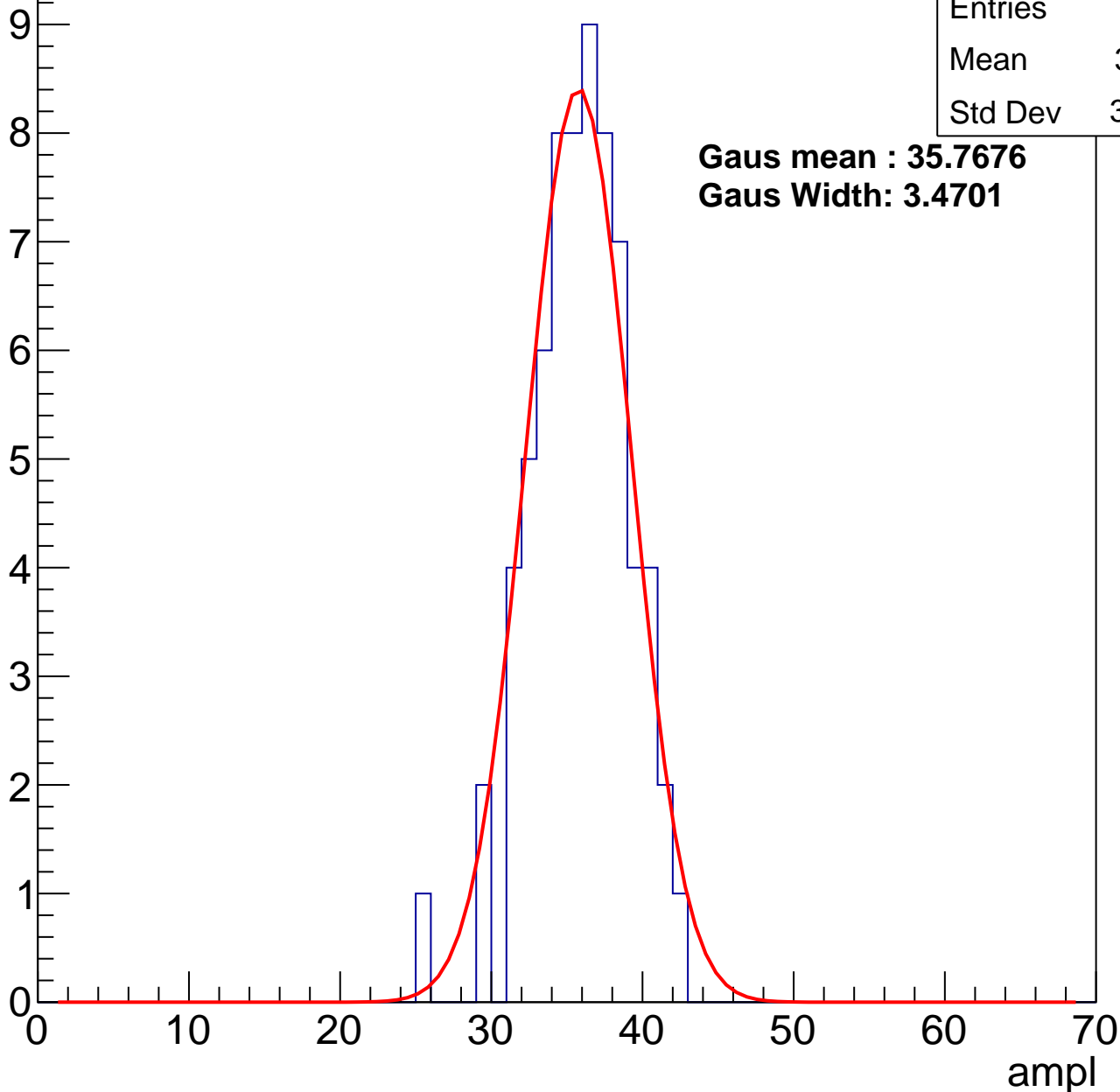
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.41
Std Dev	3.164

**Gaus mean : 35.7676**

**Gaus Width: 3.4701**



# B1L000S, U24-ch126, adc2

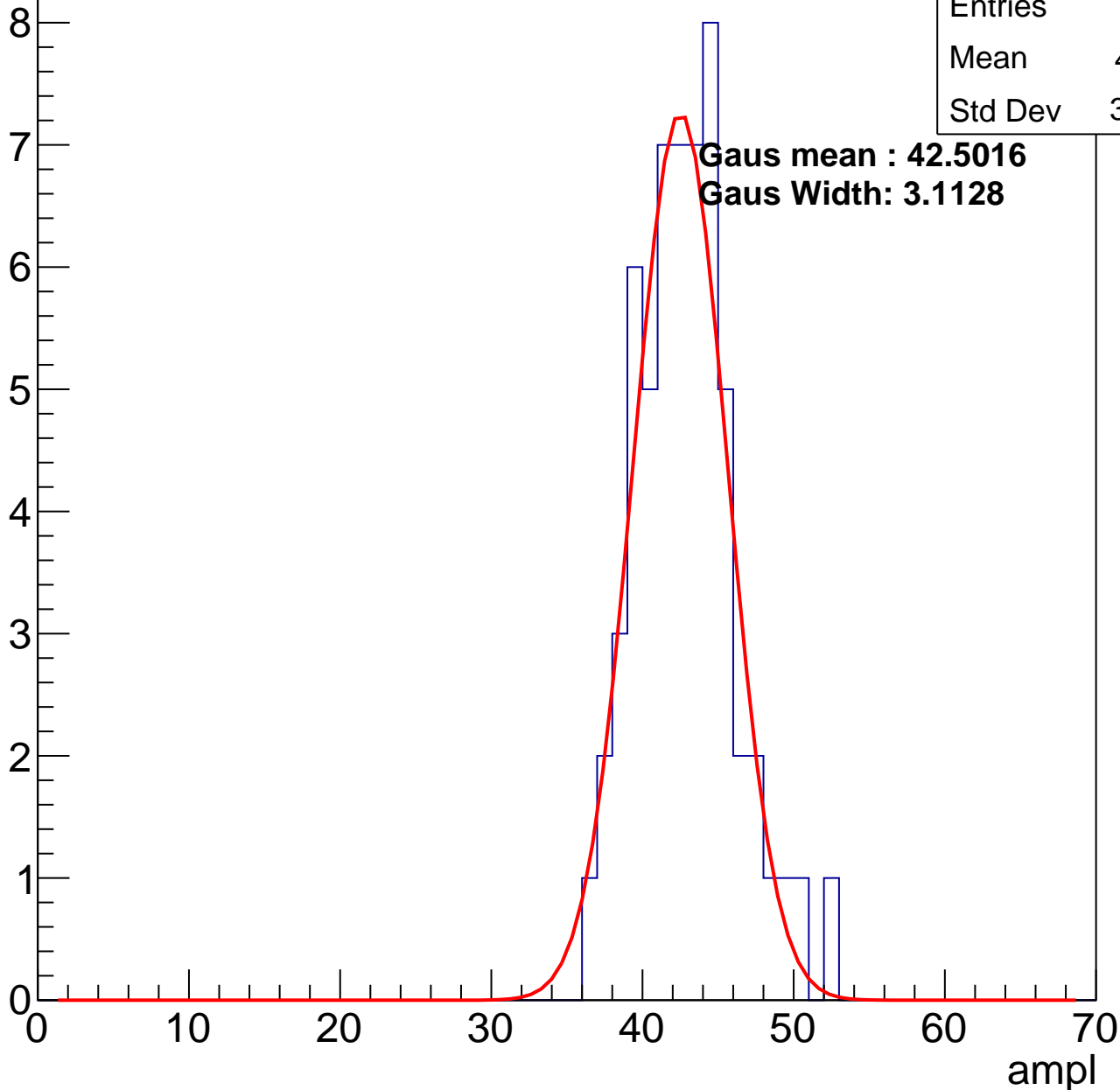
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.41
Std Dev	3.242

**Gaus mean : 42.5016**

**Gaus Width: 3.1128**

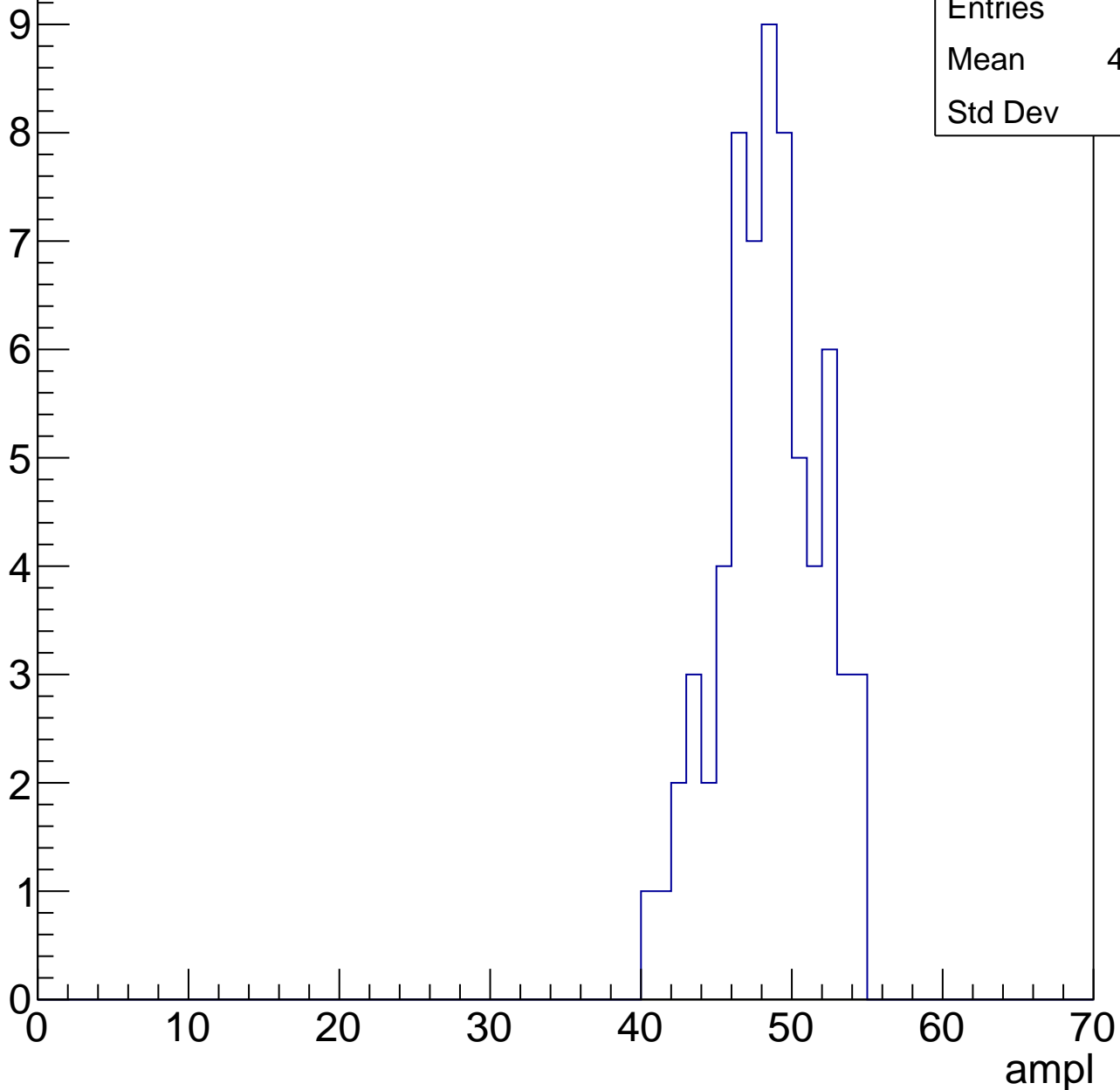


# B1L000S, U24-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	48.03
Std Dev	3.28

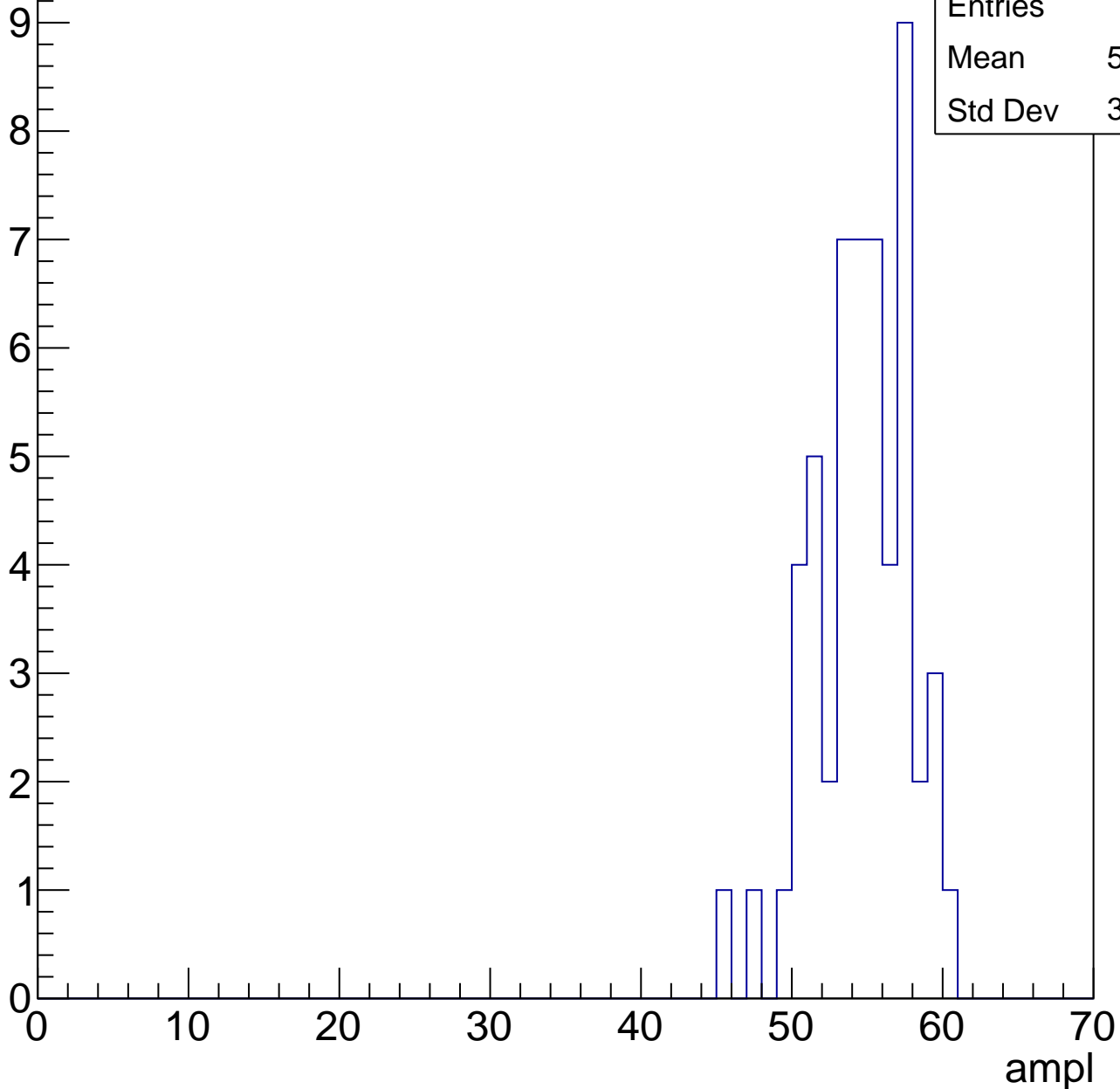


# B1L000S, U24-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.15
Std Dev	3.117



# B1L000S, U24-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	55
Mean	57.95
Std Dev	8.293

Entry

10

8

6

4

2

0

0

10

20

30

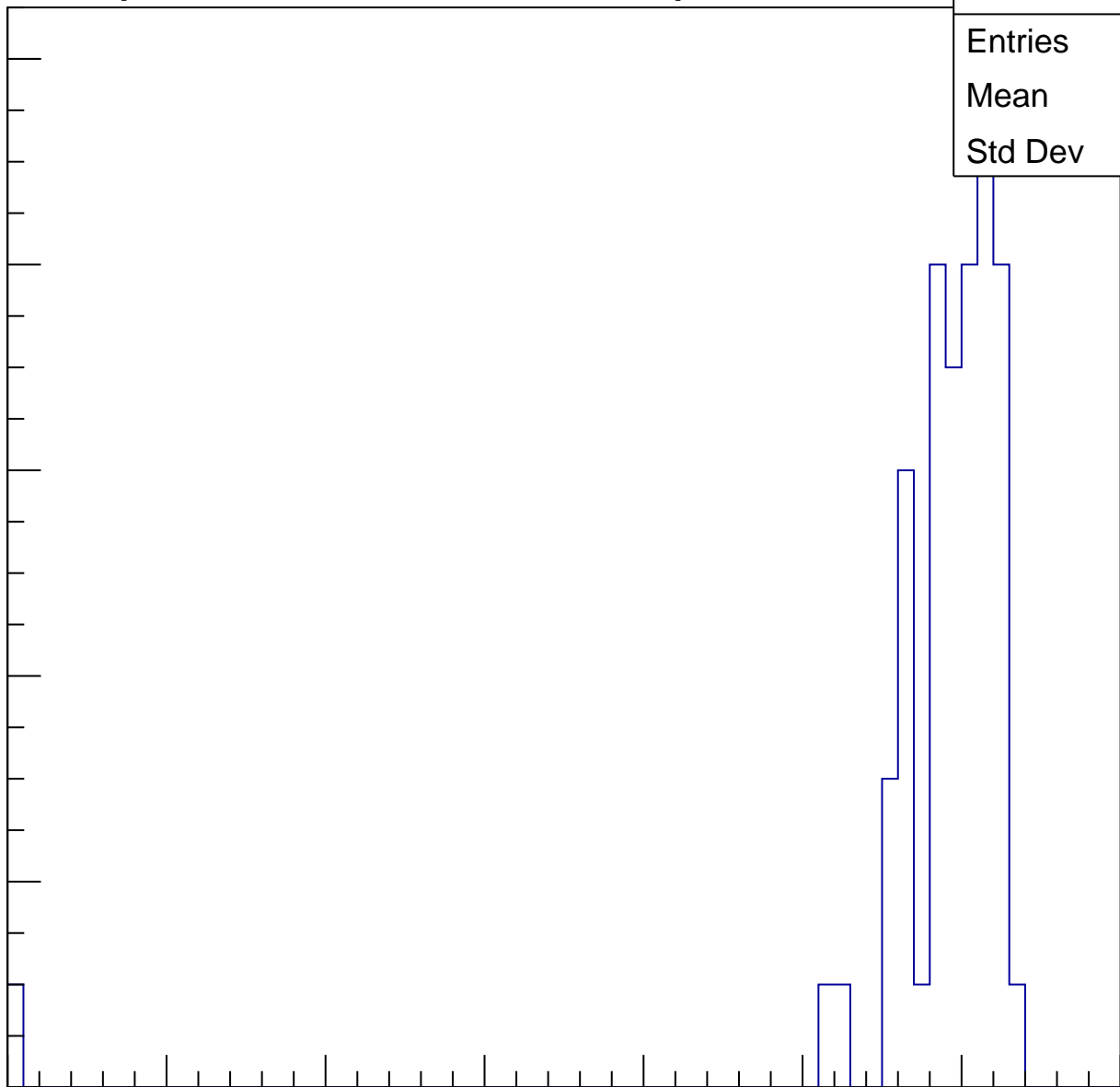
40

50

60

70

ampl

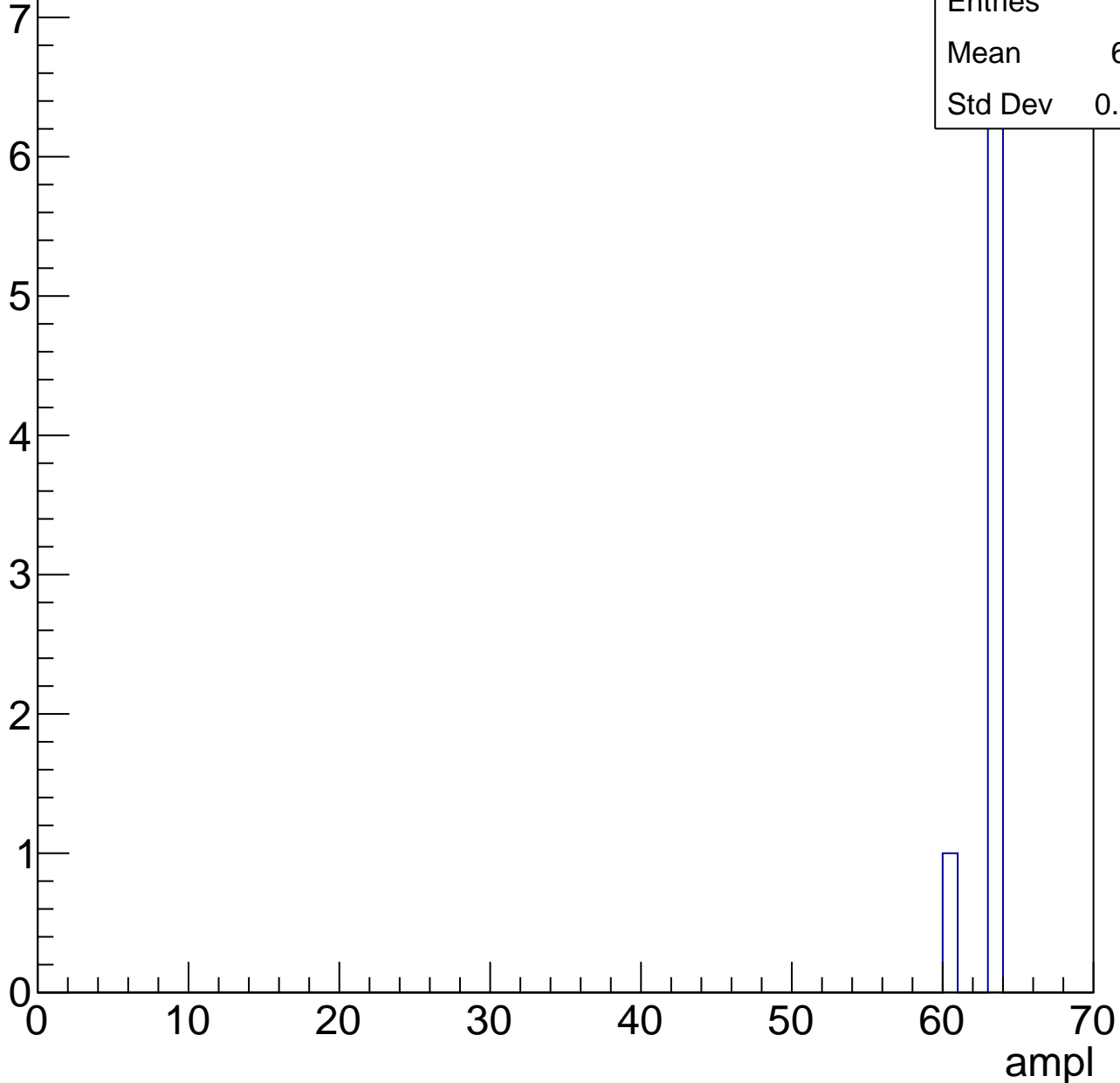


# B1L000S, U24-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.62
Std Dev	0.9922

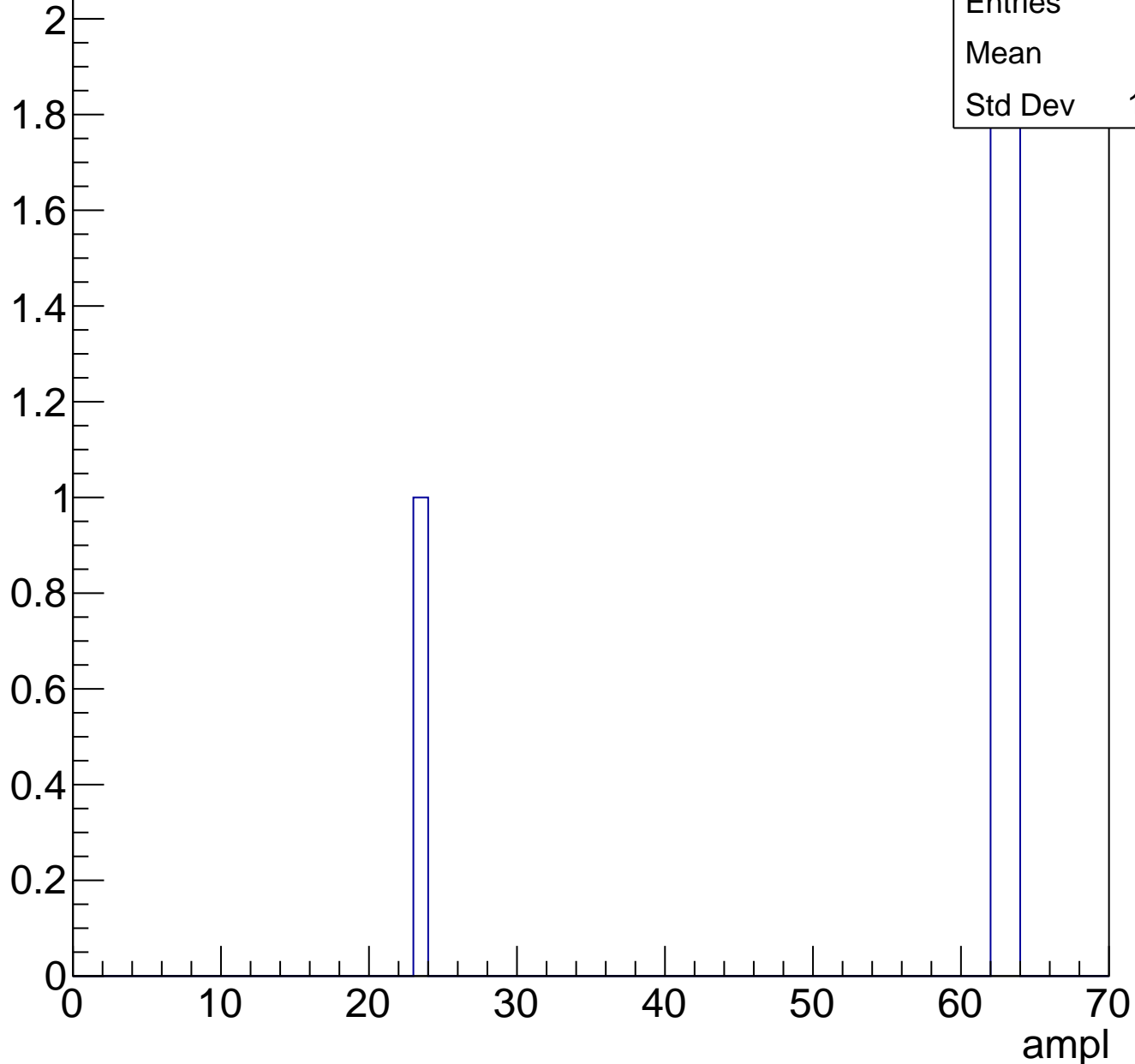




# B1L000S, U24-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch127, adc0

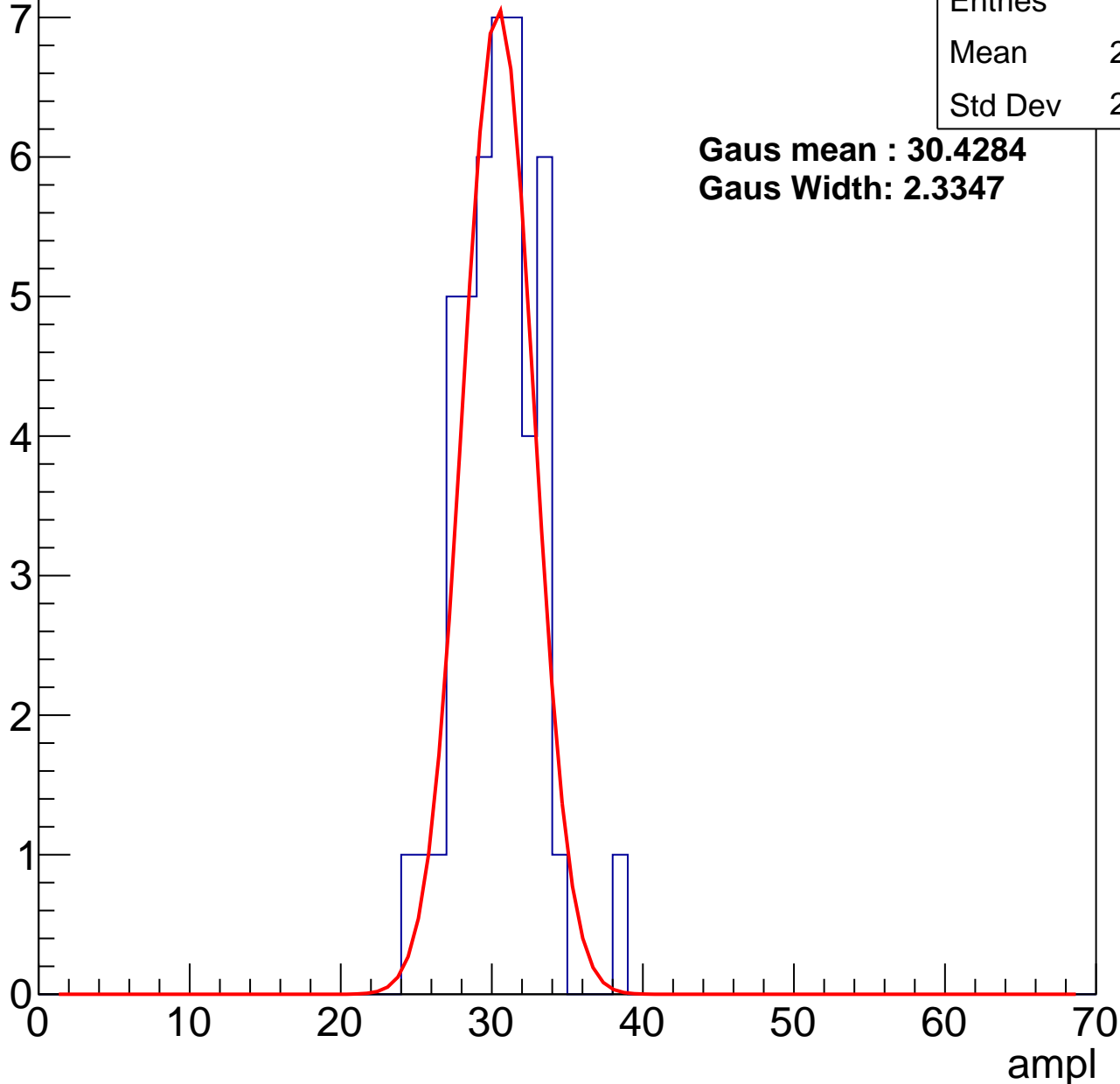
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	29.98
Std Dev	2.603

**Gaus mean : 30.4284**

**Gaus Width: 2.3347**



# B1L000S, U24-ch127, adc1

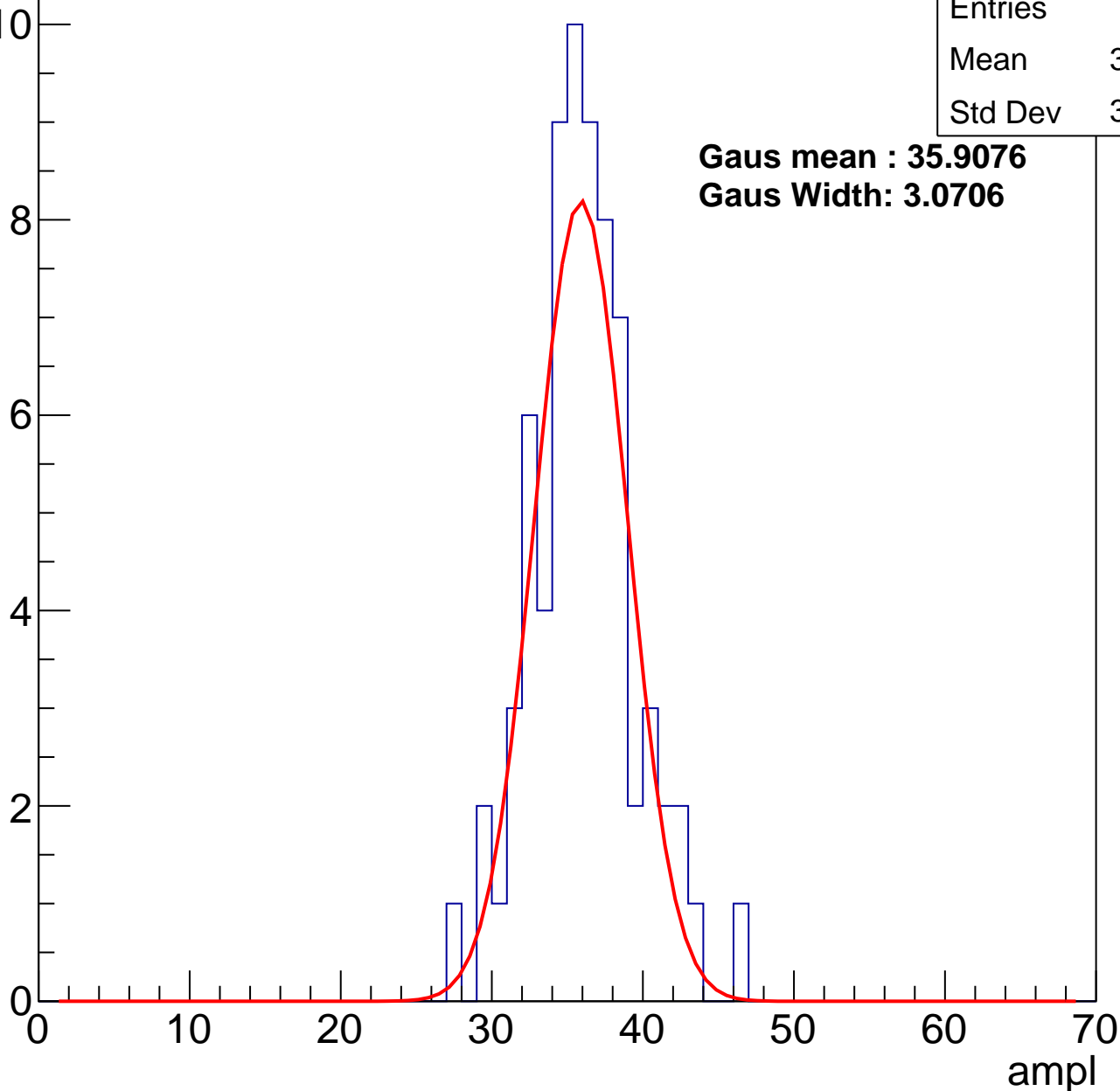
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.59
Std Dev	3.417

**Gaus mean : 35.9076**

**Gaus Width: 3.0706**



# B1L000S, U24-ch127, adc2

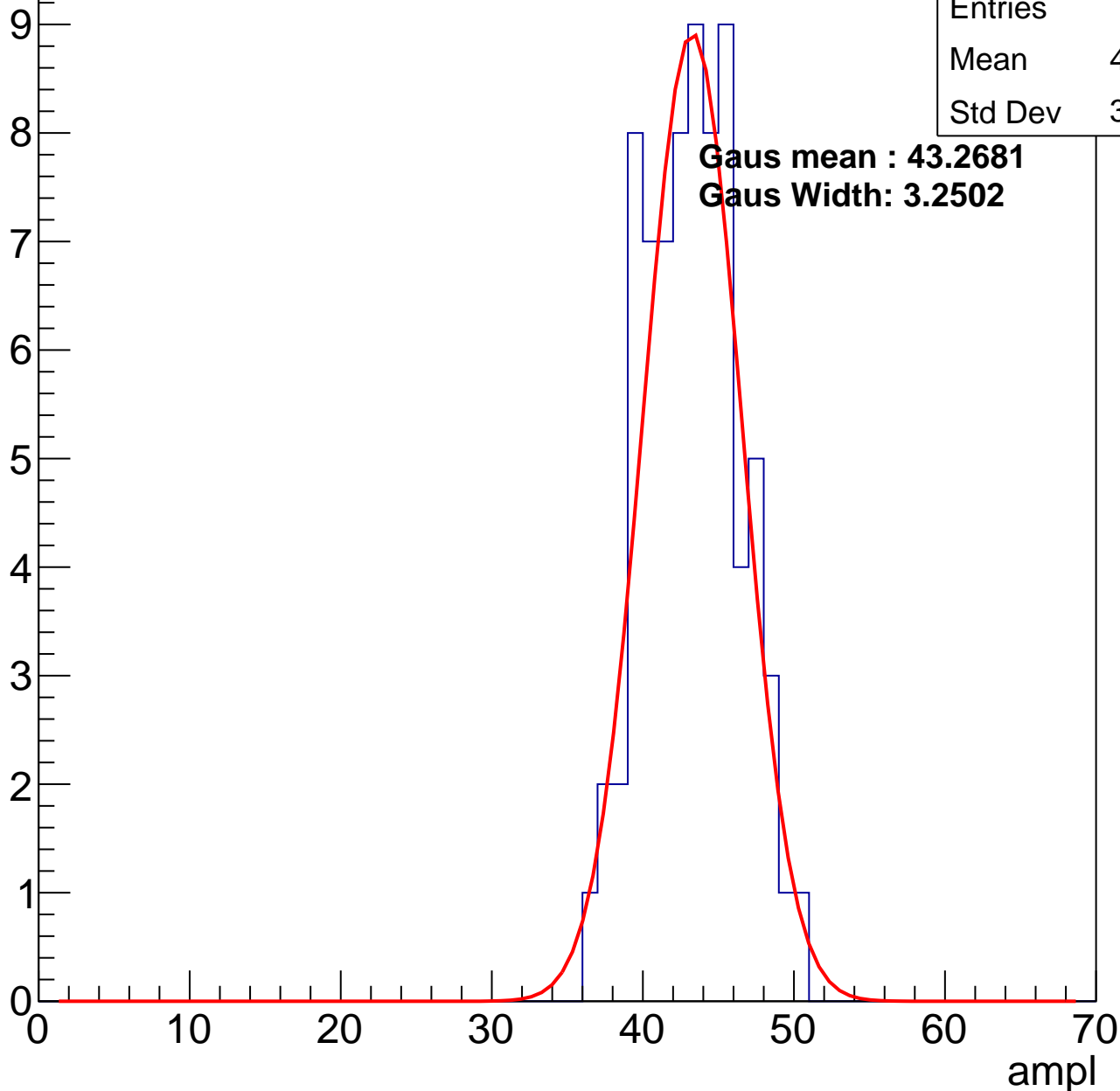
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.76
Std Dev	3.085

**Gaus mean : 43.2681**

**Gaus Width: 3.2502**

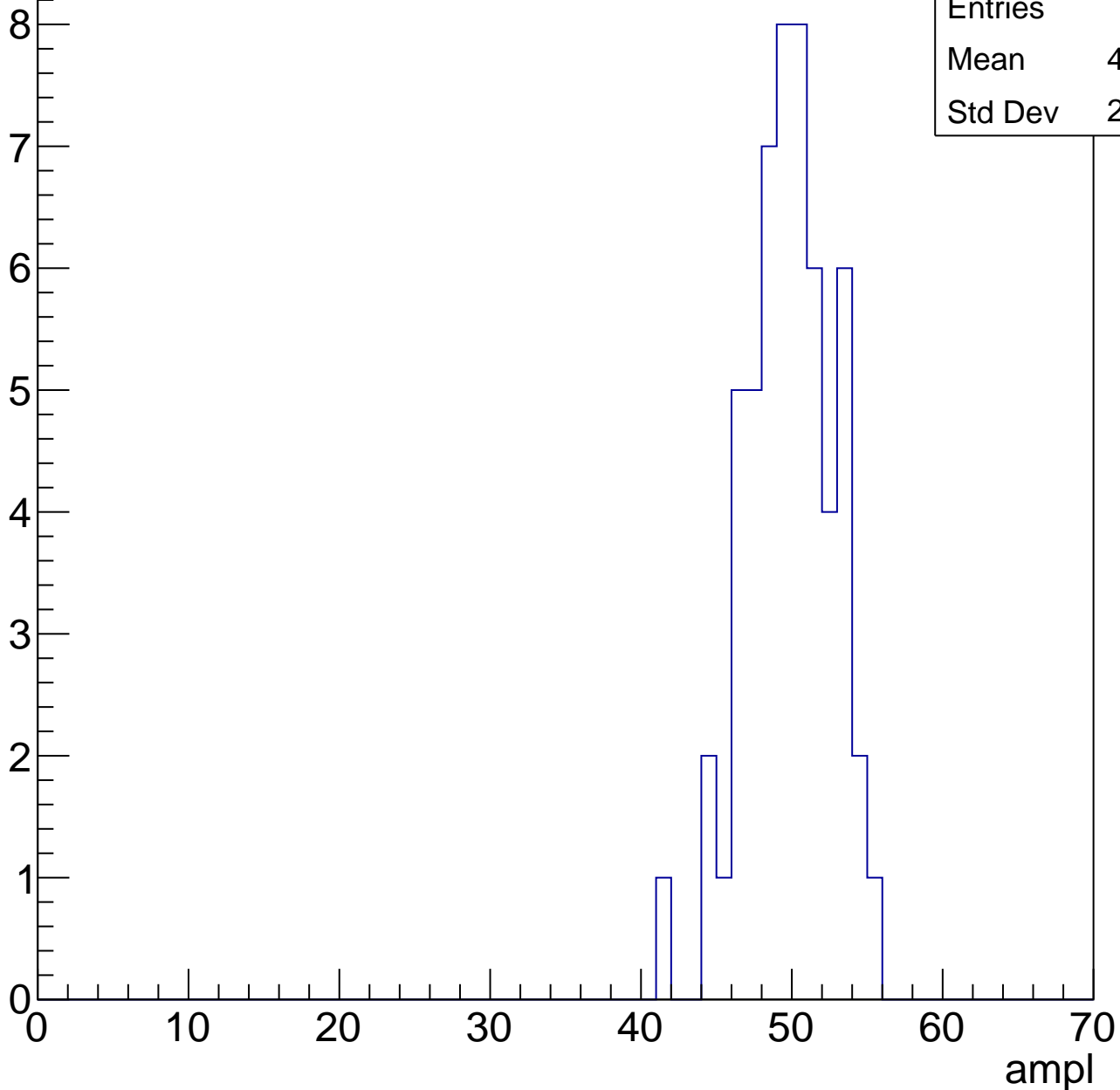


# B1L000S, U24-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	49.32
Std Dev	2.823

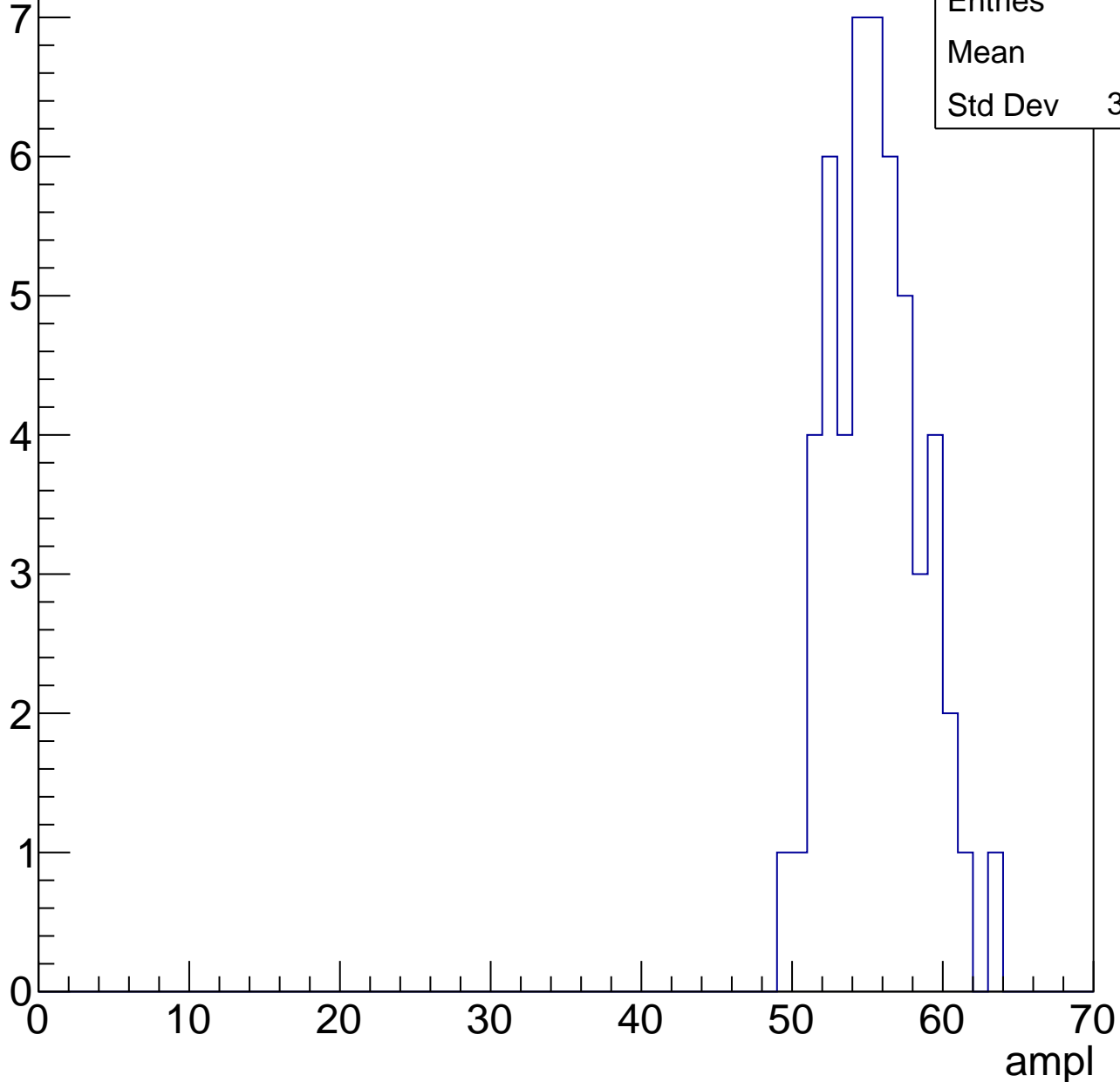


# B1L000S, U24-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	55.1
Std Dev	3.002



# B1L000S, U24-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	58.72
Std Dev	8.002

Entry

10

8

6

4

2

0

0

10

20

30

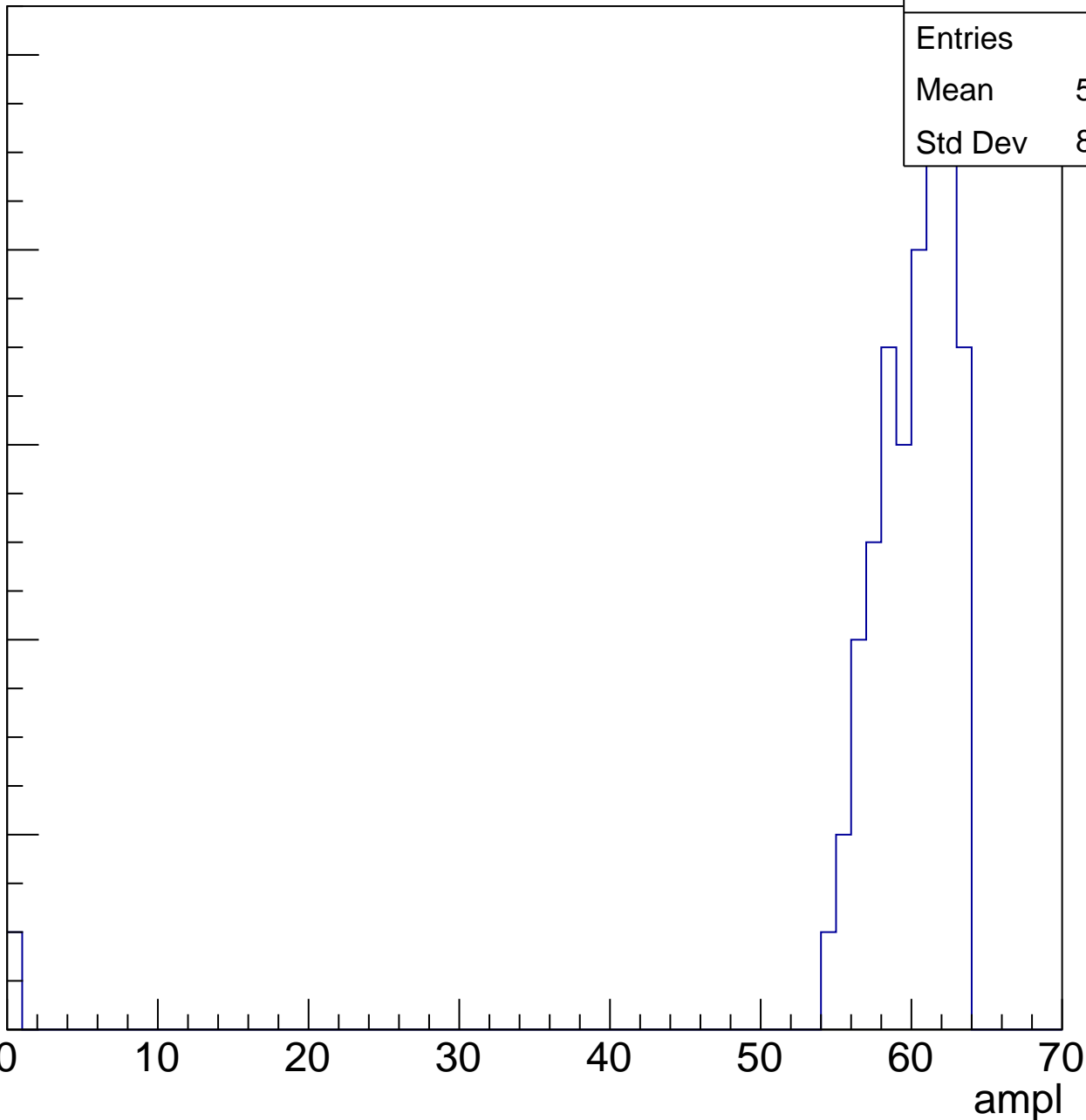
40

50

60

70

ampl



# B1L000S, U24-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U24-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U24-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

