

B1L101S, U1-ch0, adc0

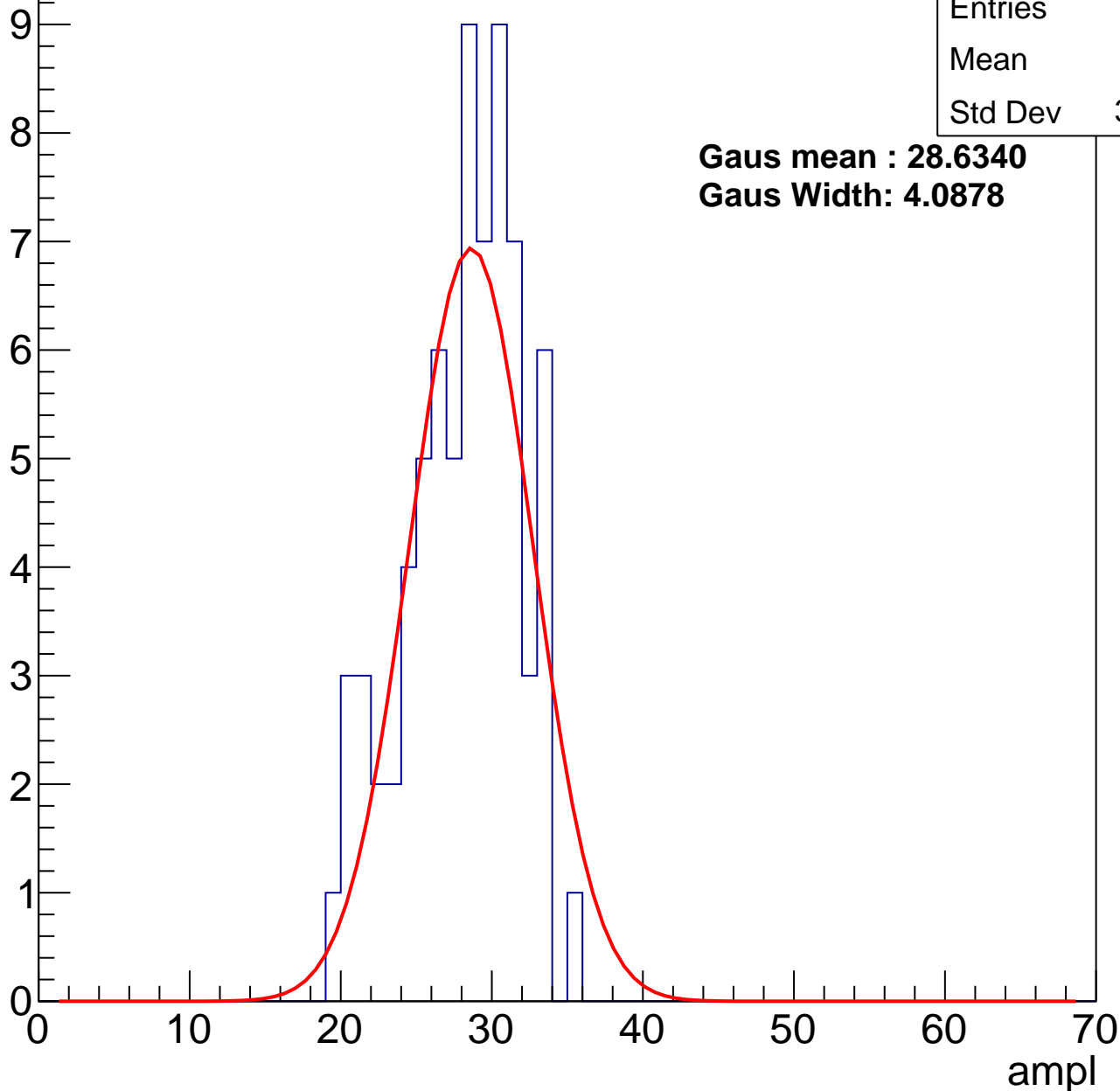
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.6
Std Dev	3.741

Gaus mean : 28.6340

Gaus Width: 4.0878



B1L101S, U1-ch0, adc1

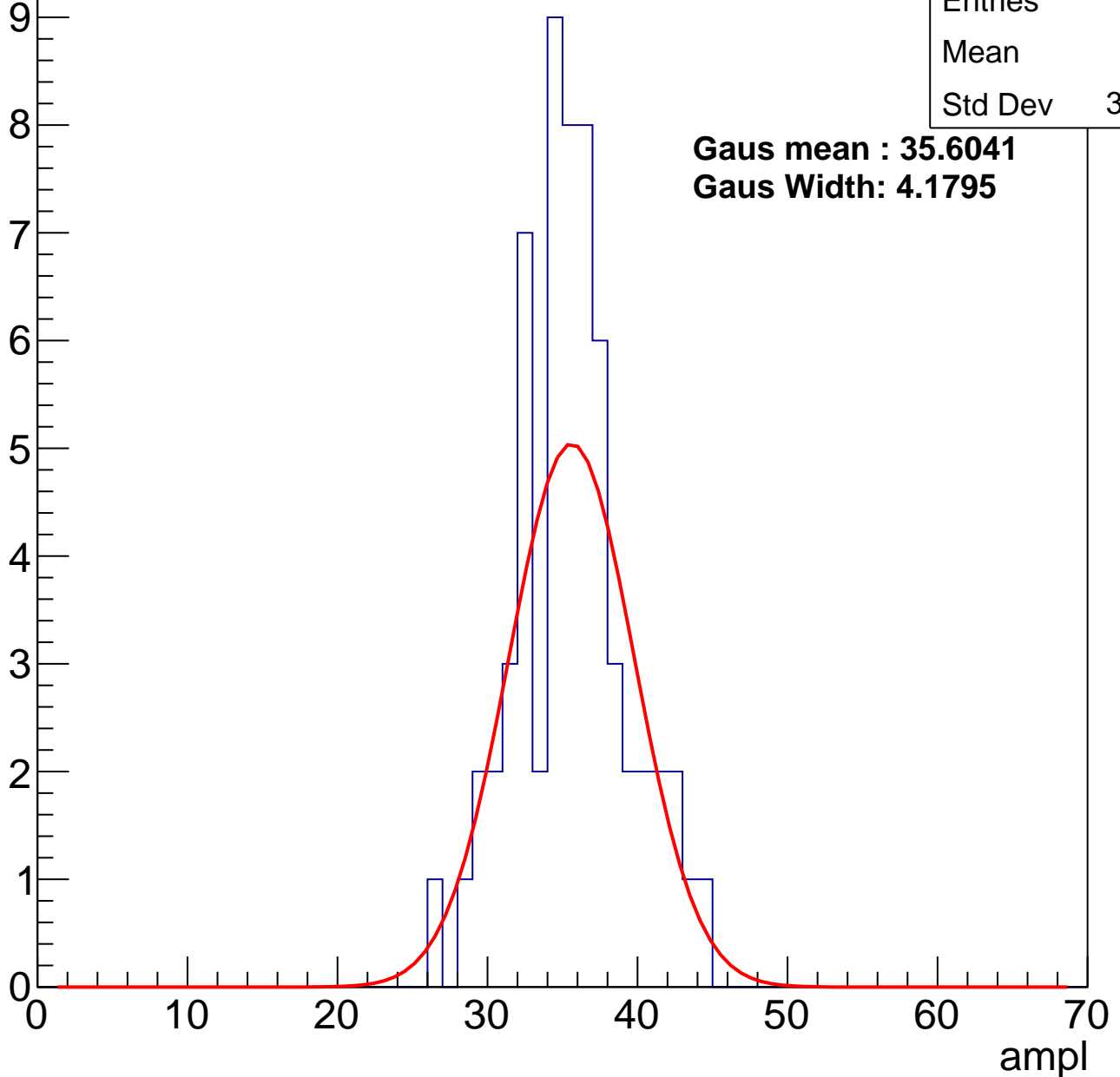
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	35.1
Std Dev	3.671

Gaus mean : 35.6041

Gaus Width: 4.1795



B1L101S, U1-ch0, adc2

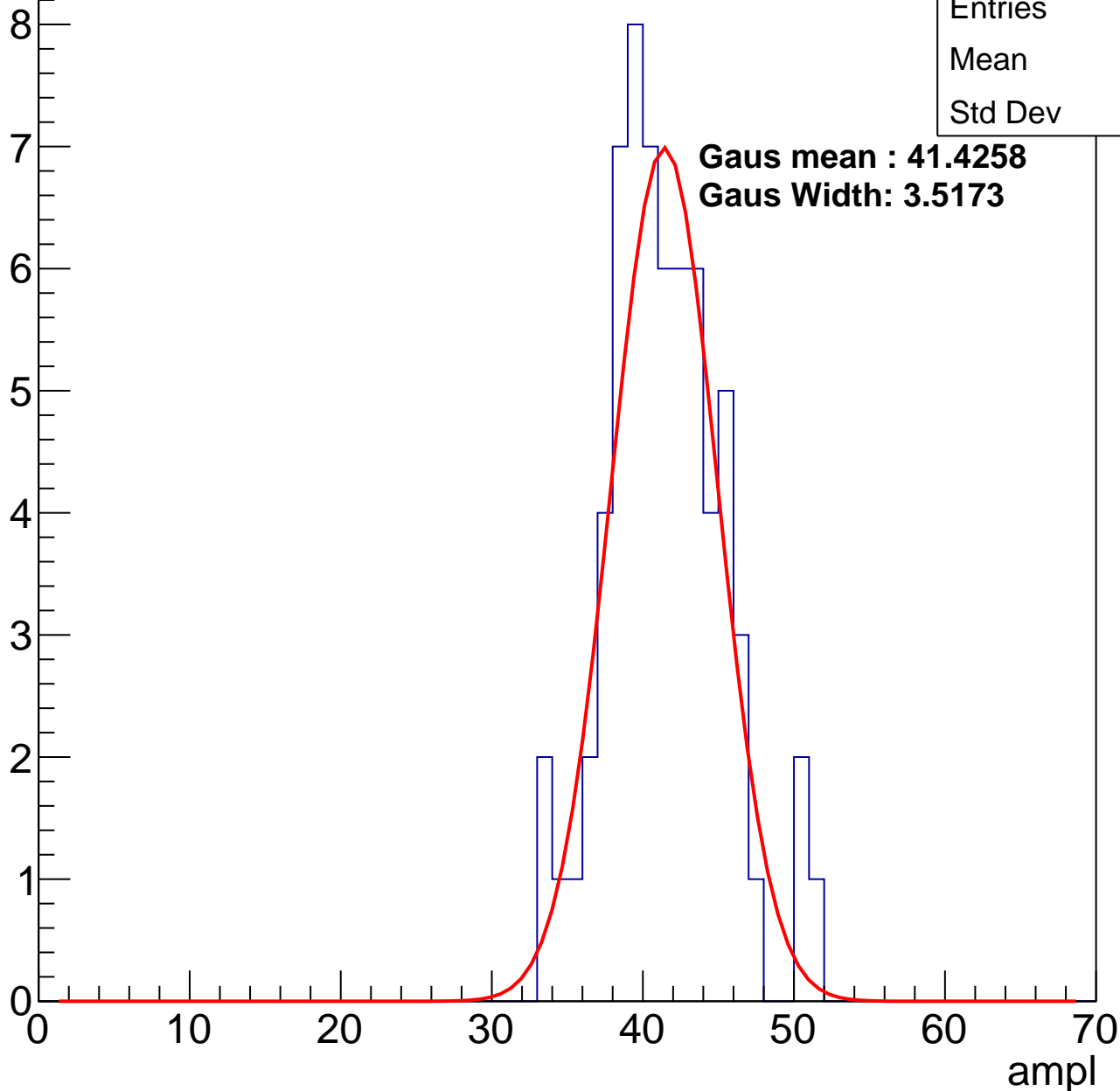
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41
Std Dev	3.79

Gaus mean : 41.4258

Gaus Width: 3.5173



B1L101S, U1-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

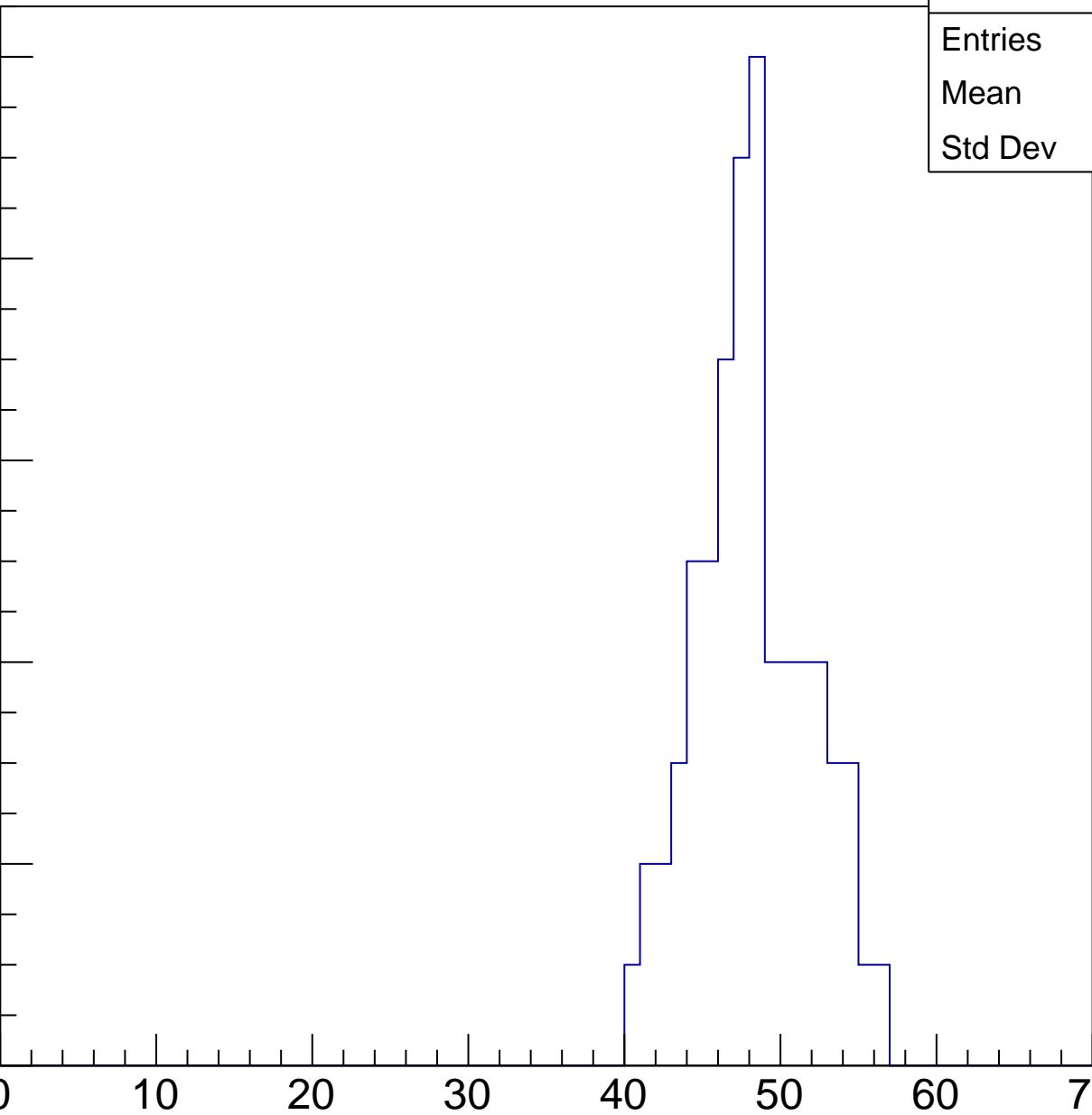
Entries	68
Mean	47.72
Std Dev	3.601

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

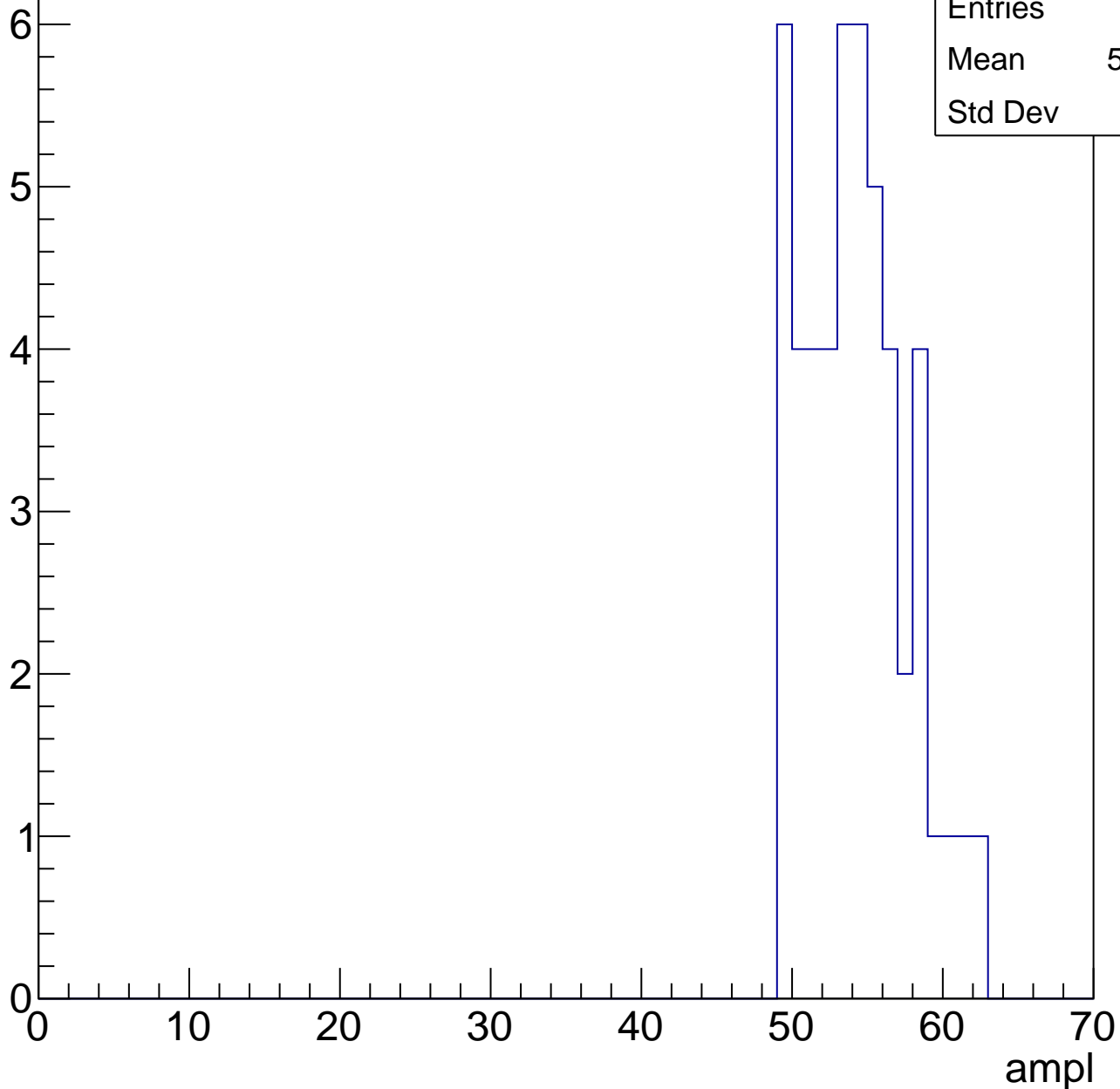


B1L101S, U1-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

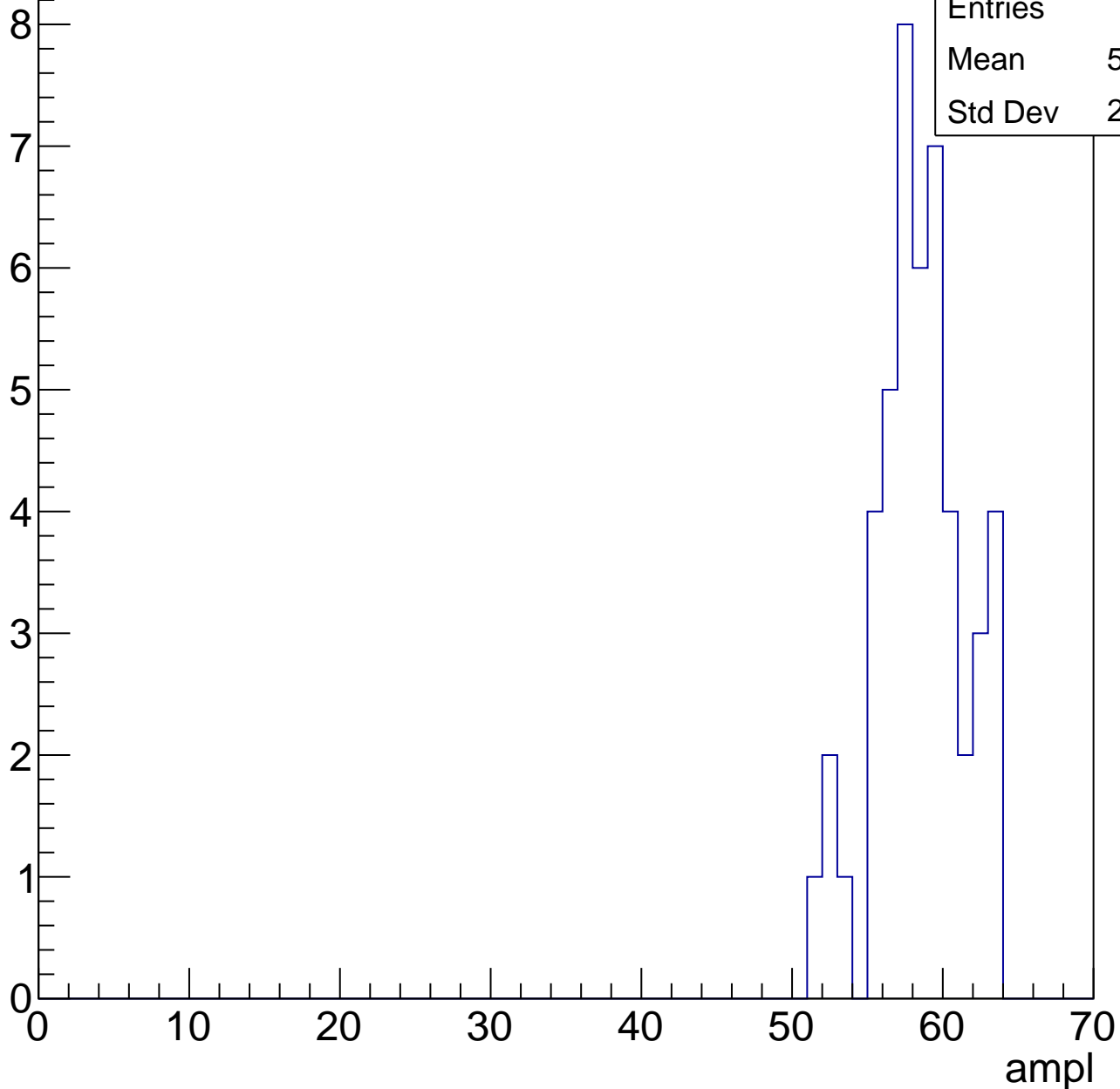
Entries	49
Mean	53.78
Std Dev	3.34



B1L101S, U1-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

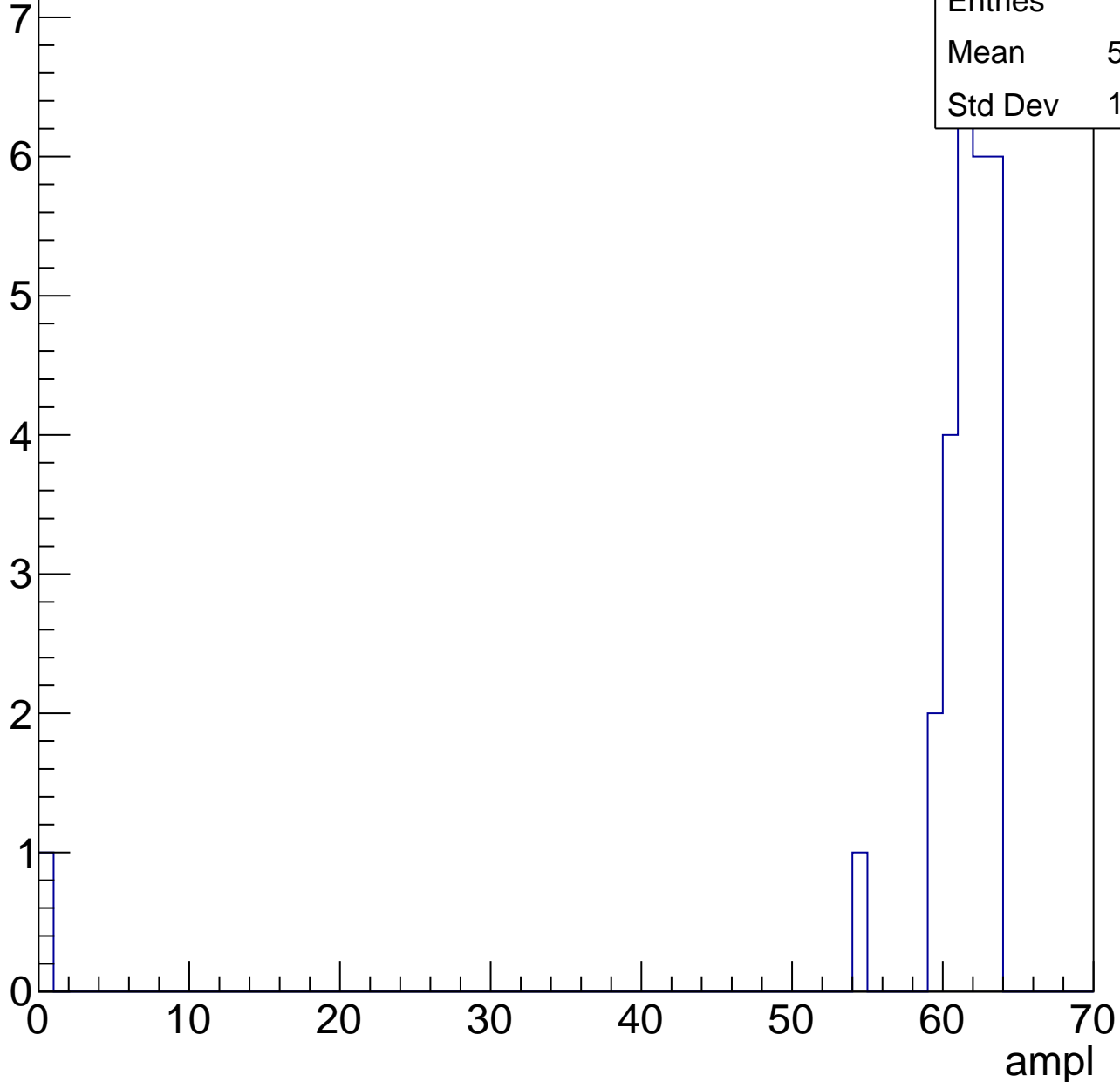


B1L101S, U1-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

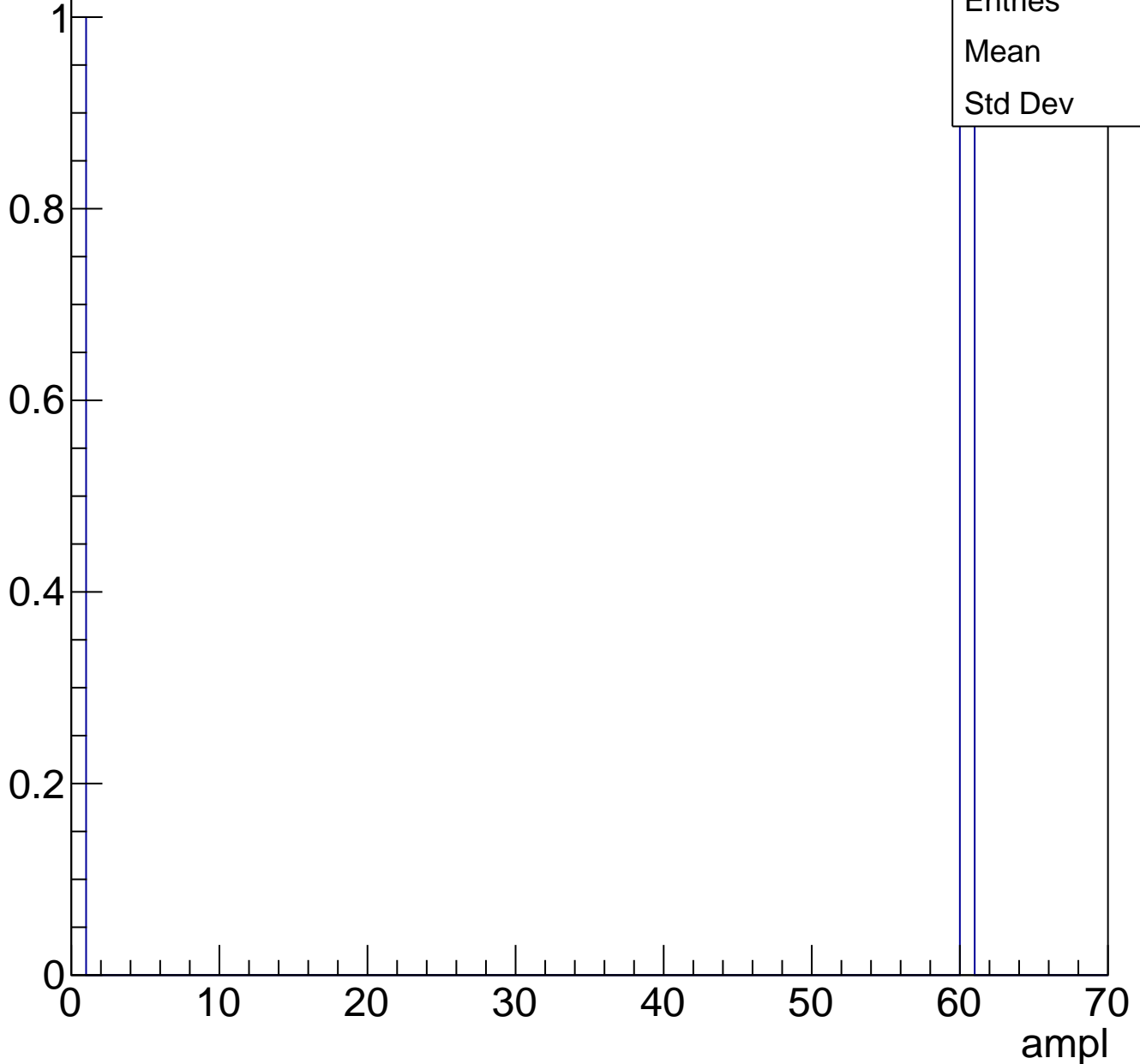
Entries	27
Mean	58.85
Std Dev	11.69



B1L101S, U1-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch1, adc0

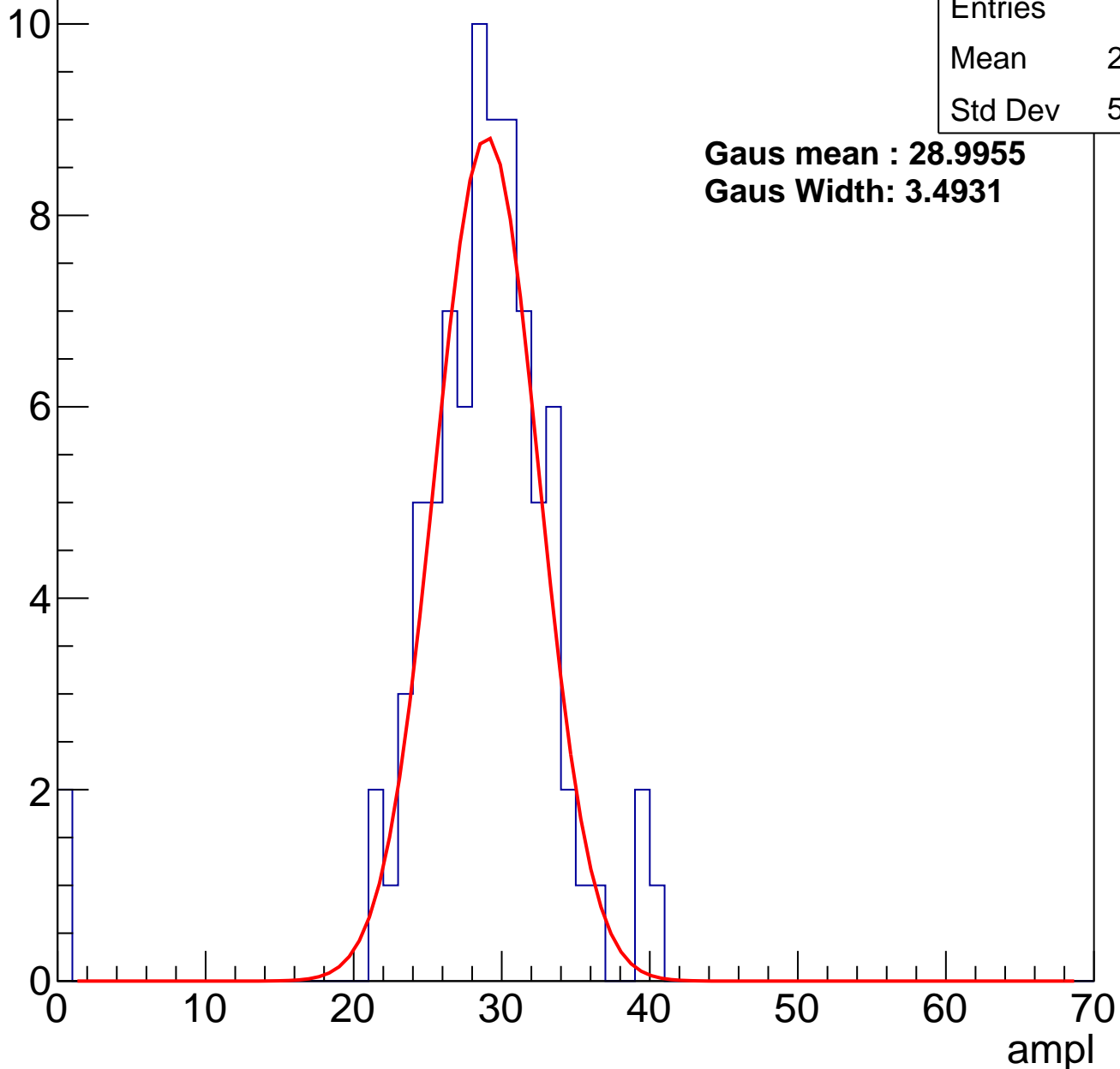
calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	28.15
Std Dev	5.818

Gaus mean : 28.9955

Gaus Width: 3.4931

Entry



B1L101S, U1-ch1, adc1

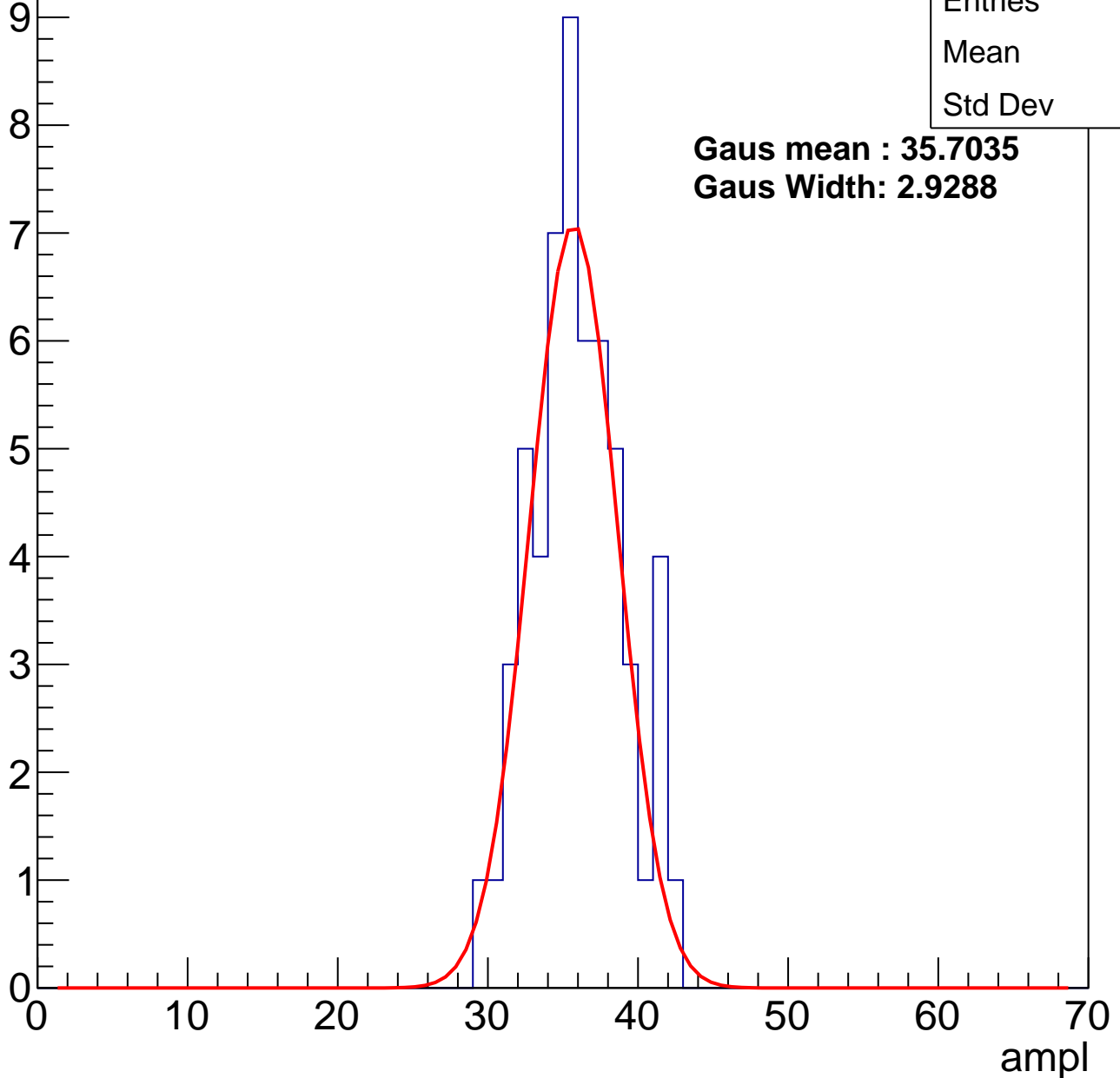
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.5
Std Dev	3

Gaus mean : 35.7035

Gaus Width: 2.9288



B1L101S, U1-ch1, adc2

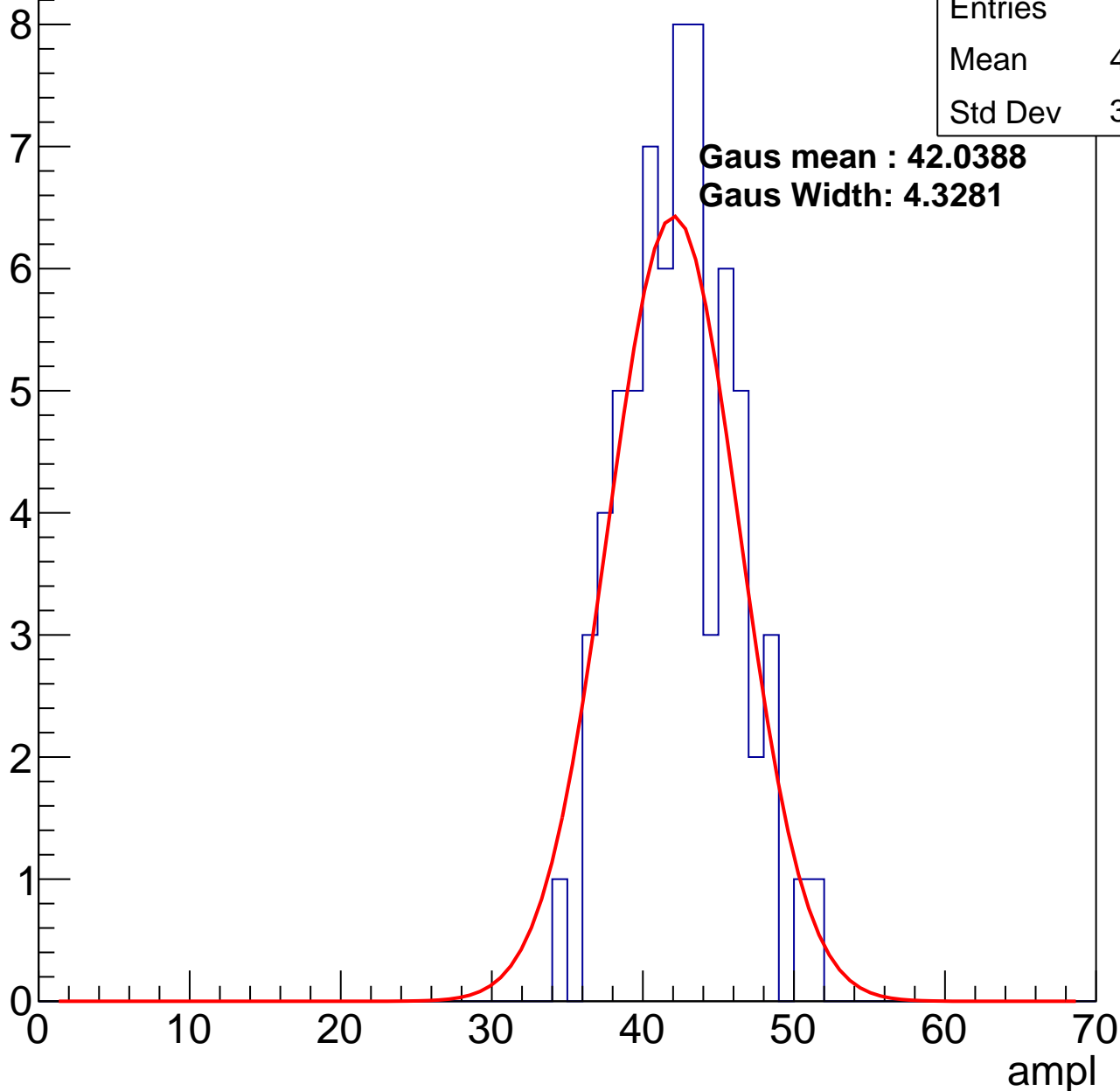
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.94
Std Dev	3.613

Gaus mean : 42.0388

Gaus Width: 4.3281

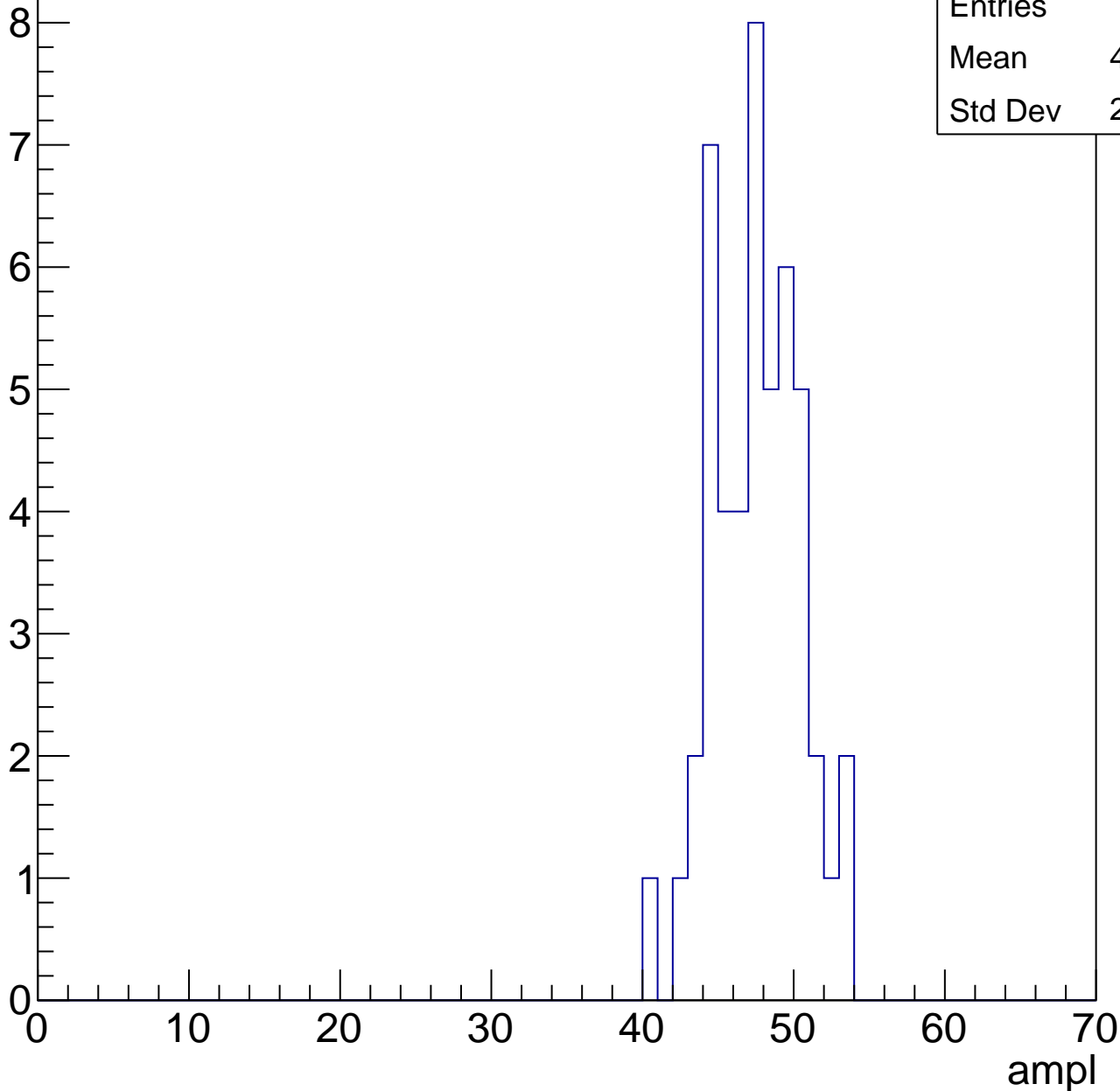


B1L101S, U1-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

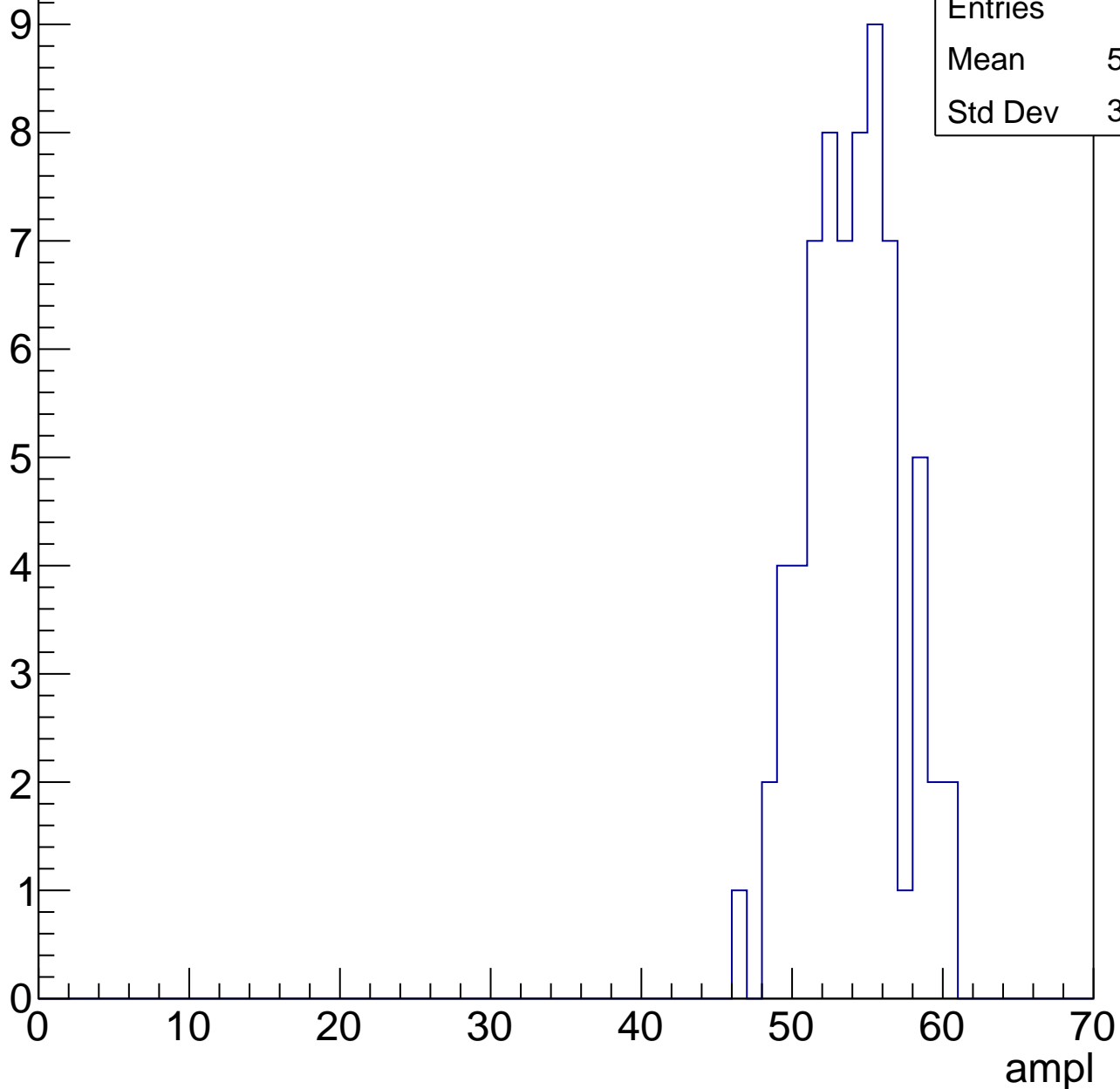
Entries	48
Mean	47.08
Std Dev	2.857



B1L101S, U1-ch1, adc4

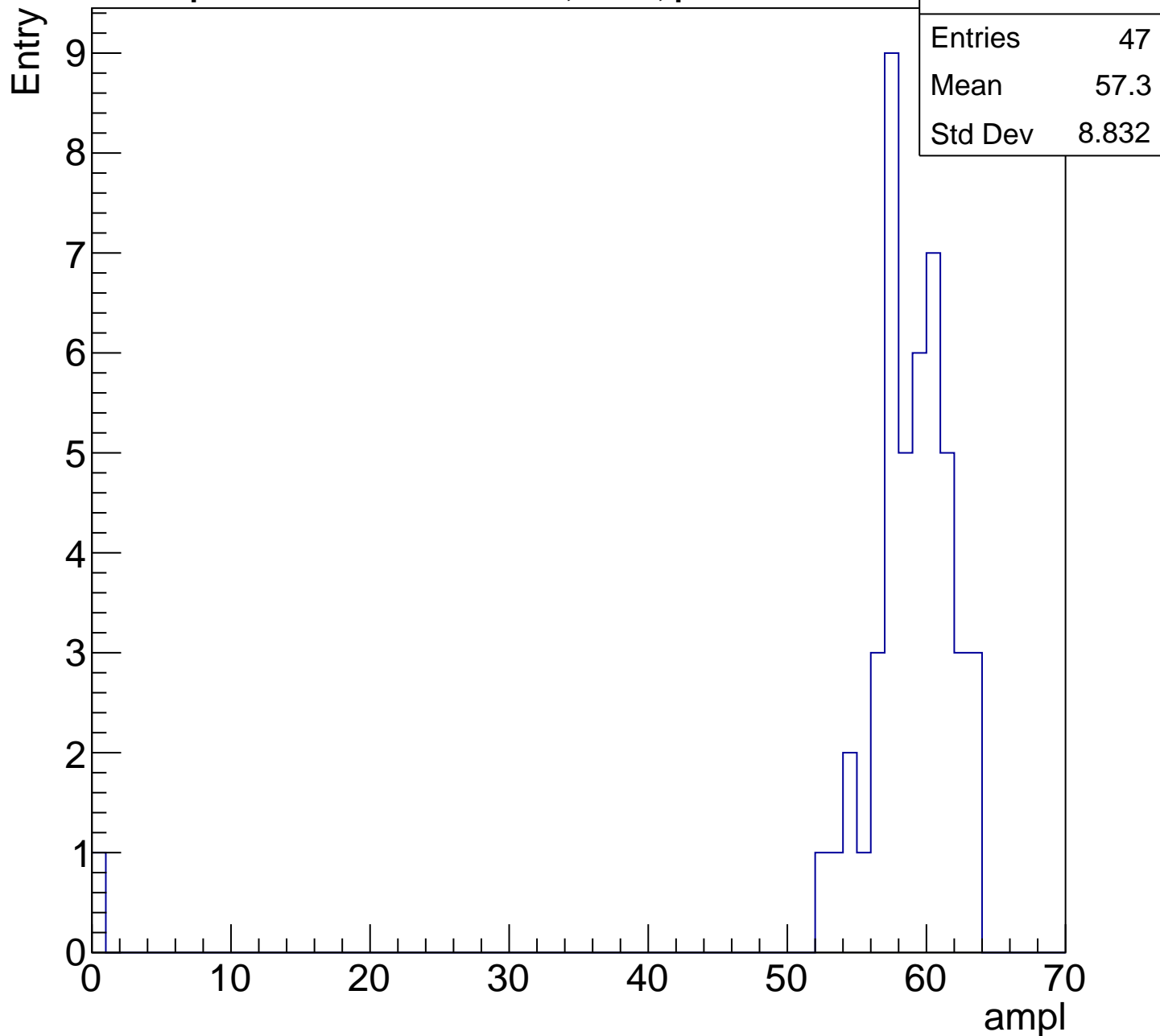
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

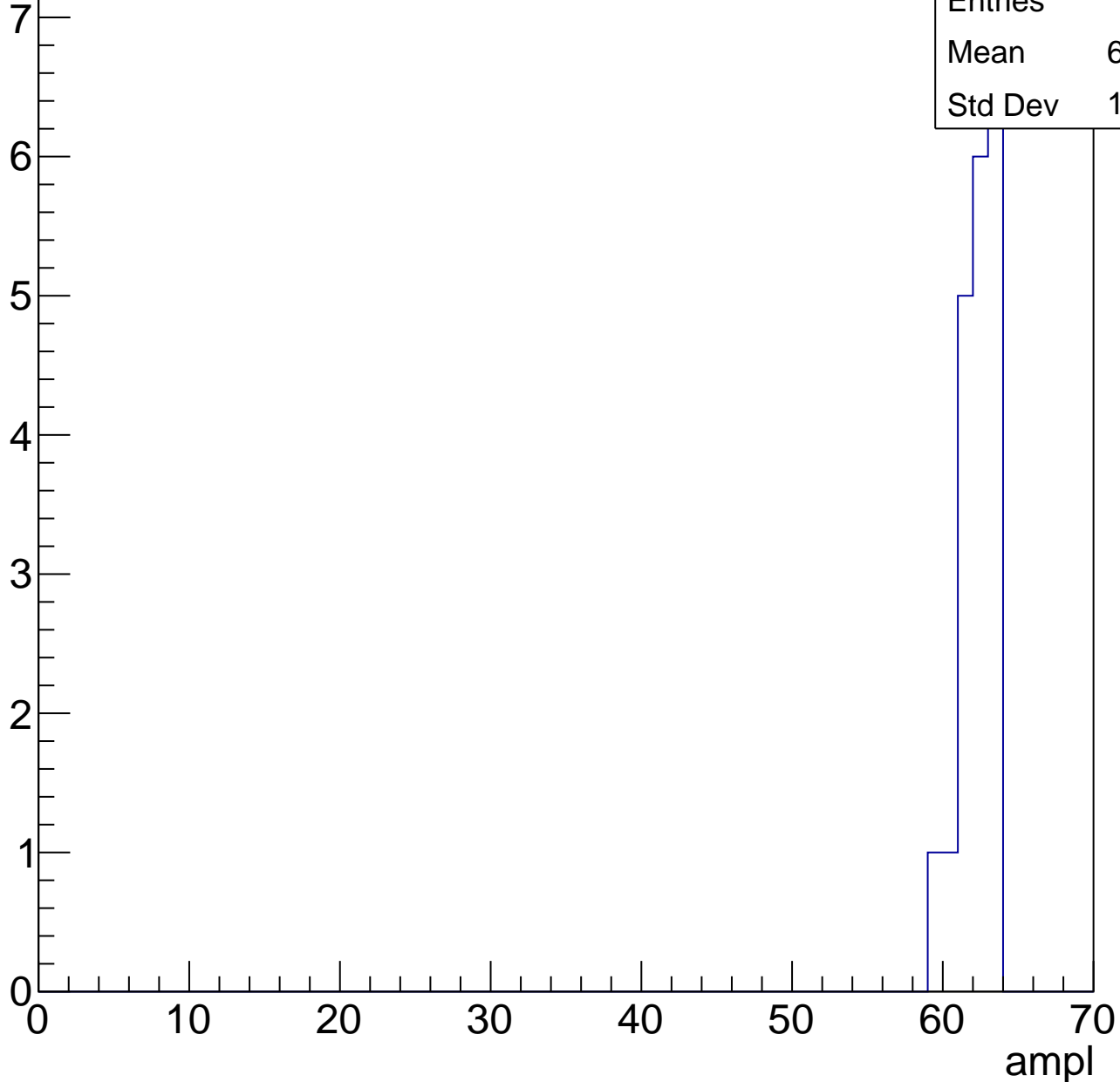


B1L101S, U1-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

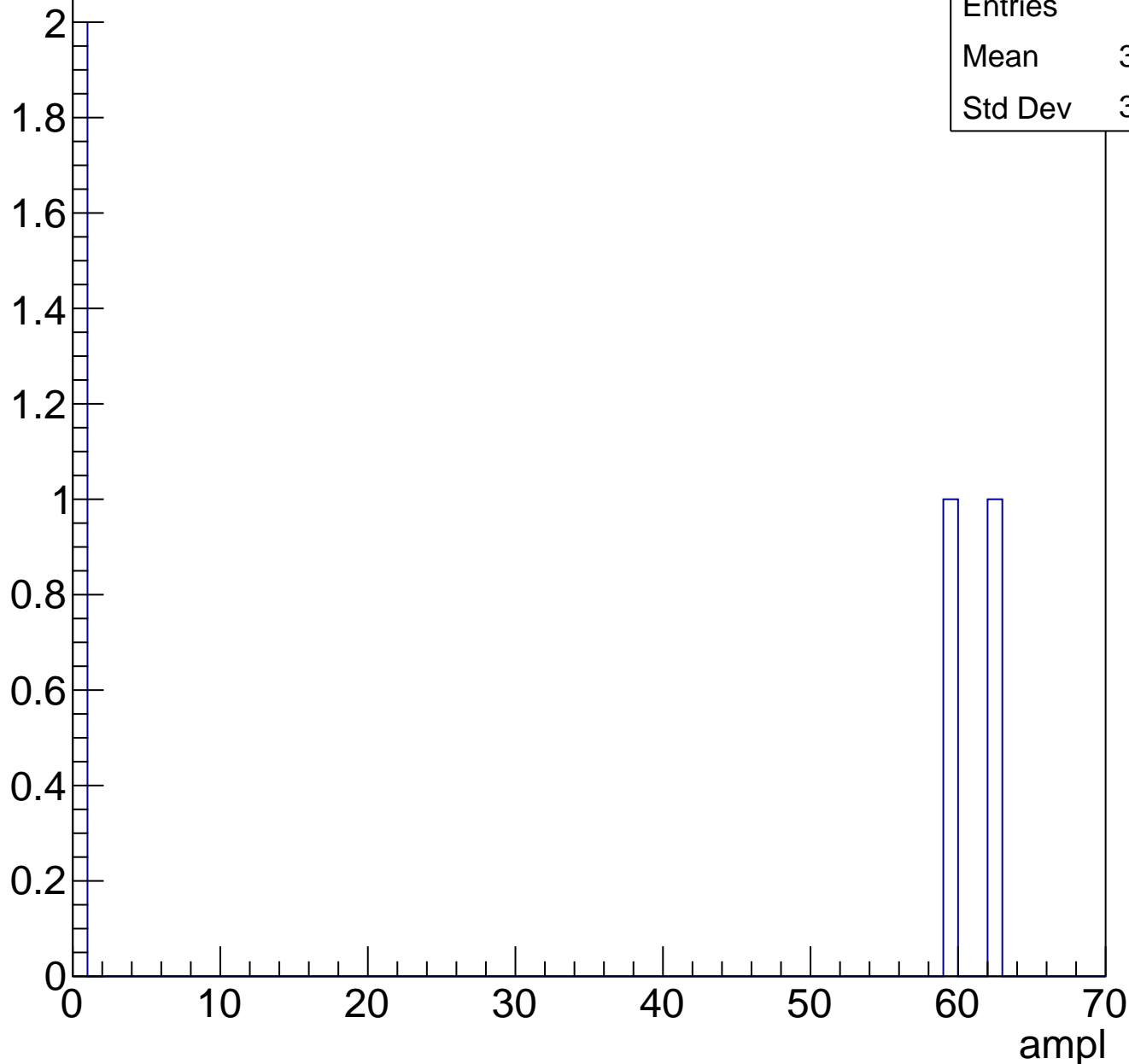
Entries	20
Mean	61.85
Std Dev	1.108



B1L101S, U1-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	30.25
Std Dev	30.27

B1L101S, U1-ch2, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	27.94
Std Dev	6.009

Gaus mean : 29.0211

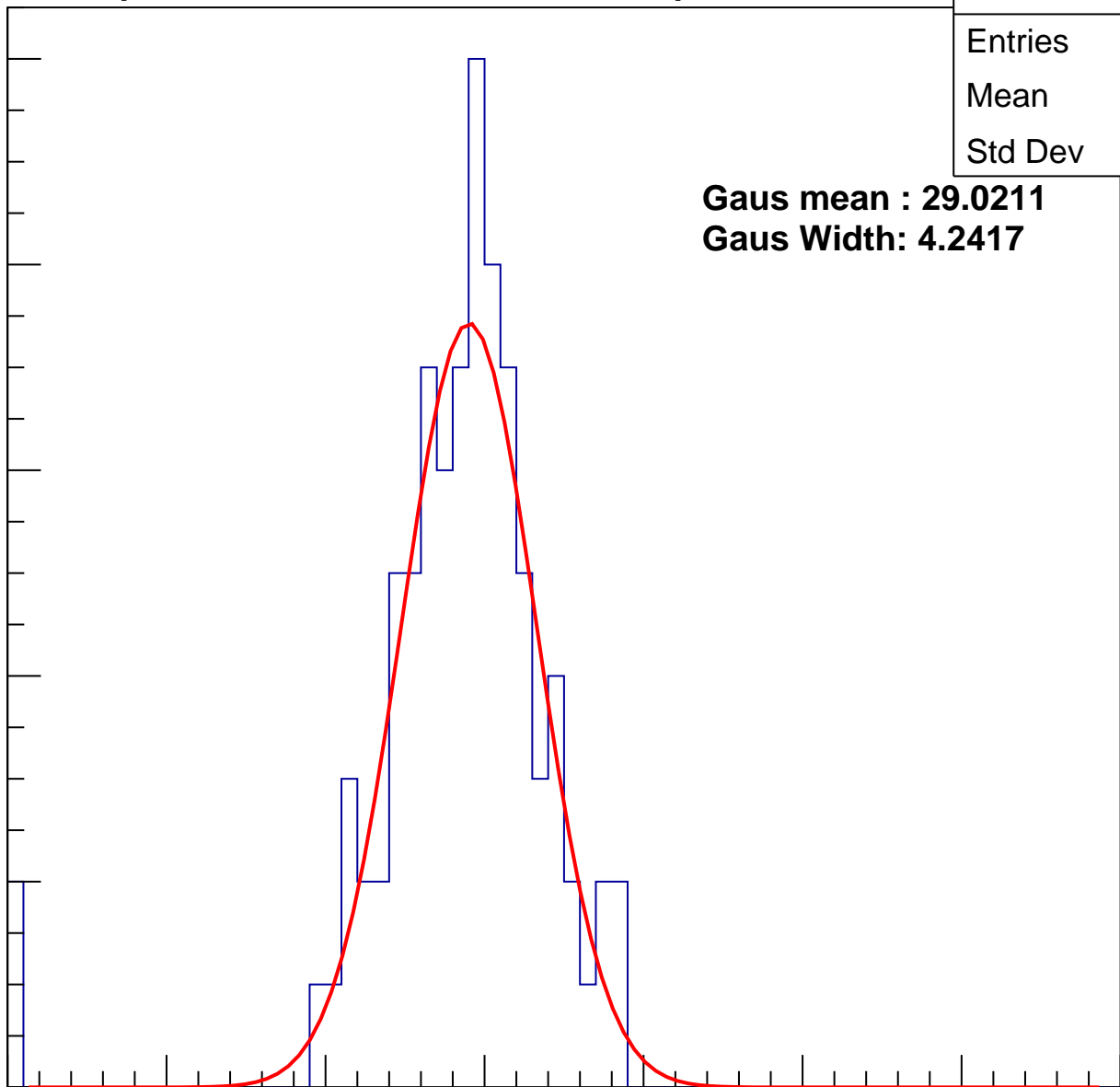
Gaus Width: 4.2417

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch2, adc1

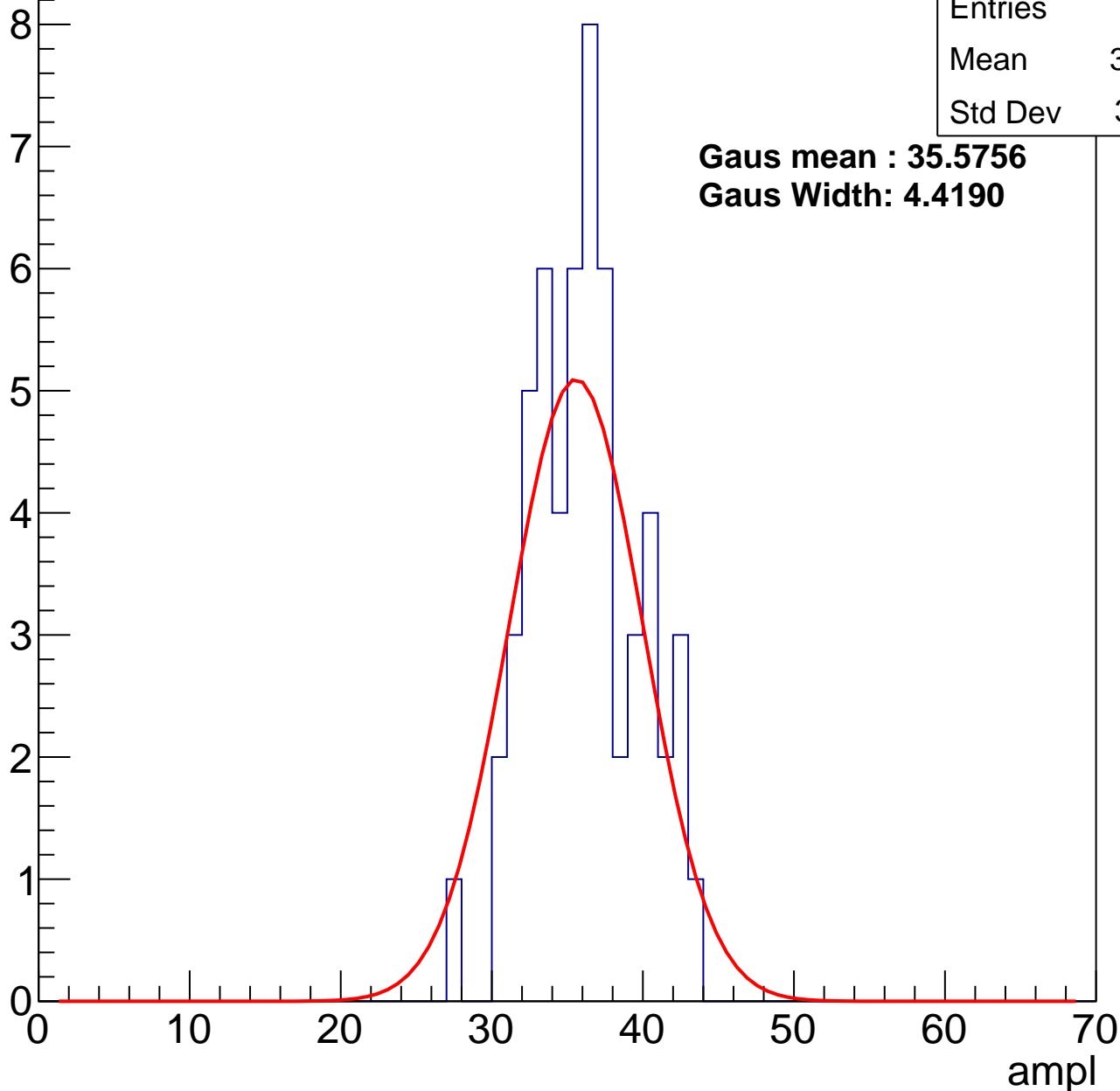
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.68
Std Dev	3.511

Gaus mean : 35.5756

Gaus Width: 4.4190



B1L101S, U1-ch2, adc2

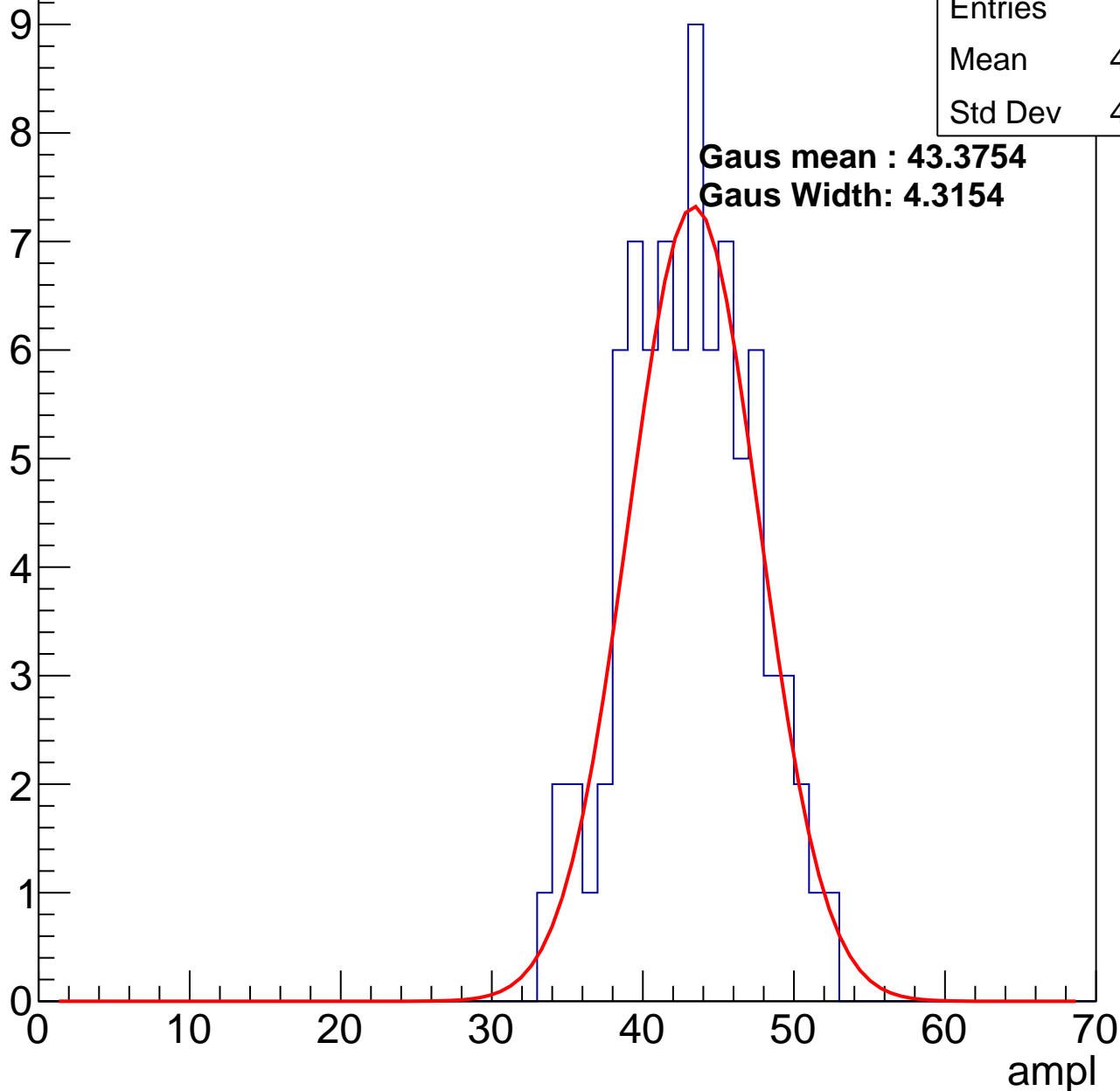
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	42.57
Std Dev	4.174

Gaus mean : 43.3754

Gaus Width: 4.3154

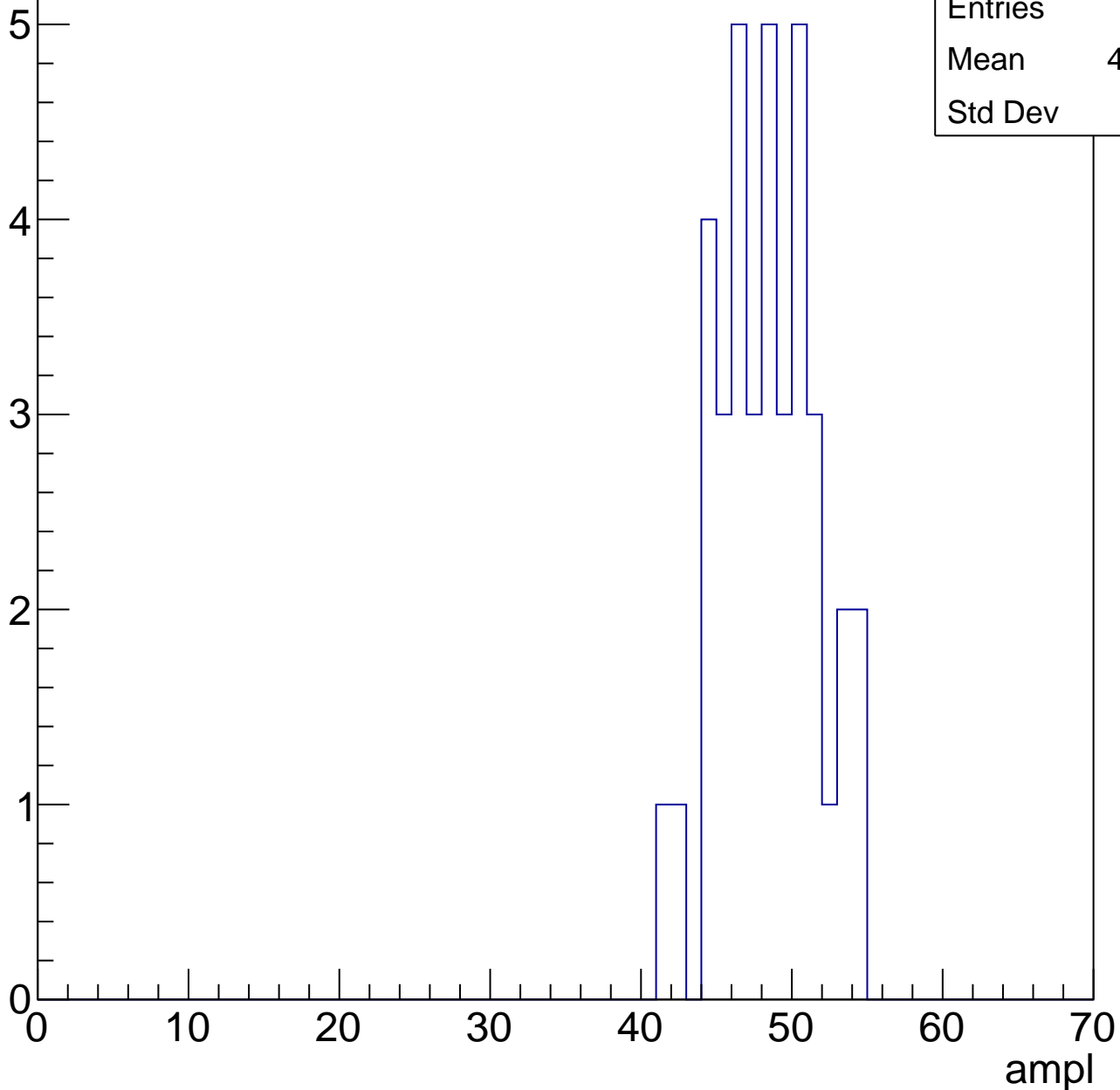


B1L101S, U1-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

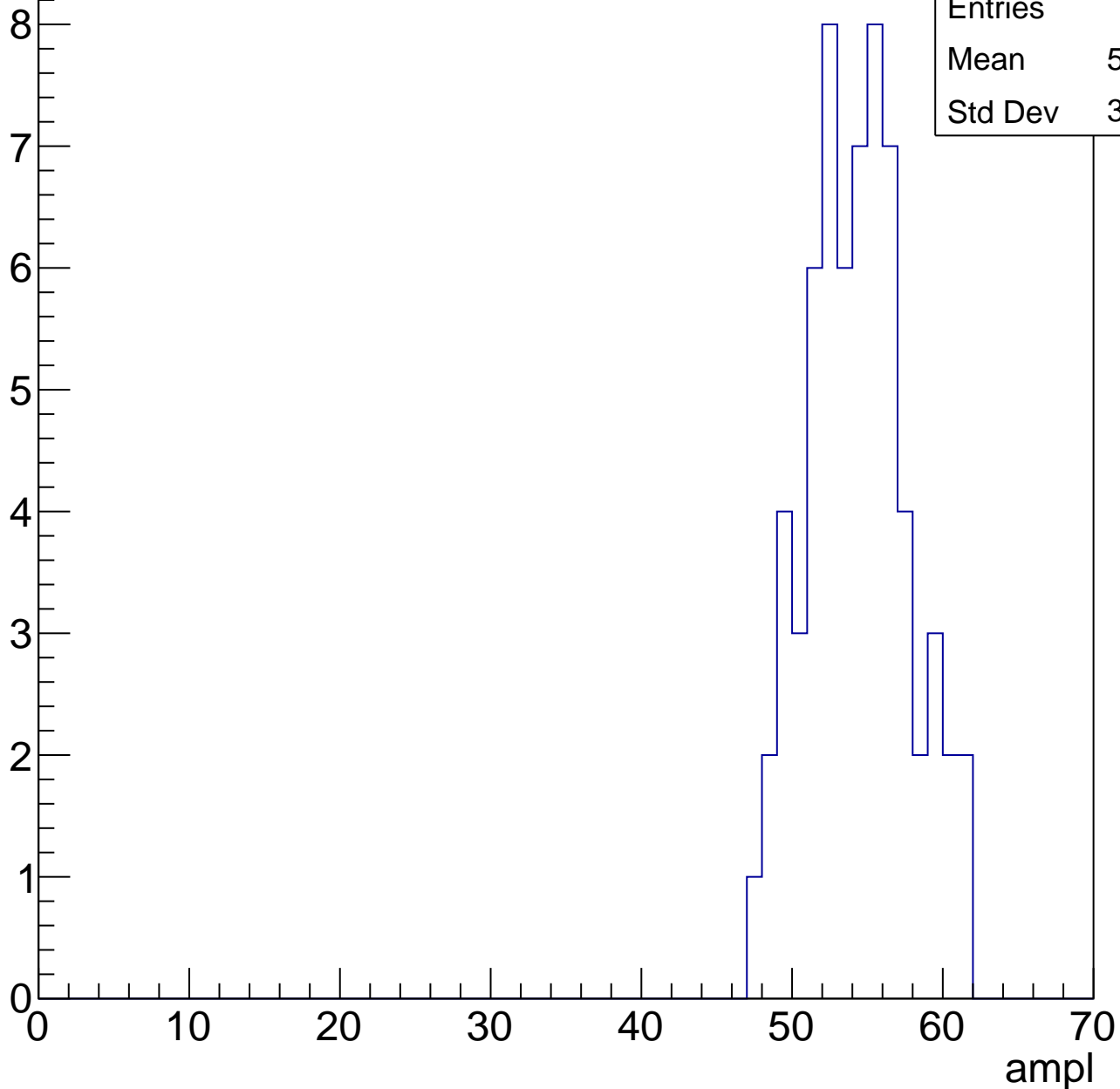
Entries	38
Mean	47.92
Std Dev	3.19



B1L101S, U1-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



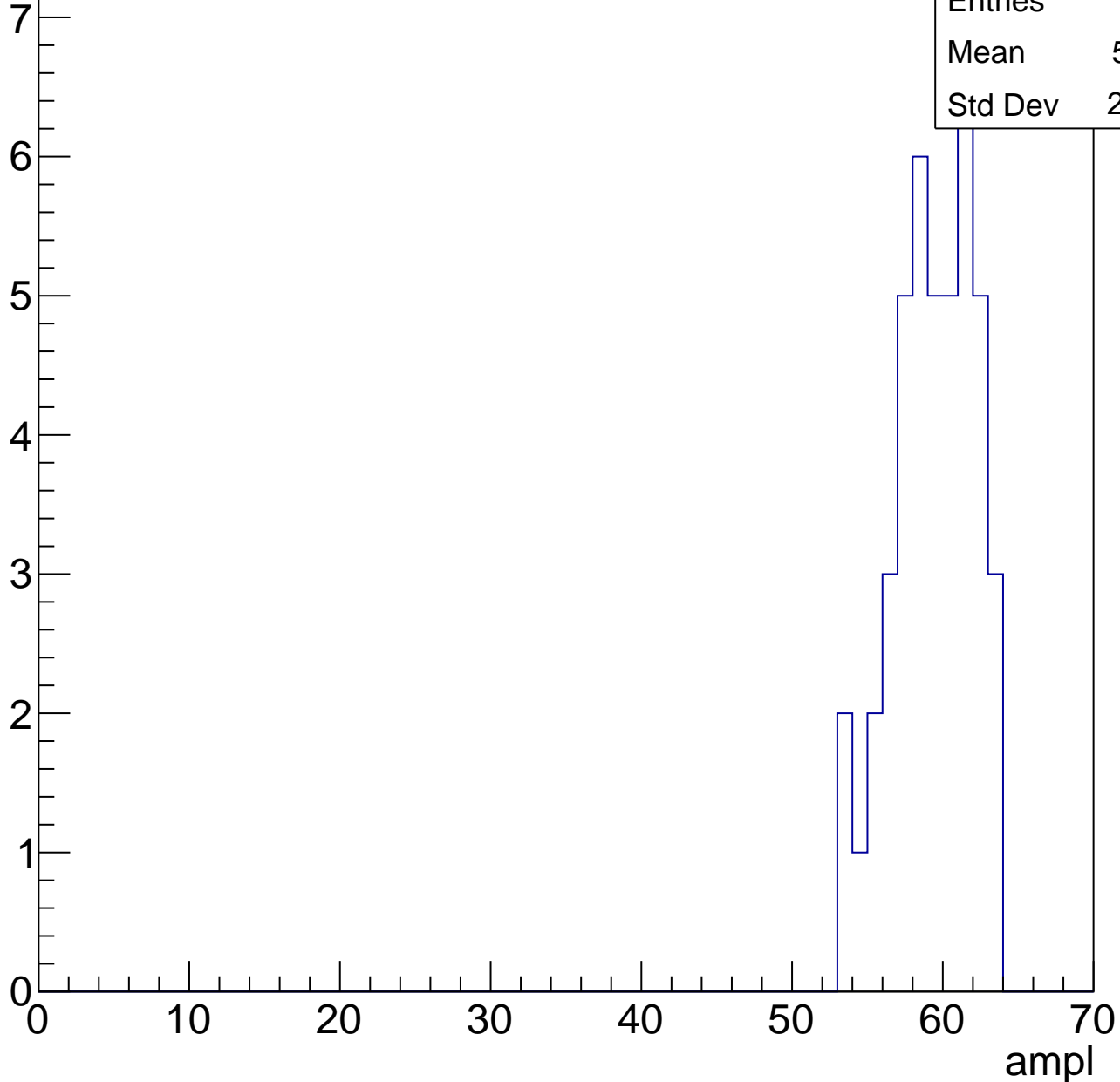
Entries	65
Mean	53.88
Std Dev	3.303

B1L101S, U1-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.91
Std Dev	2.644

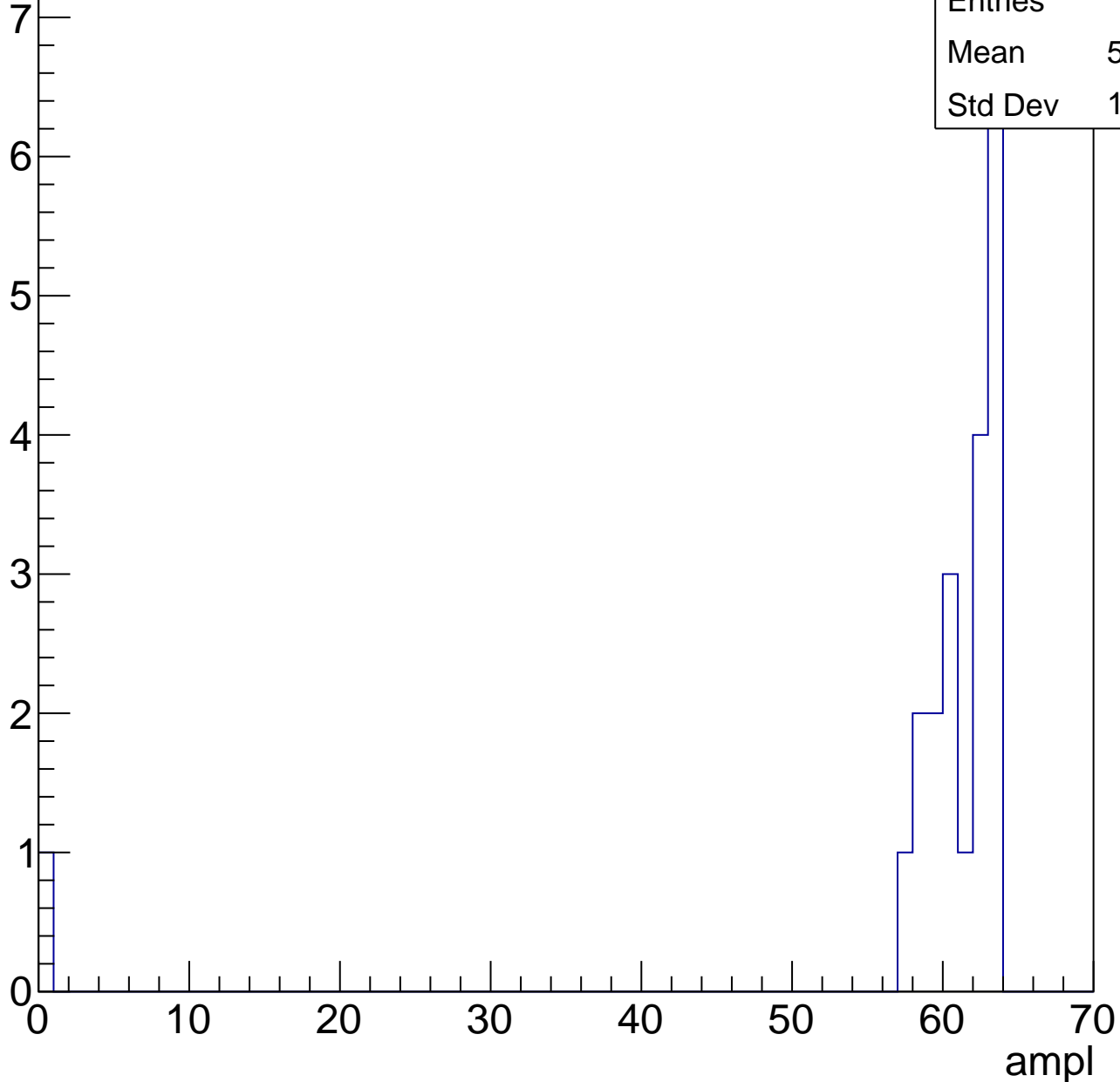


B1L101S, U1-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

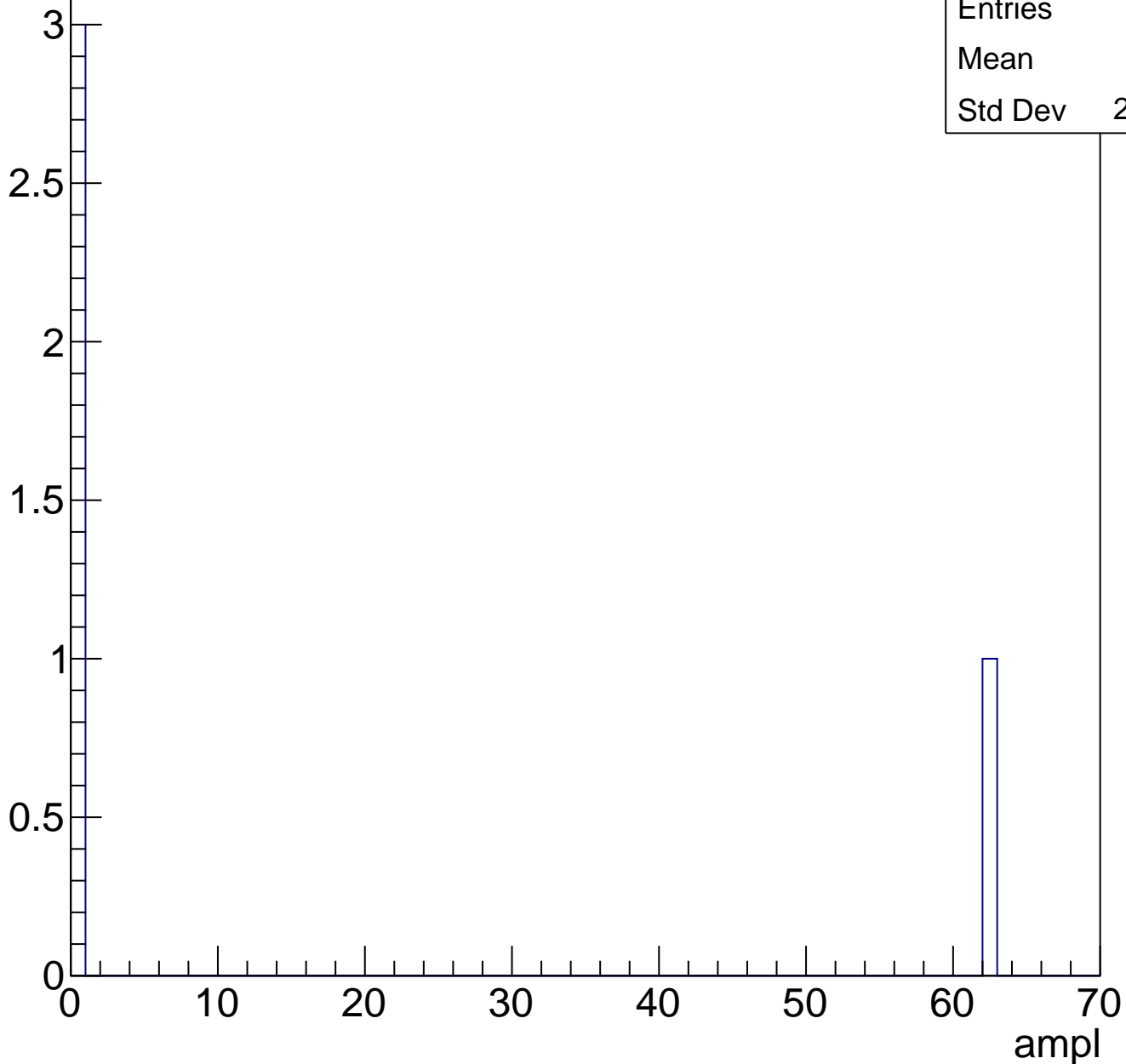
Entries	21
Mean	58.14
Std Dev	13.14



B1L101S, U1-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch3, adc0

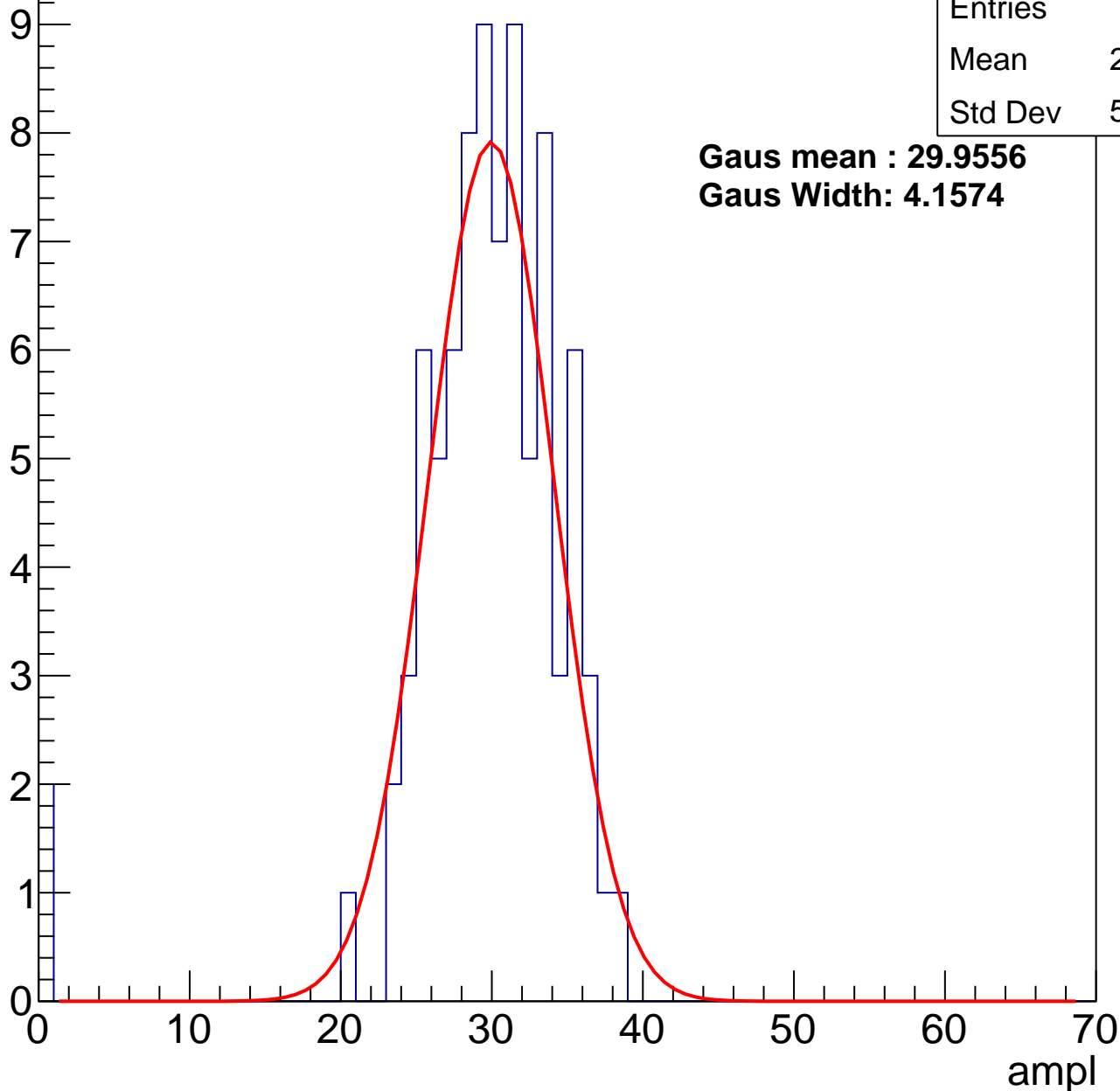
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	29.09
Std Dev	5.822

Gaus mean : 29.9556

Gaus Width: 4.1574



B1L101S, U1-ch3, adc1

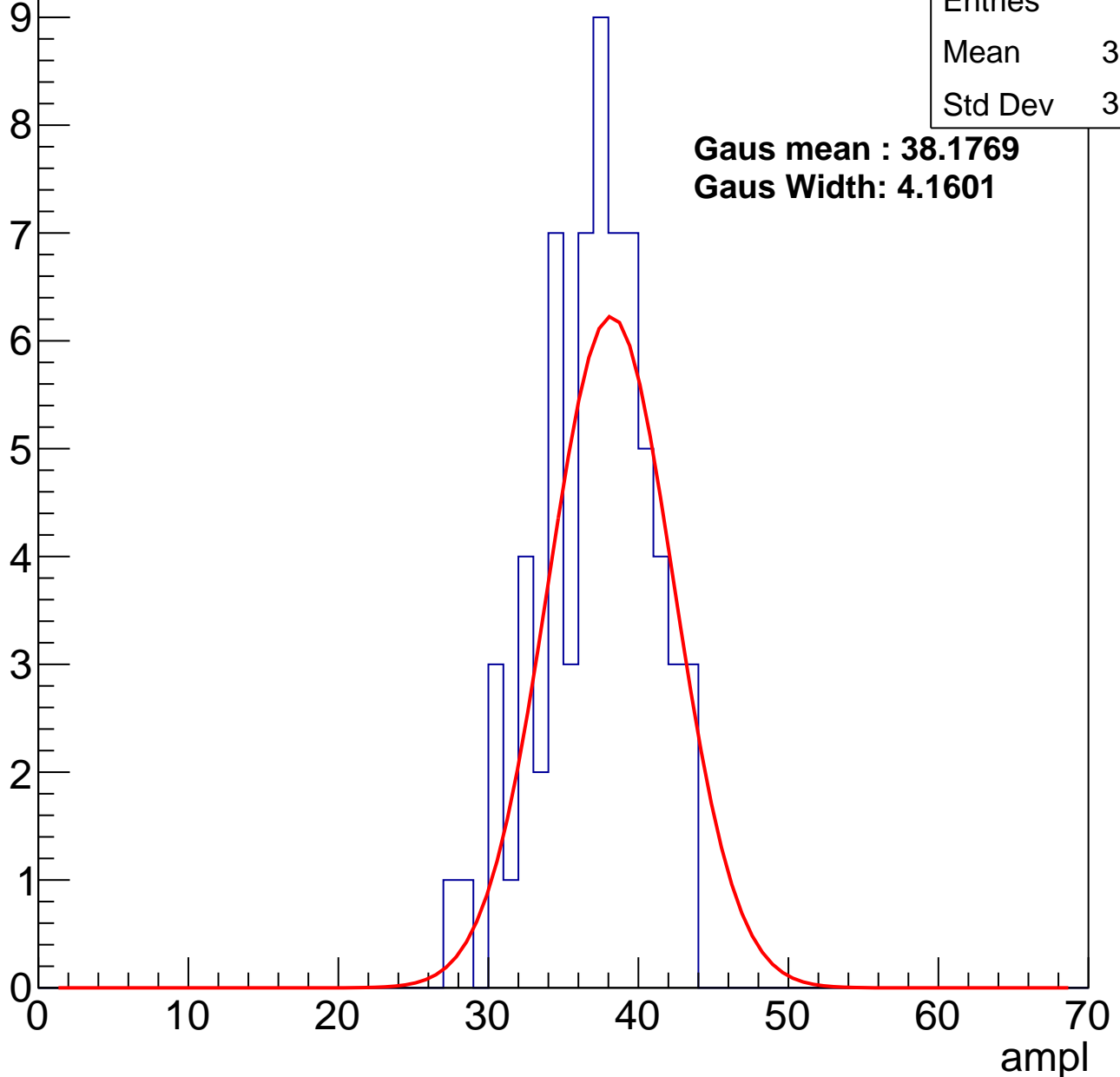
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.66
Std Dev	3.659

Gaus mean : 38.1769

Gaus Width: 4.1601



B1L101S, U1-ch3, adc2

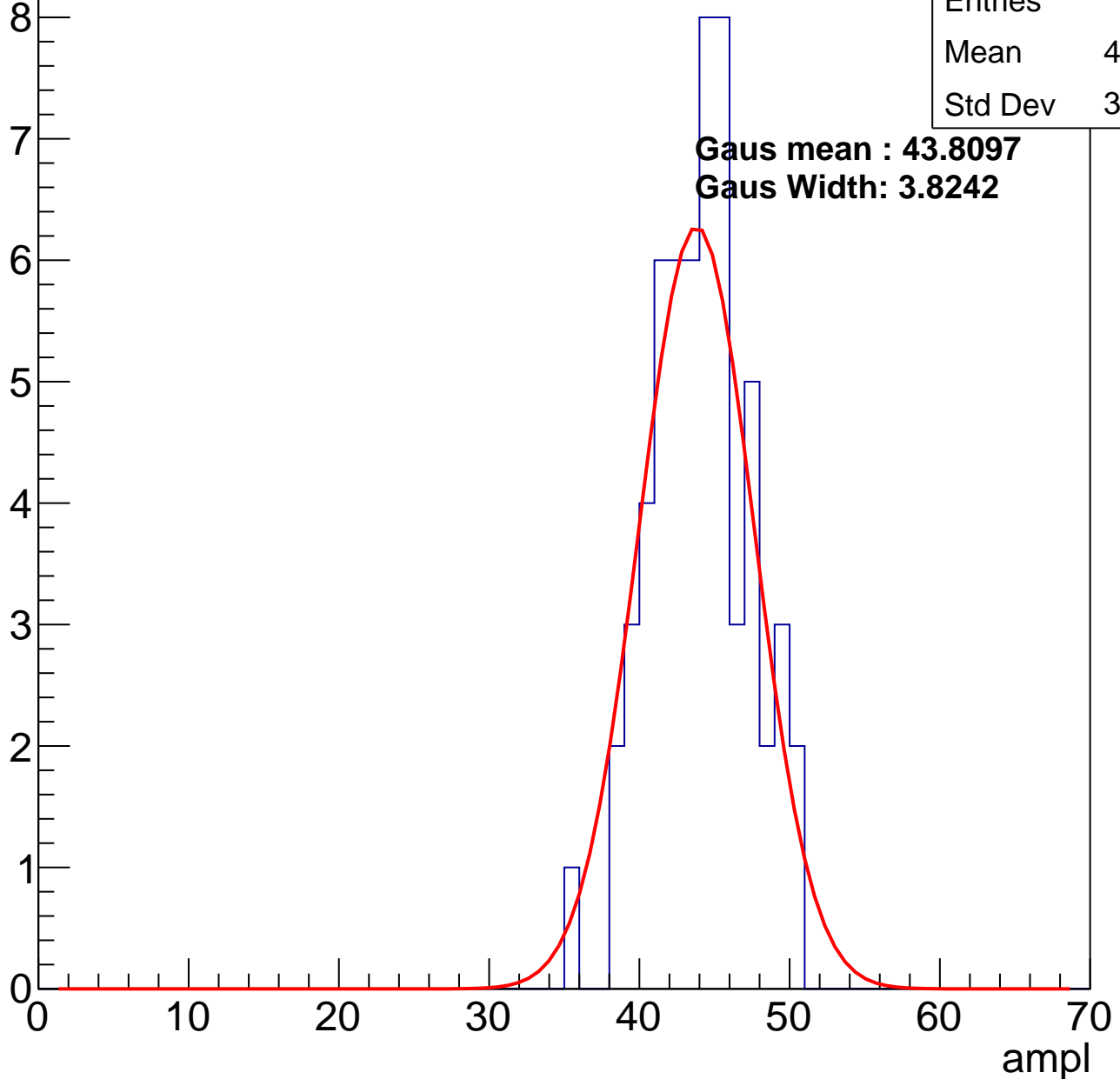
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.59
Std Dev	3.216

Gaus mean : 43.8097

Gaus Width: 3.8242

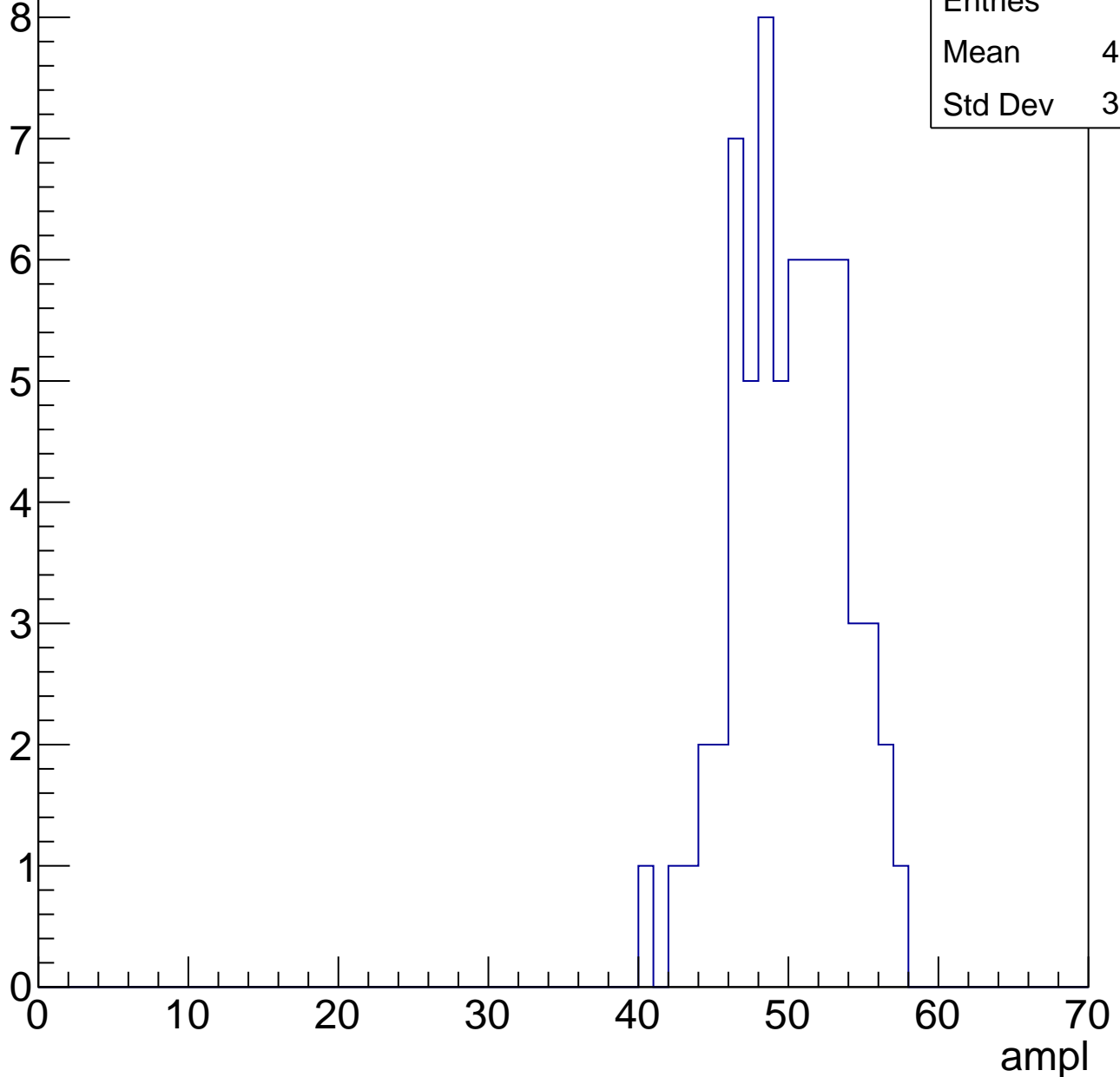


B1L101S, U1-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.55
Std Dev	3.604

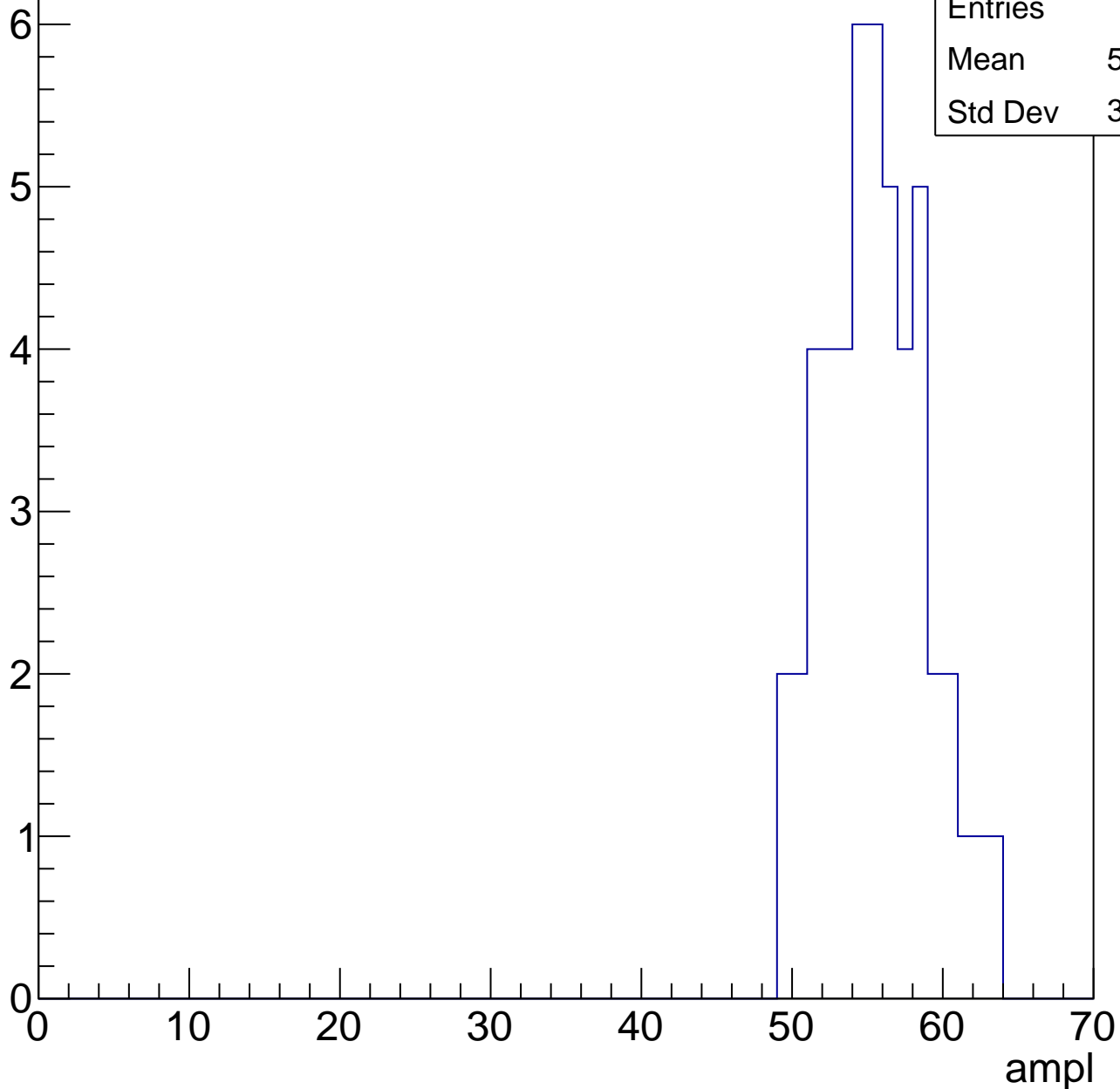


B1L101S, U1-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

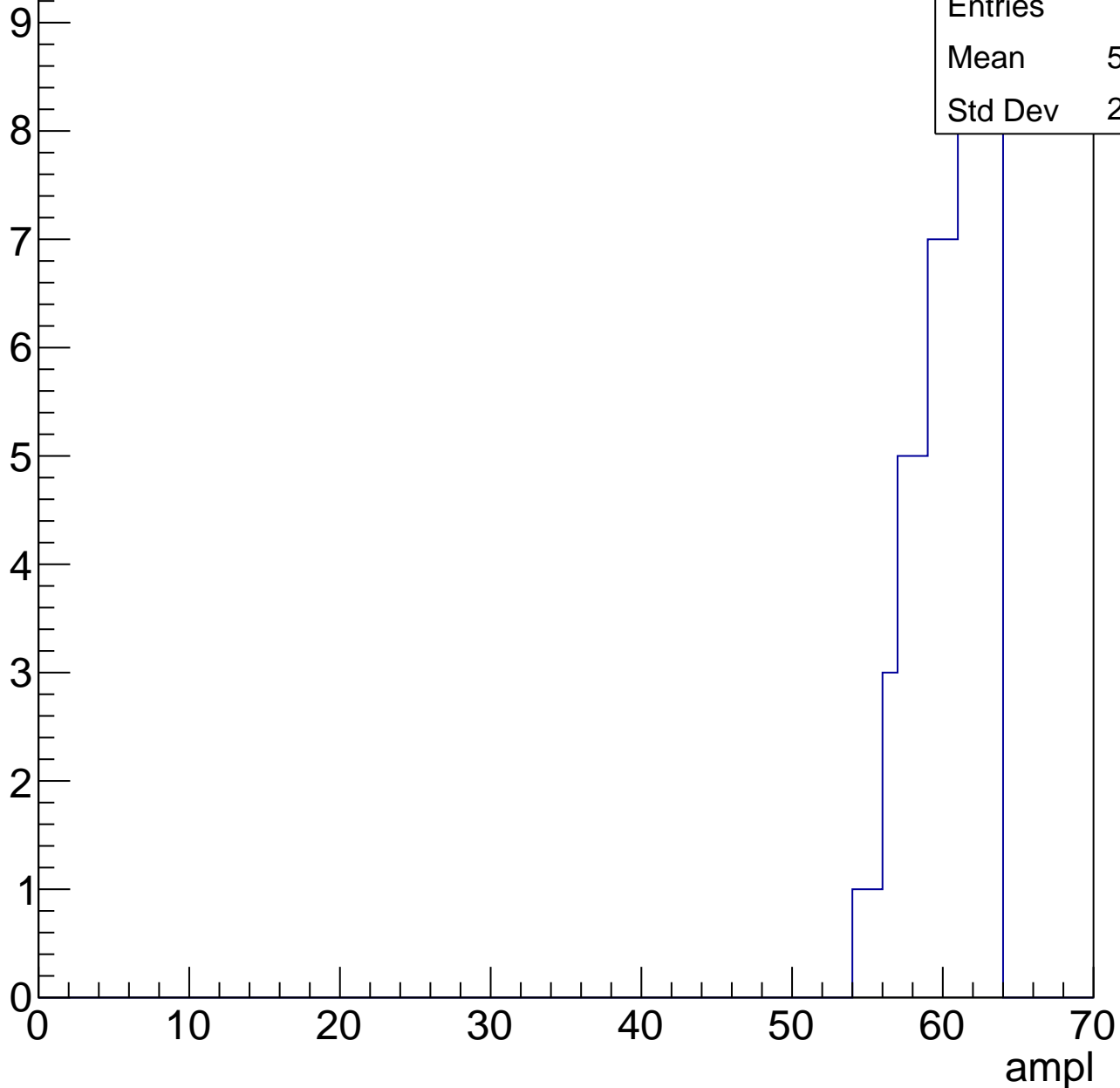
Entries	49
Mean	55.06
Std Dev	3.322



B1L101S, U1-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

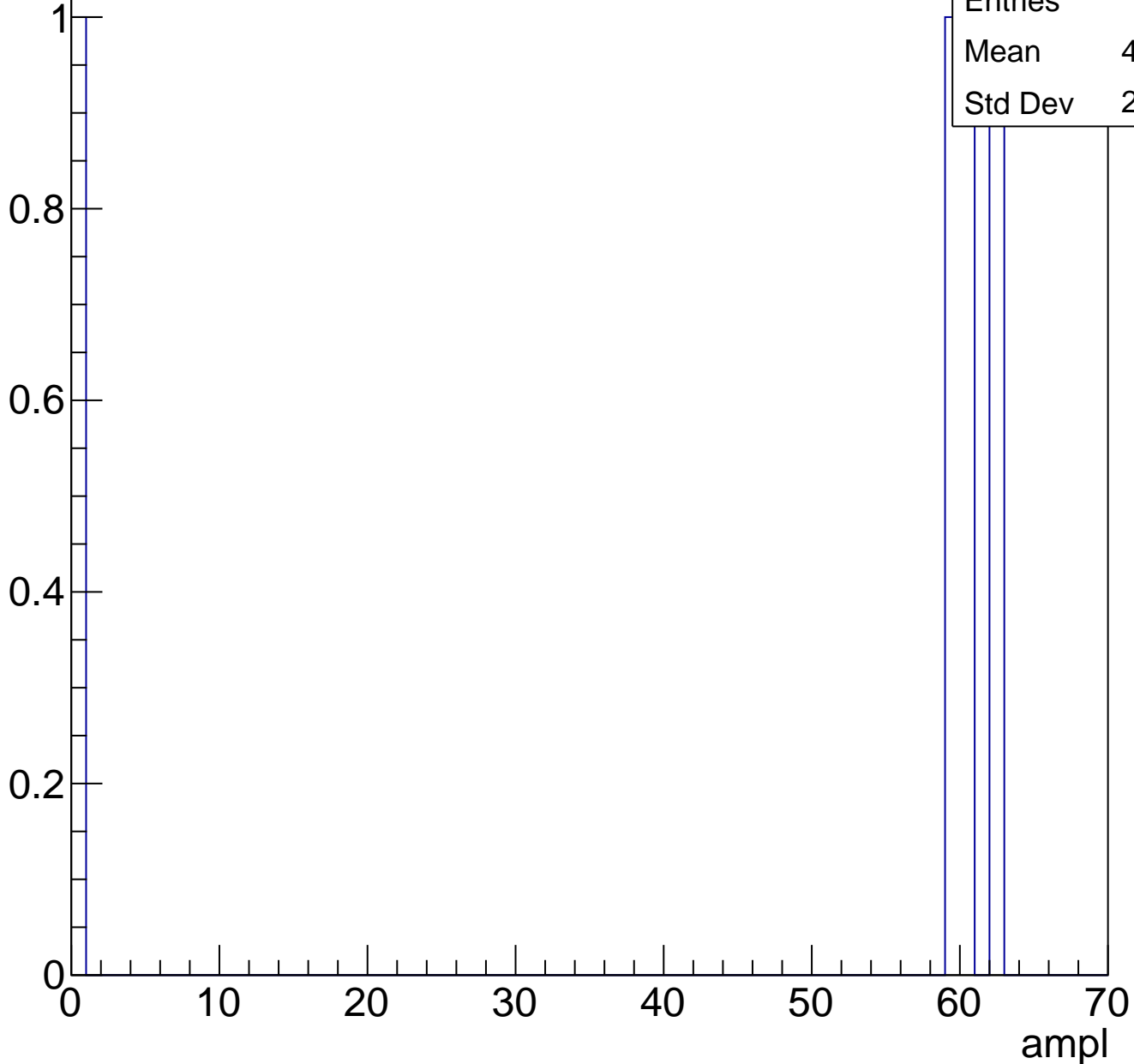
Entry



B1L101S, U1-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch4, adc0

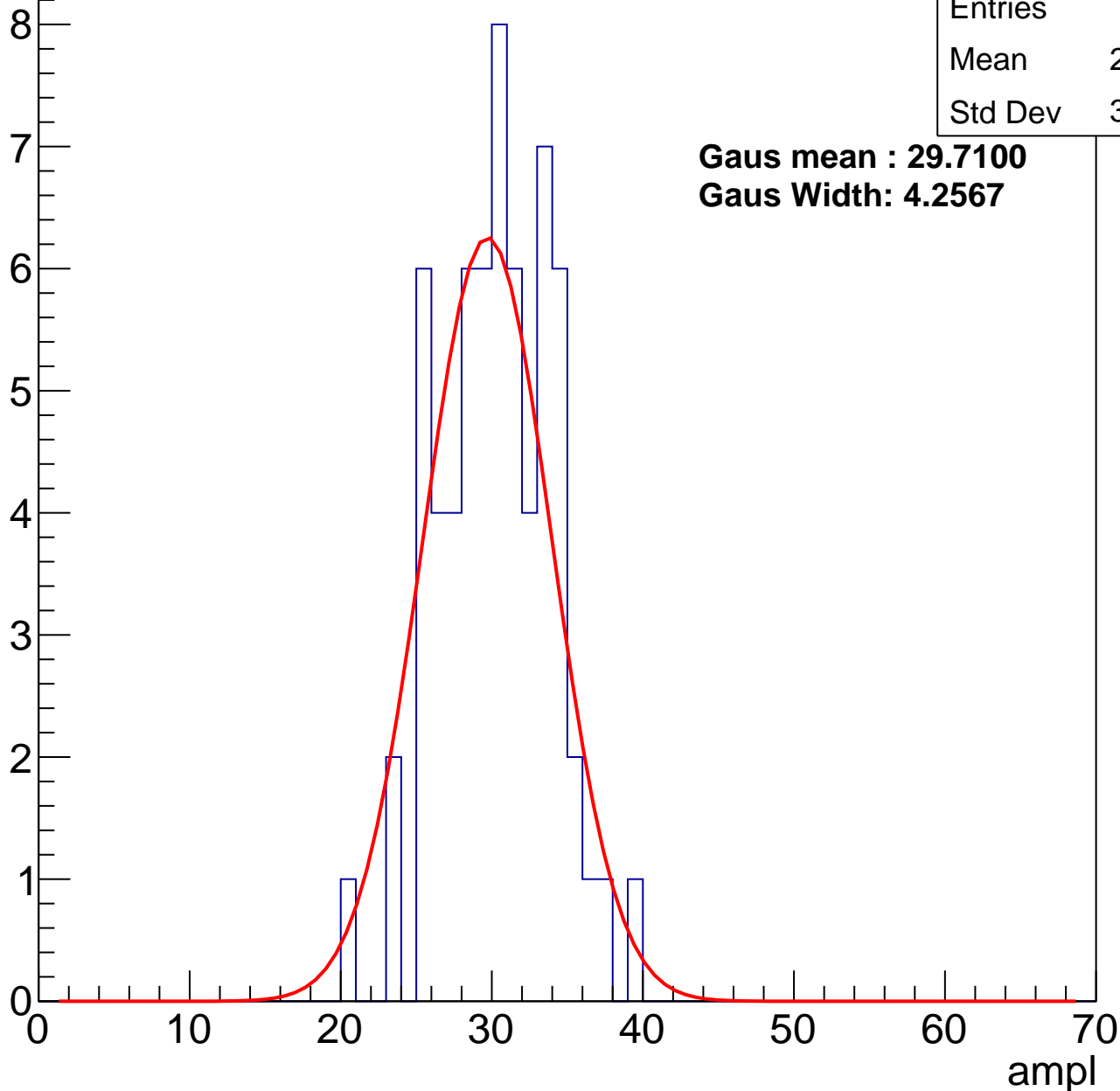
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.86
Std Dev	3.666

Gaus mean : 29.7100

Gaus Width: 4.2567



B1L101S, U1-ch4, adc1

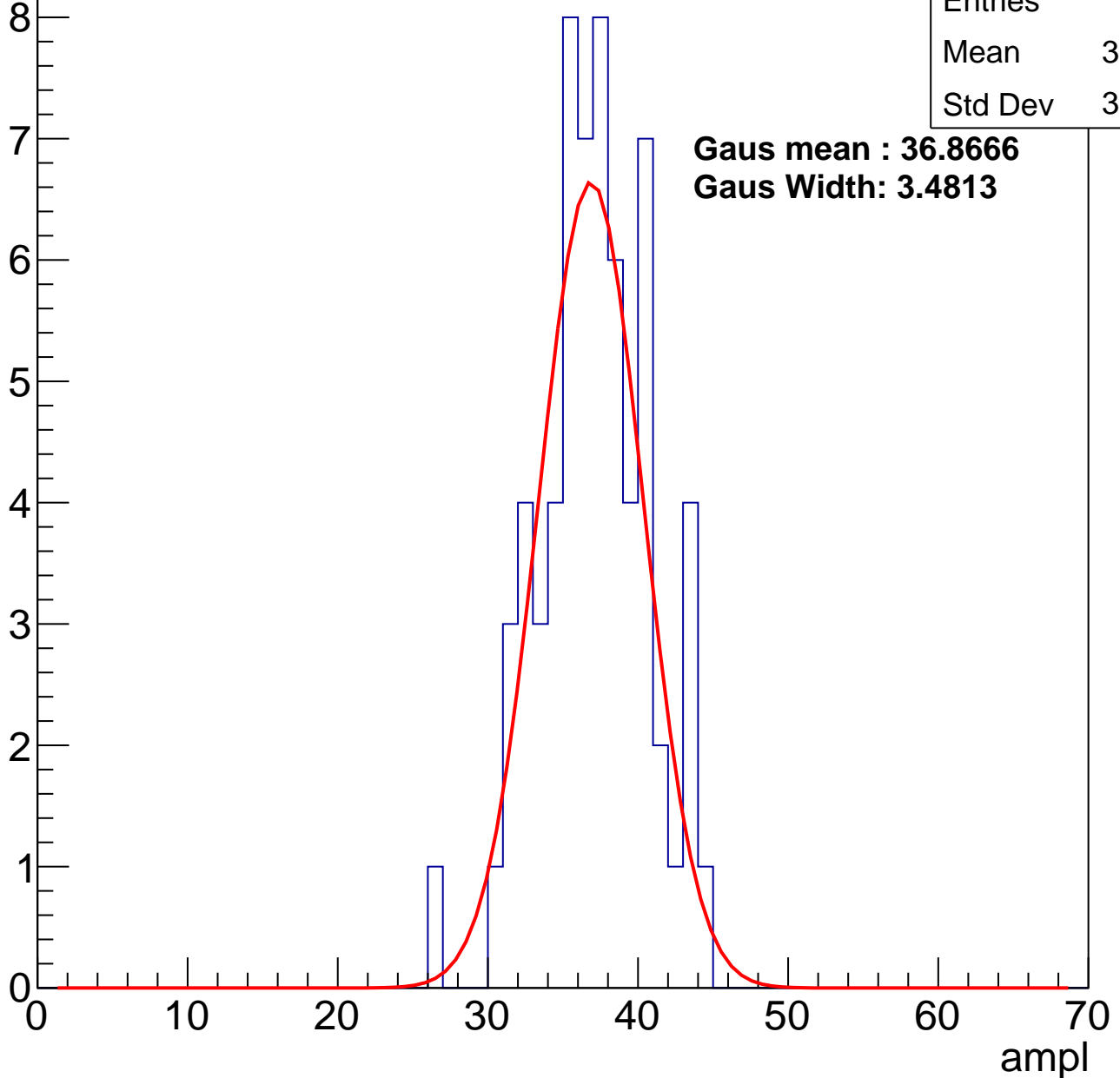
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.62
Std Dev	3.595

Gaus mean : 36.8666

Gaus Width: 3.4813



B1L101S, U1-ch4, adc2

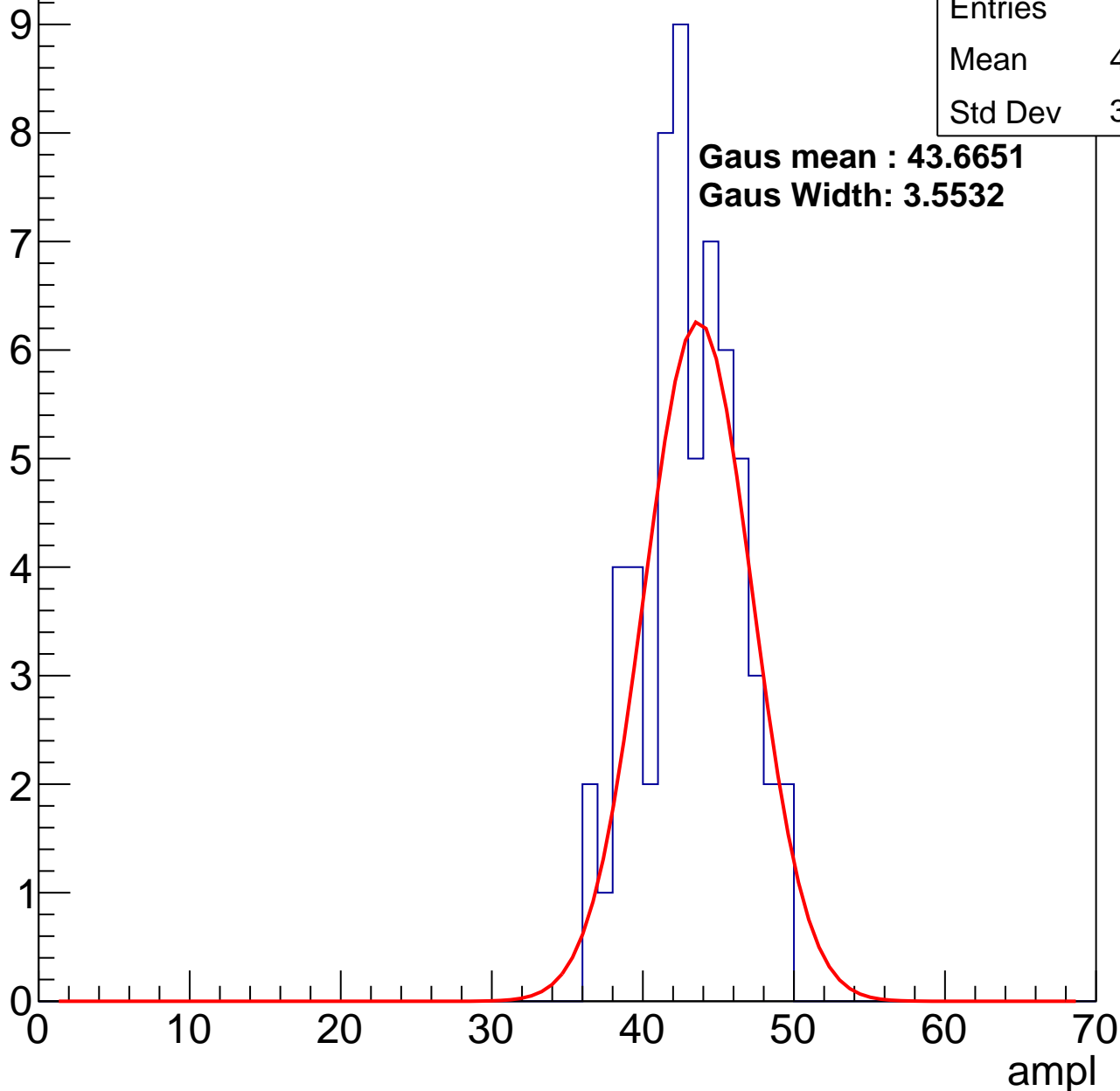
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.68
Std Dev	3.154

Gaus mean : 43.6651

Gaus Width: 3.5532

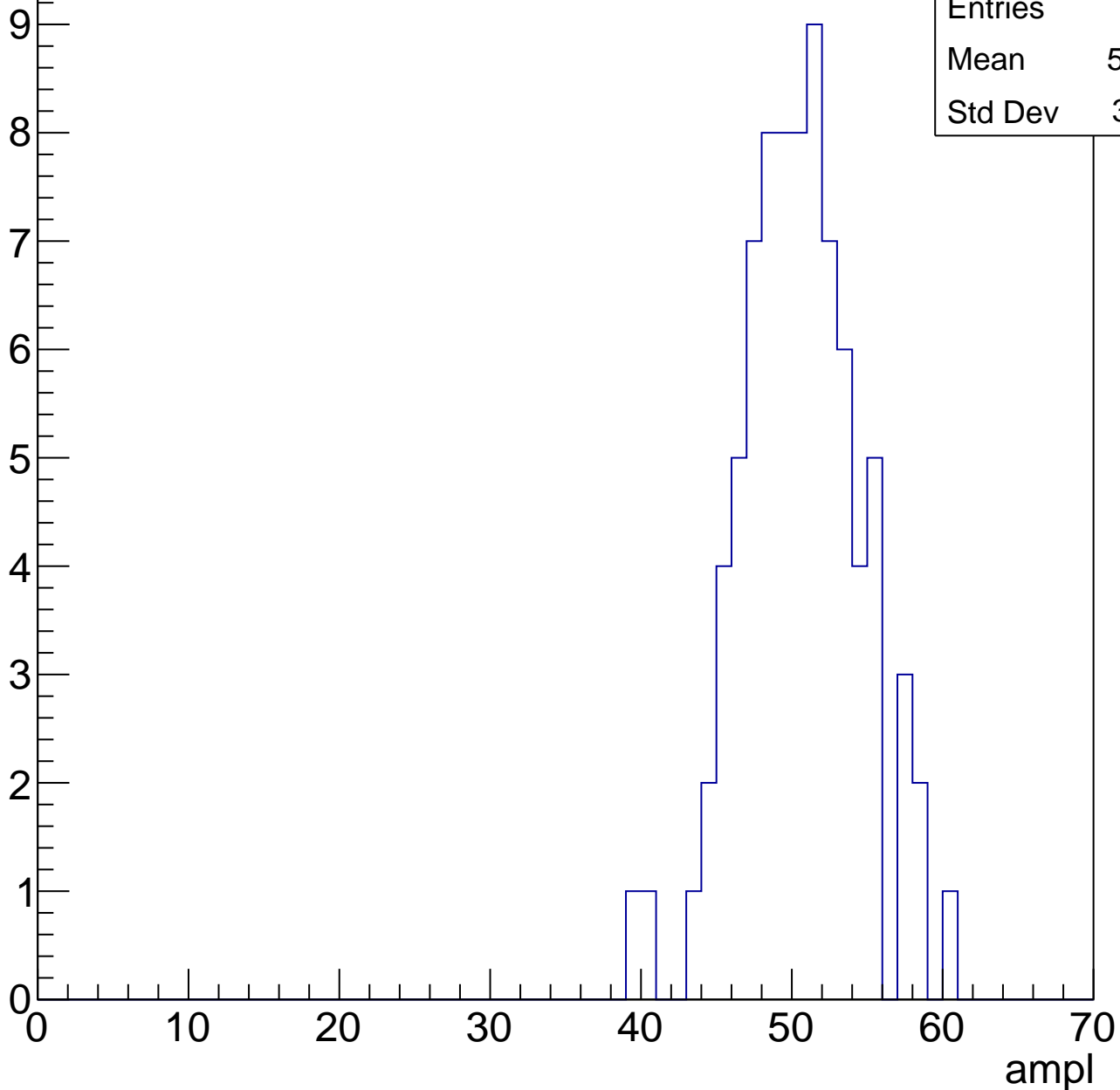


B1L101S, U1-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	50.05
Std Dev	3.951

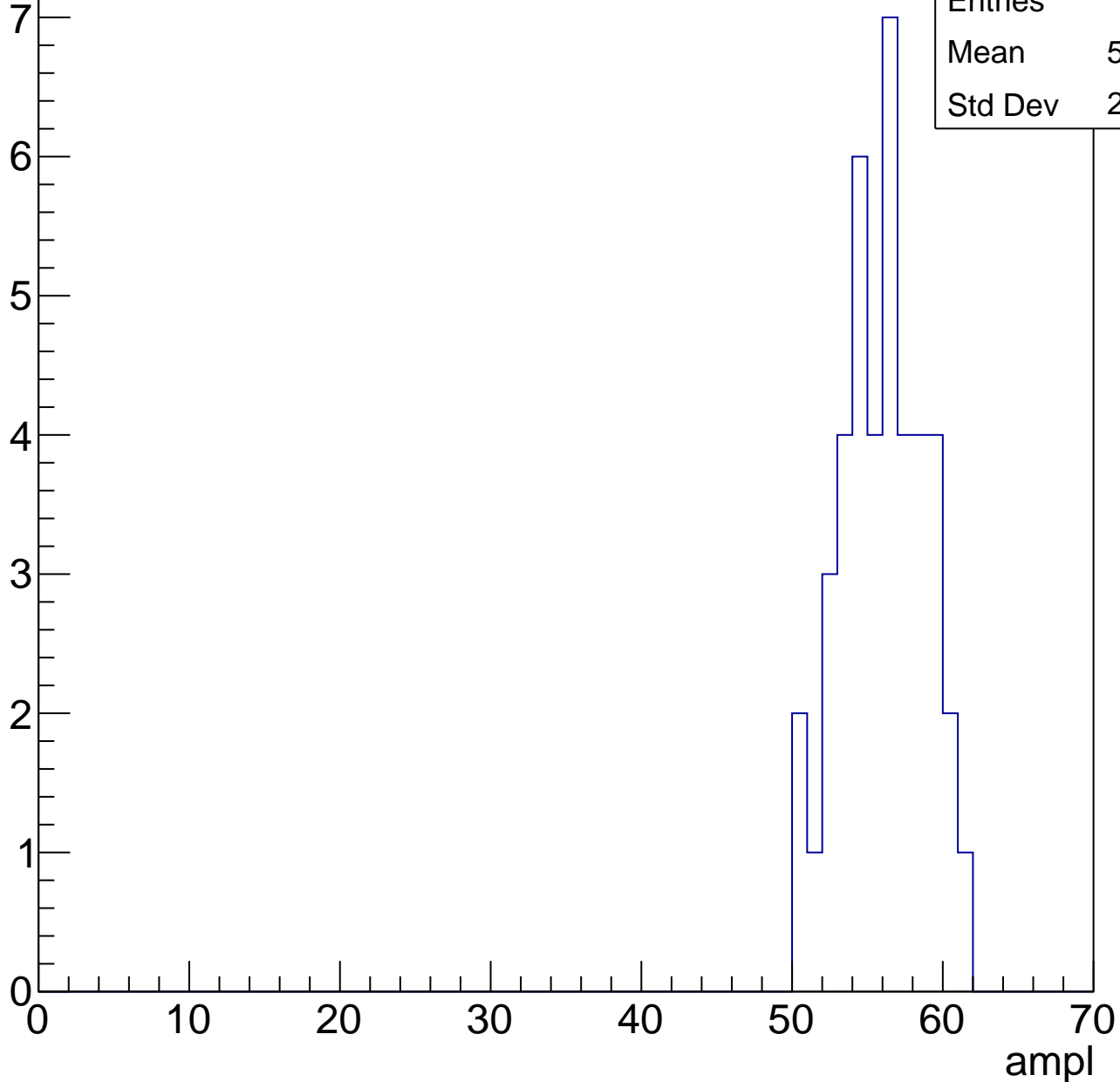


B1L101S, U1-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	55.52
Std Dev	2.728



B1L101S, U1-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	60.05
Std Dev	2.278

ampl

10

20

30

40

50

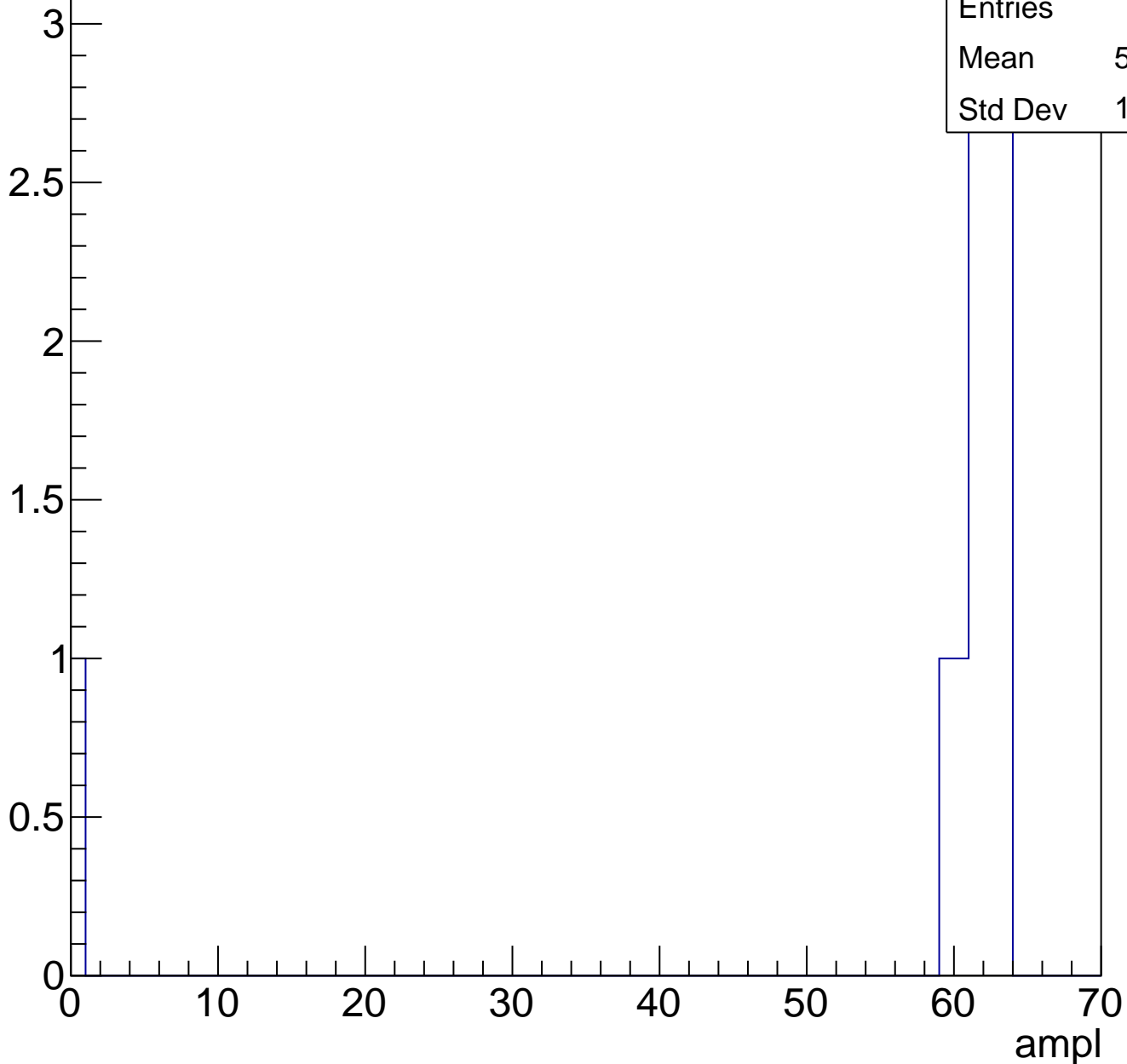
60

70

B1L101S, U1-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch5, adc0

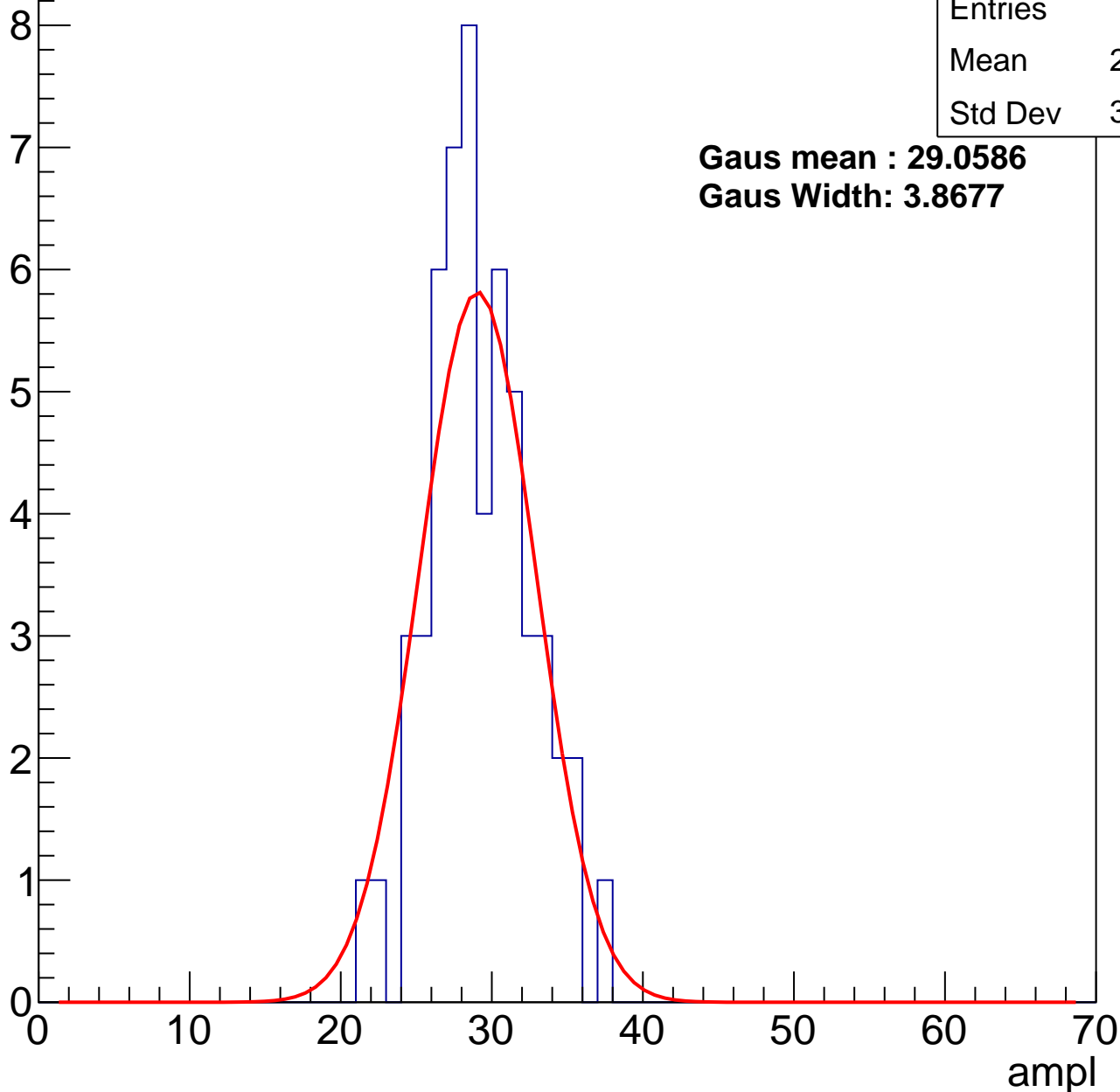
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.73
Std Dev	3.327

Gaus mean : 29.0586

Gaus Width: 3.8677



B1L101S, U1-ch5, adc1

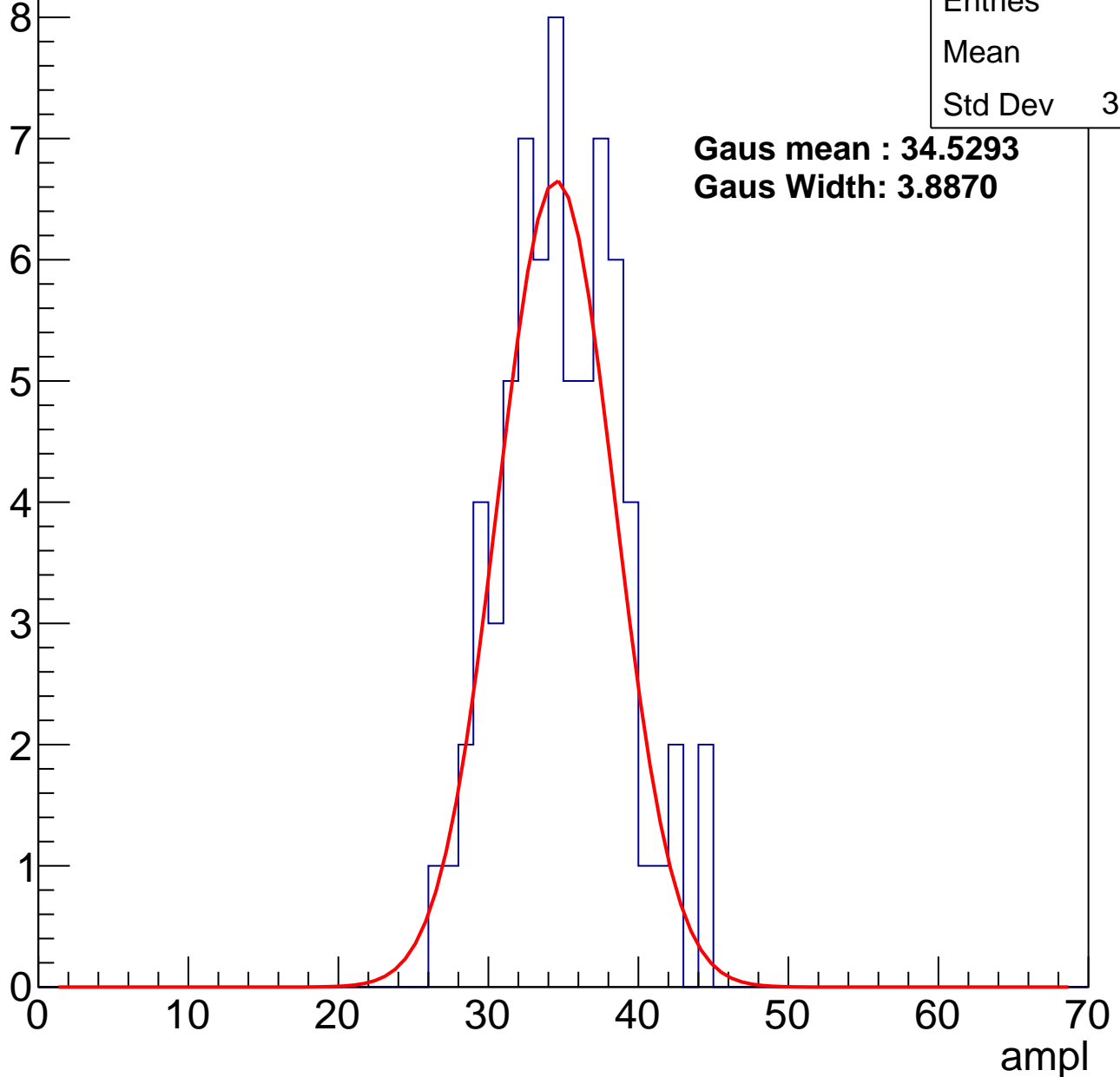
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	34.5
Std Dev	3.949

Gaus mean : 34.5293

Gaus Width: 3.8870



B1L101S, U1-ch5, adc2

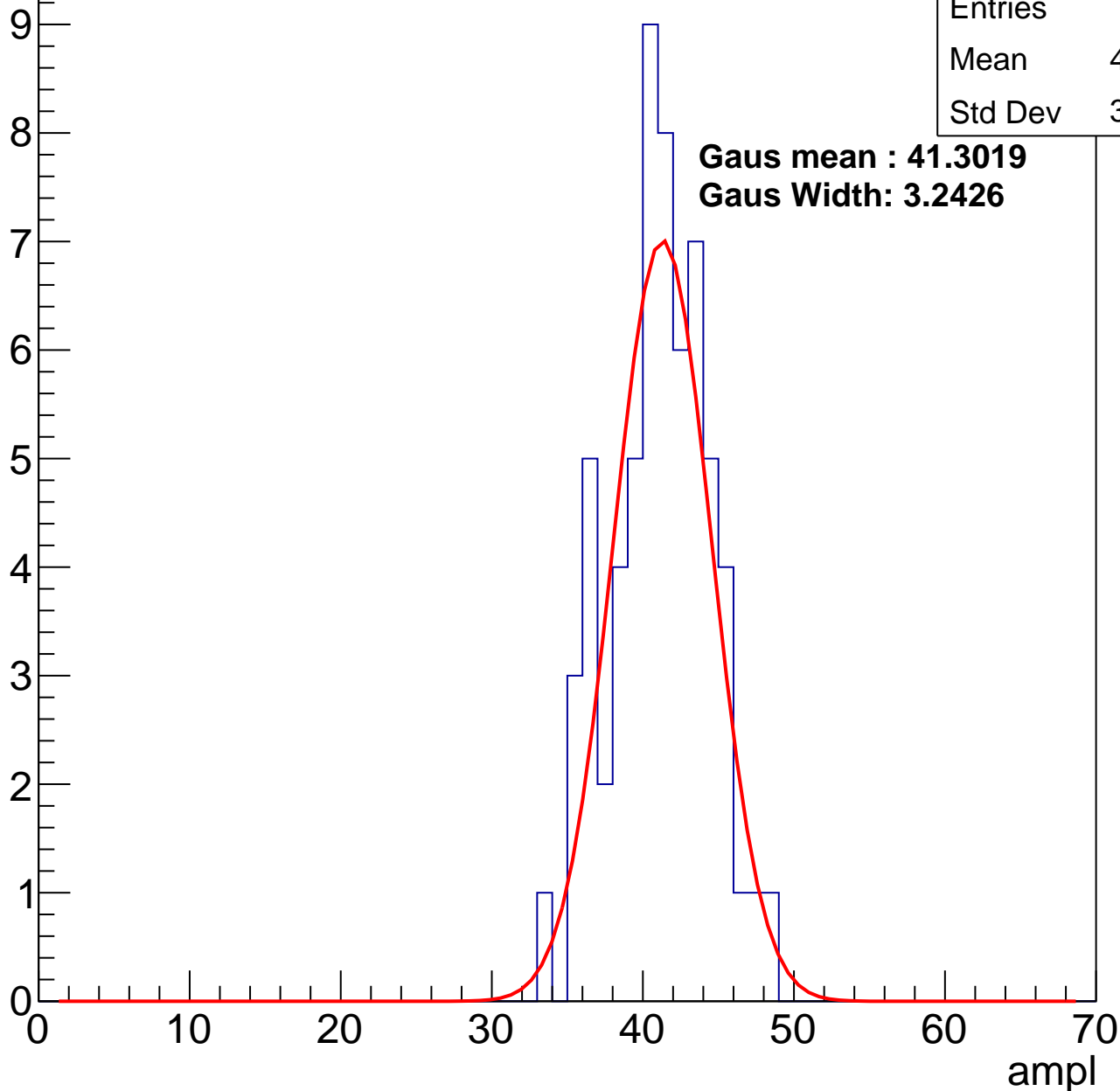
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	40.66
Std Dev	3.213

Gaus mean : 41.3019

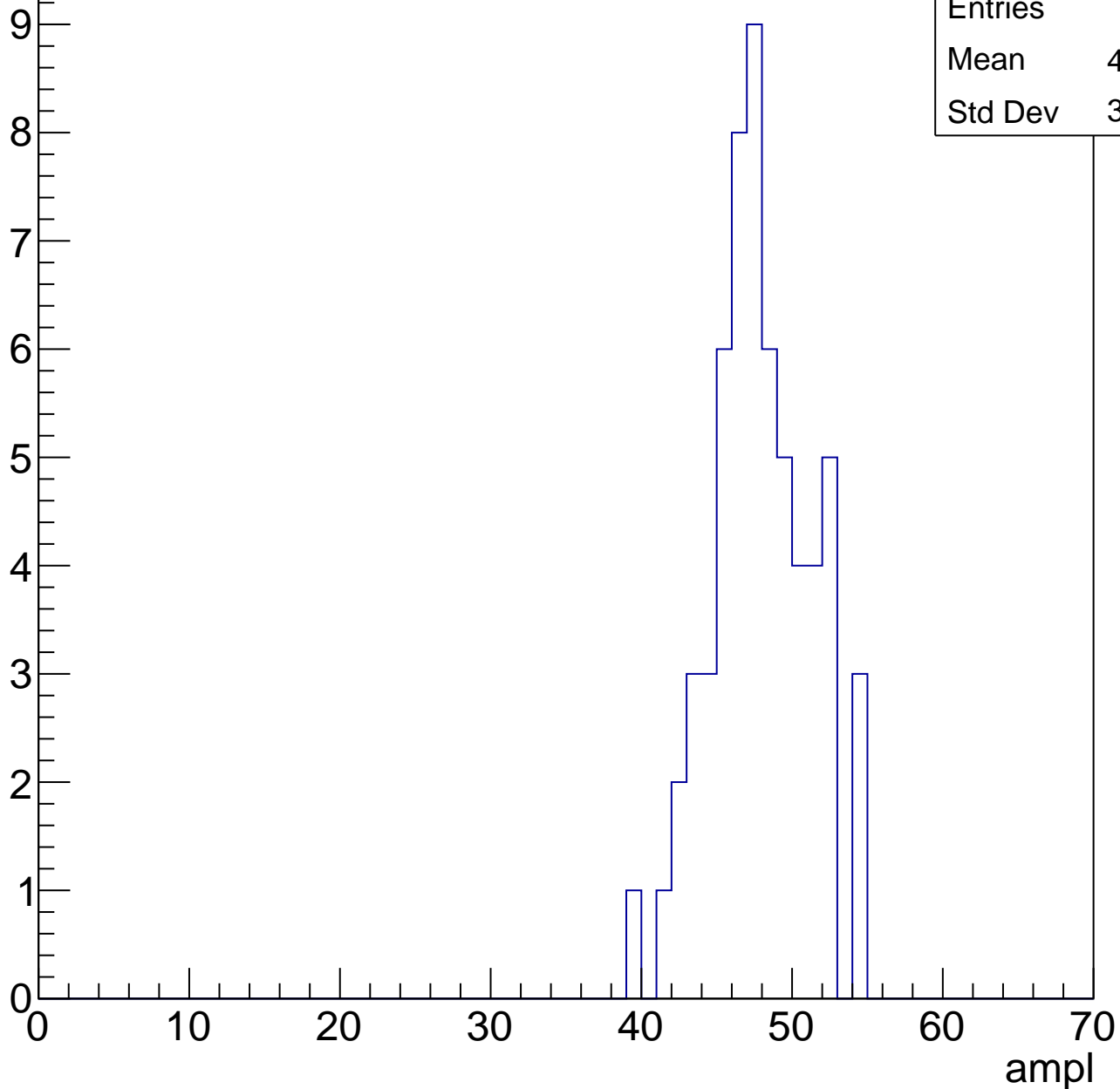
Gaus Width: 3.2426



B1L101S, U1-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



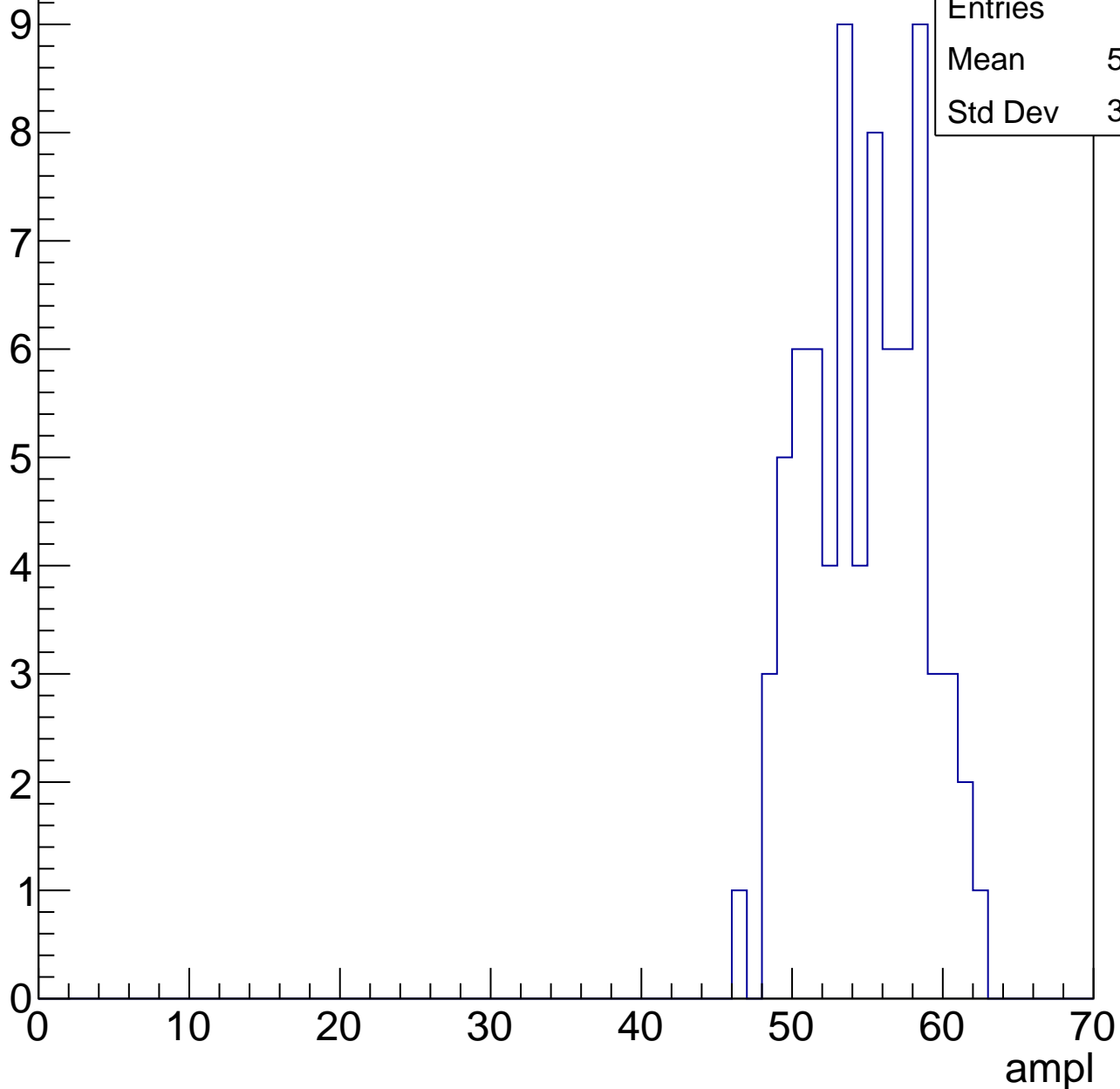
Entries	60
Mean	47.42
Std Dev	3.278

B1L101S, U1-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	54.25
Std Dev	3.703



B1L101S, U1-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 57.59

Std Dev 9.066

ampl

0

10

20

30

40

50

60

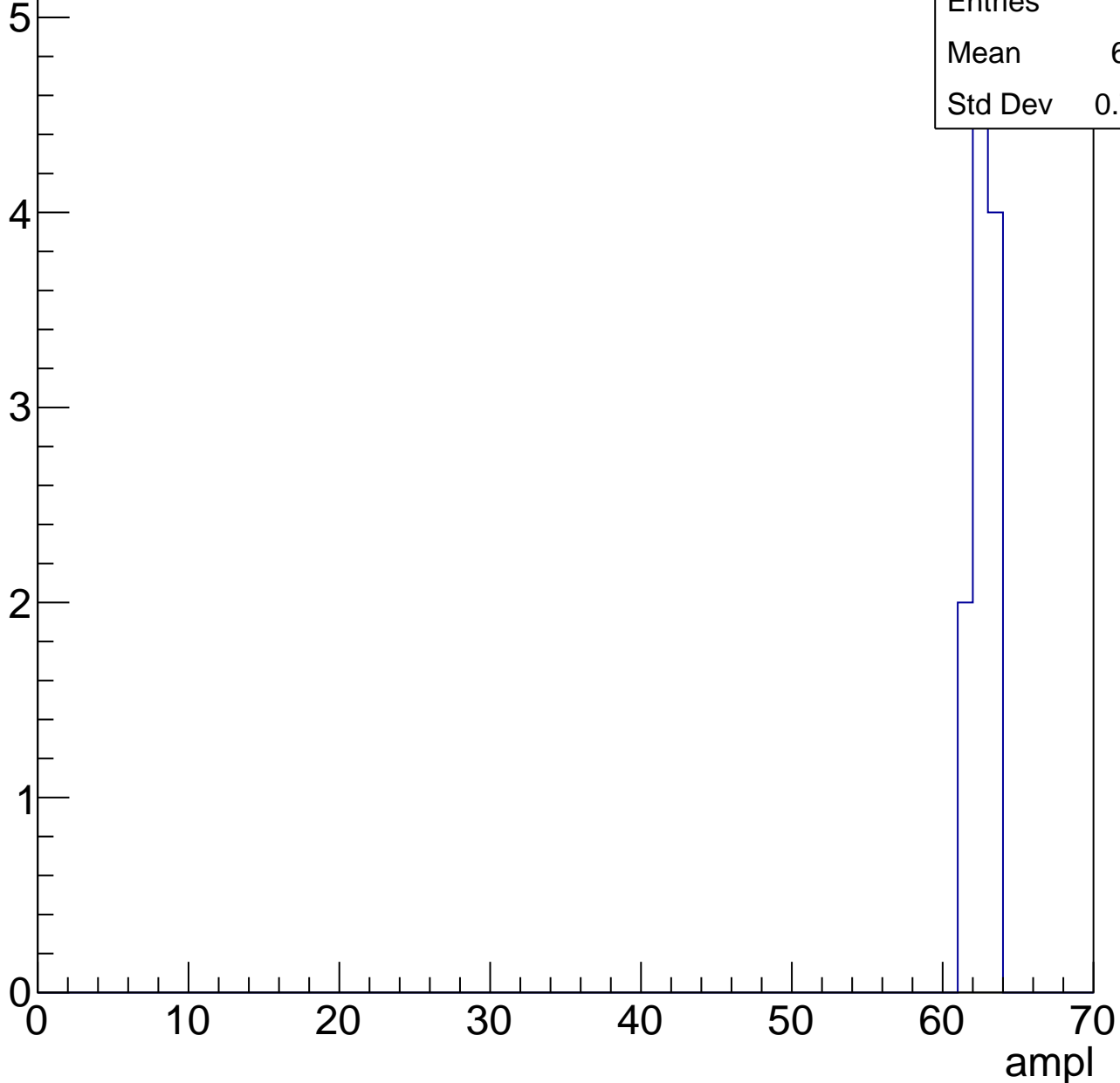
70

B1L101S, U1-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	62.18
Std Dev	0.7158



B1L101S, U1-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch6, adc0

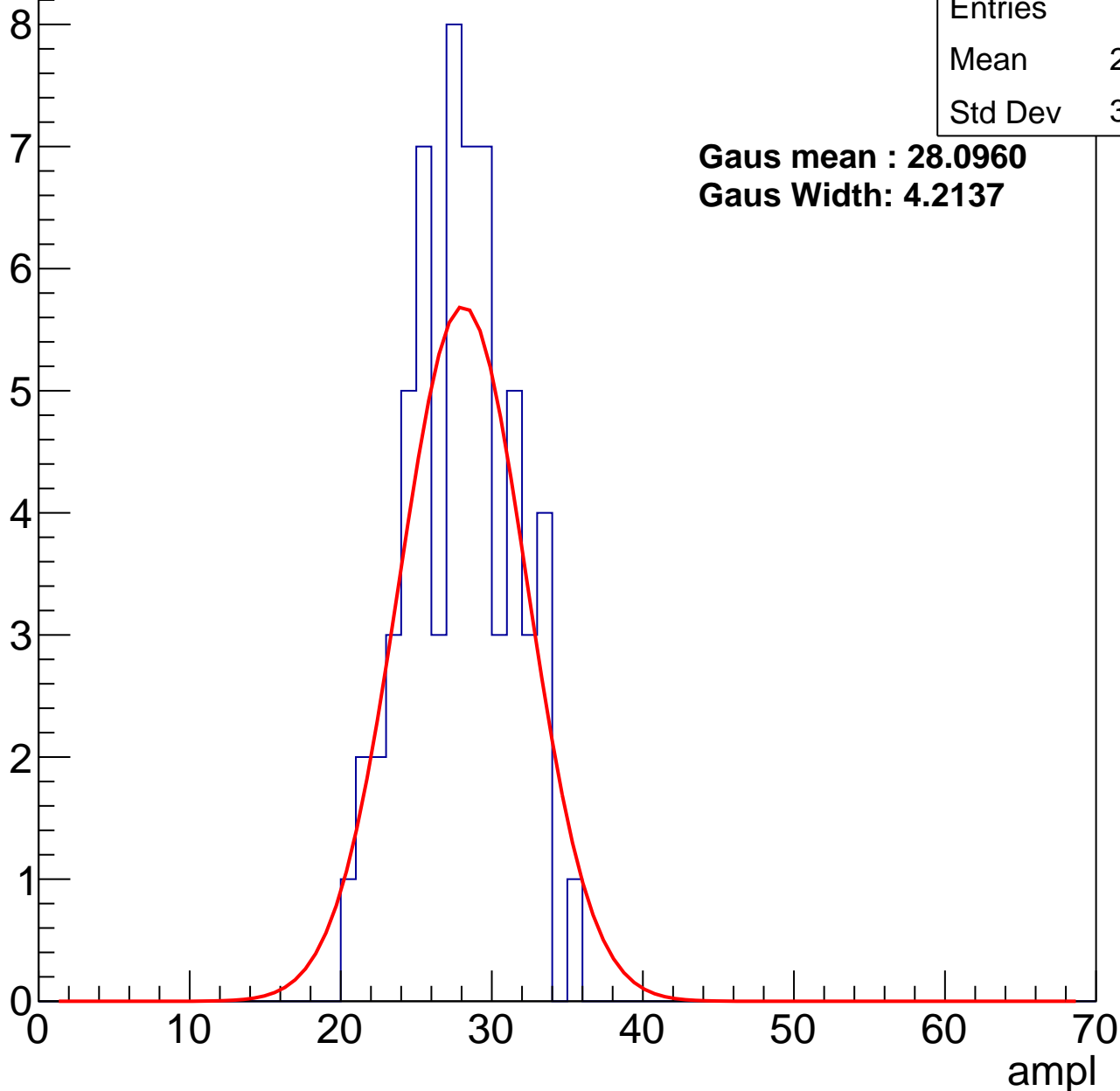
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	27.39
Std Dev	3.413

Gaus mean : 28.0960

Gaus Width: 4.2137



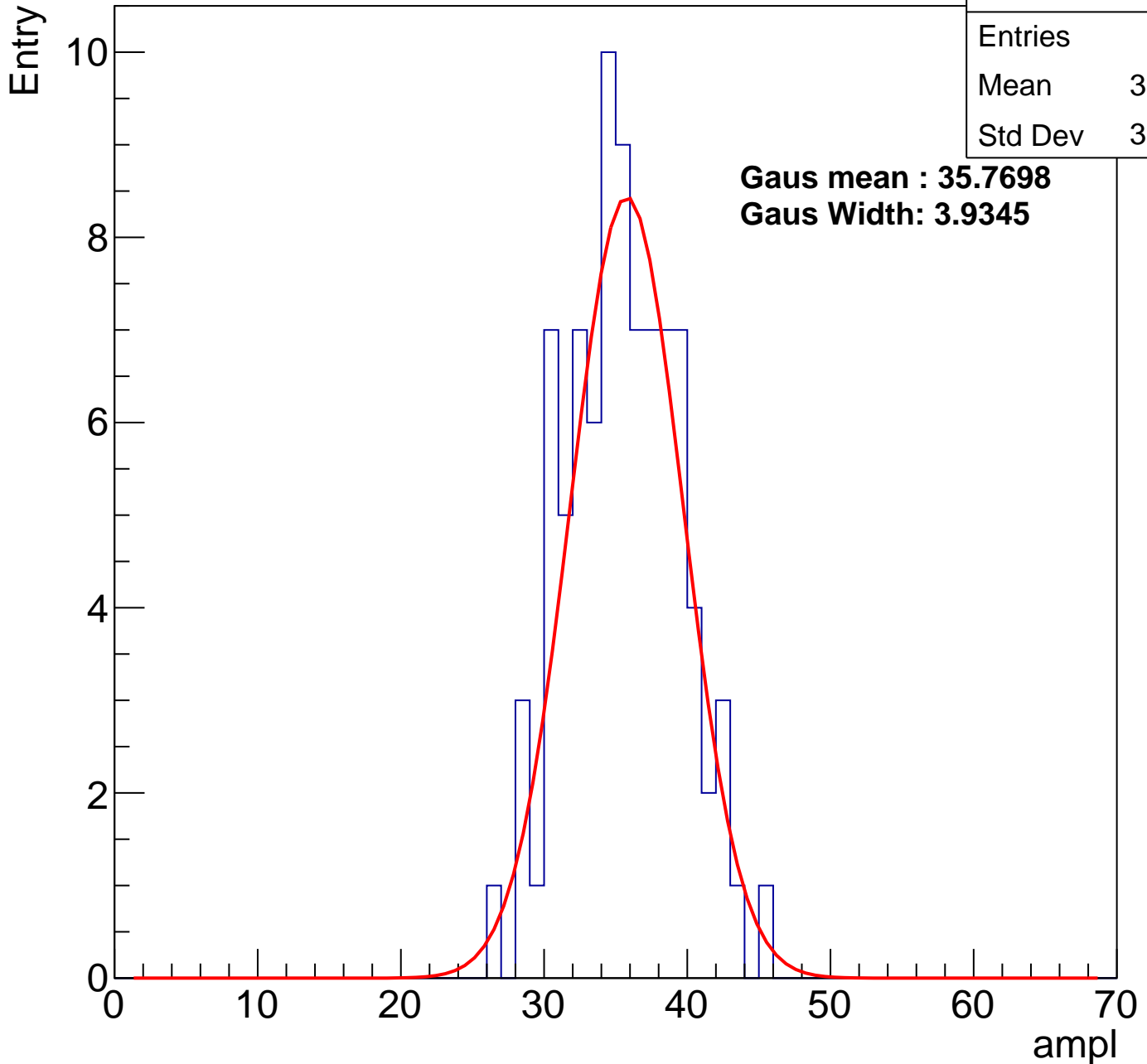
B1L101S, U1-ch6, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	35.08
Std Dev	3.856

Gaus mean : 35.7698

Gaus Width: 3.9345



B1L101S, U1-ch6, adc2

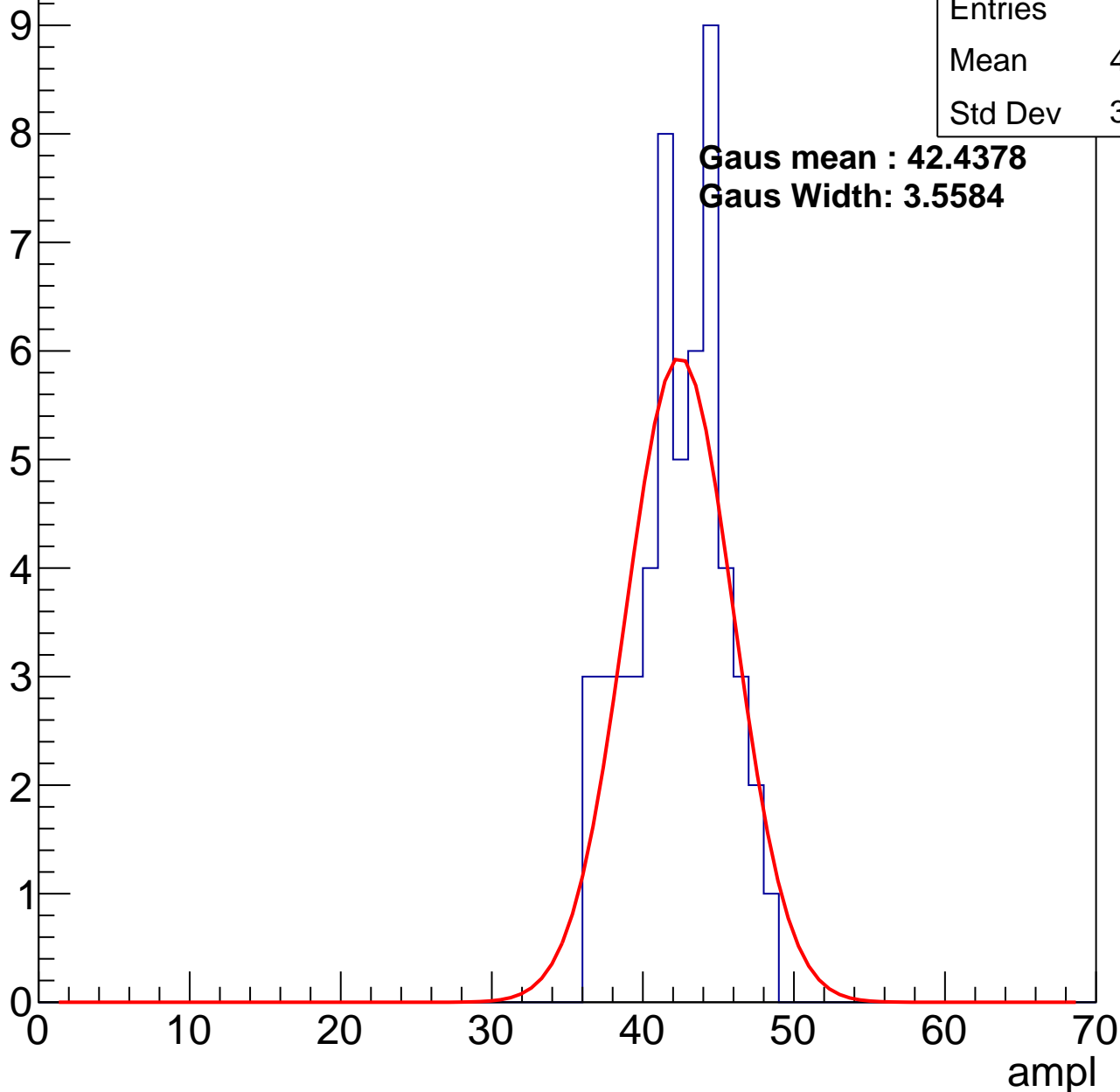
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.89
Std Dev	3.023

Gaus mean : 42.4378

Gaus Width: 3.5584

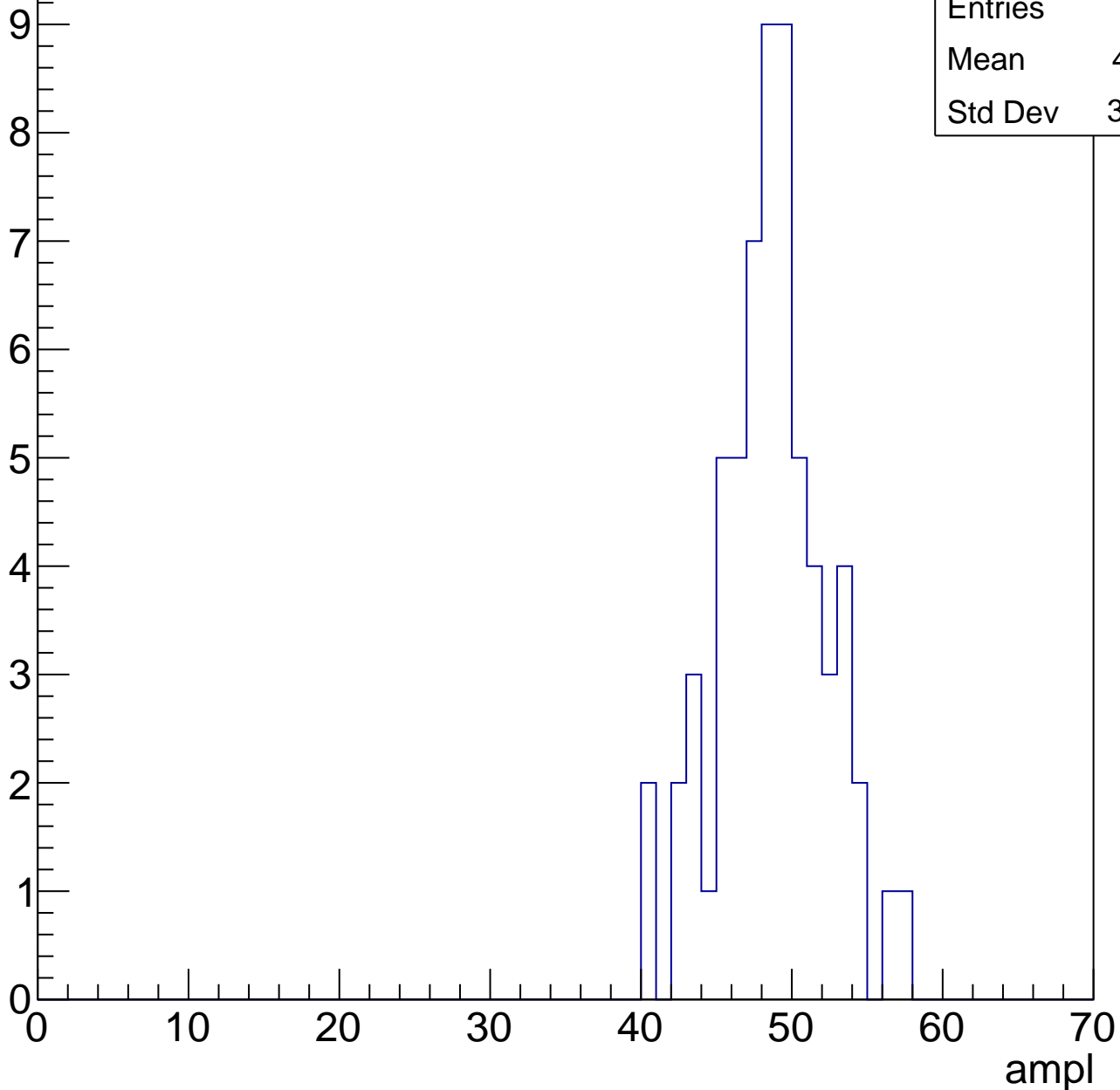


B1L101S, U1-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	48.21
Std Dev	3.542

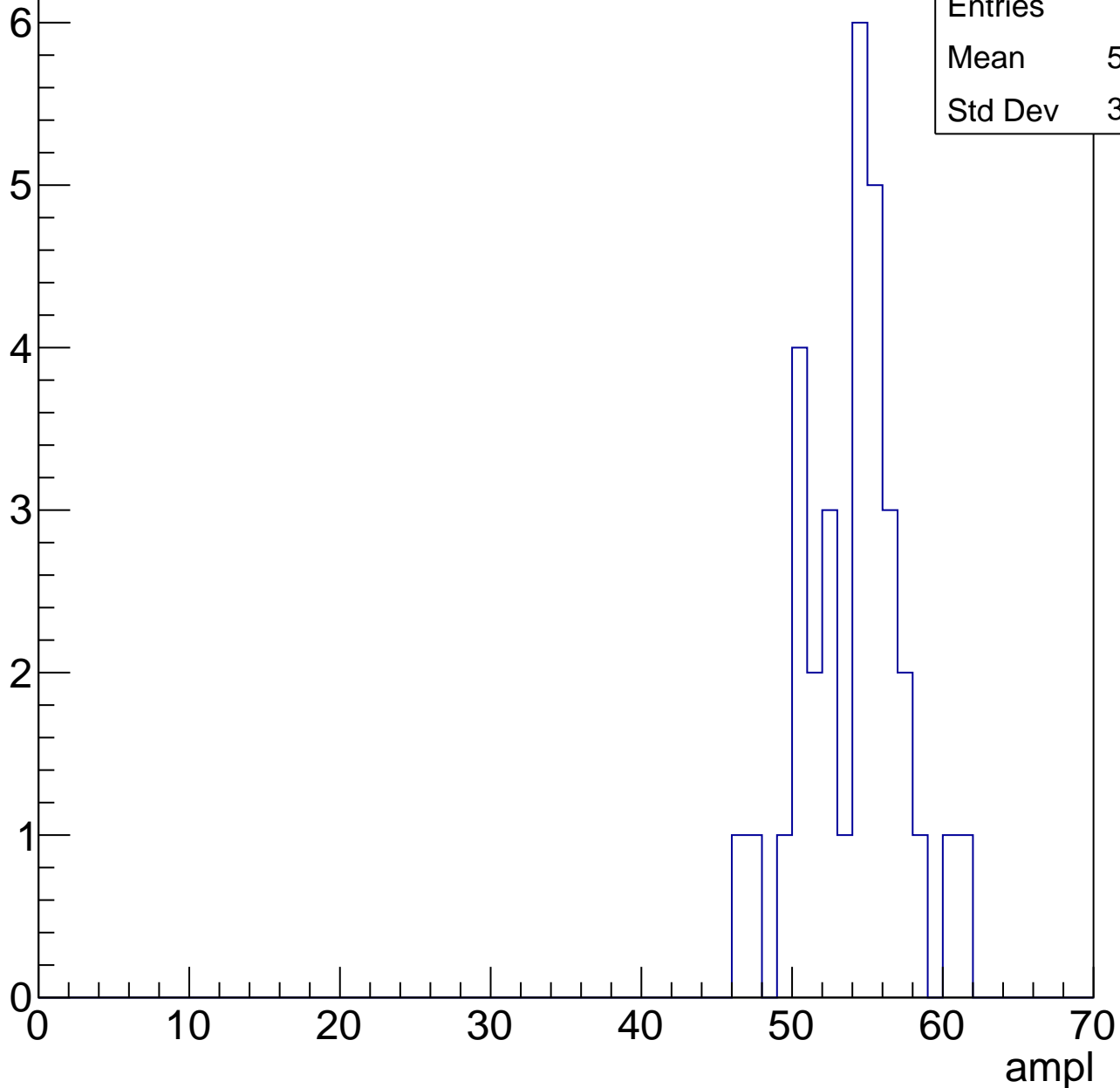


B1L101S, U1-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

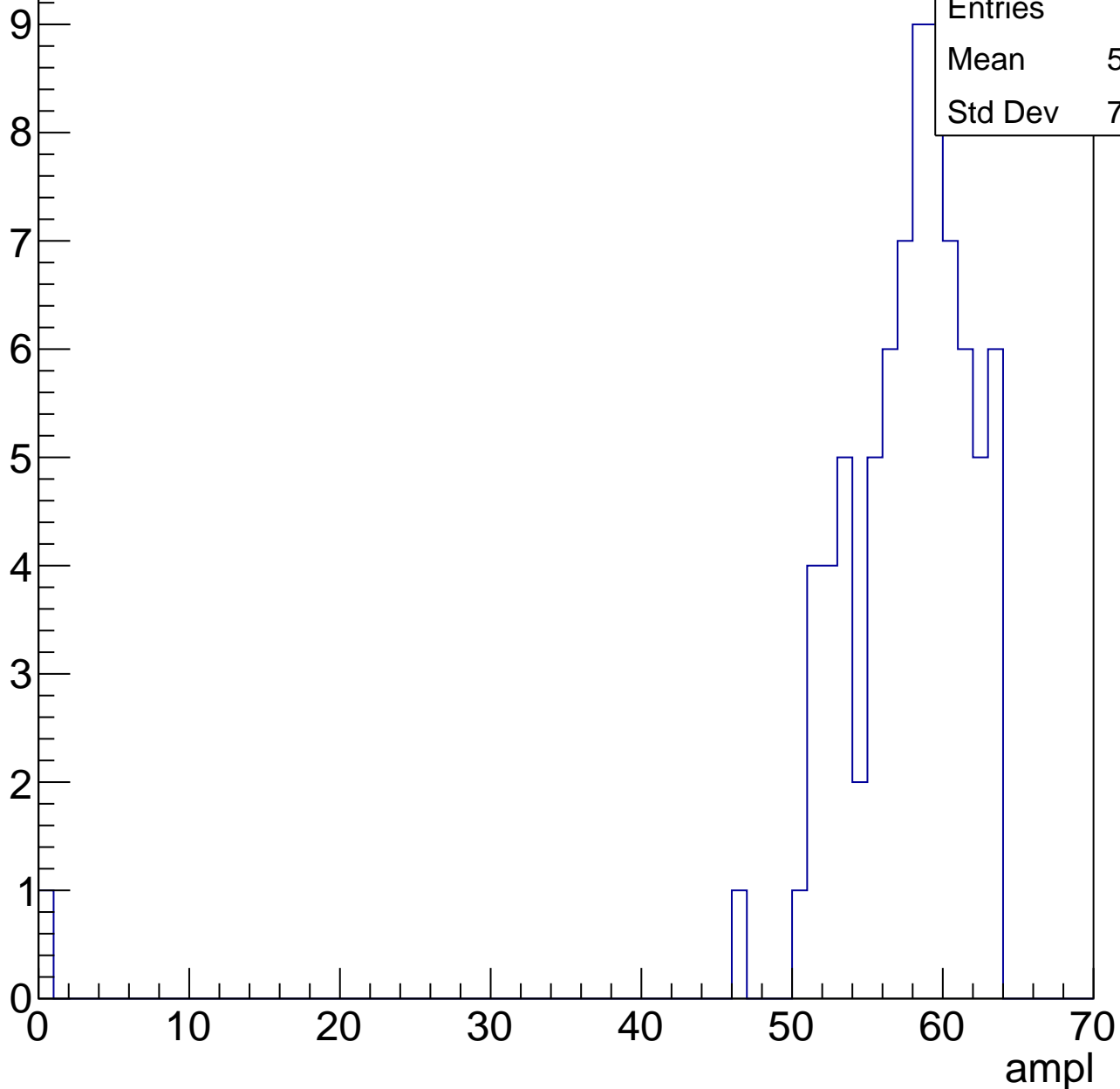
Entries	32
Mean	53.53
Std Dev	3.363



B1L101S, U1-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



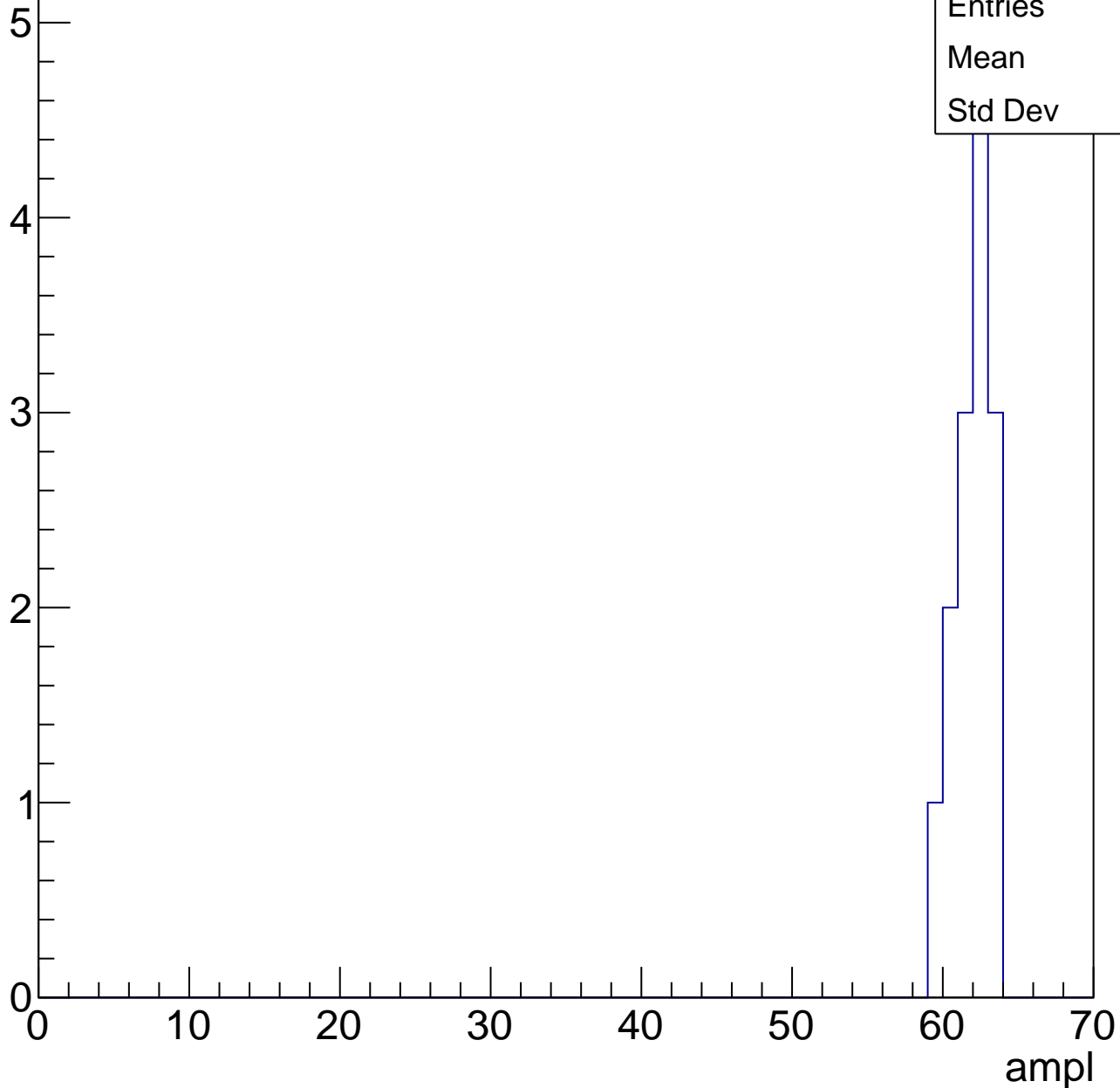
Entries	78
Mean	56.64
Std Dev	7.444

B1L101S, U1-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.5
Std Dev	1.18



B1L101S, U1-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch7, adc0

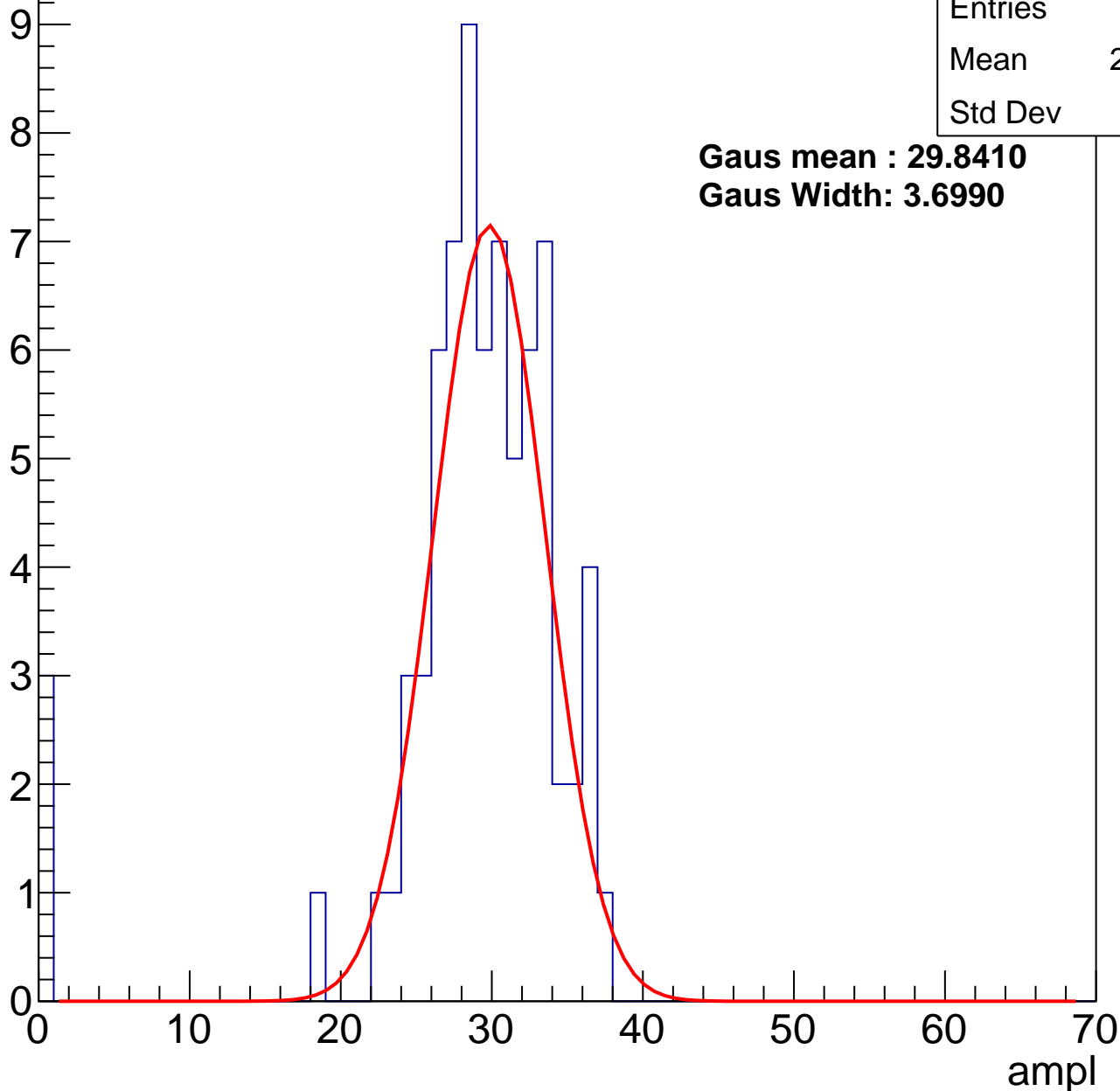
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.22
Std Dev	6.85

Gaus mean : 29.8410

Gaus Width: 3.6990



B1L101S, U1-ch7, adc1

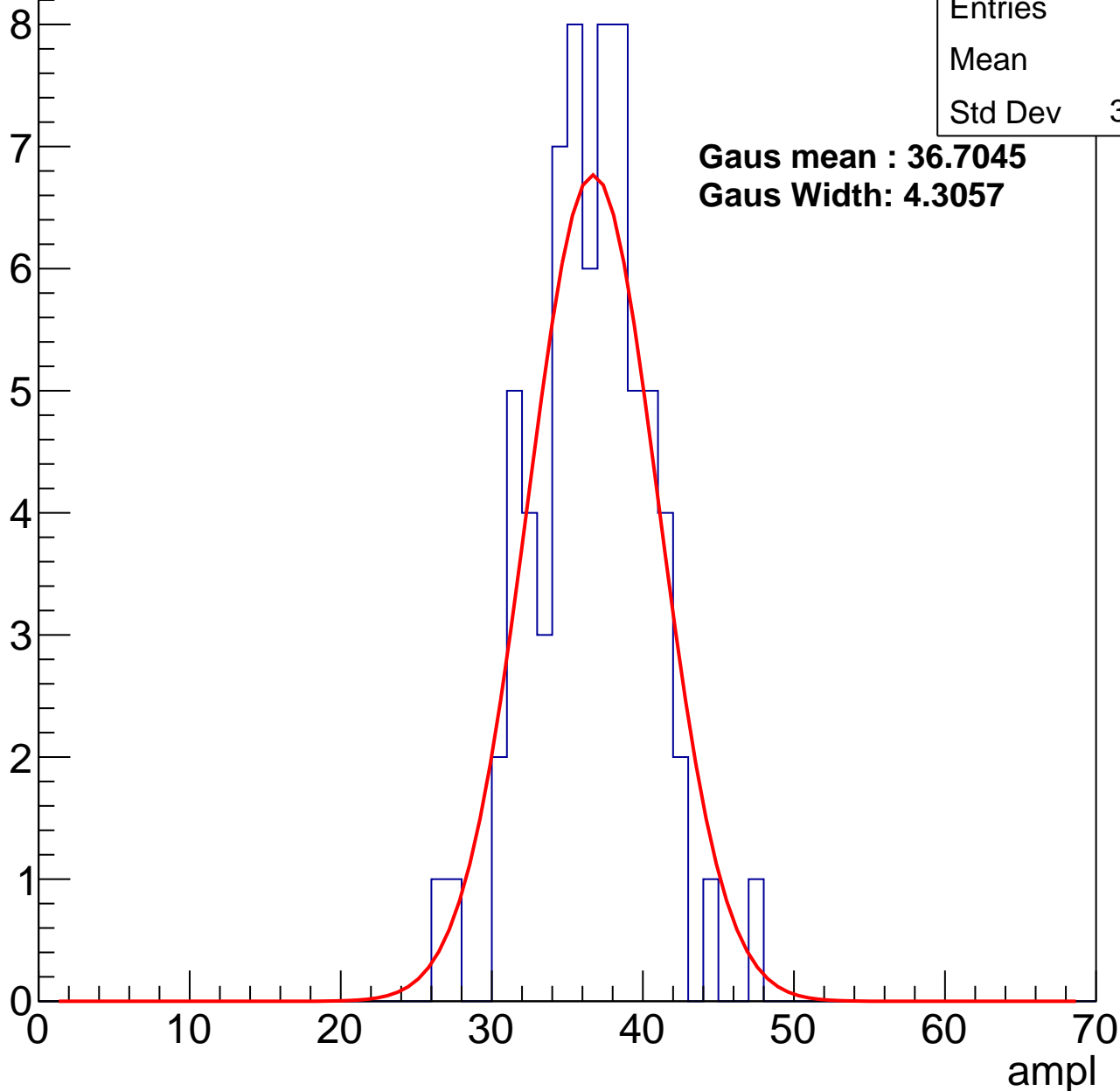
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.1
Std Dev	3.802

Gaus mean : 36.7045

Gaus Width: 4.3057



B1L101S, U1-ch7, adc2

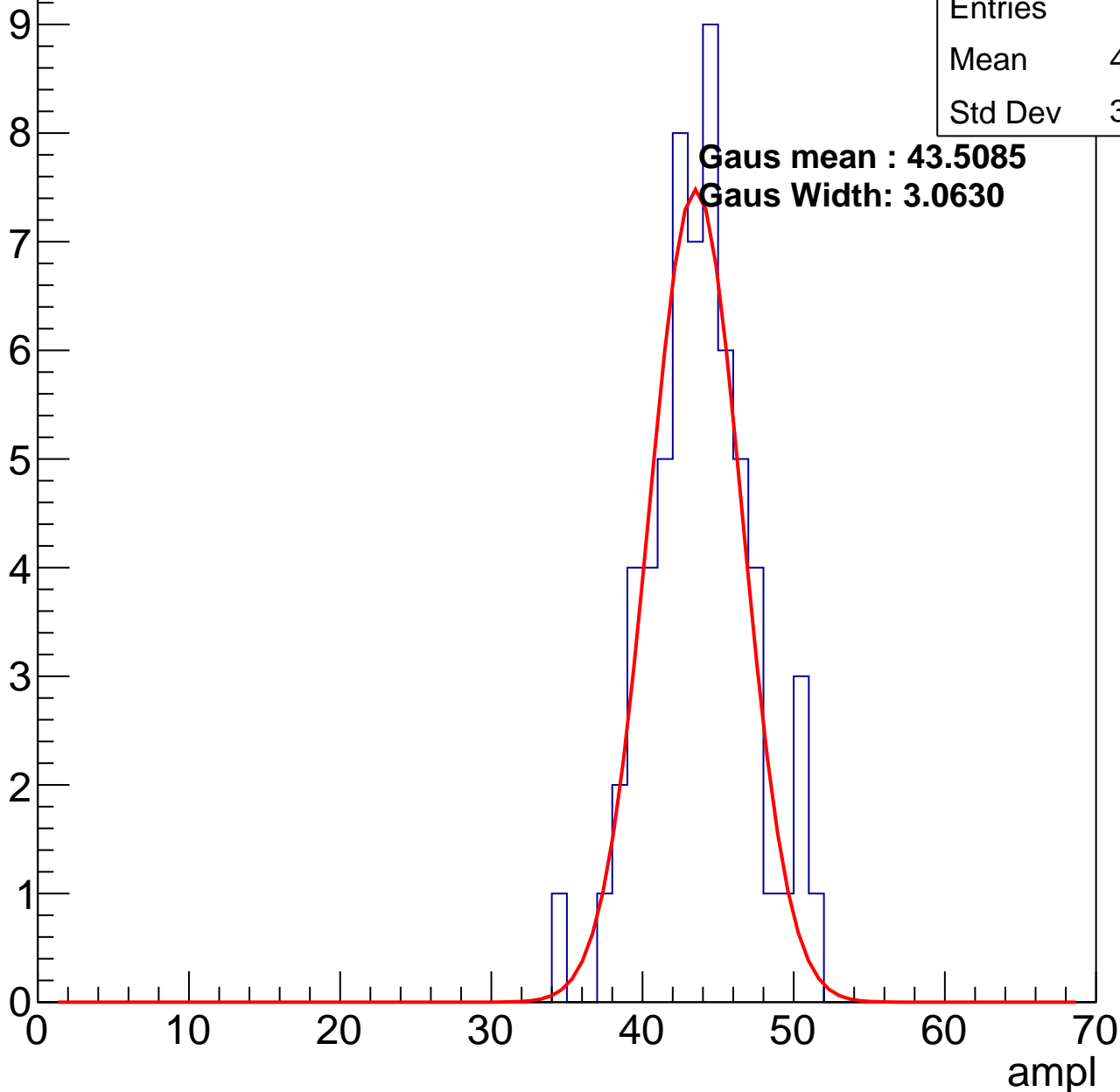
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.34
Std Dev	3.365

Gaus mean : 43.5085

Gaus Width: 3.0630

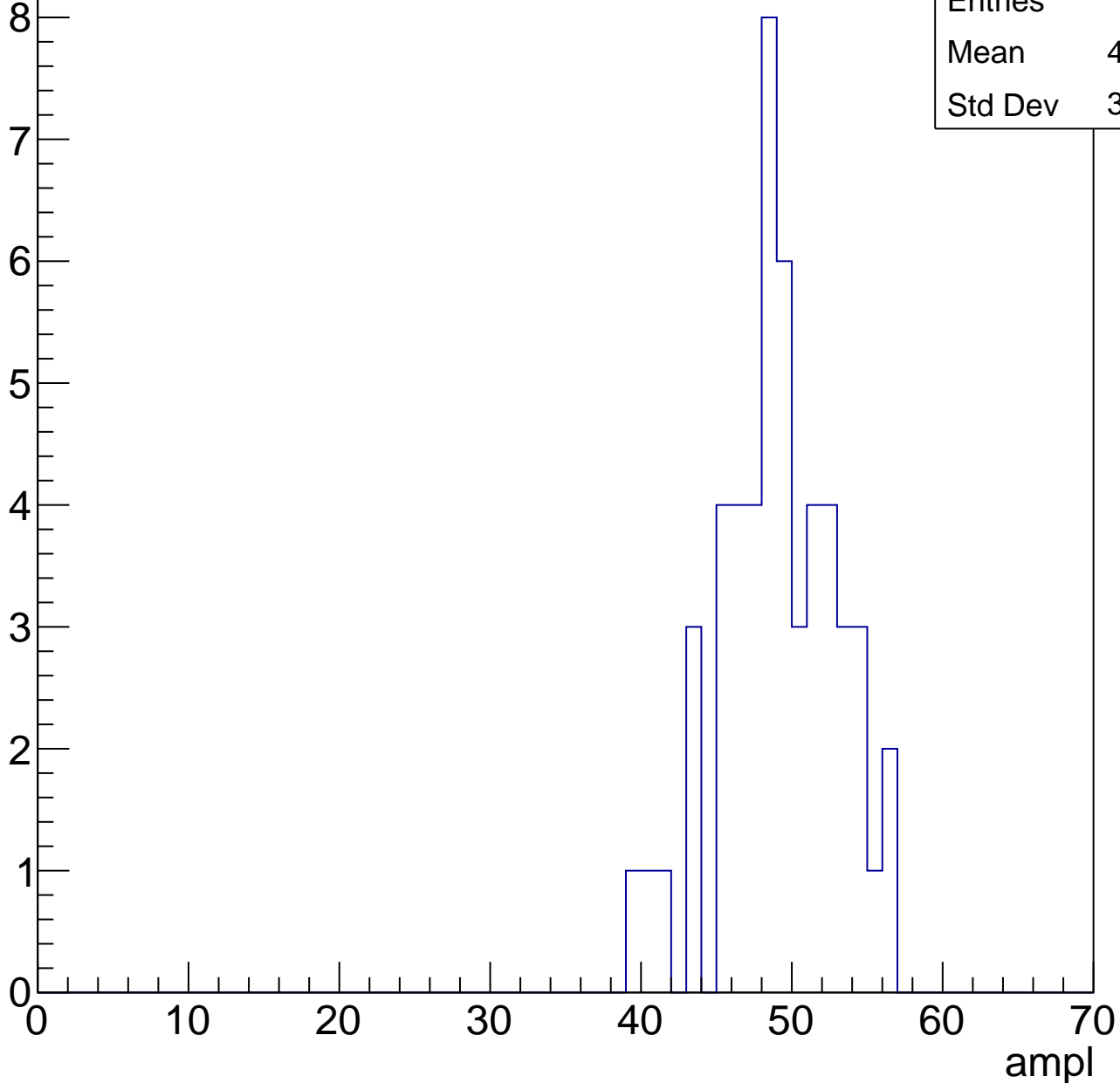


B1L101S, U1-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.63
Std Dev	3.878

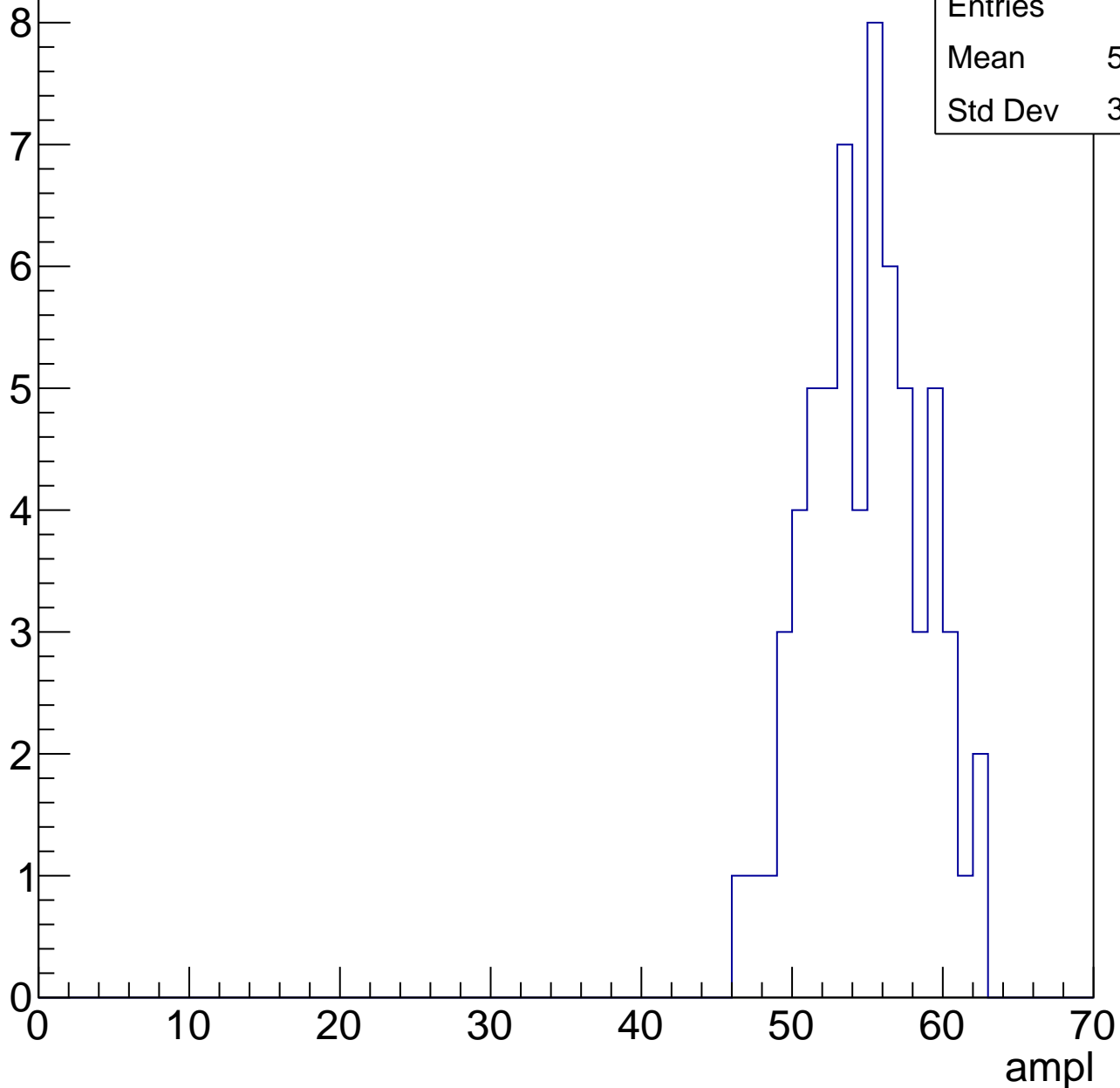


B1L101S, U1-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	54.45
Std Dev	3.712

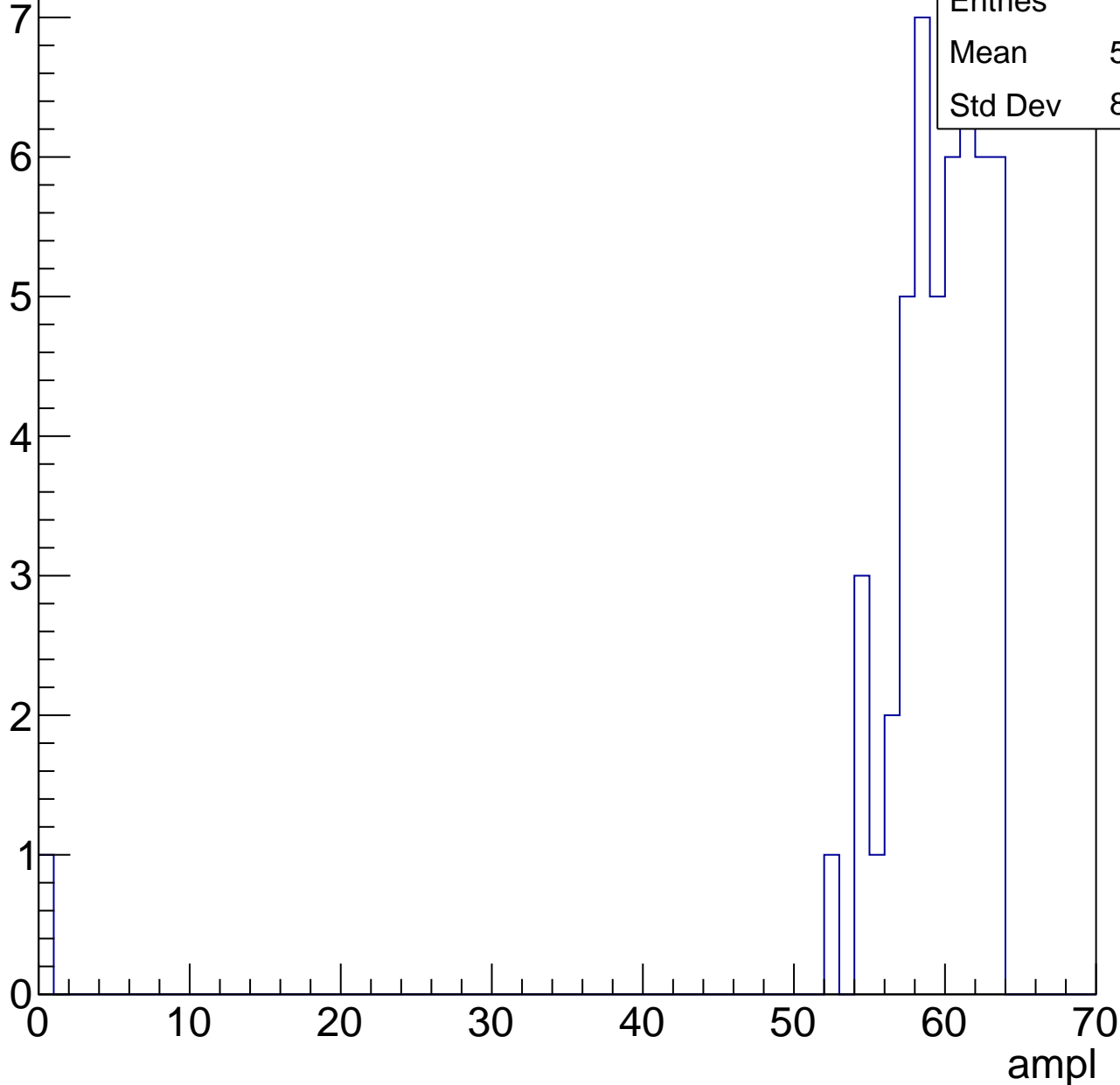


B1L101S, U1-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

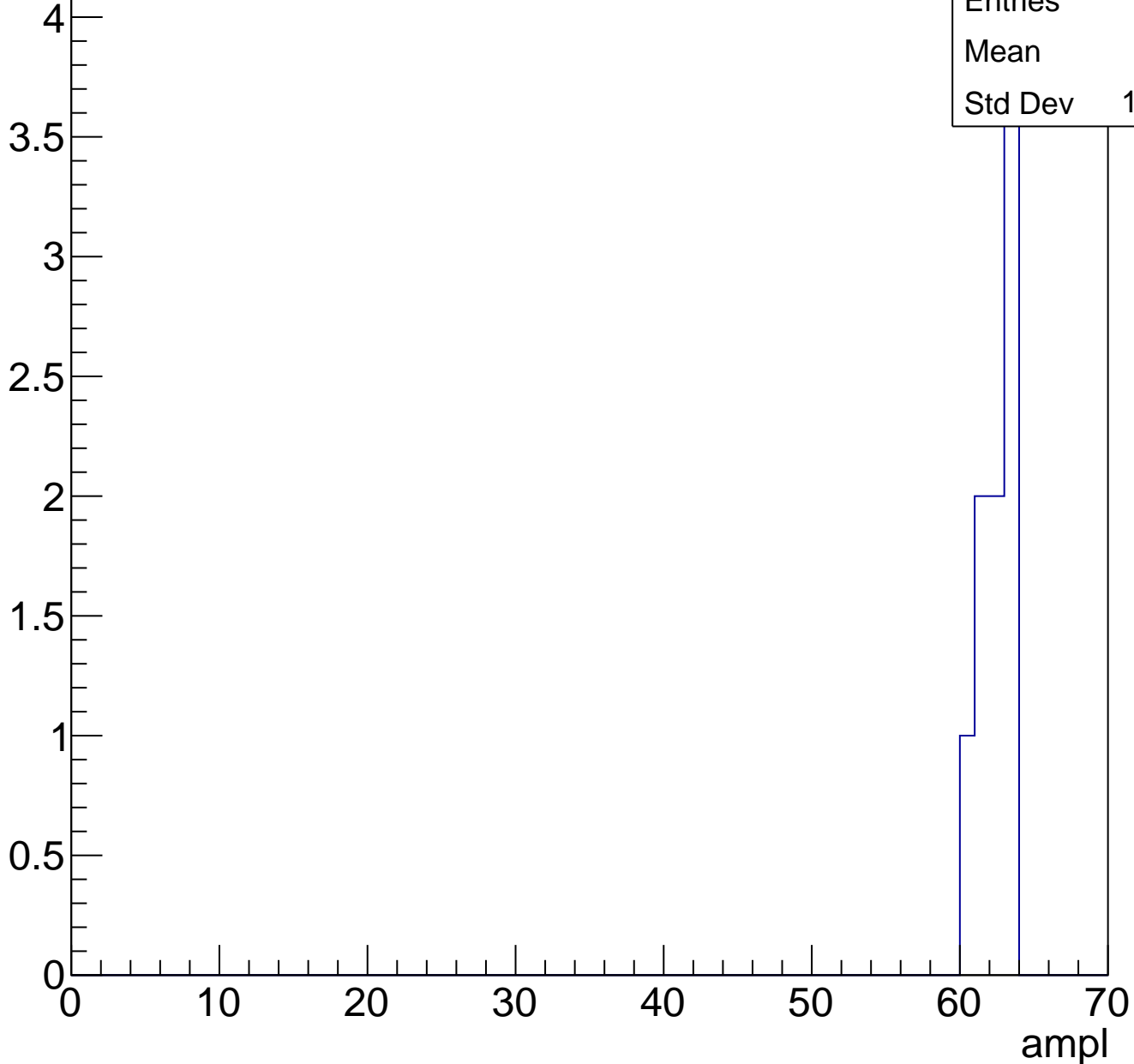
Entries	50
Mean	58.08
Std Dev	8.727



B1L101S, U1-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch8, adc0

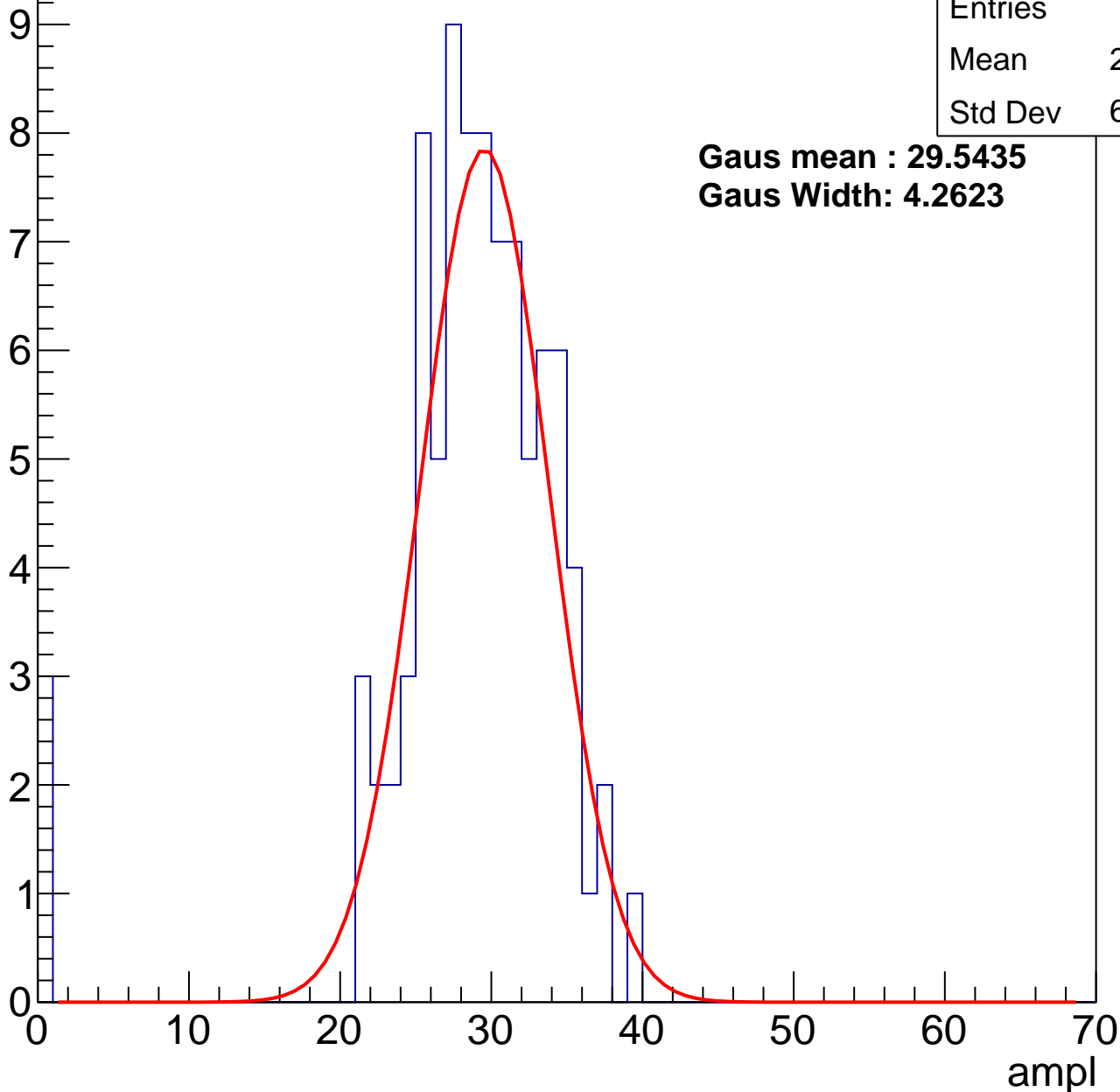
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	28.13
Std Dev	6.546

Gaus mean : 29.5435

Gaus Width: 4.2623



B1L101S, U1-ch8, adc1

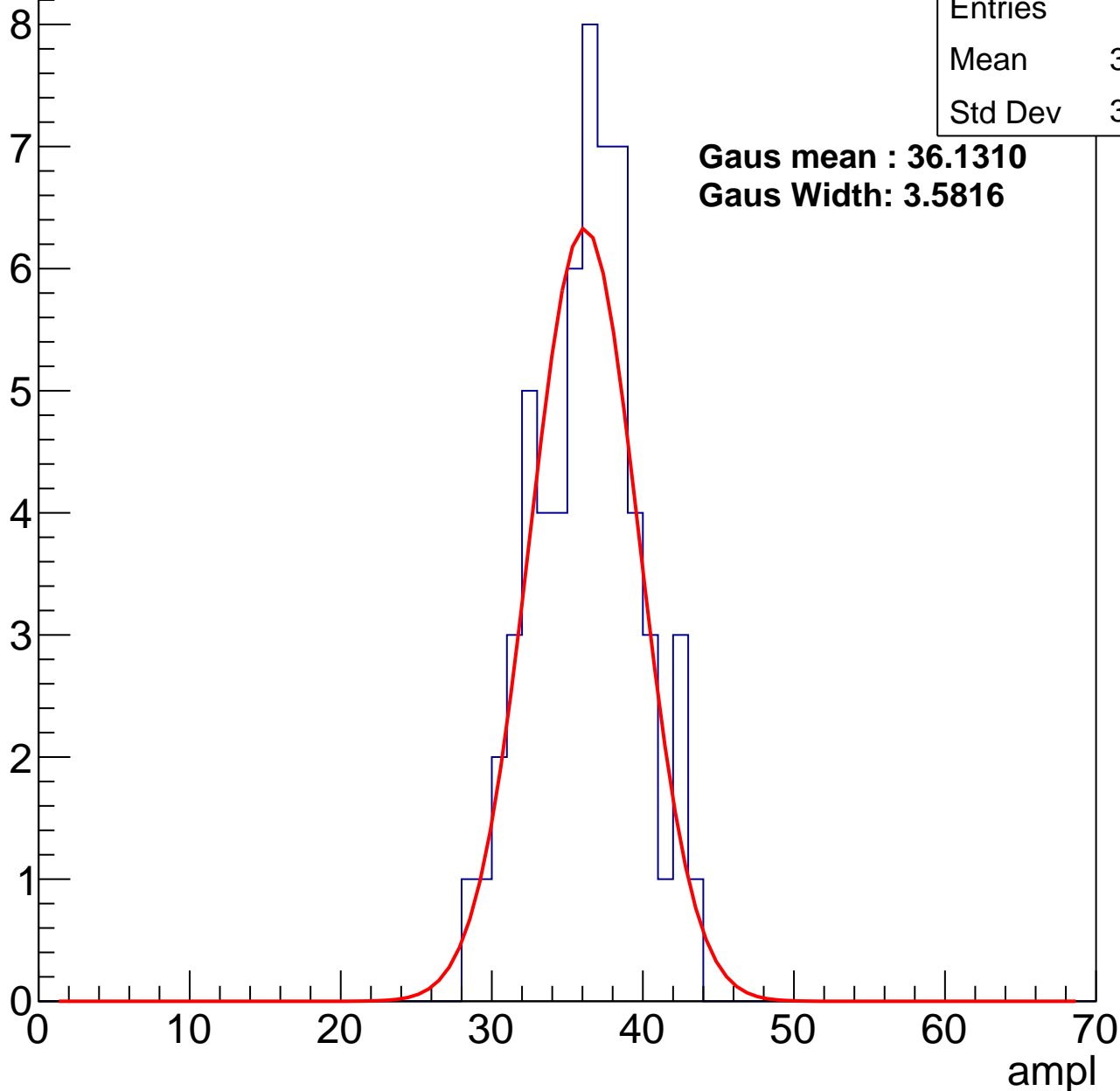
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.78
Std Dev	3.406

Gaus mean : 36.1310

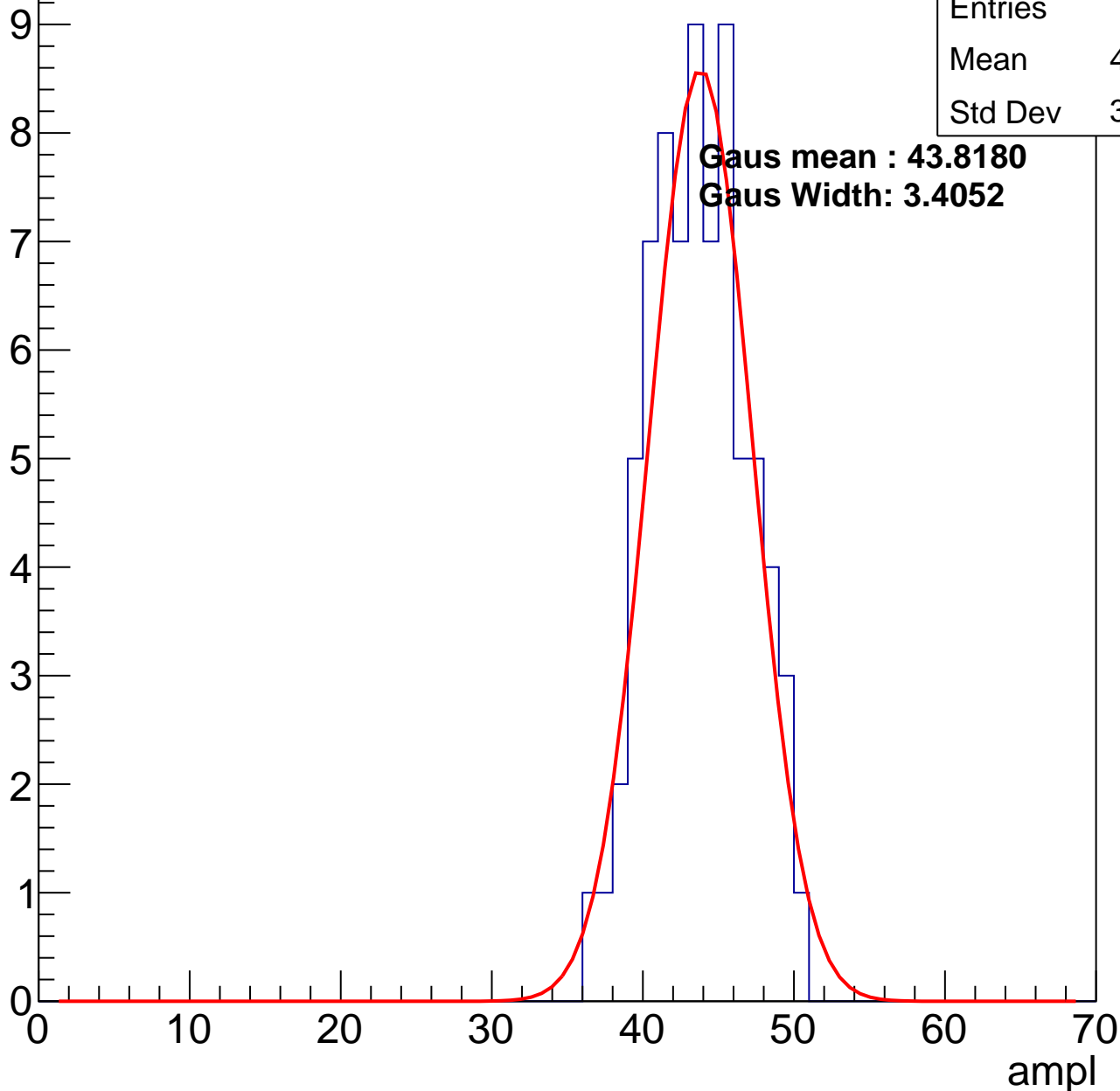
Gaus Width: 3.5816



B1L101S, U1-ch8, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry



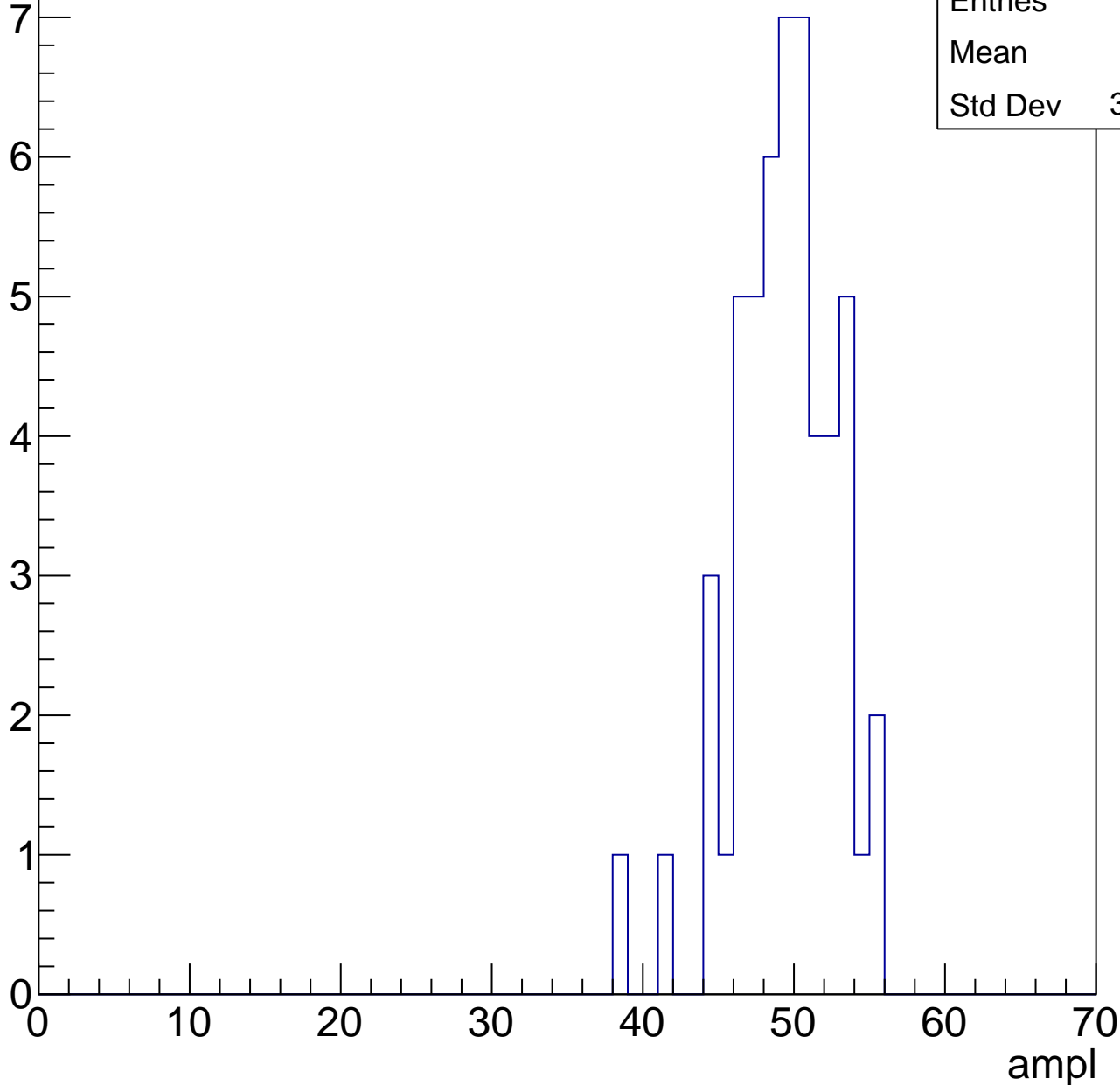
Entries	74
Mean	43.24
Std Dev	3.157

B1L101S, U1-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.9
Std Dev	3.347

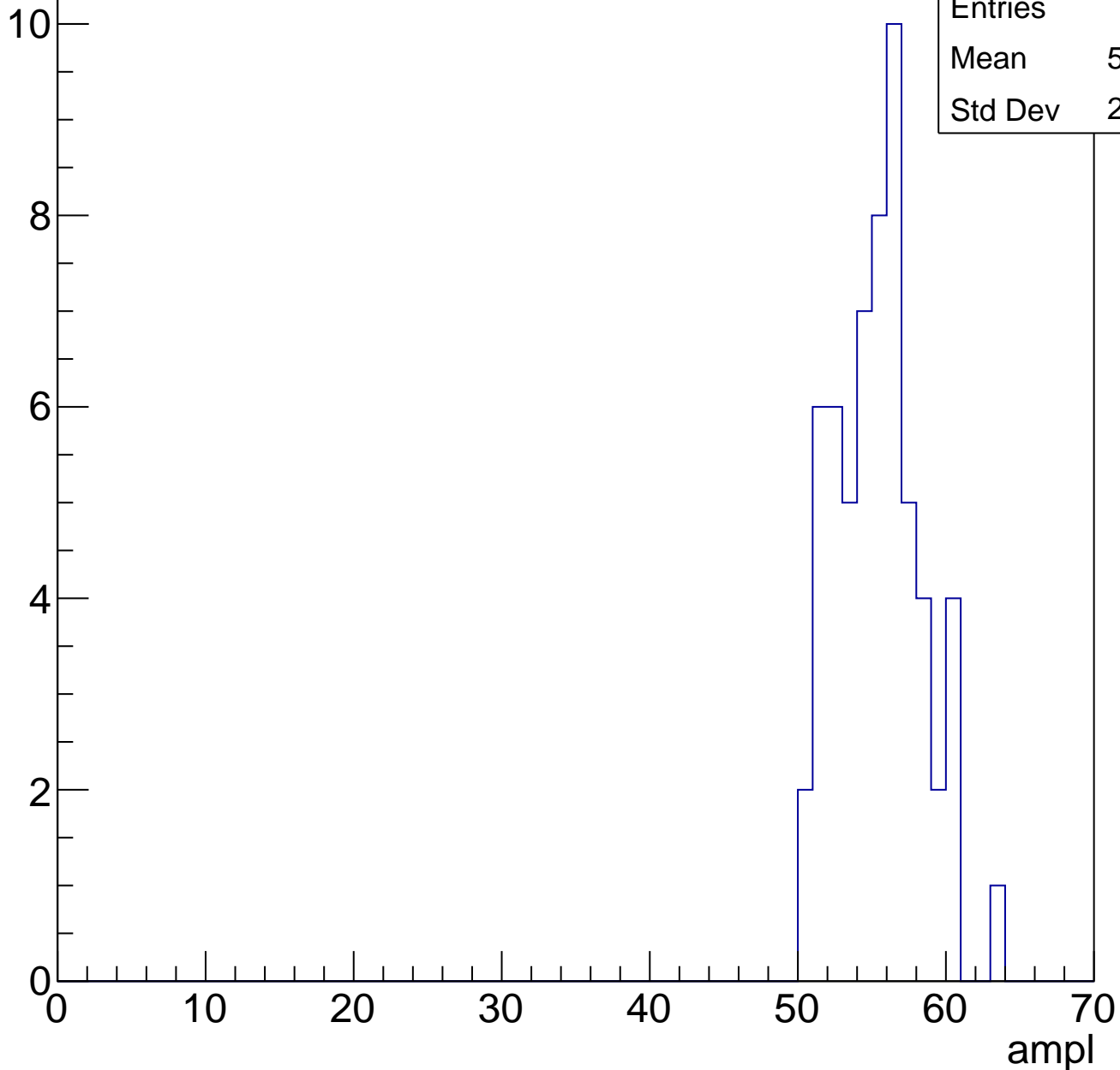


B1L101S, U1-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	54.98
Std Dev	2.855

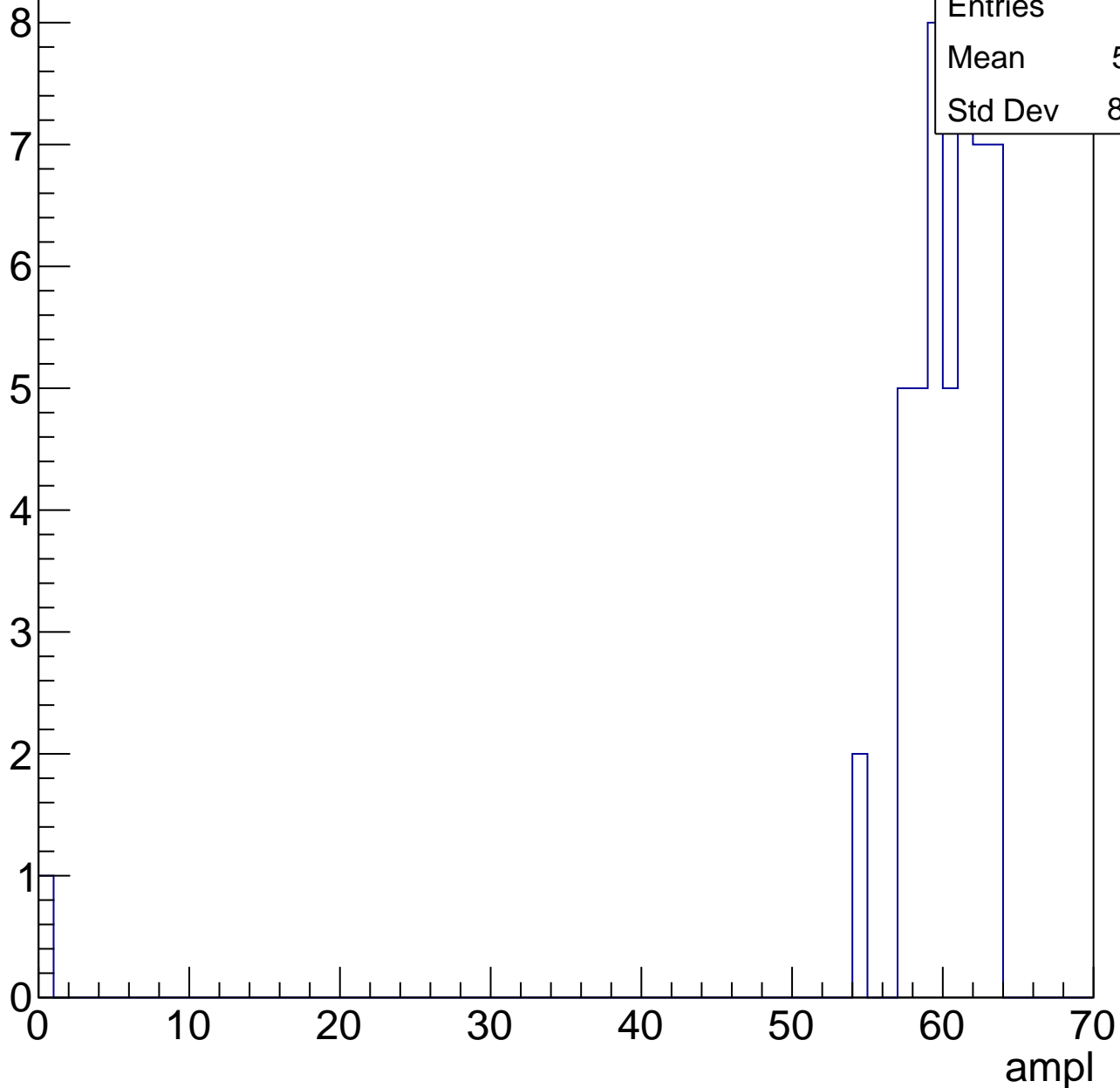
Entry



B1L101S, U1-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

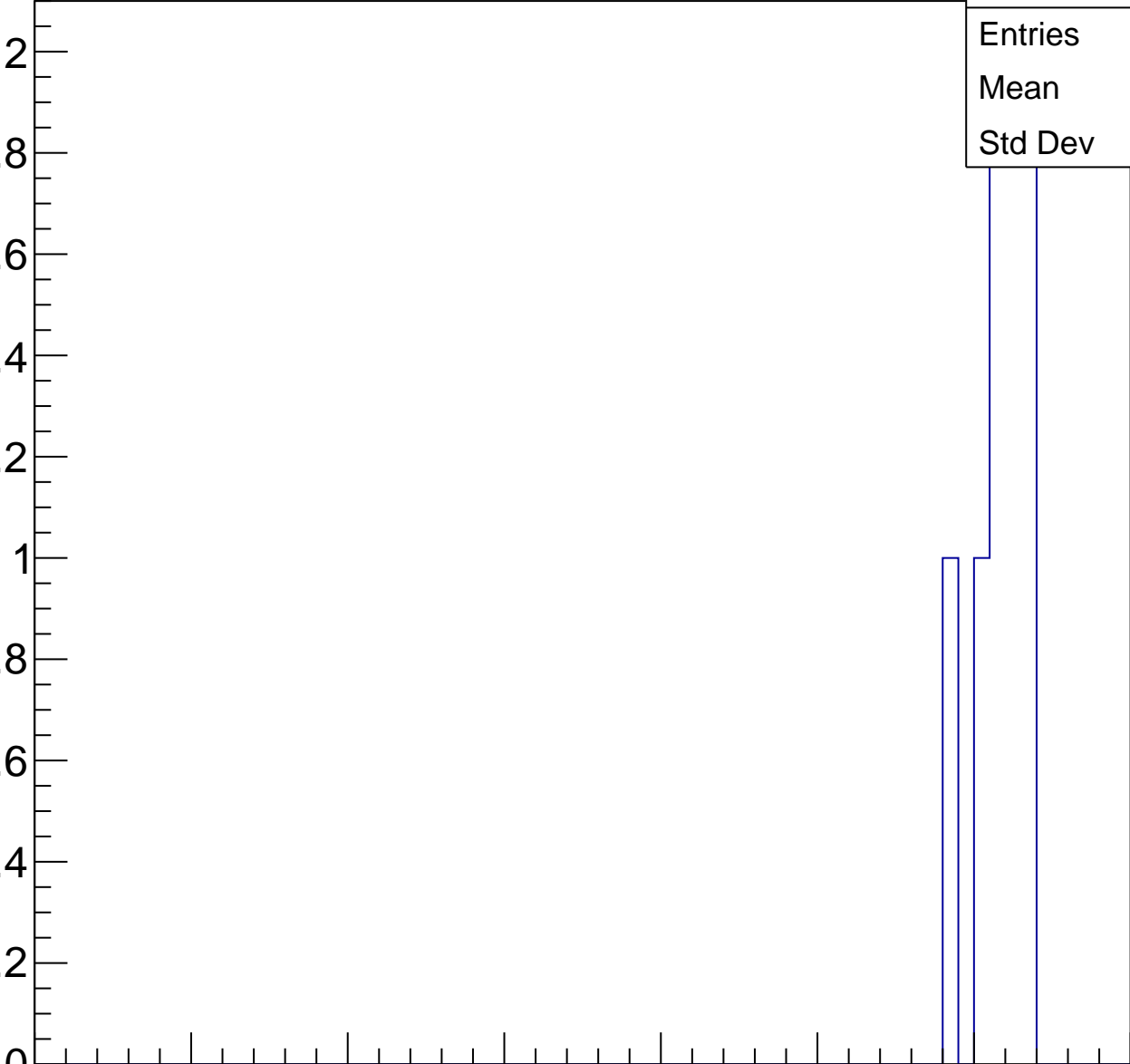
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.25
Std Dev	1.561

ampl

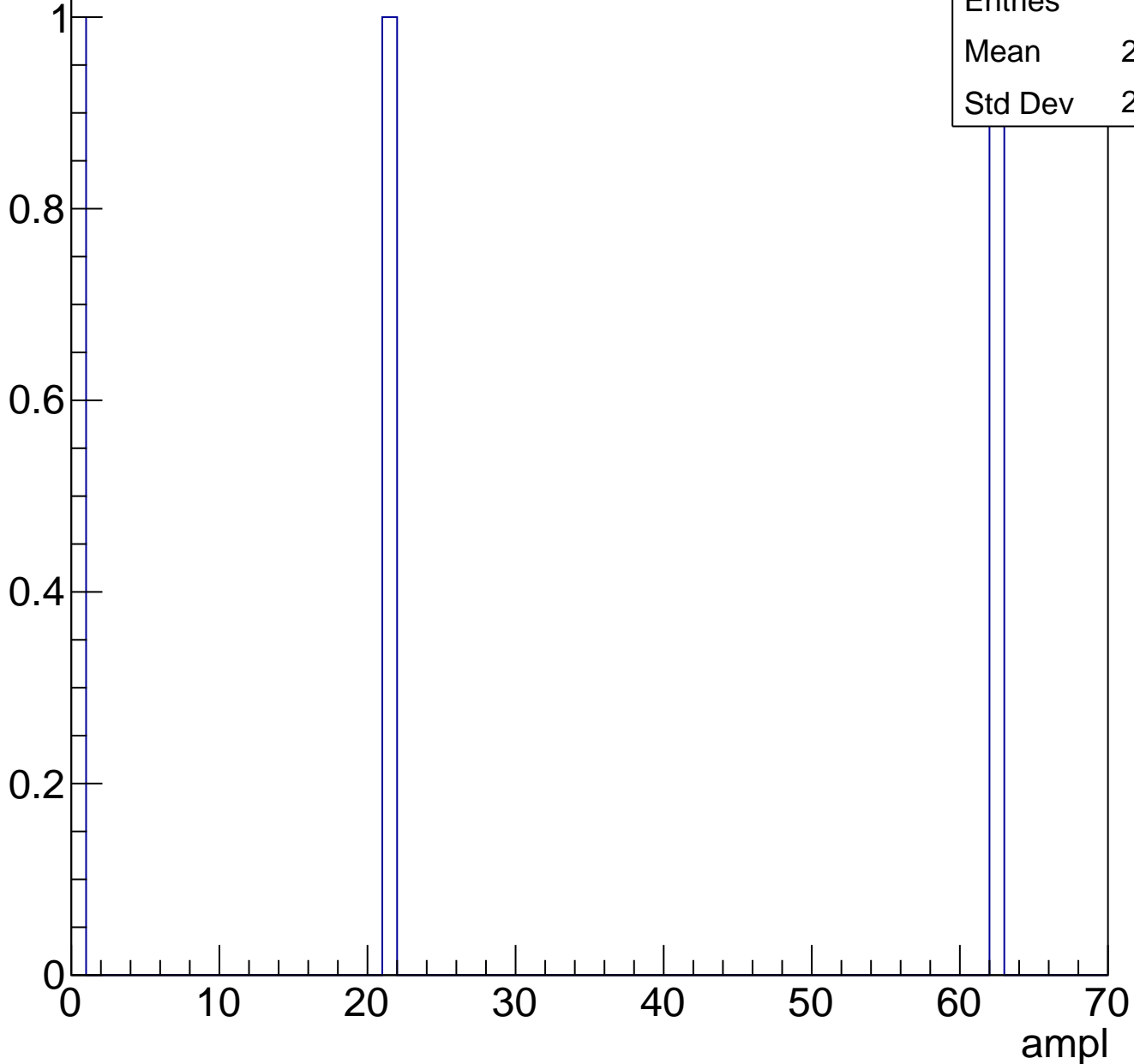
0 10 20 30 40 50 60 70



B1L101S, U1-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B1L101S, U1-ch9, adc0

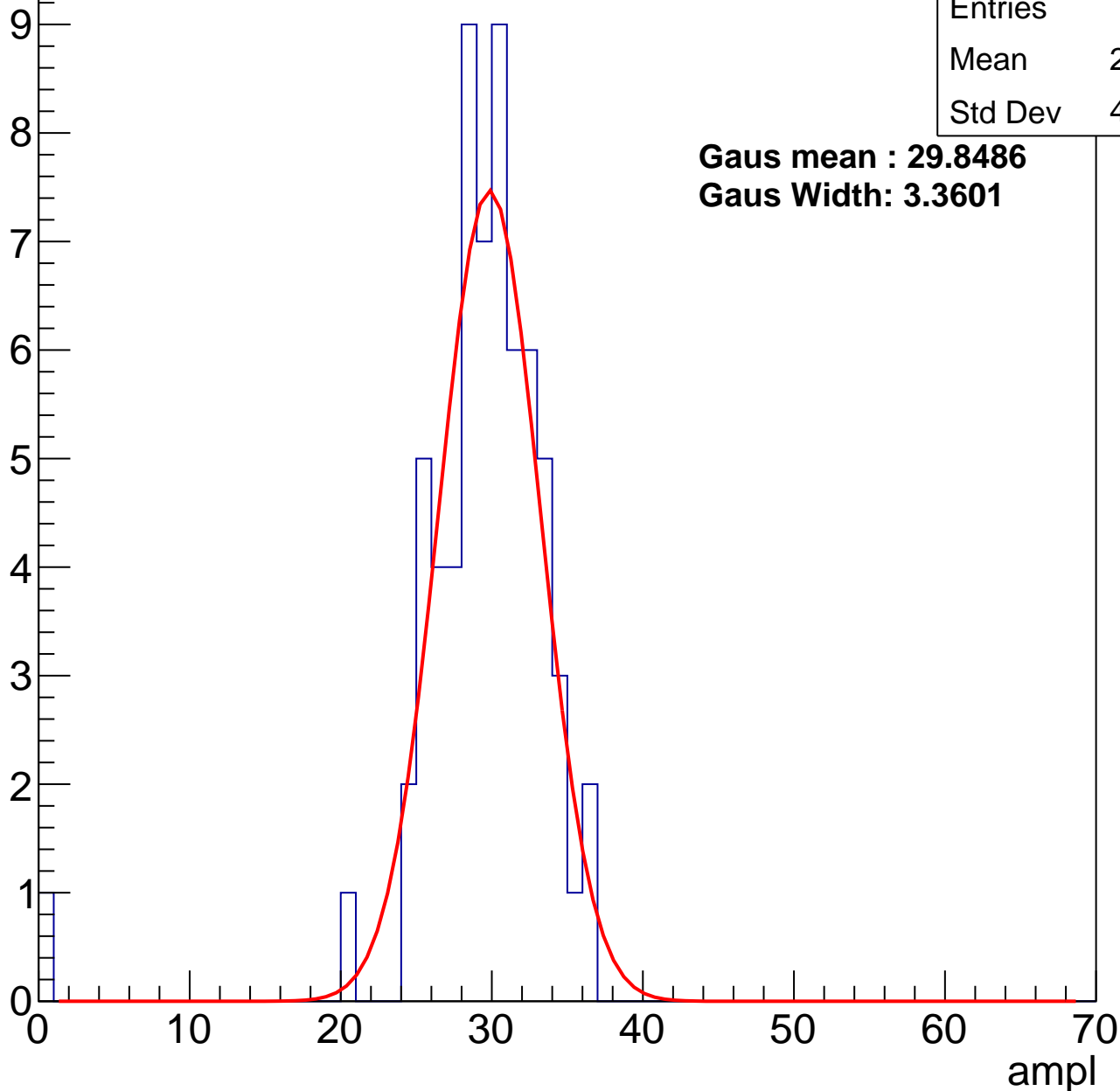
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.95
Std Dev	4.786

Gaus mean : 29.8486

Gaus Width: 3.3601



B1L101S, U1-ch9, adc1

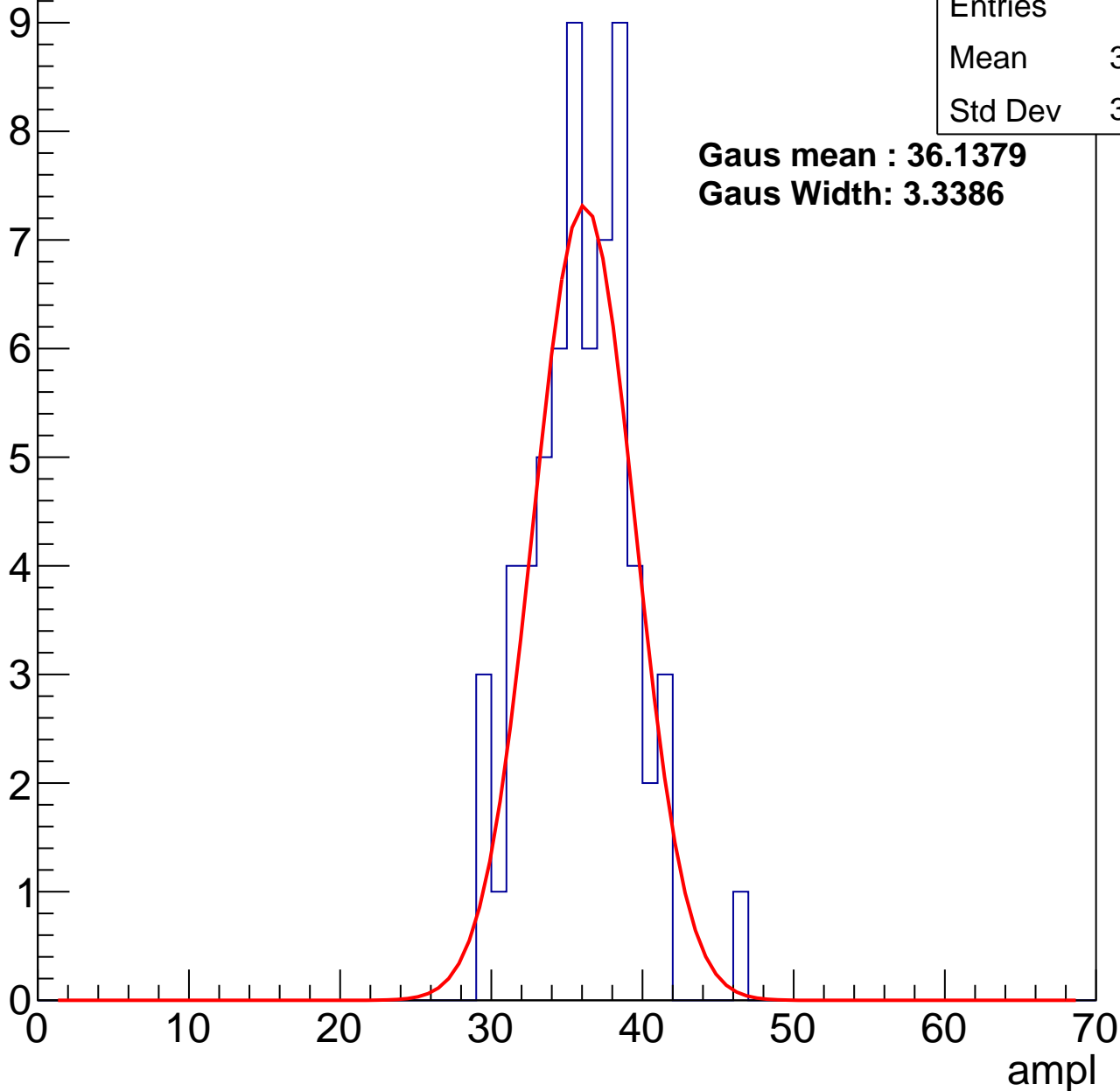
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.55
Std Dev	3.302

Gaus mean : 36.1379

Gaus Width: 3.3386



B1L101S, U1-ch9, adc2

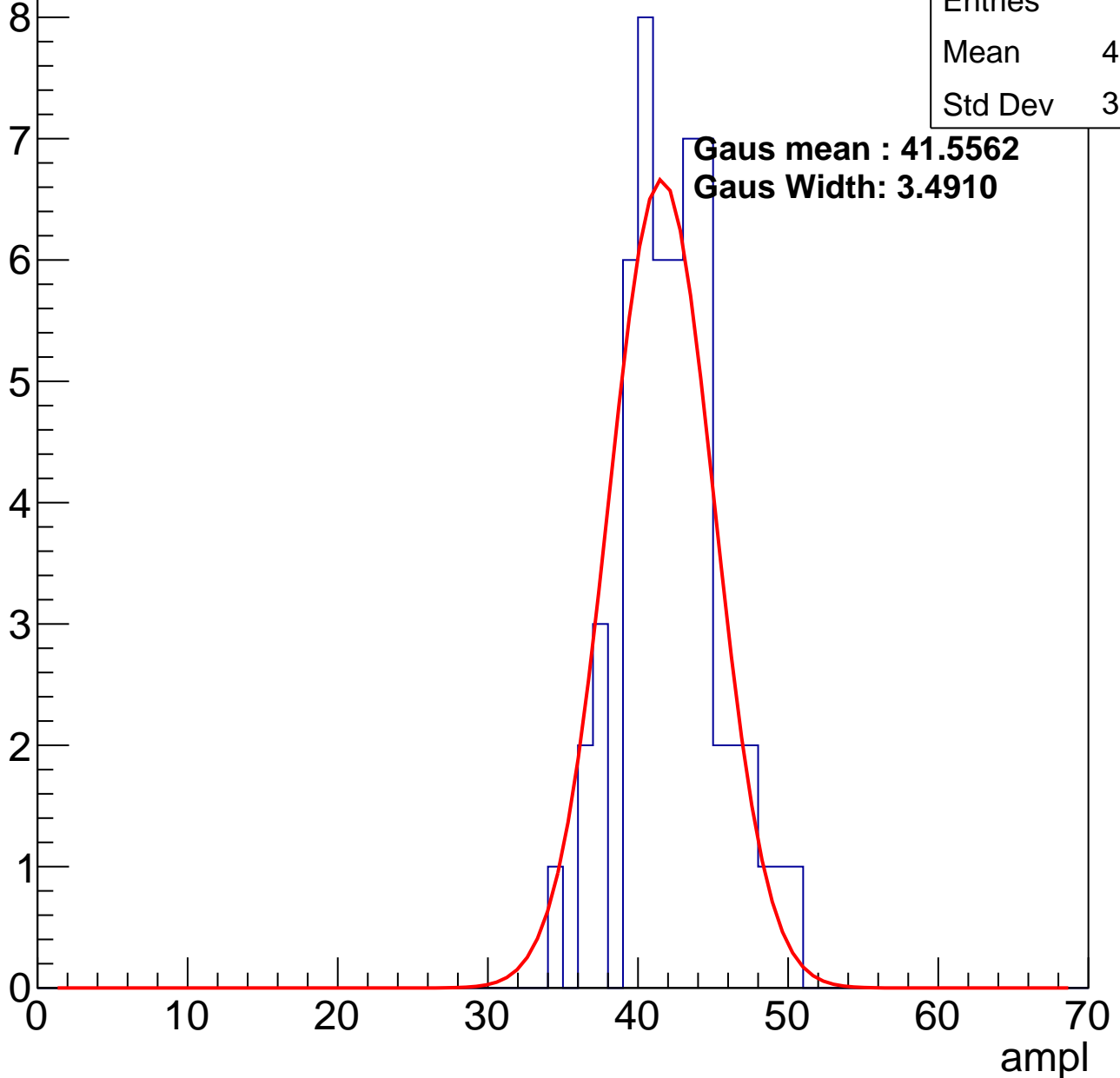
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.84
Std Dev	3.263

Gaus mean : 41.5562

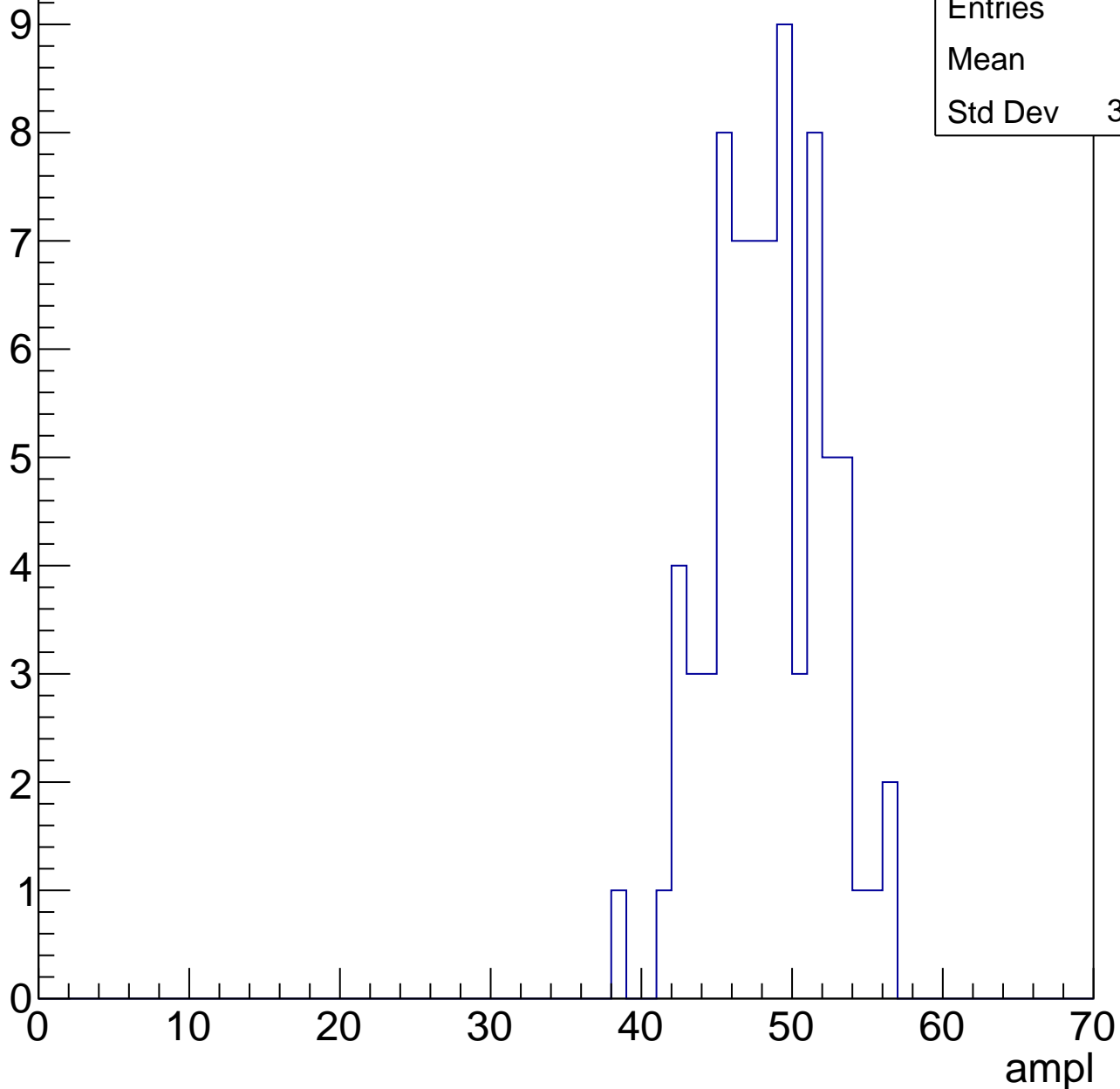
Gaus Width: 3.4910



B1L101S, U1-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

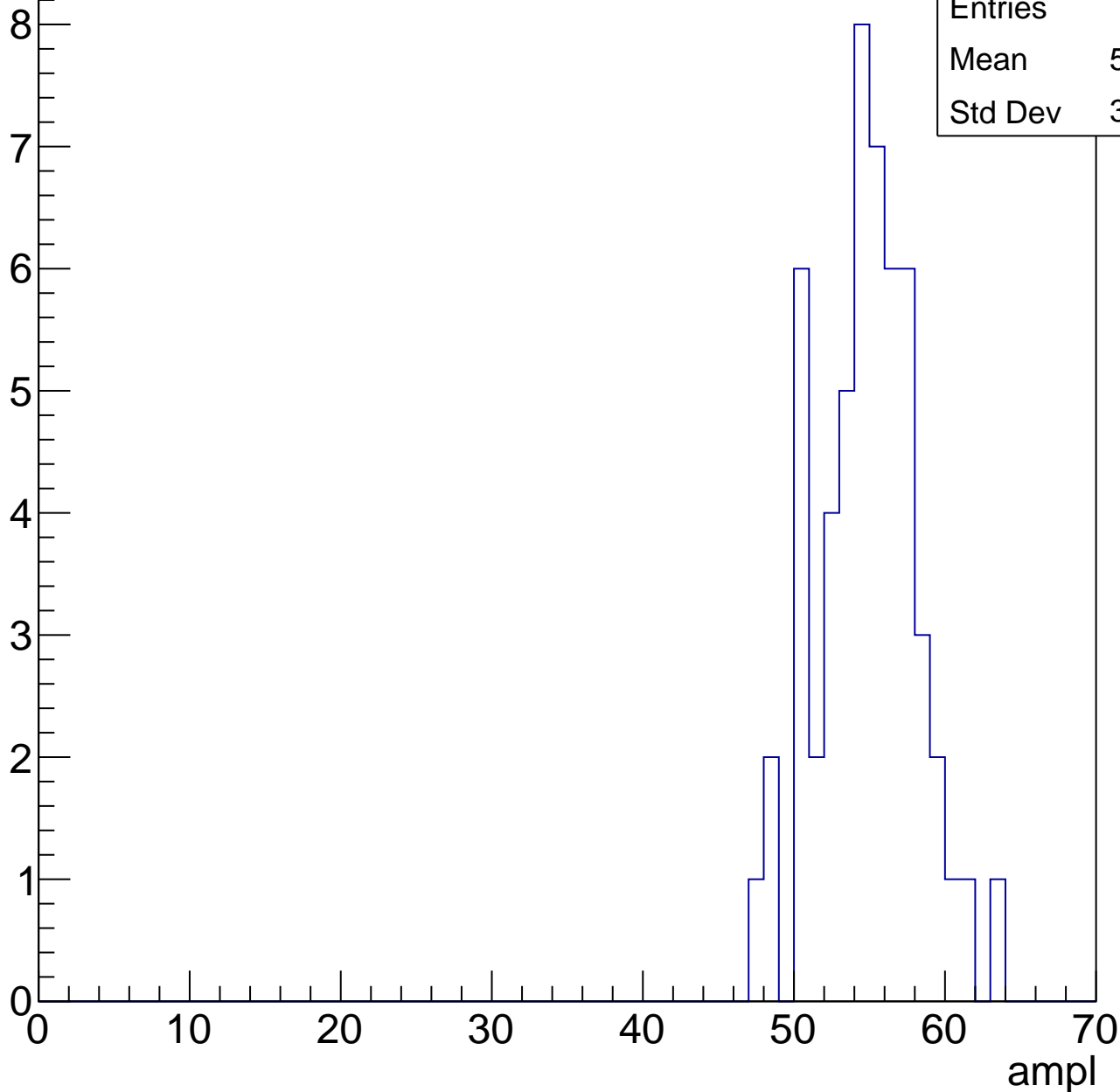


B1L101S, U1-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.35
Std Dev	3.299

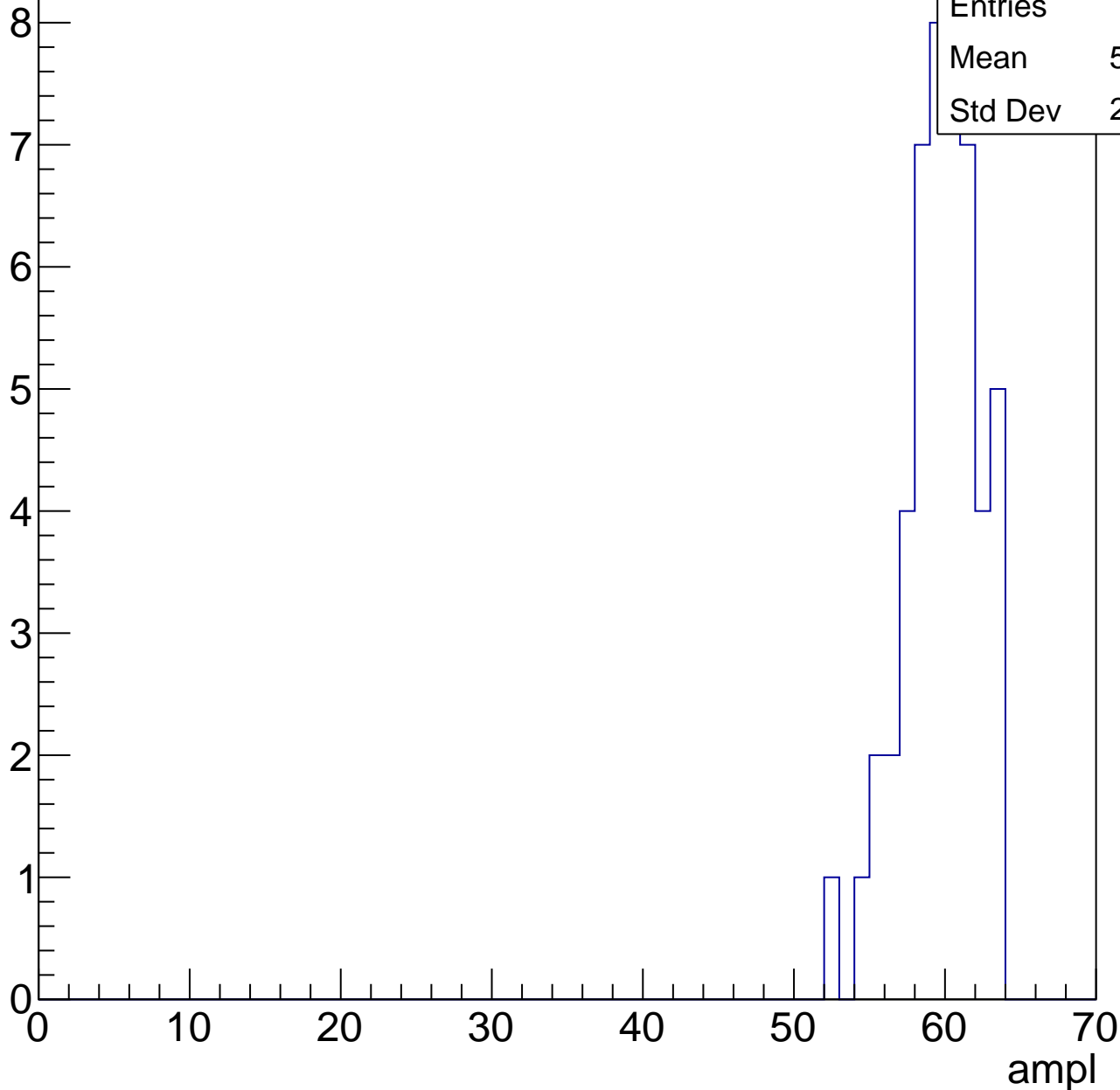


B1L101S, U1-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.27
Std Dev	2.456

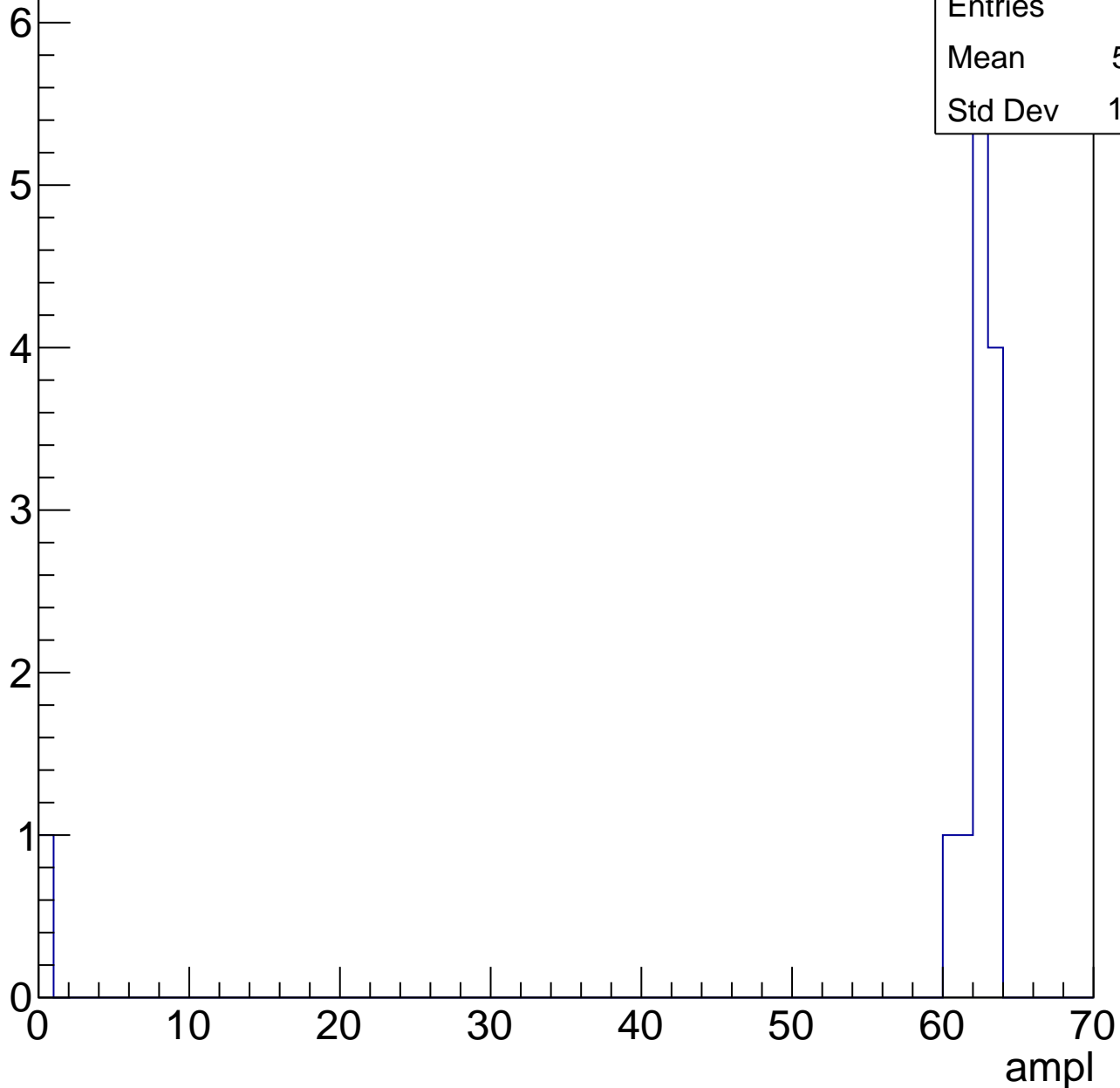


B1L101S, U1-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

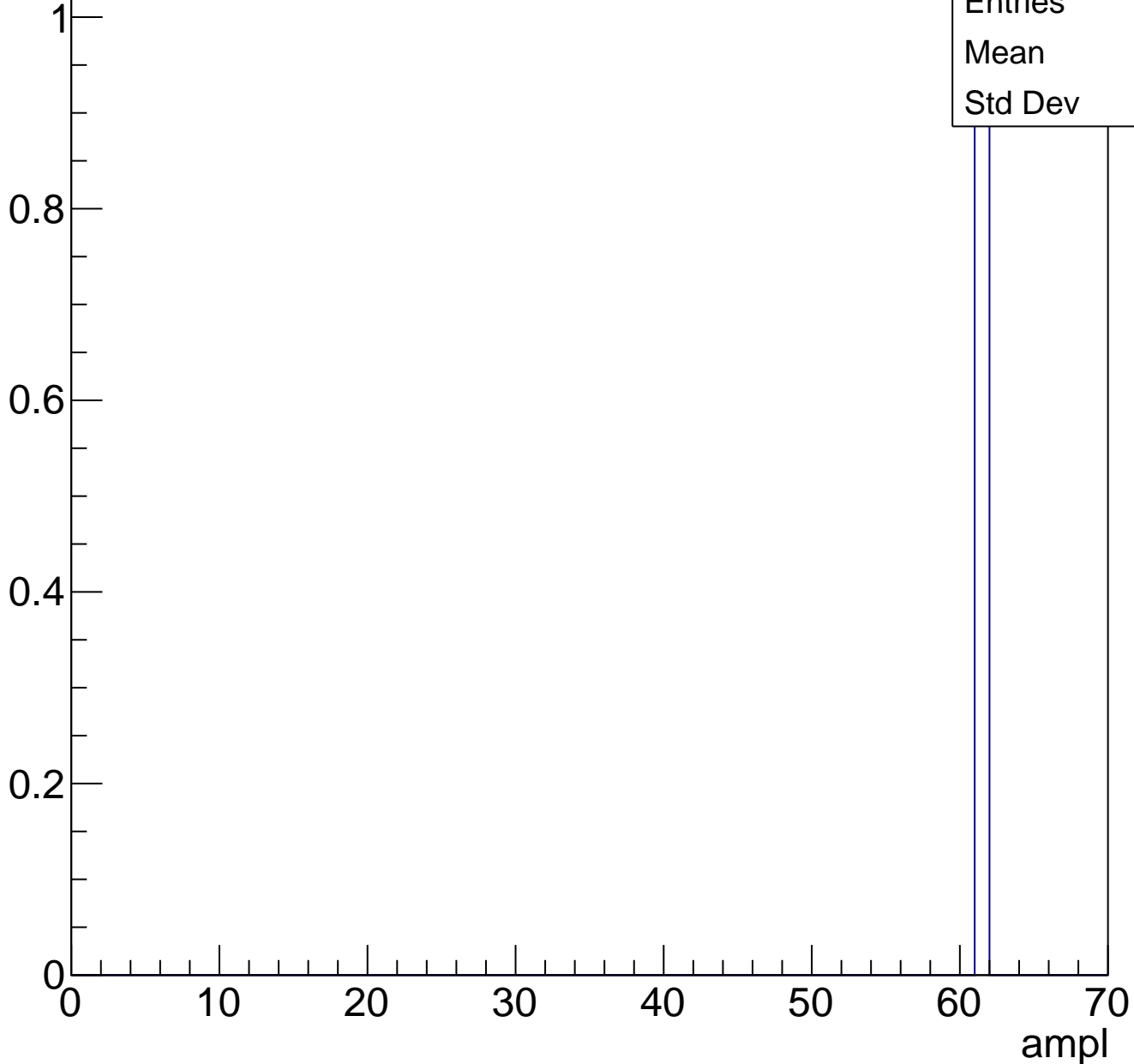
Entries	13
Mean	57.31
Std Dev	16.56



B1L101S, U1-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch10, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	27.96
Std Dev	3.796

Gaus mean : 29.1041

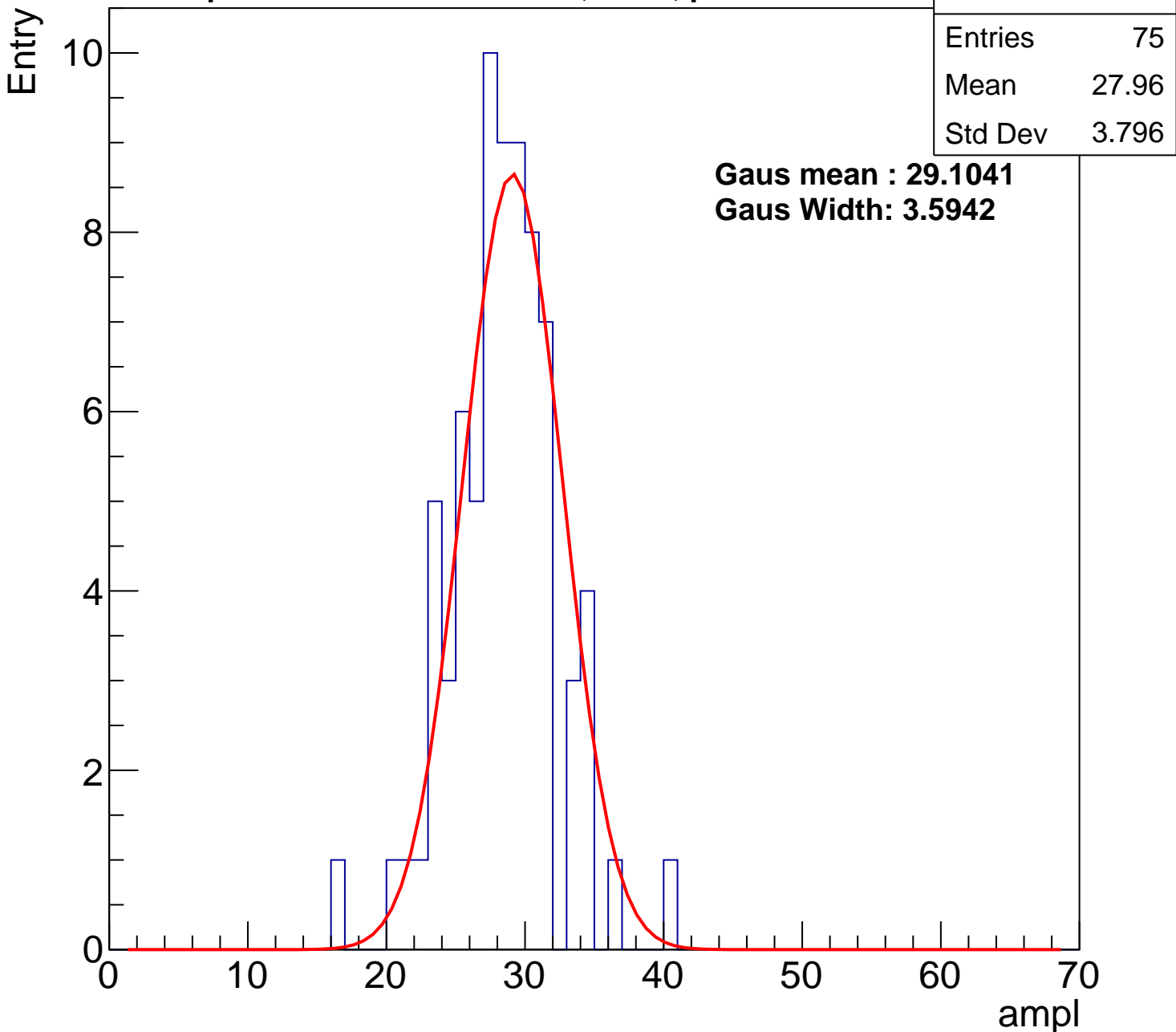
Gaus Width: 3.5942

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch10, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	34.99
Std Dev	3.491

Gaus mean : 36.1668

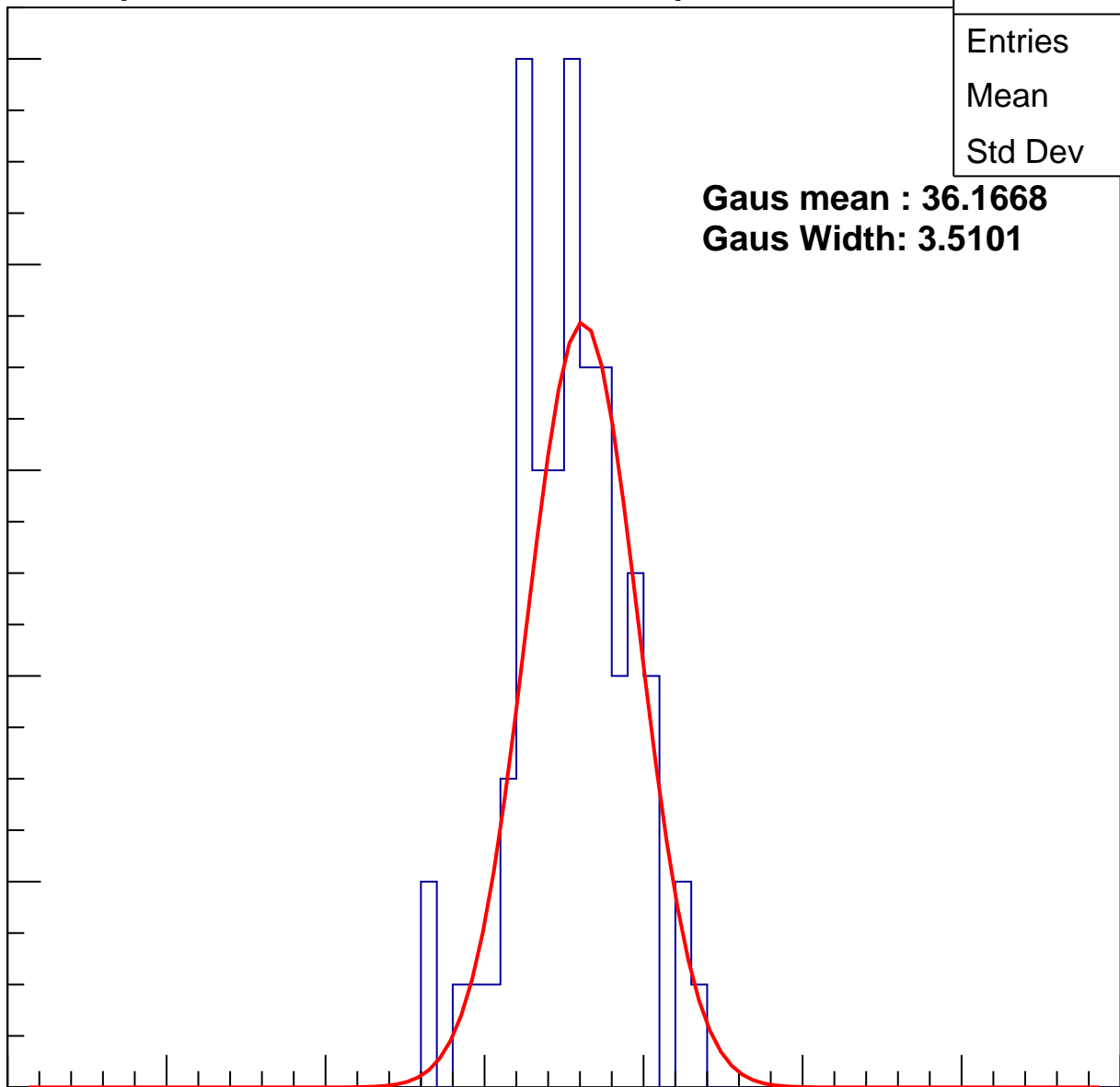
Gaus Width: 3.5101

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch10, adc2

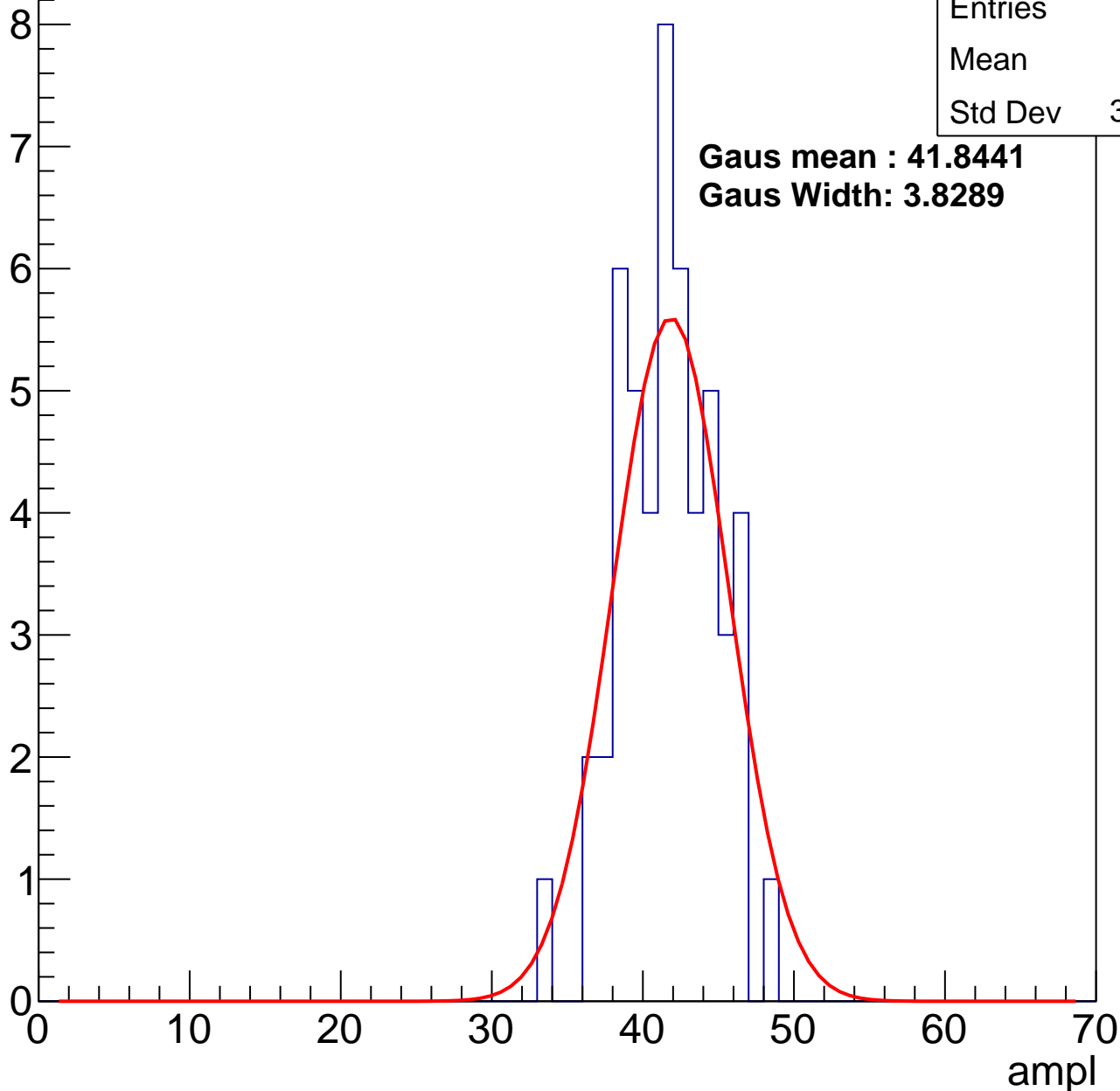
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	41.2
Std Dev	3.087

Gaus mean : 41.8441

Gaus Width: 3.8289



B1L101S, U1-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

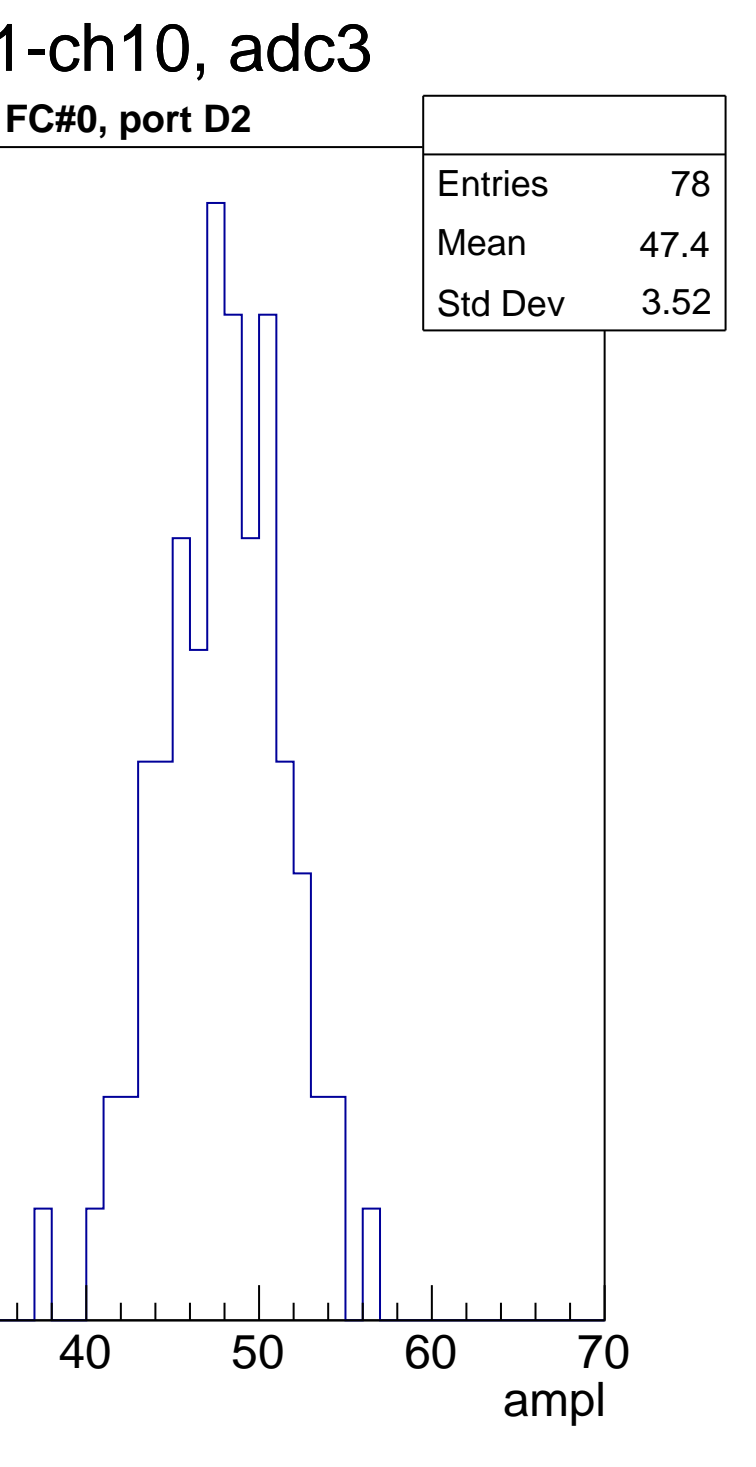
Entries	78
Mean	47.4
Std Dev	3.52

Entry

10
8
6
4
2
0

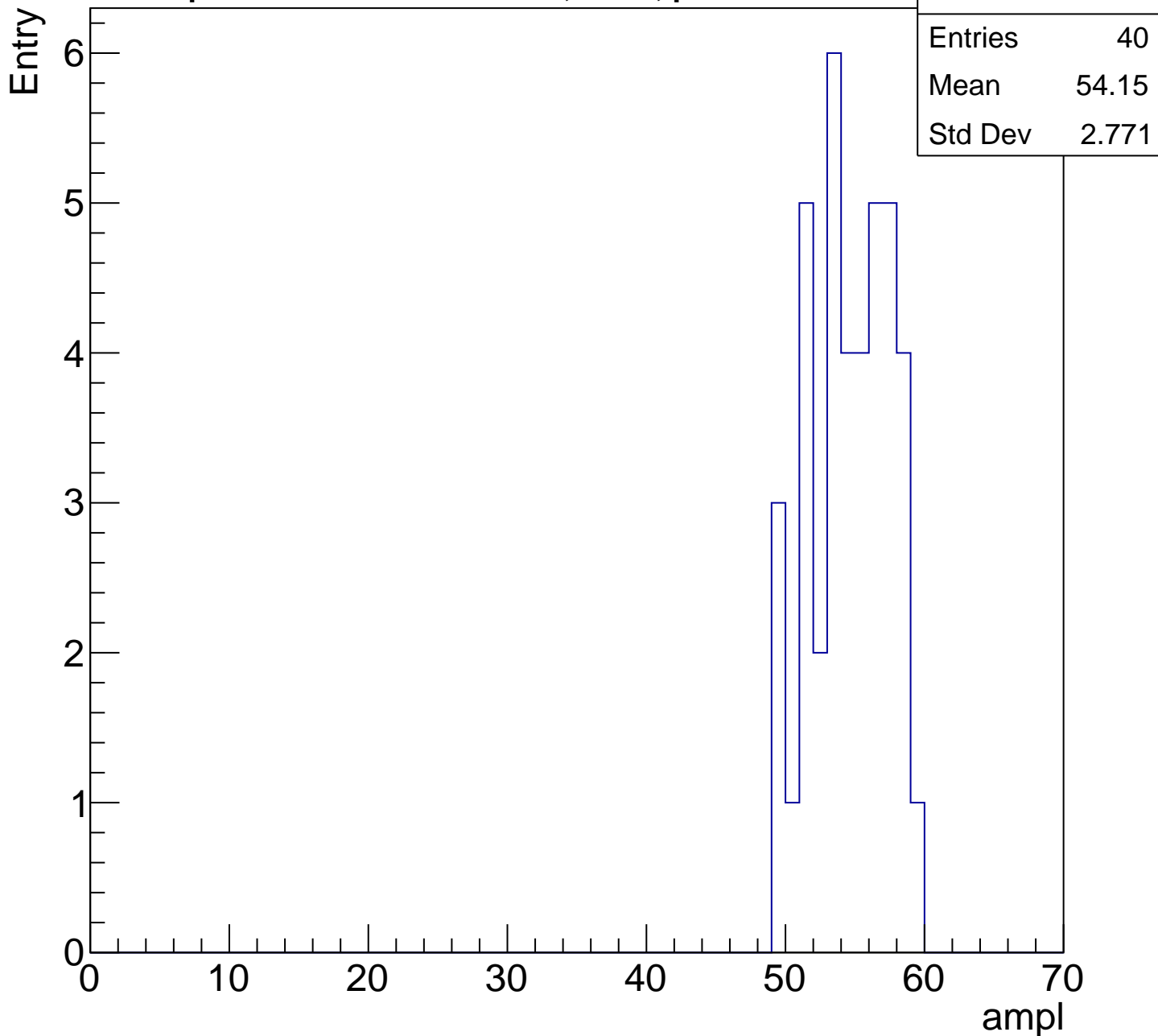
0 10 20 30 40 50 60 70

ampl



B1L101S, U1-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch10, adc5

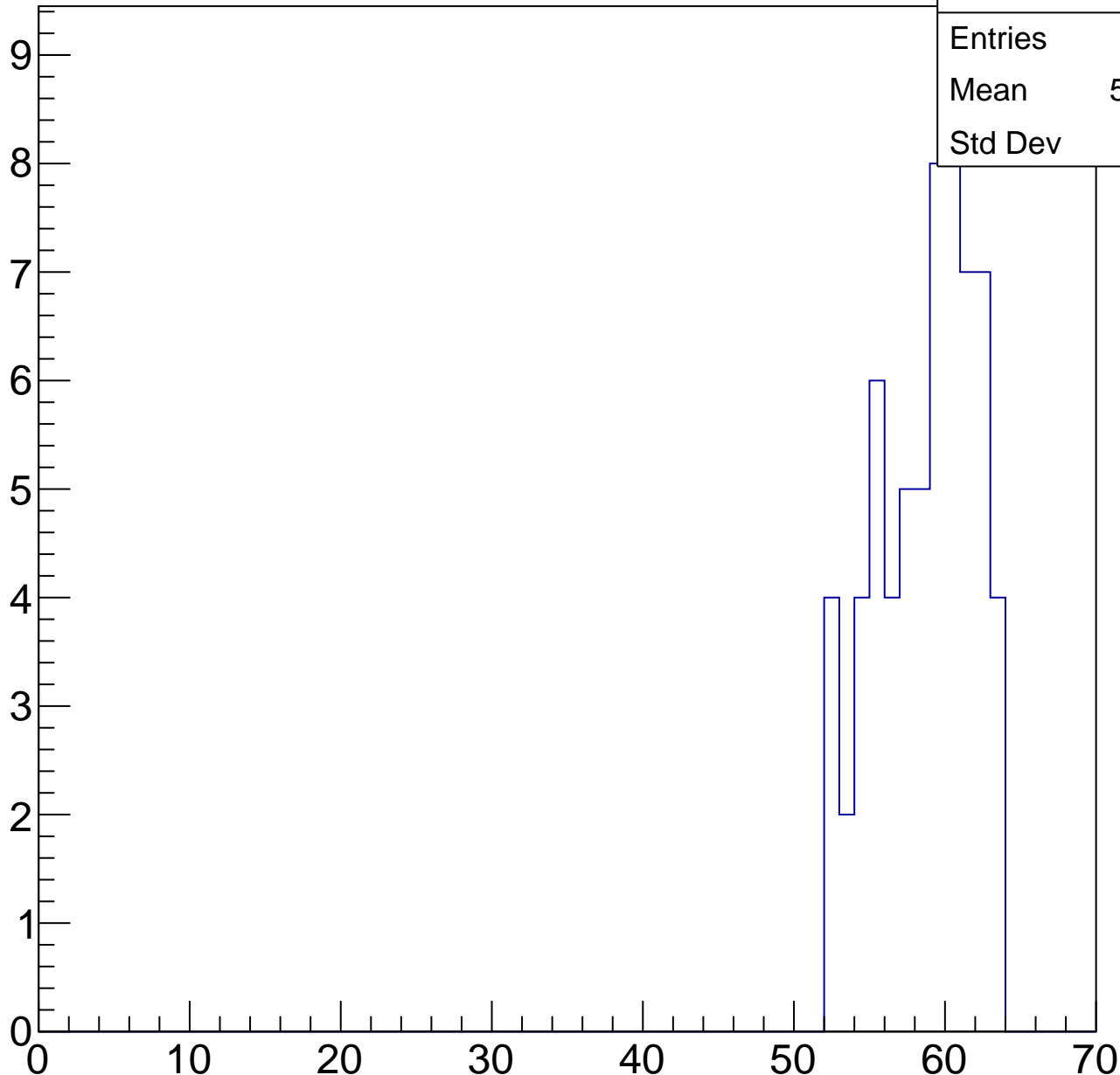
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	58.22
Std Dev	3.16

ampl

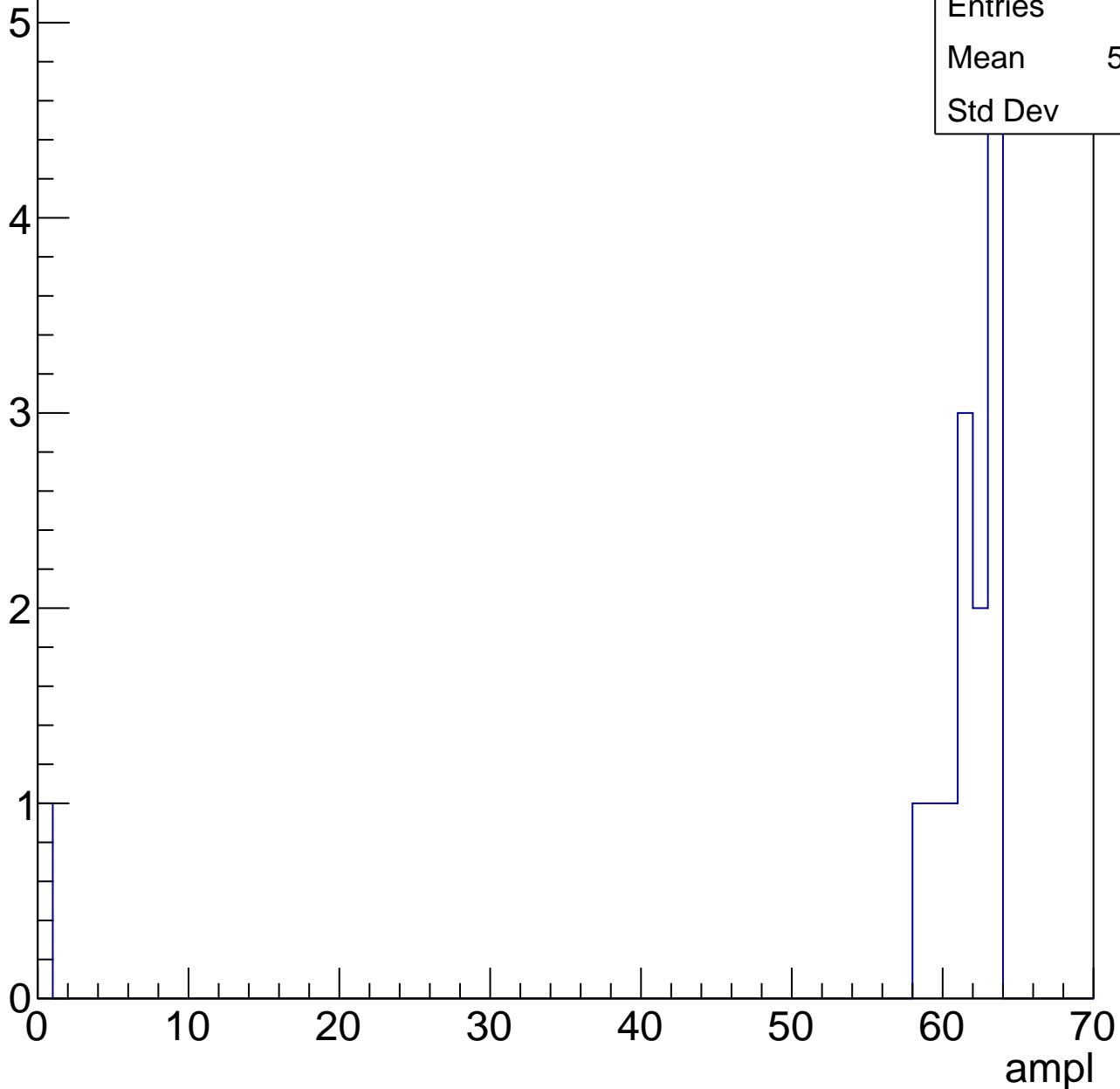


B1L101S, U1-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

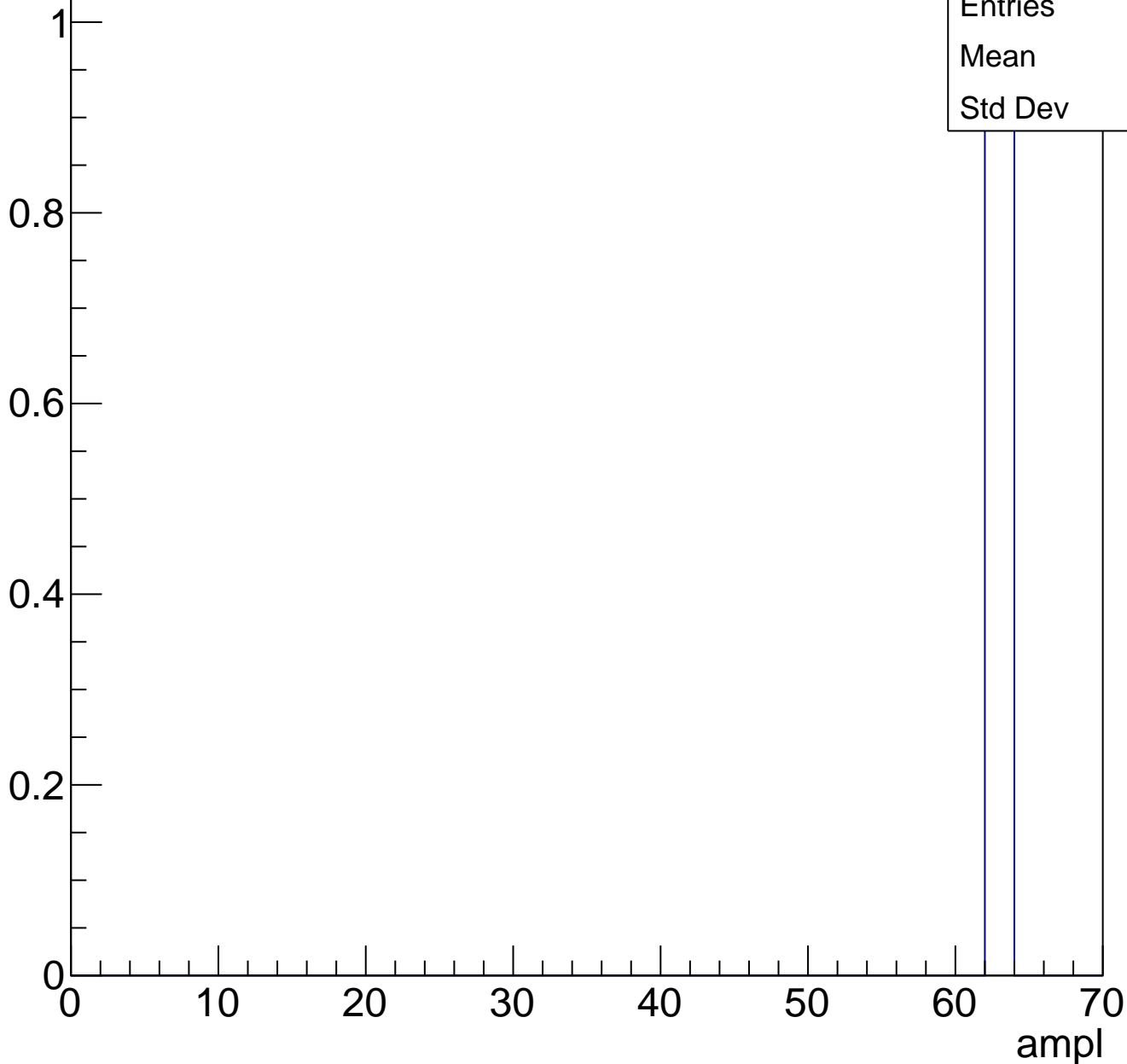
Entries	14
Mean	57.07
Std Dev	15.9



B1L101S, U1-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch11, adc0

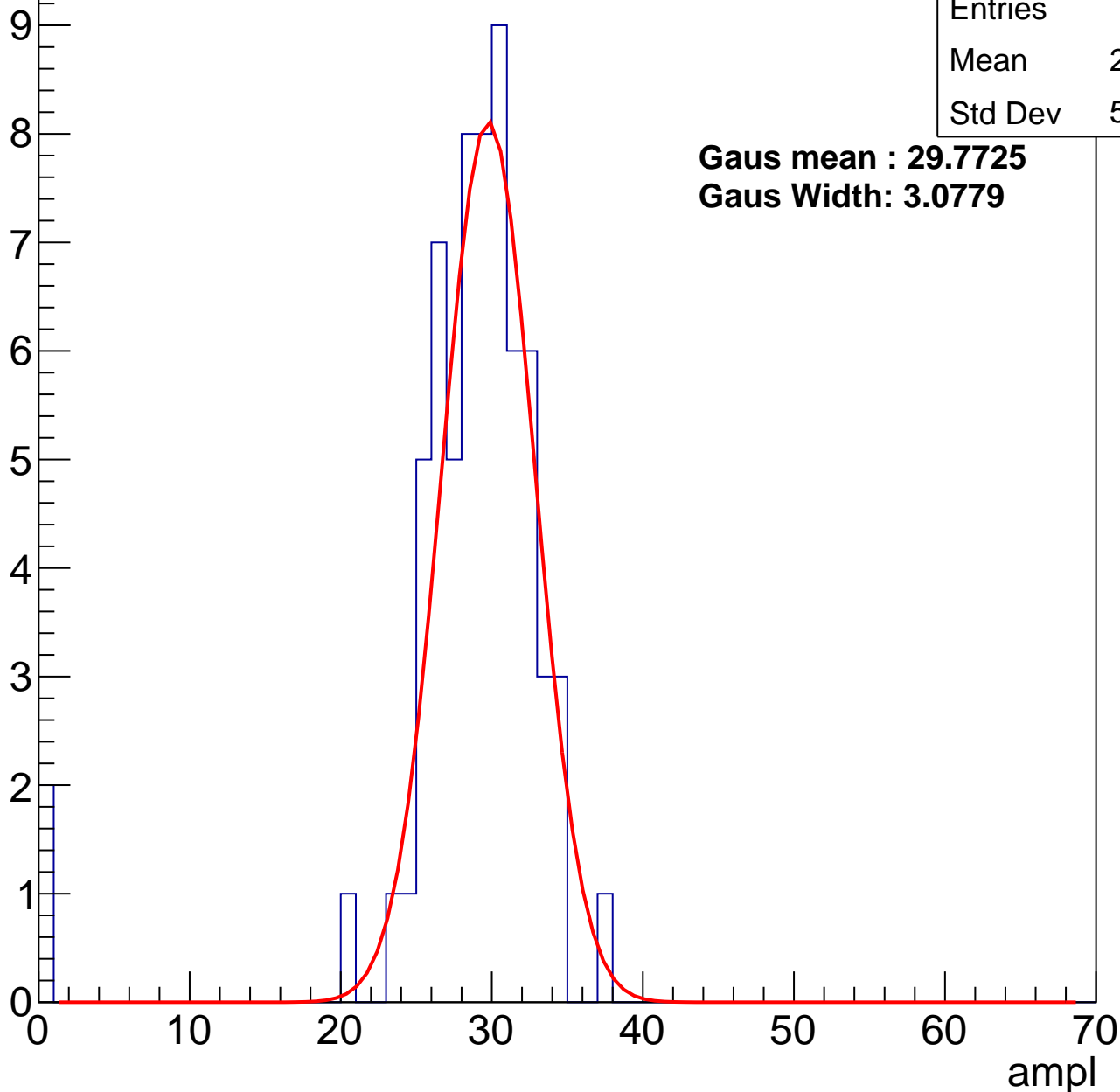
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.05
Std Dev	5.785

Gaus mean : 29.7725

Gaus Width: 3.0779



B1L101S, U1-ch11, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	35.16
Std Dev	5.476

Gaus mean : 36.0088

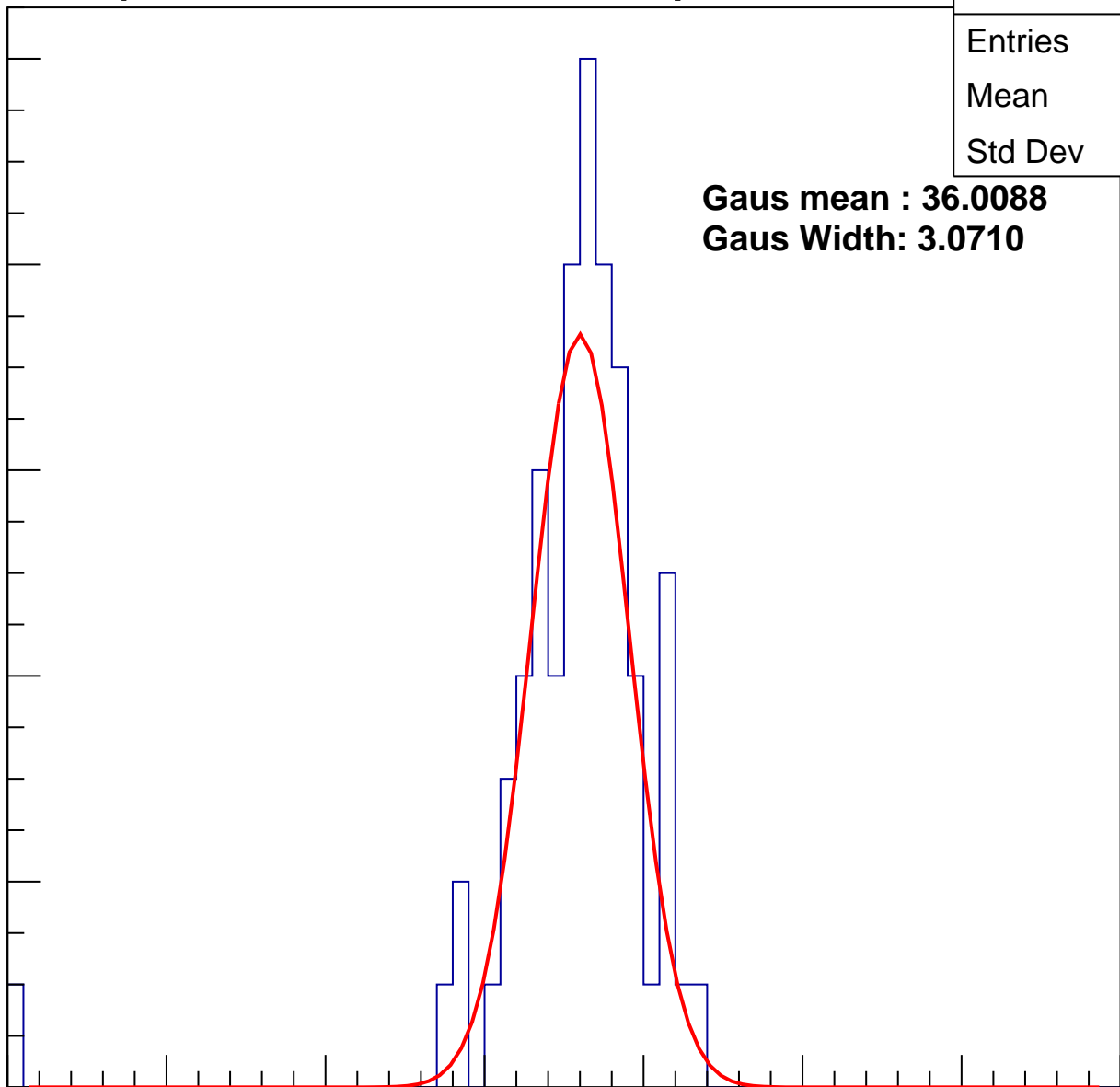
Gaus Width: 3.0710

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch11, adc2

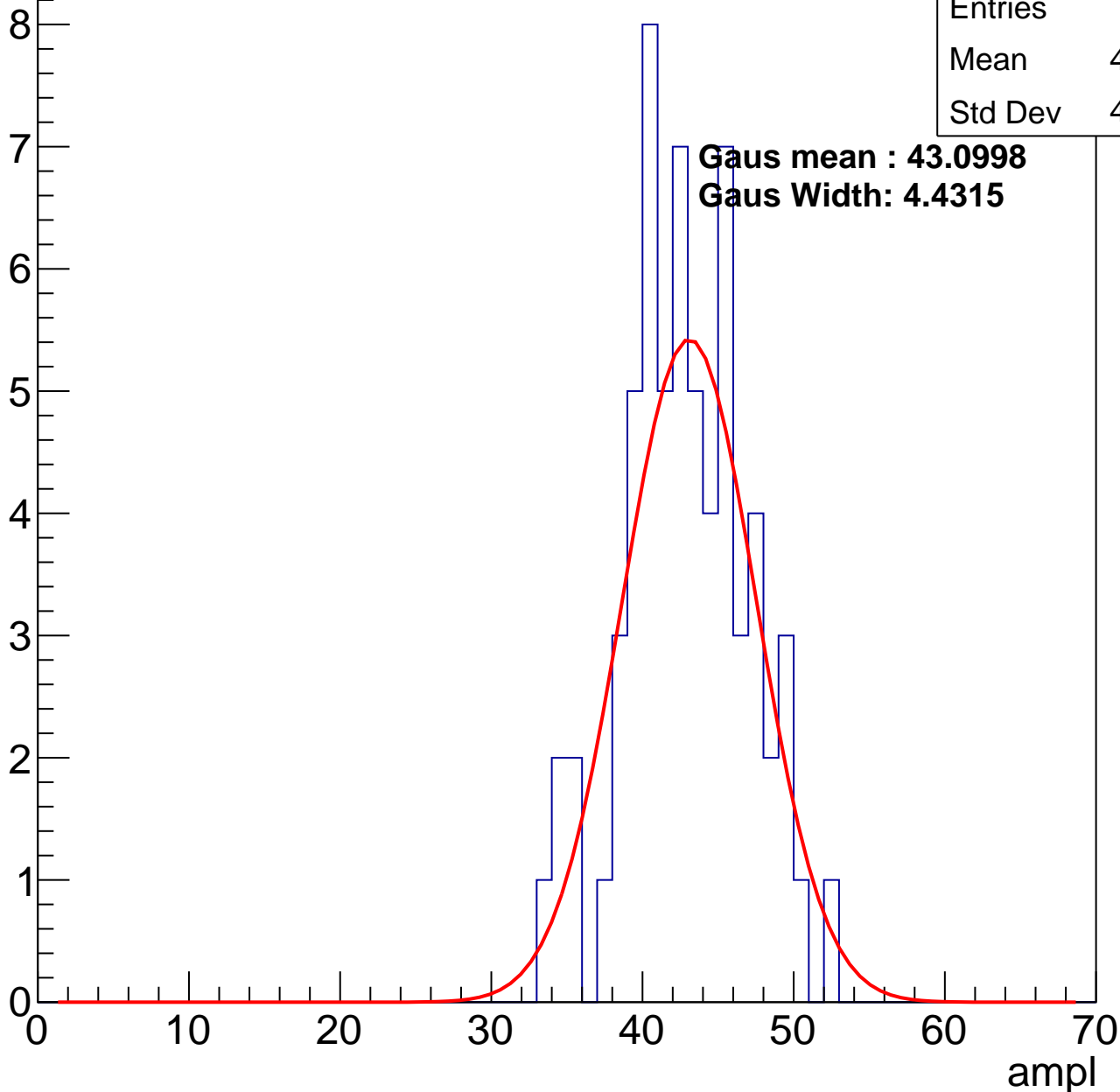
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.39
Std Dev	4.084

Gaus mean : 43.0998

Gaus Width: 4.4315

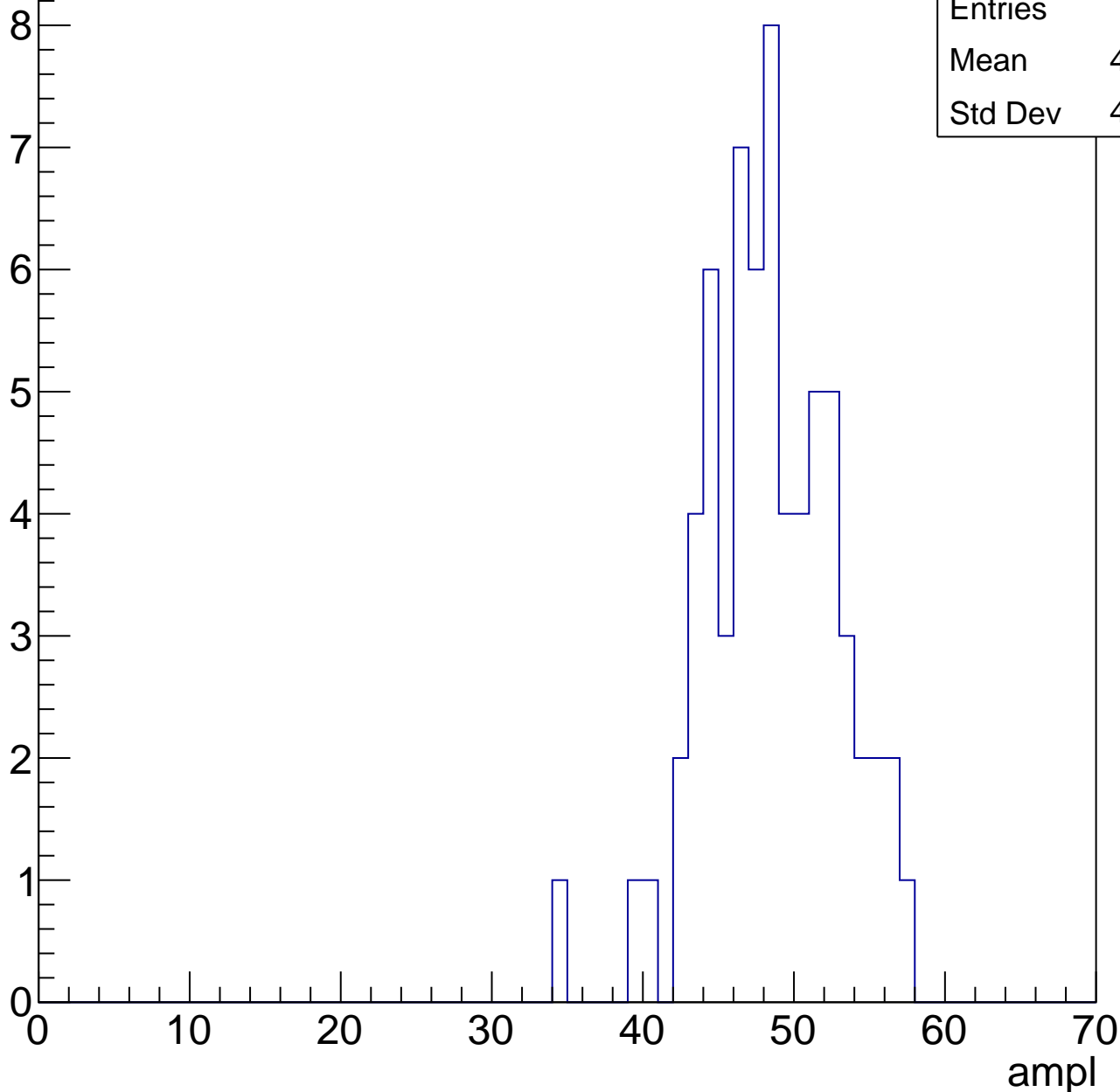


B1L101S, U1-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	47.96
Std Dev	4.352

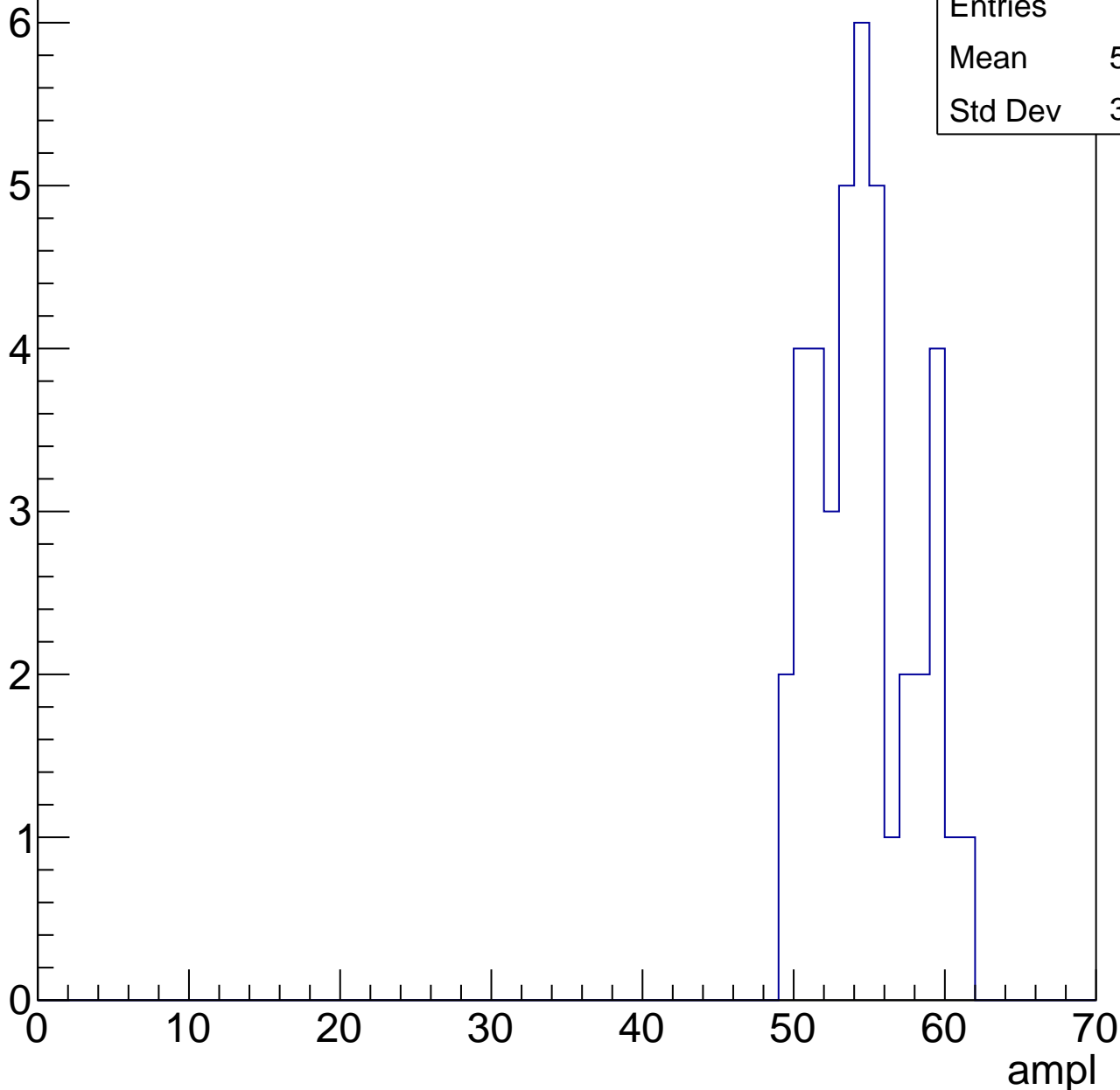


B1L101S, U1-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

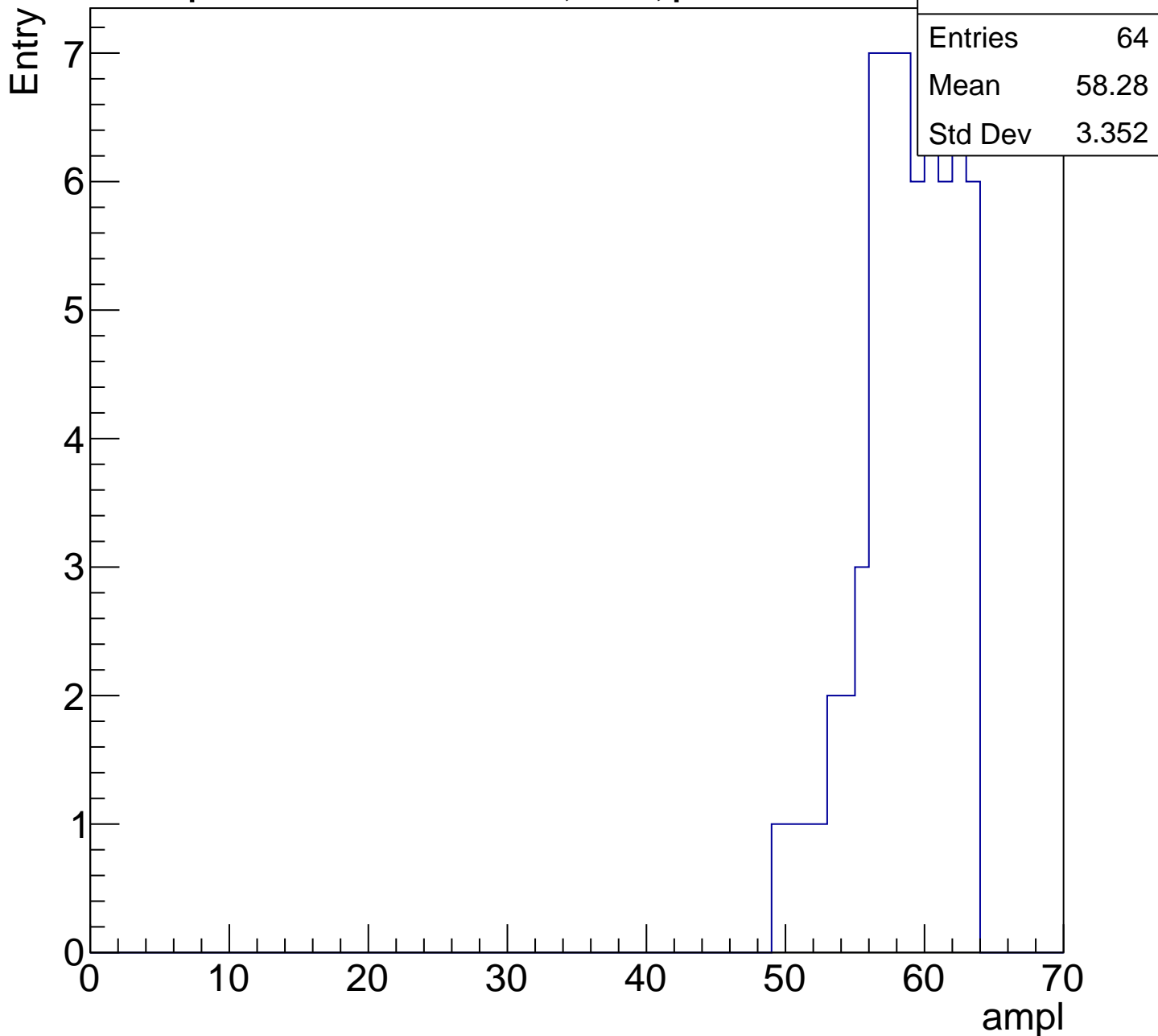
Entry

Entries	40
Mean	54.12
Std Dev	3.203



B1L101S, U1-ch11, adc5

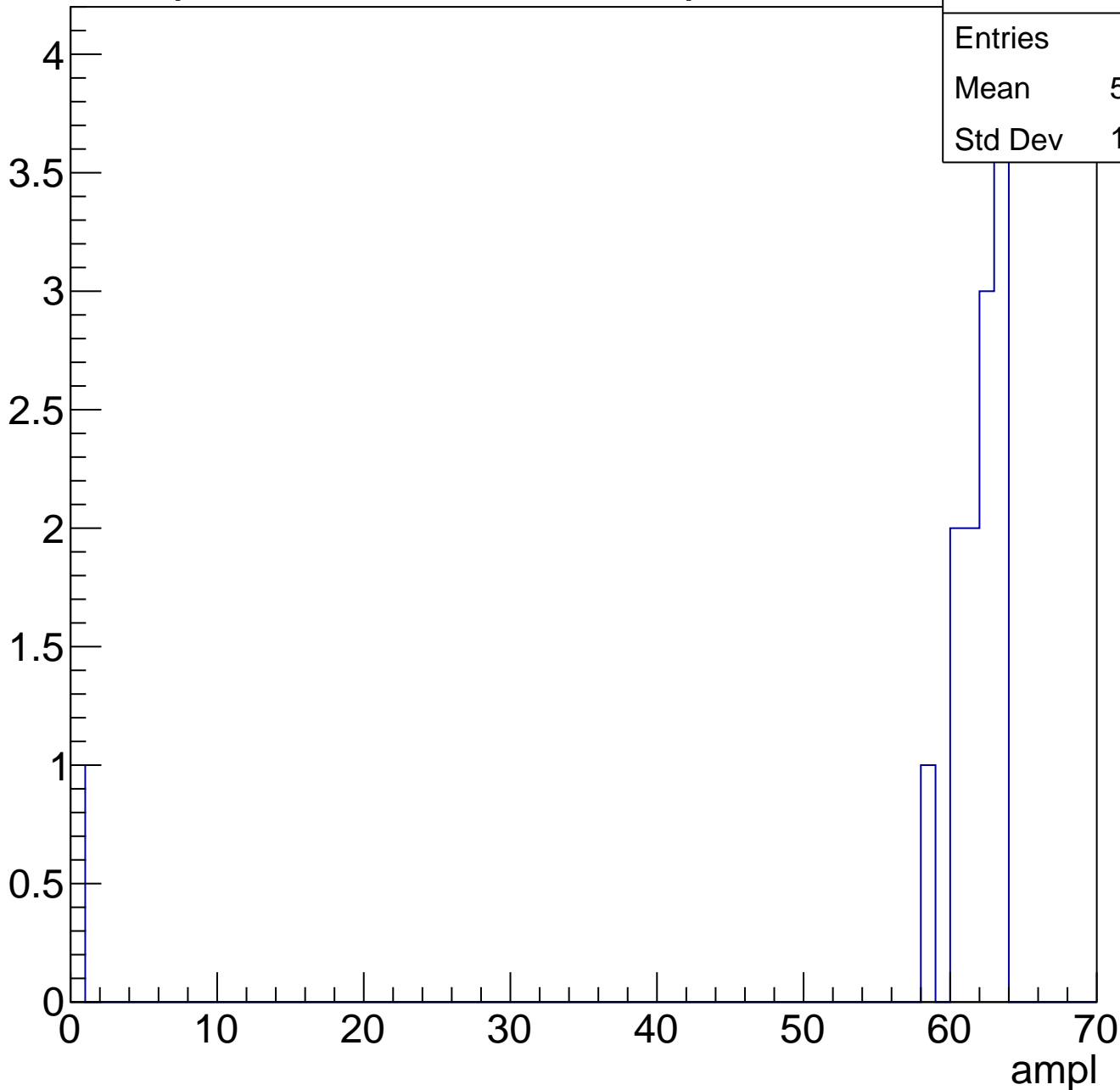
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch12, adc0

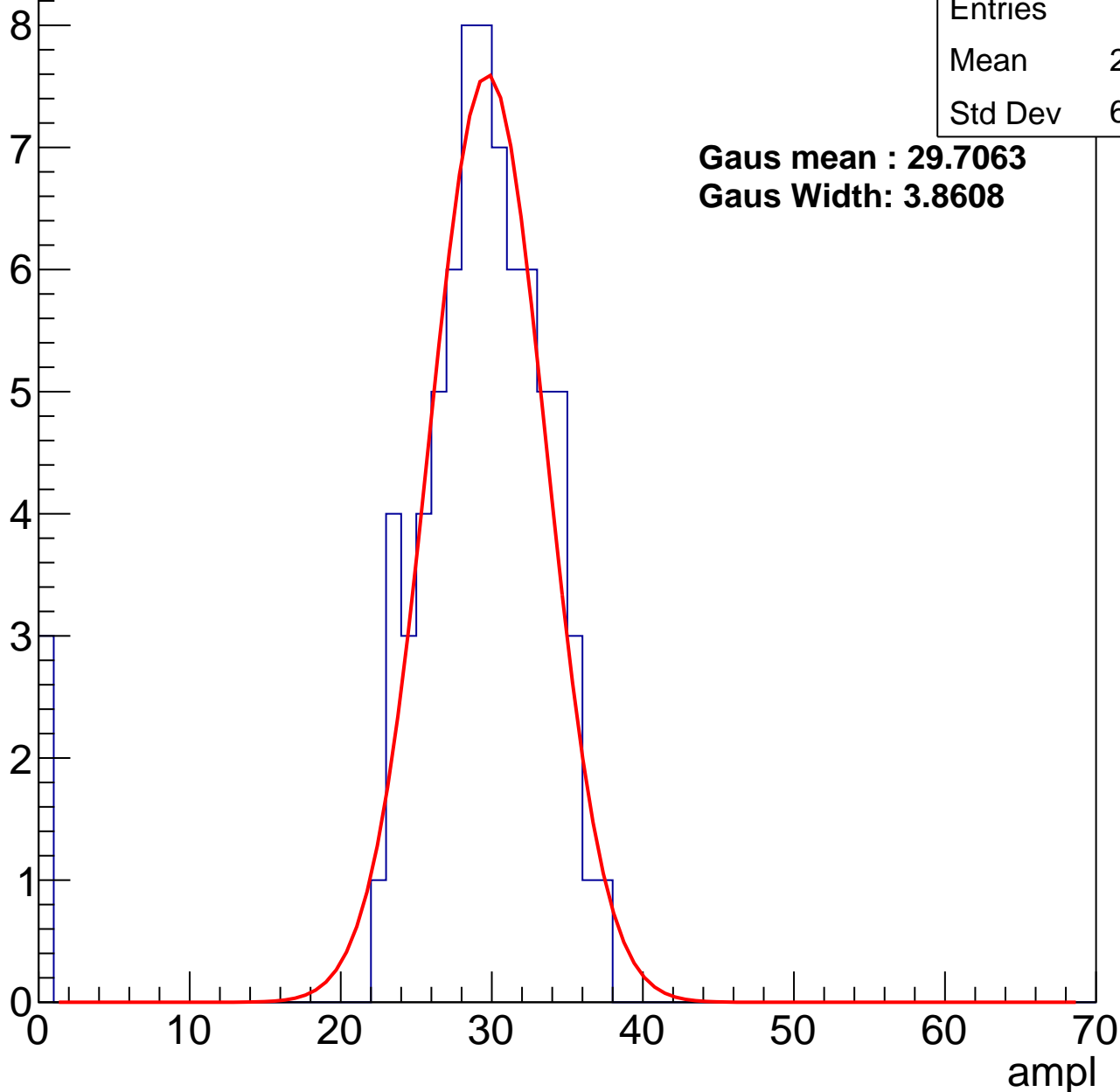
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.09
Std Dev	6.669

Gaus mean : 29.7063

Gaus Width: 3.8608



B1L101S, U1-ch12, adc1

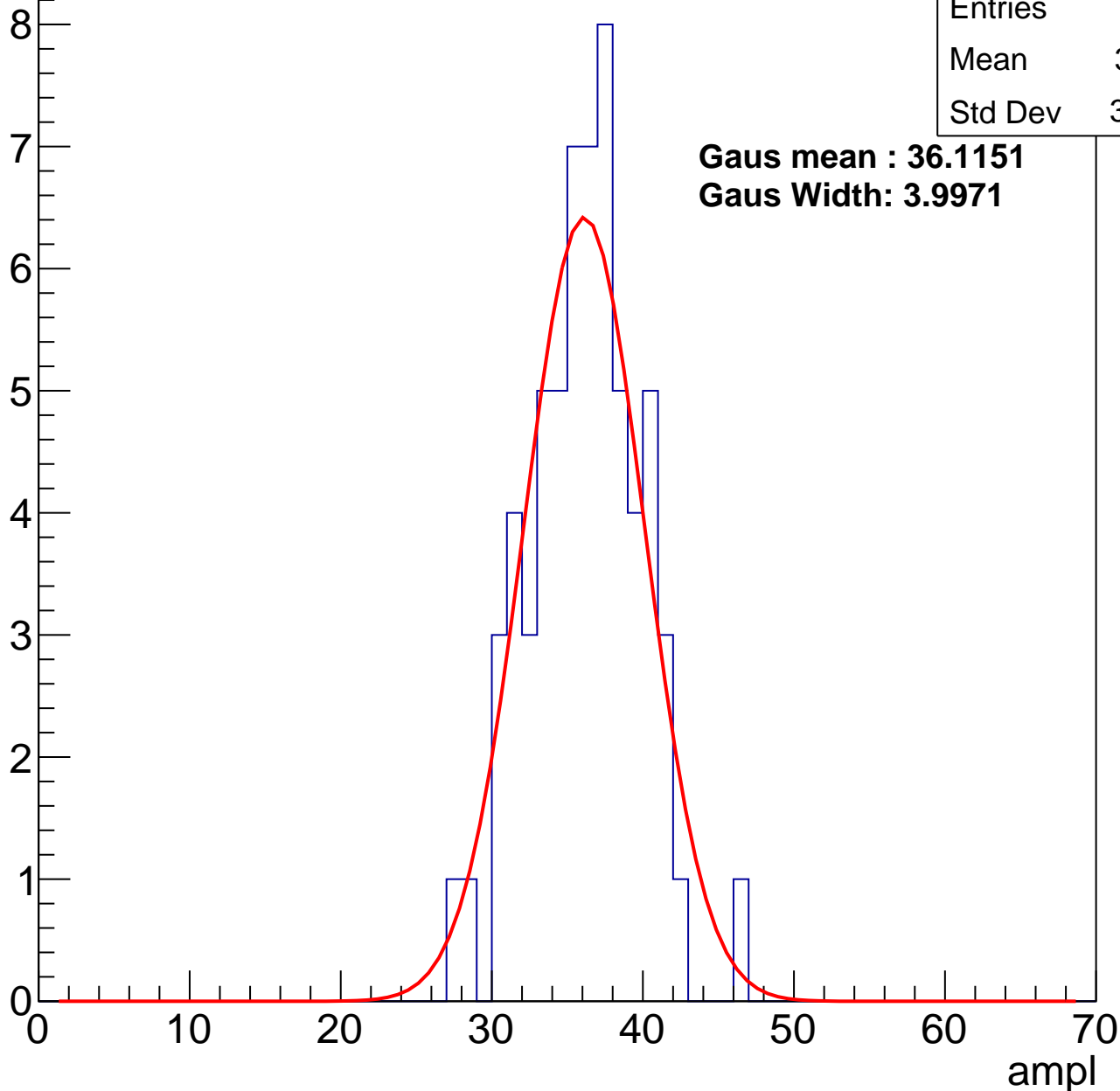
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.71
Std Dev	3.614

Gaus mean : 36.1151

Gaus Width: 3.9971



B1L101S, U1-ch12, adc2

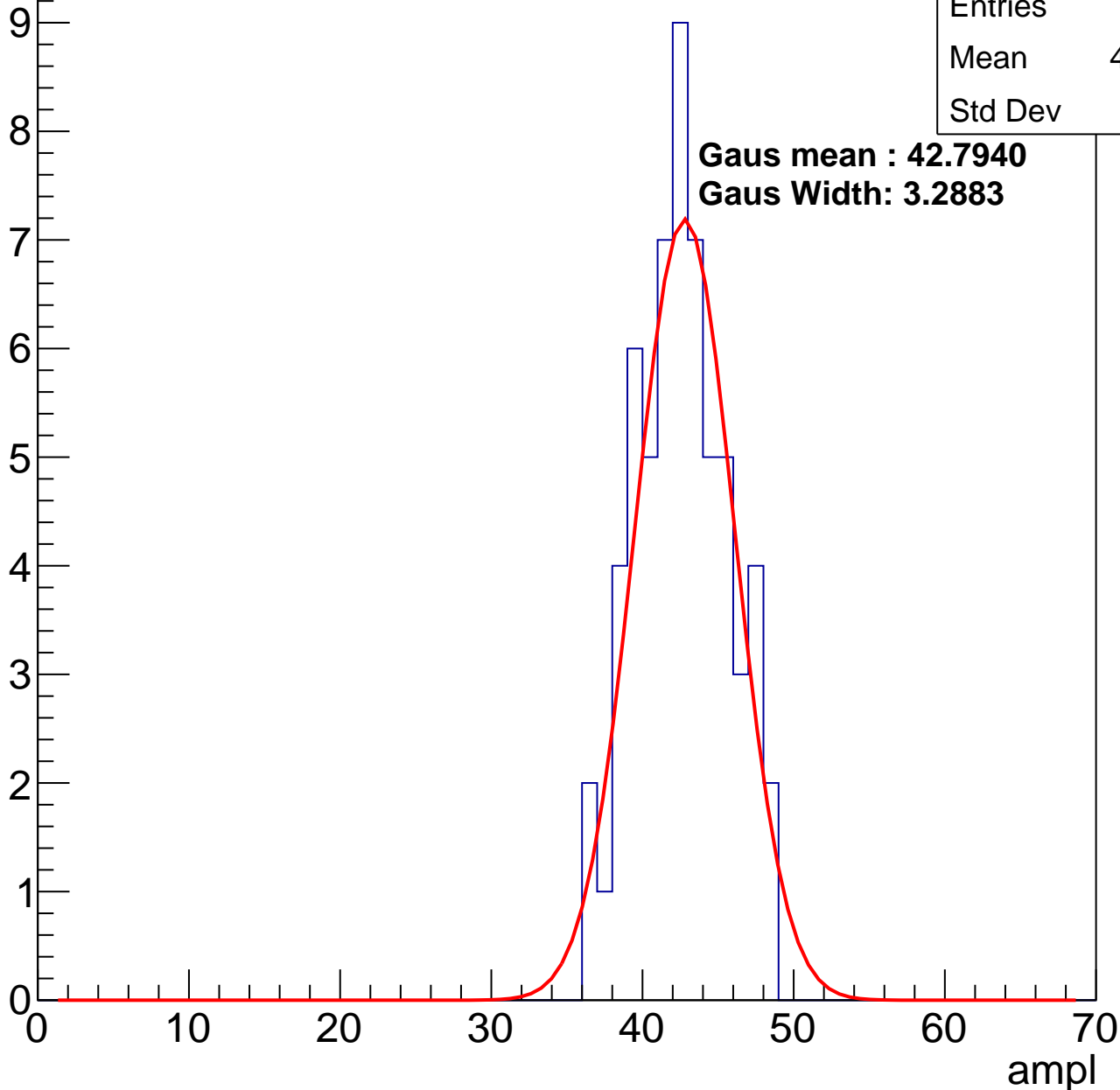
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.13
Std Dev	2.98

Gaus mean : 42.7940

Gaus Width: 3.2883



B1L101S, U1-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

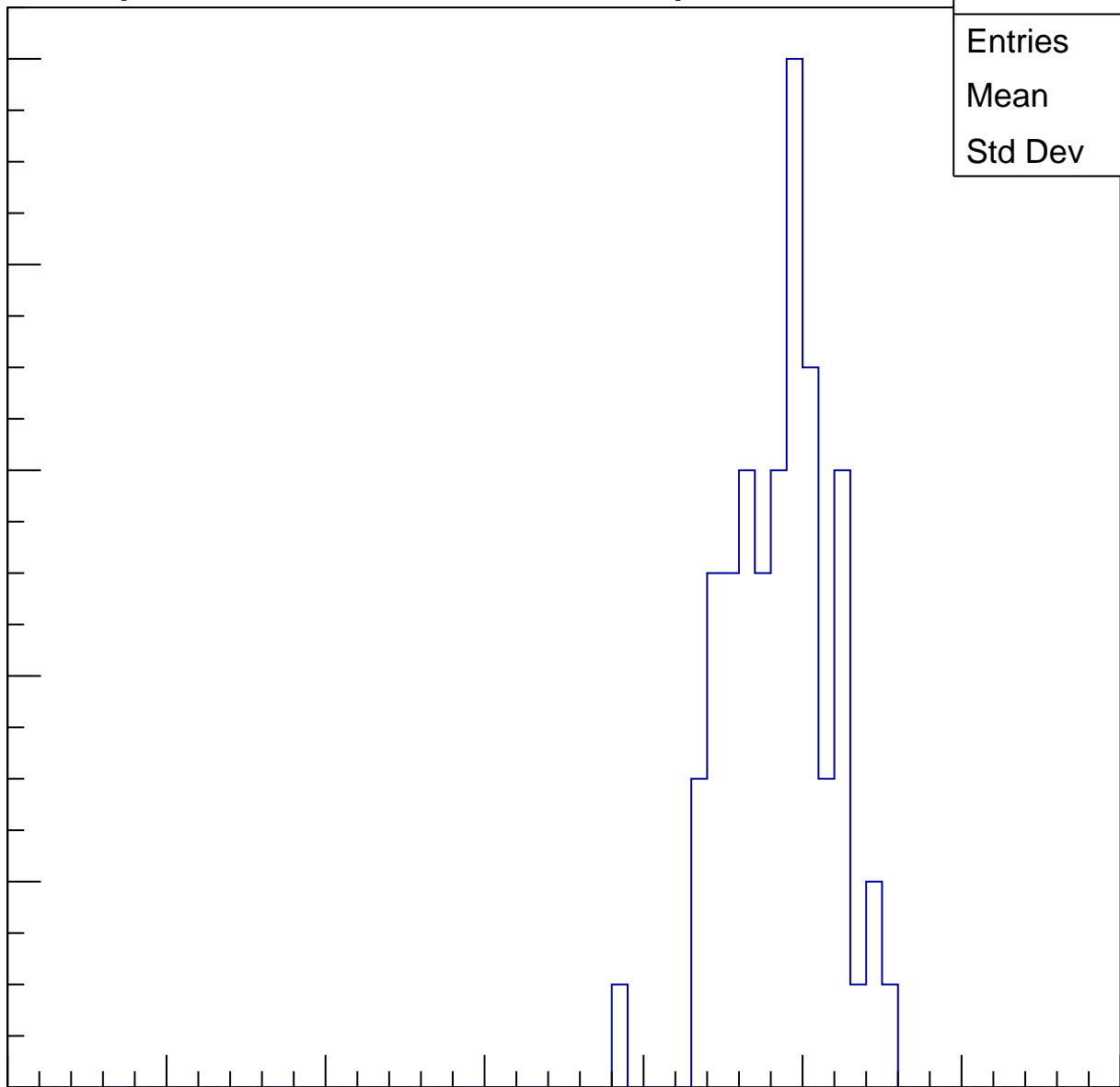
Entries	61
Mean	48.07
Std Dev	3.228

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

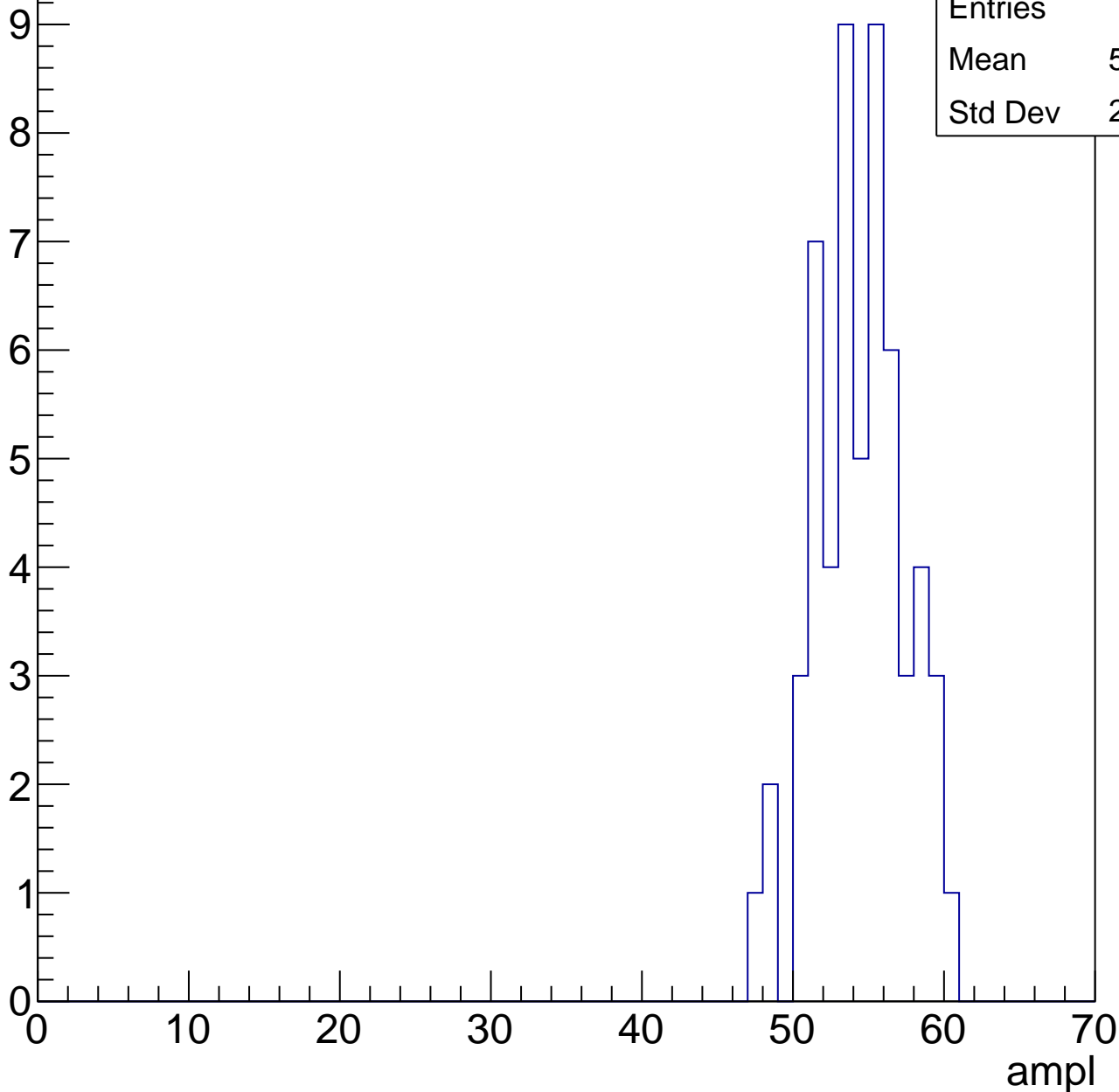


B1L101S, U1-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	53.96
Std Dev	2.938

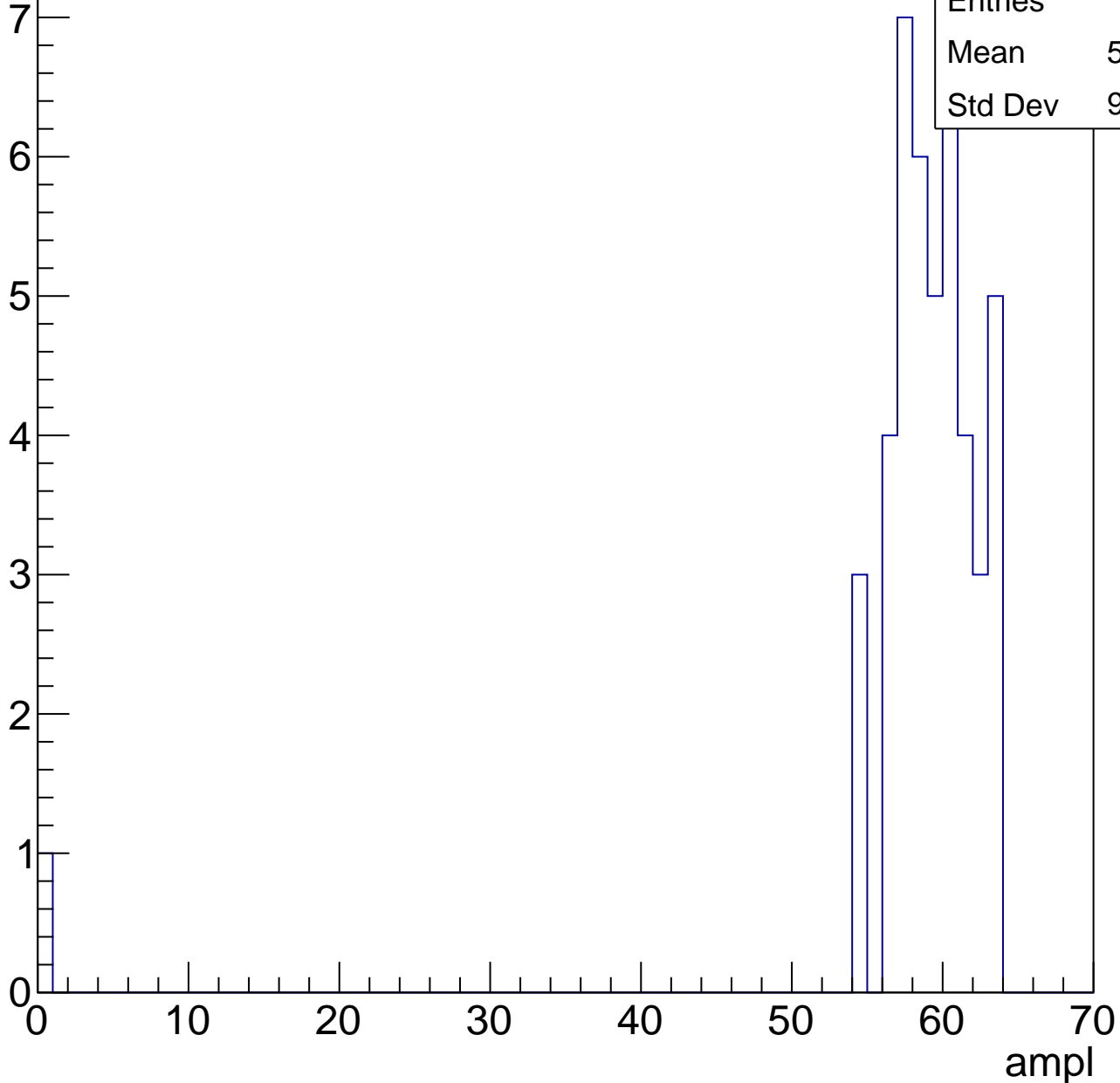


B1L101S, U1-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.62
Std Dev	9.032

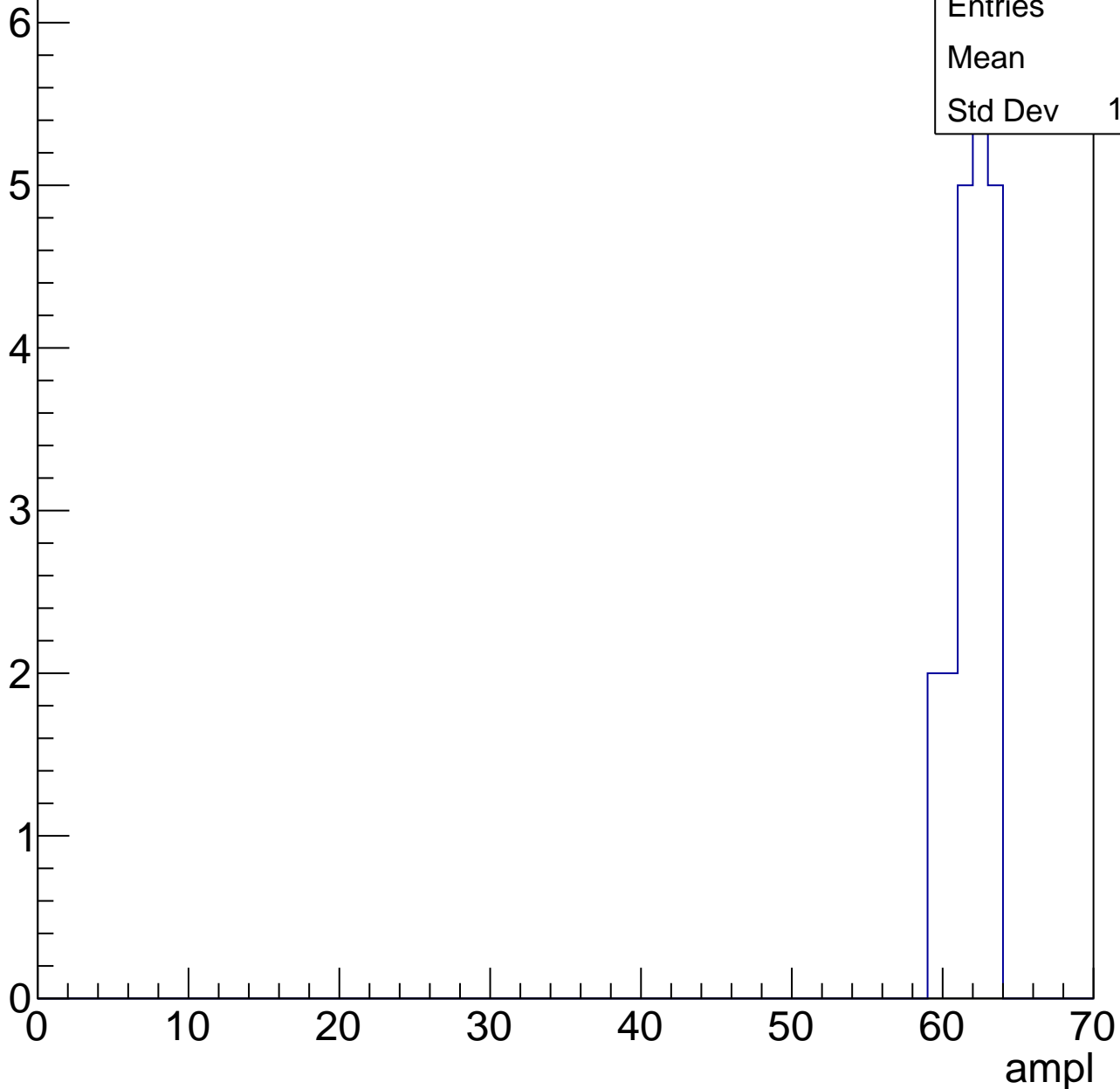


B1L101S, U1-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

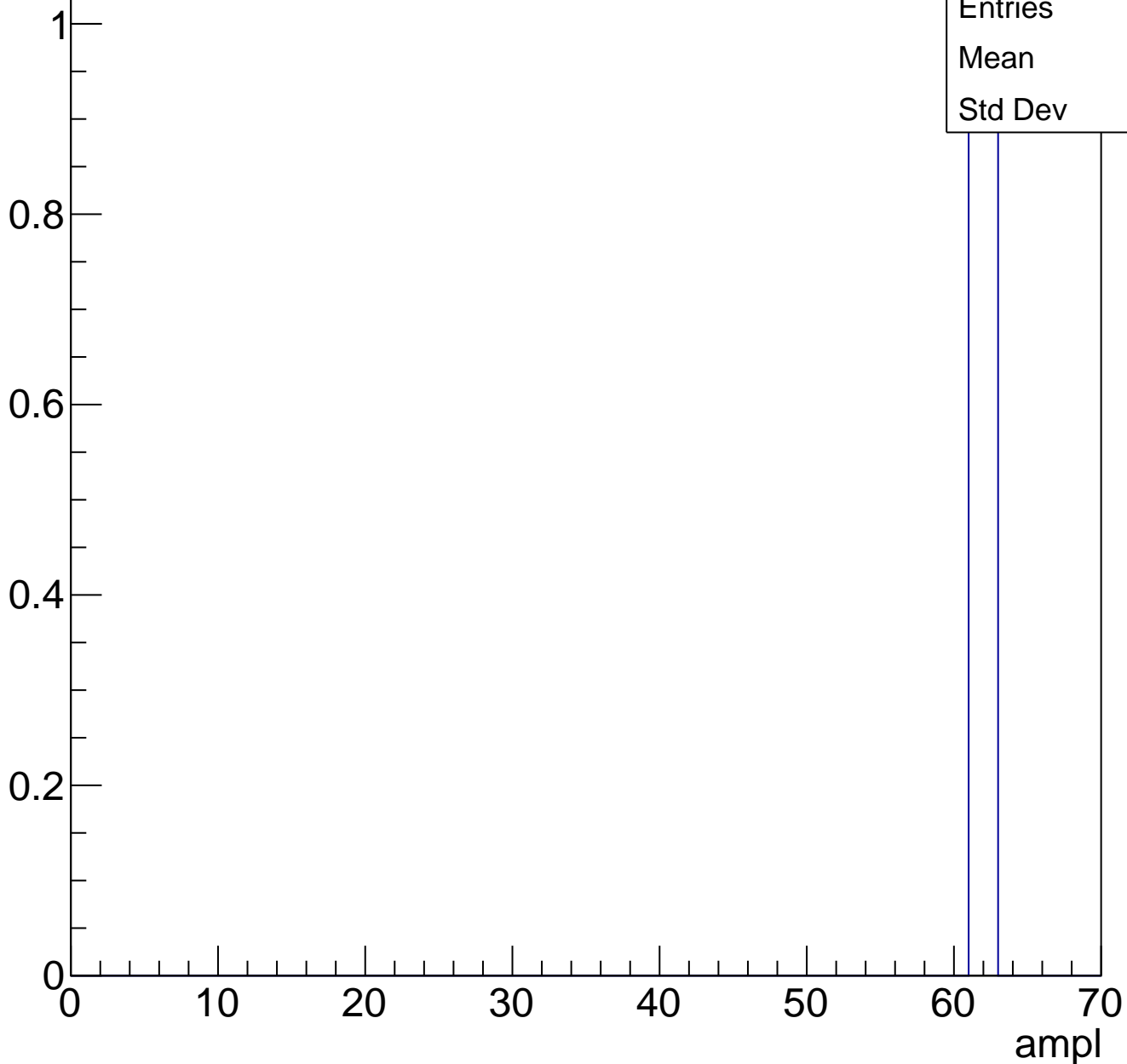
Entries	20
Mean	61.5
Std Dev	1.245



B1L101S, U1-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch13, adc0

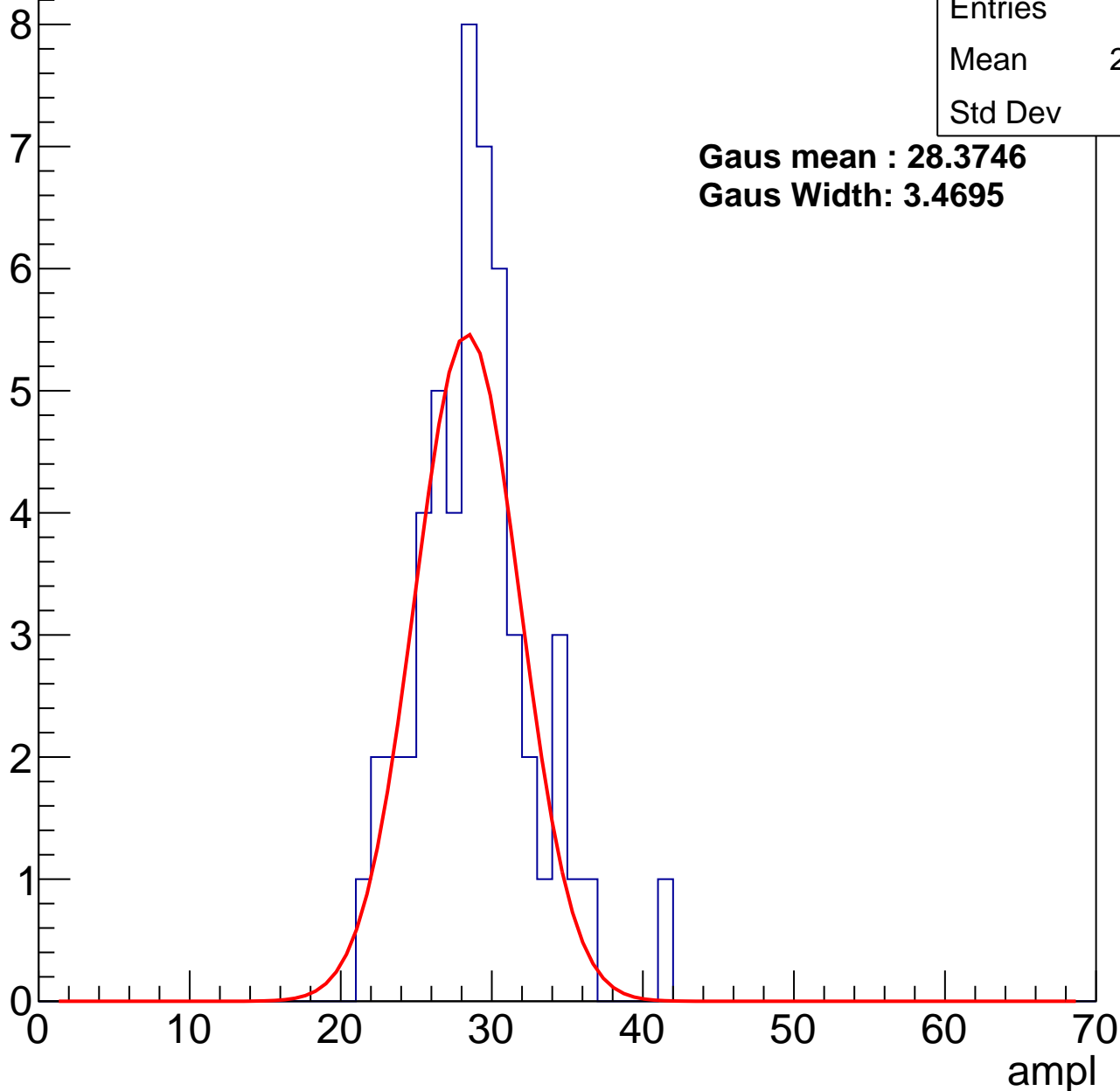
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	28.45
Std Dev	3.78

Gaus mean : 28.3746

Gaus Width: 3.4695



B1L101S, U1-ch13, adc1

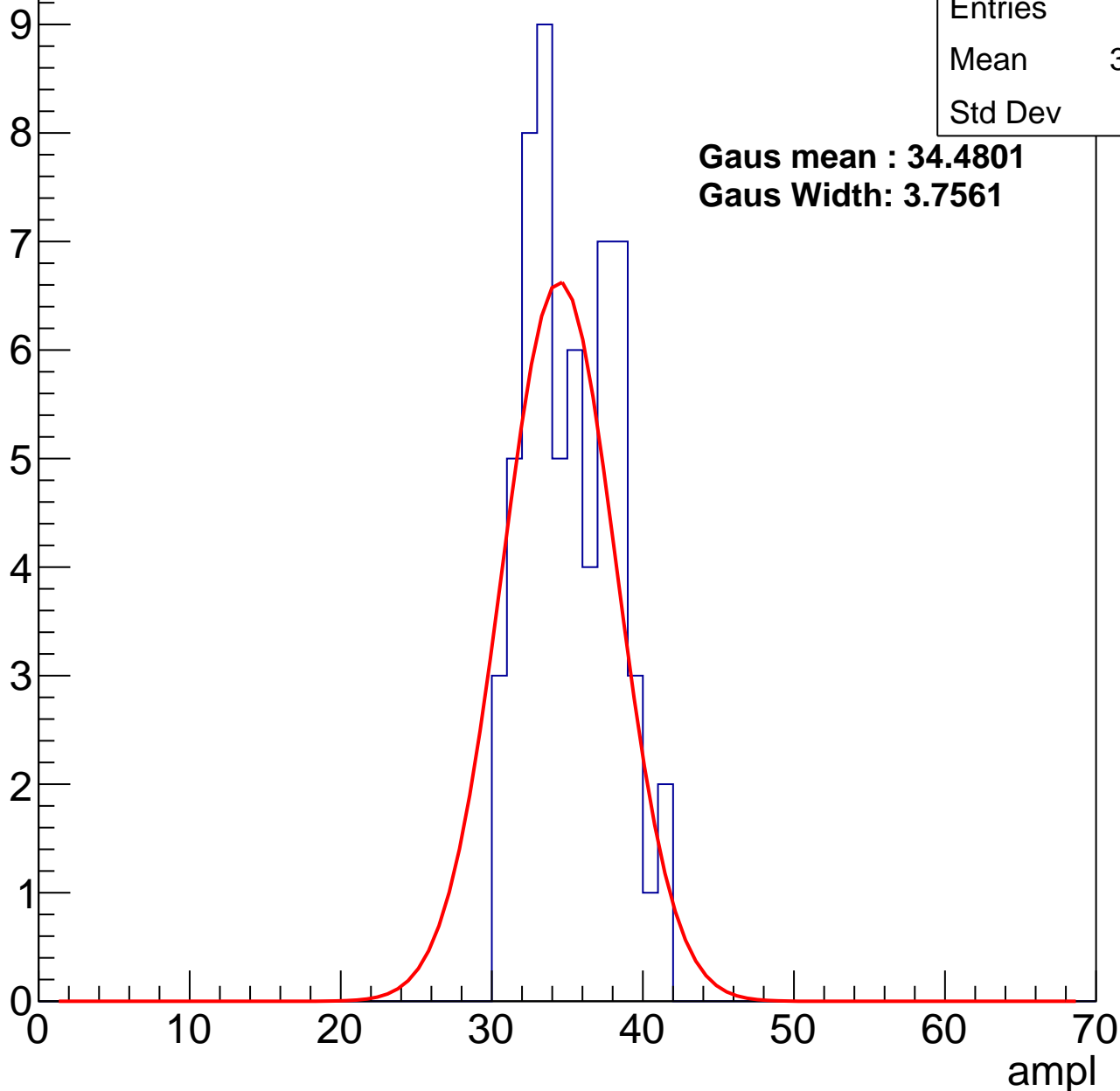
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	34.77
Std Dev	2.9

Gaus mean : 34.4801

Gaus Width: 3.7561



B1L101S, U1-ch13, adc2

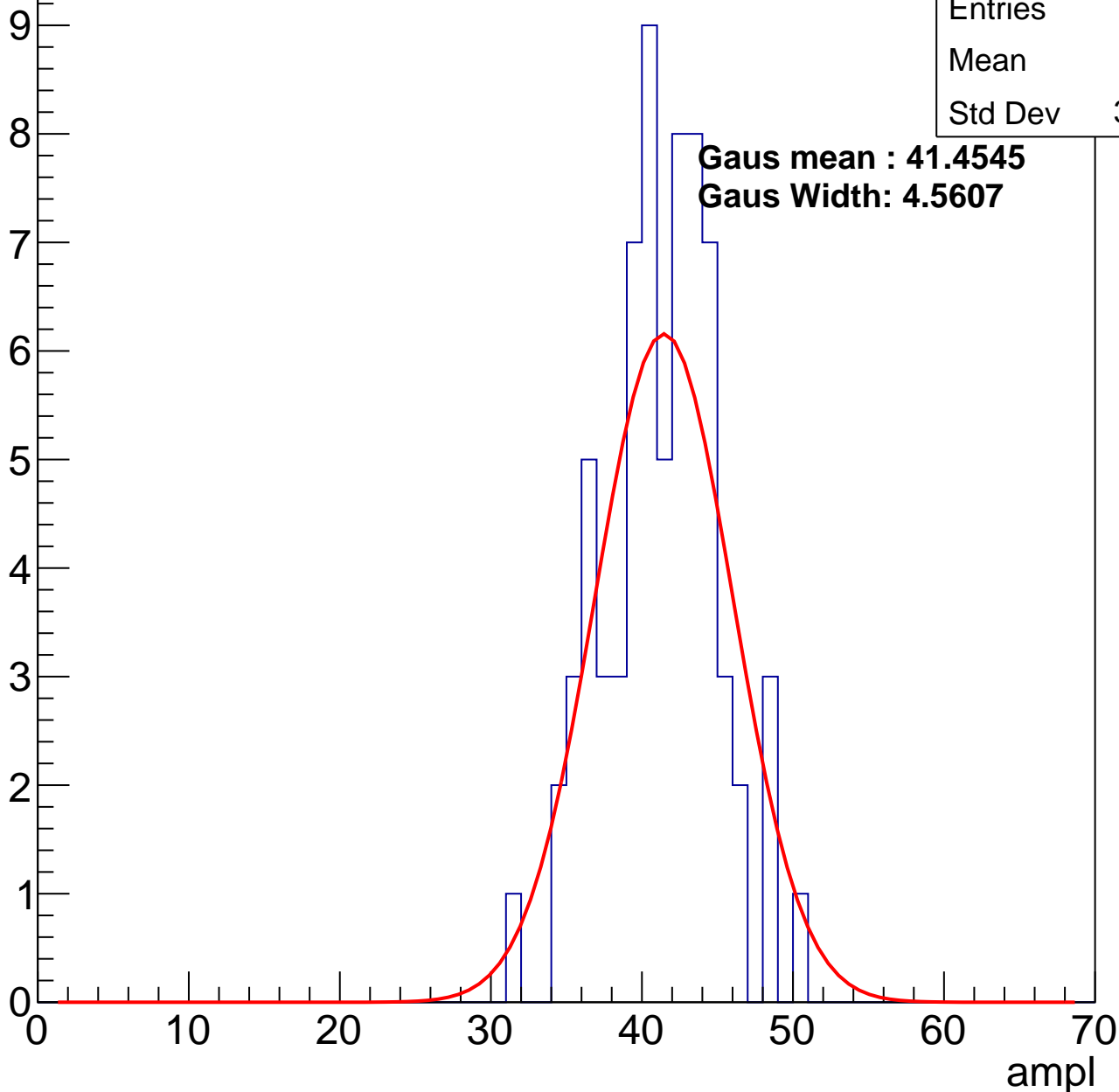
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	40.8
Std Dev	3.721

Gaus mean : 41.4545

Gaus Width: 4.5607

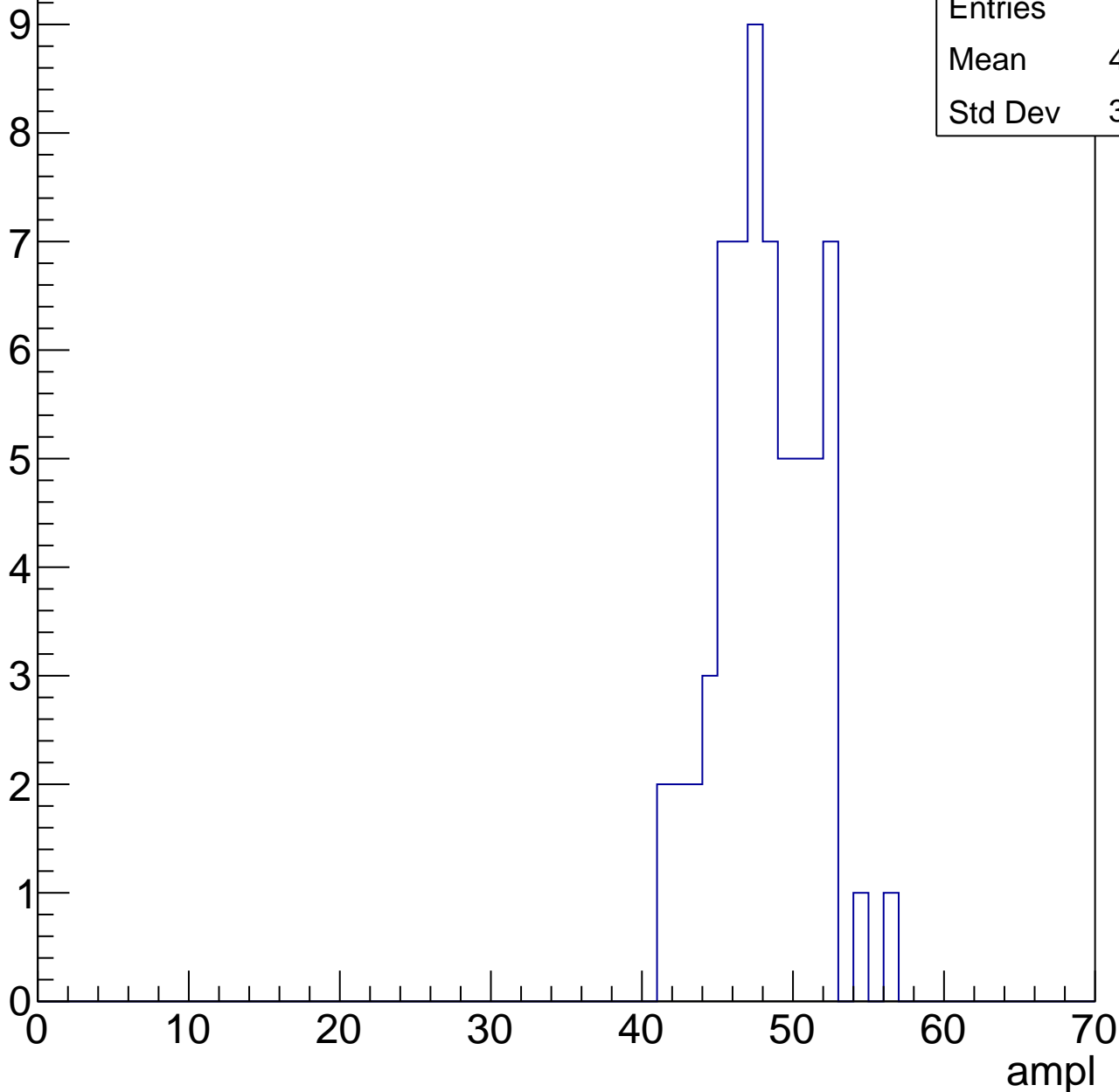


B1L101S, U1-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

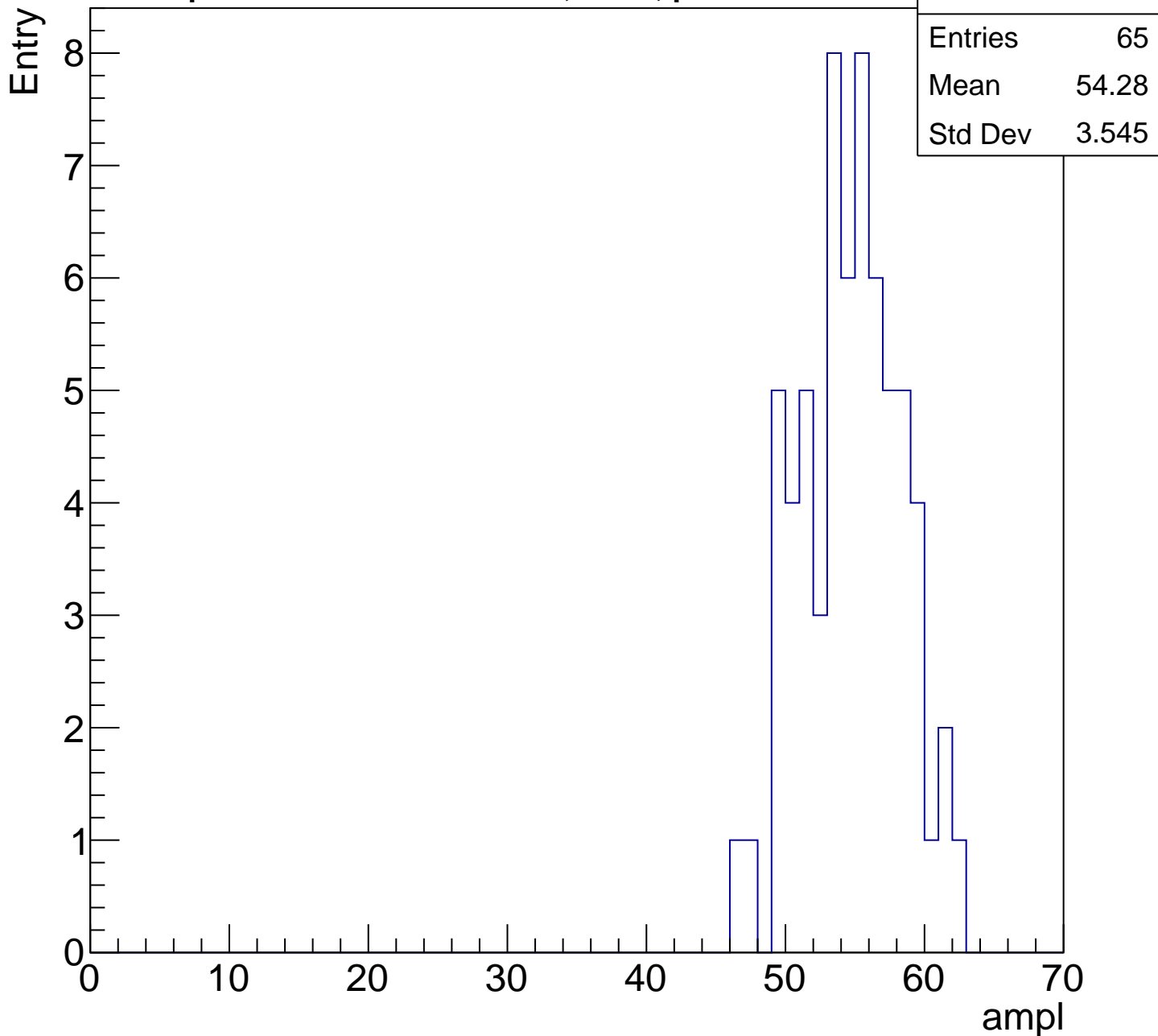
Entry

Entries	63
Mean	47.68
Std Dev	3.196



B1L101S, U1-ch13, adc4

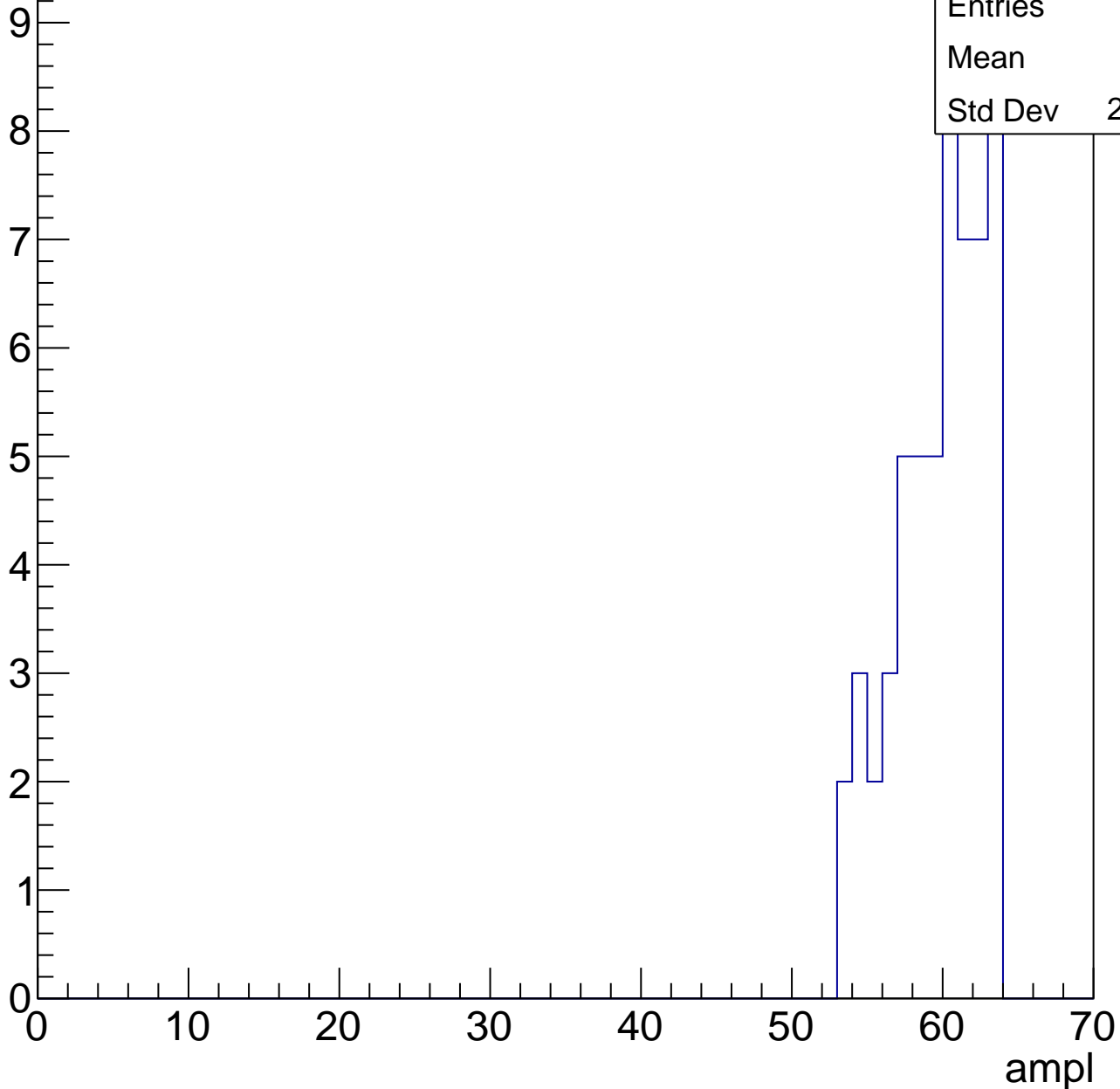
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

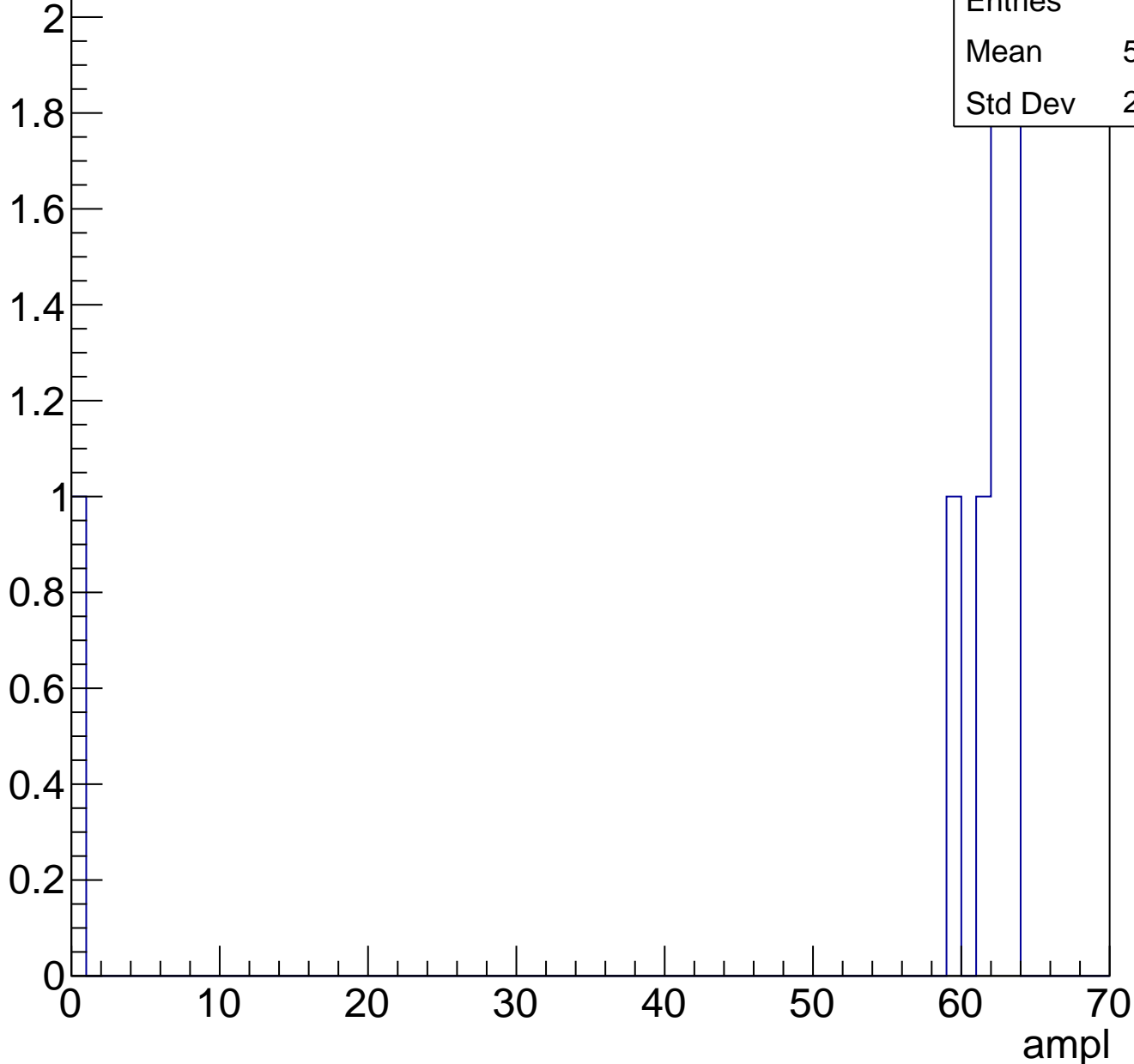
Entry



B1L101S, U1-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch14, adc0

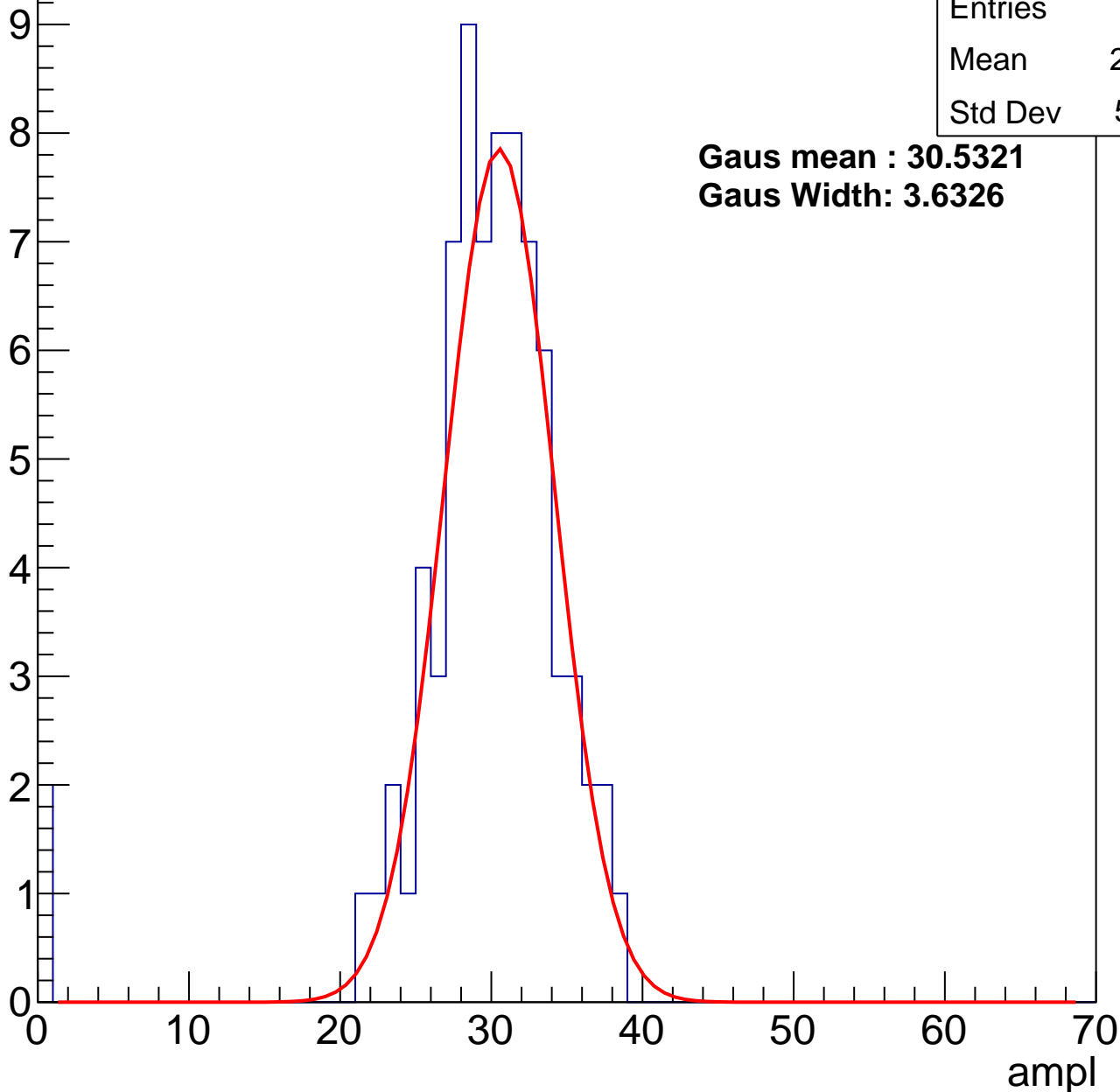
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.04
Std Dev	5.931

Gaus mean : 30.5321

Gaus Width: 3.6326



B1L101S, U1-ch14, adc1

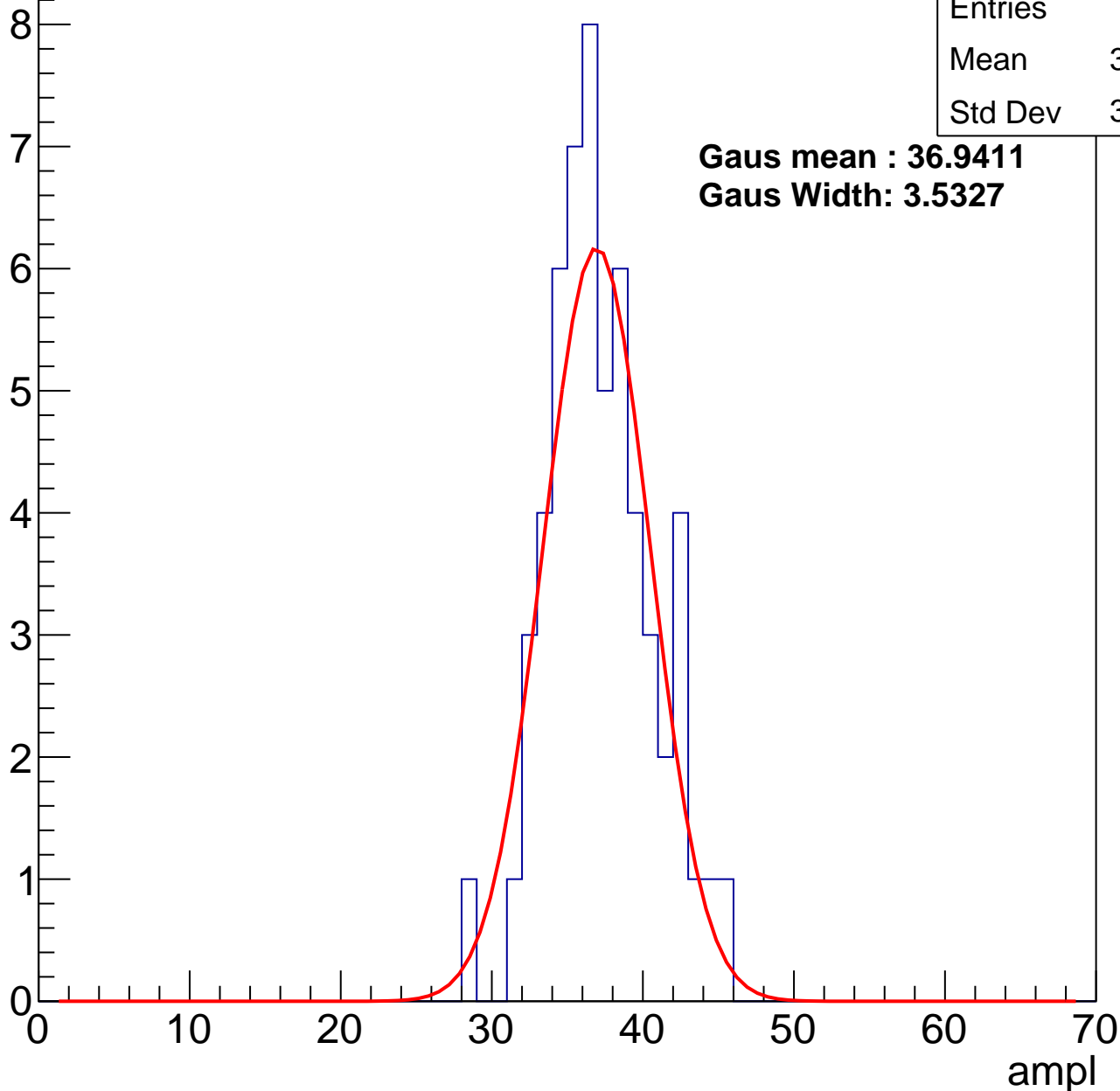
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.75
Std Dev	3.435

Gaus mean : 36.9411

Gaus Width: 3.5327



B1L101S, U1-ch14, adc2

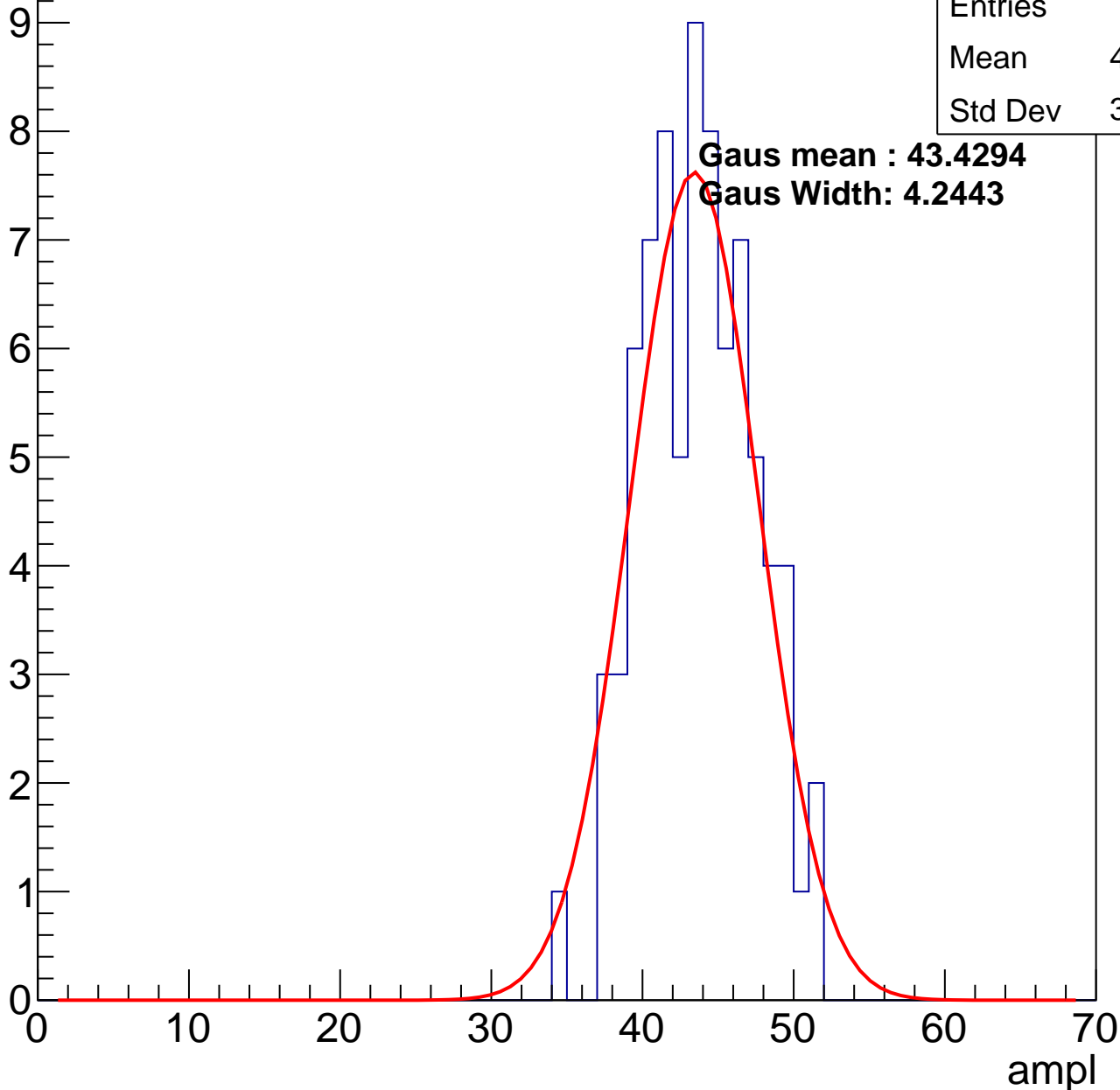
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	43.25
Std Dev	3.647

Gaus mean : 43.4294

Gaus Width: 4.2443

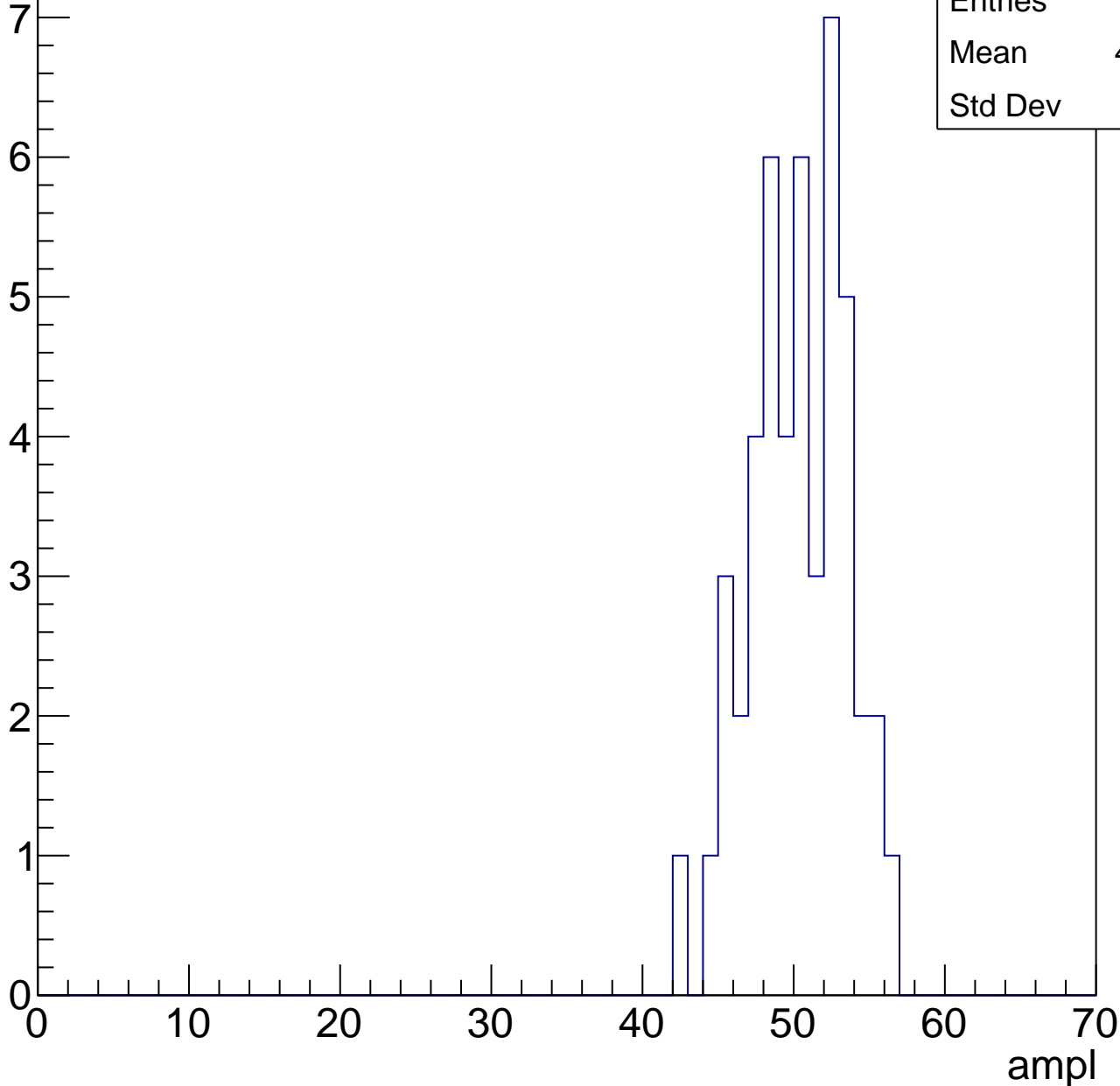


B1L101S, U1-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	49.81
Std Dev	3.14

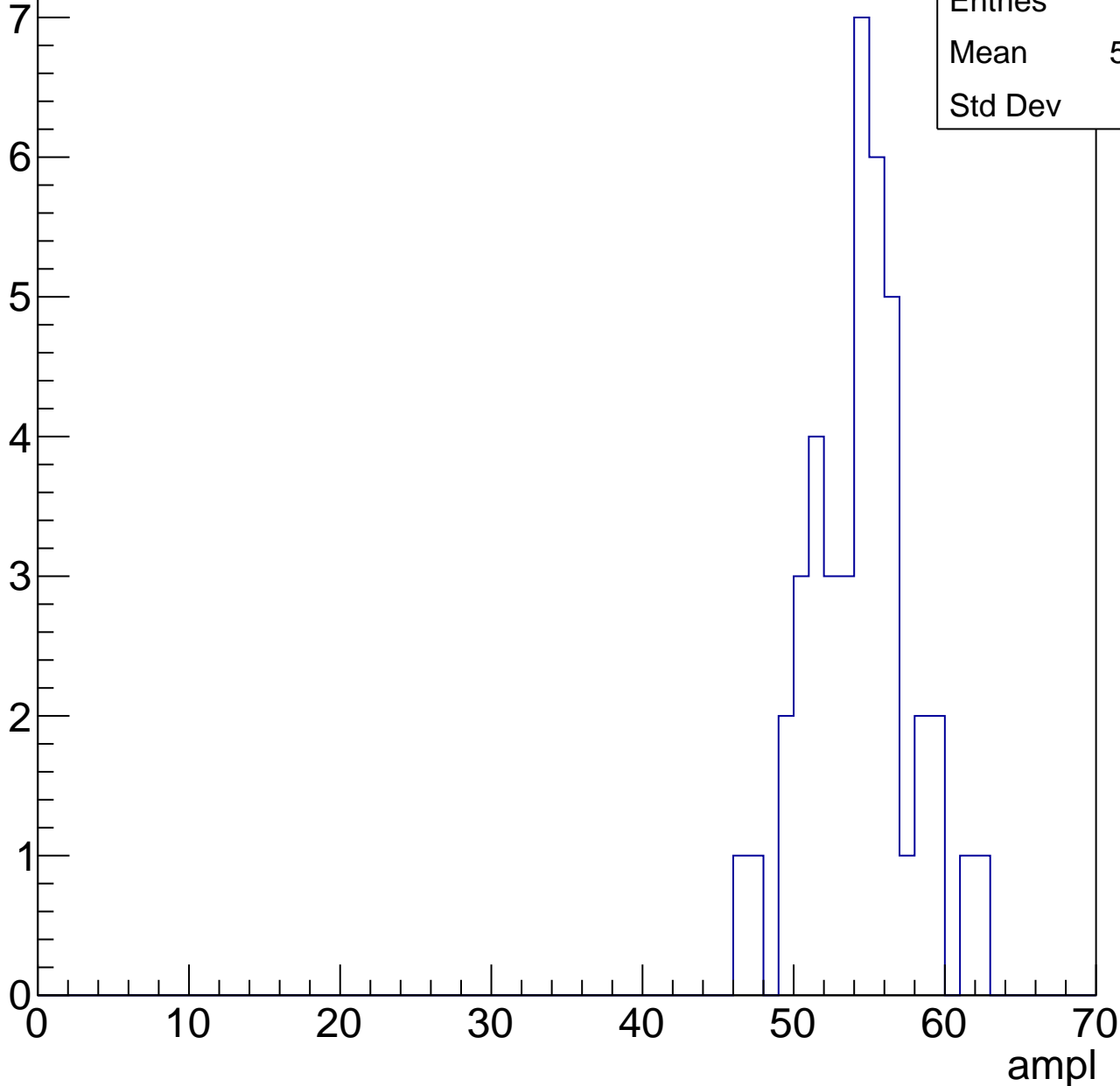


B1L101S, U1-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

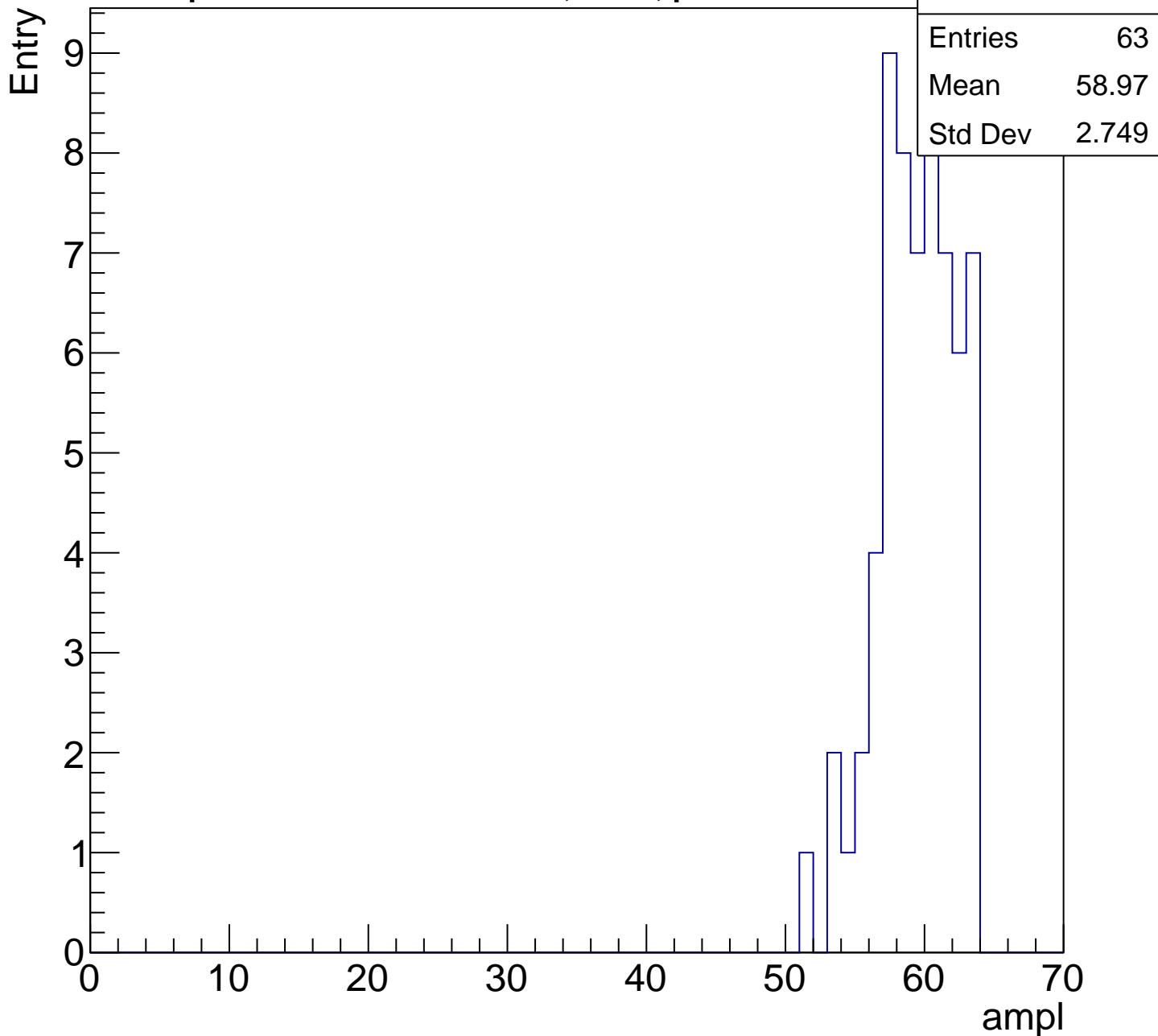
Entry

Entries	42
Mean	53.86
Std Dev	3.42



B1L101S, U1-ch14, adc5

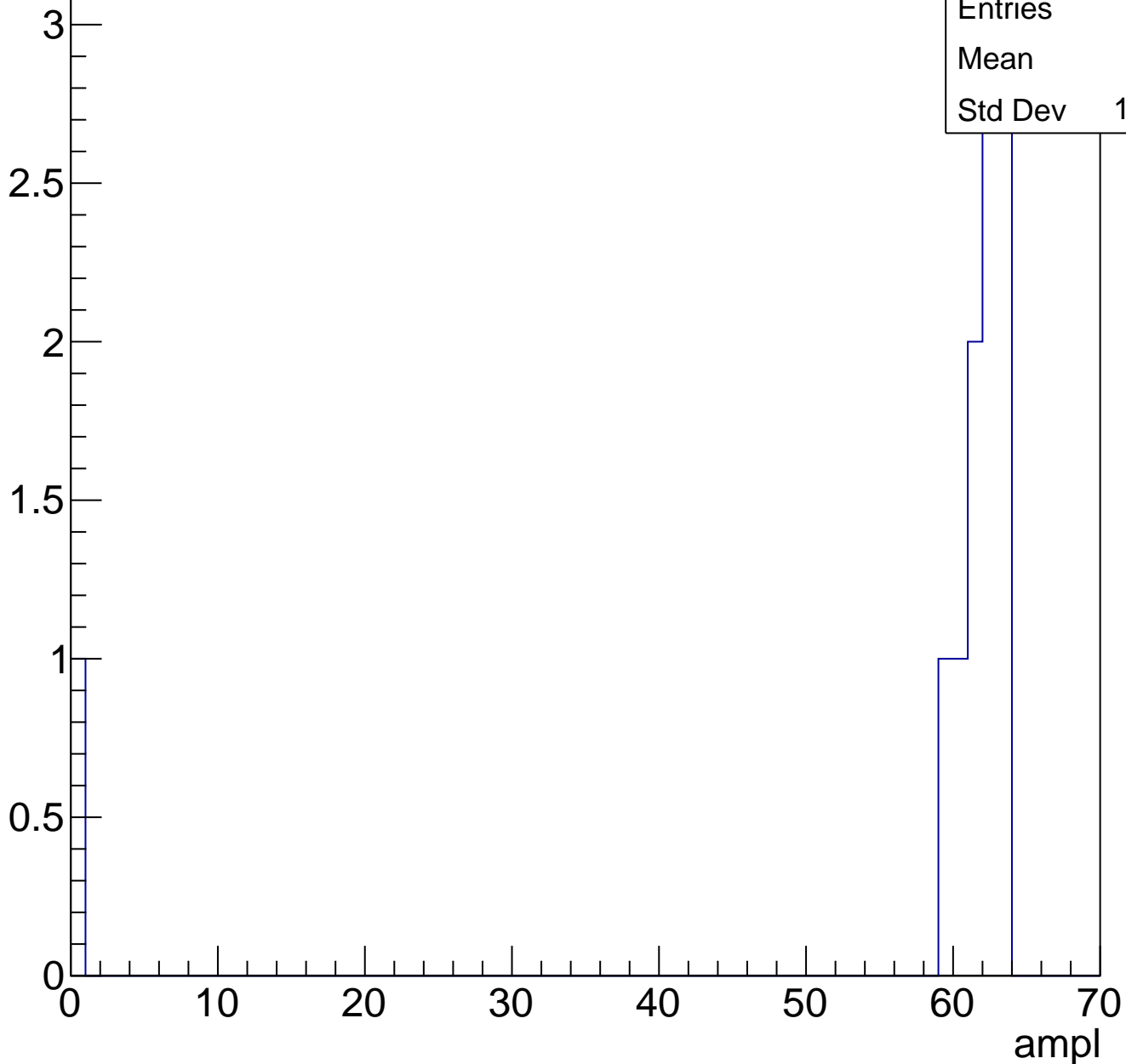
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch15, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	28.7
Std Dev	5.836

Gaus mean : 29.8785

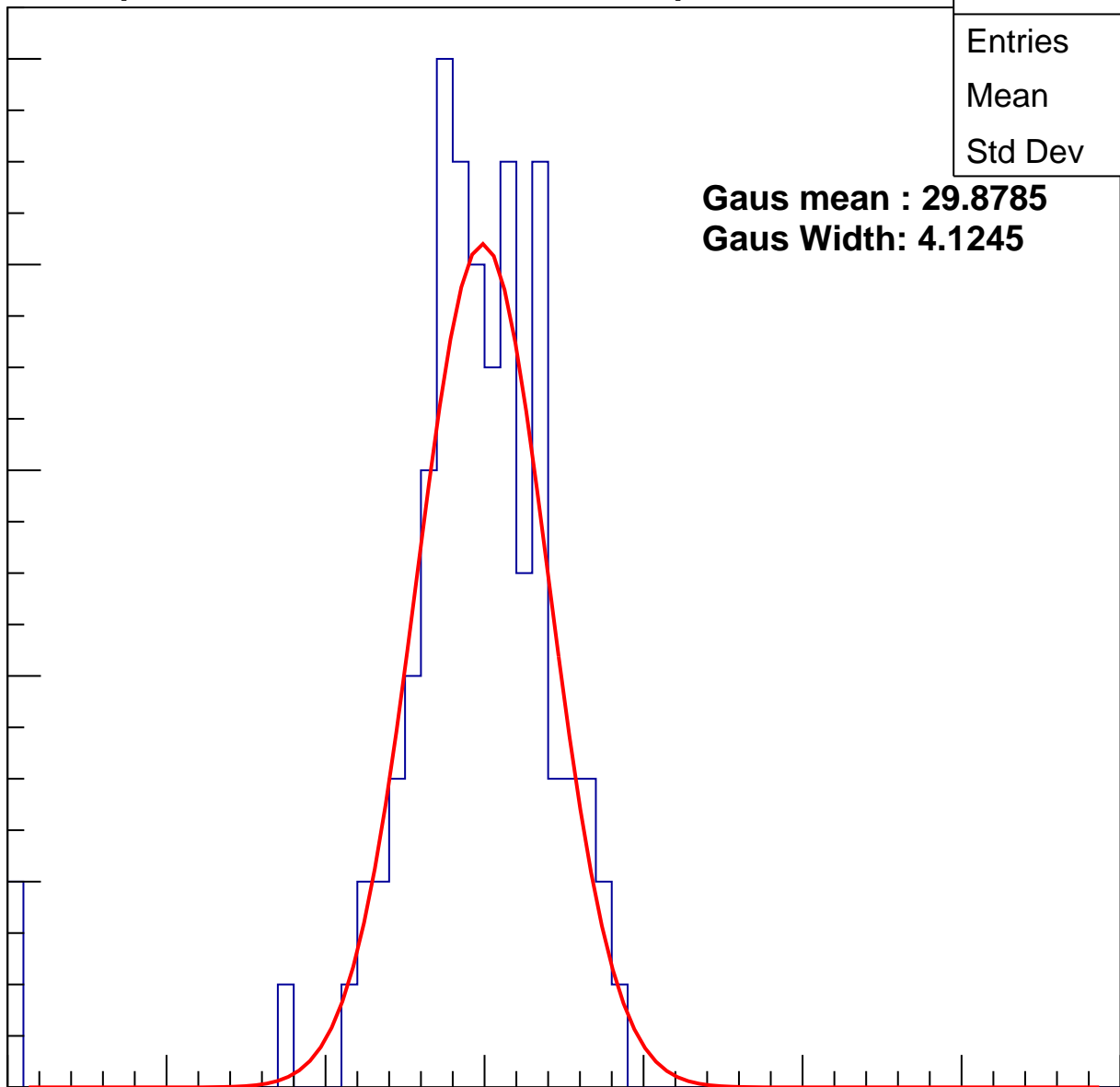
Gaus Width: 4.1245

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch15, adc1

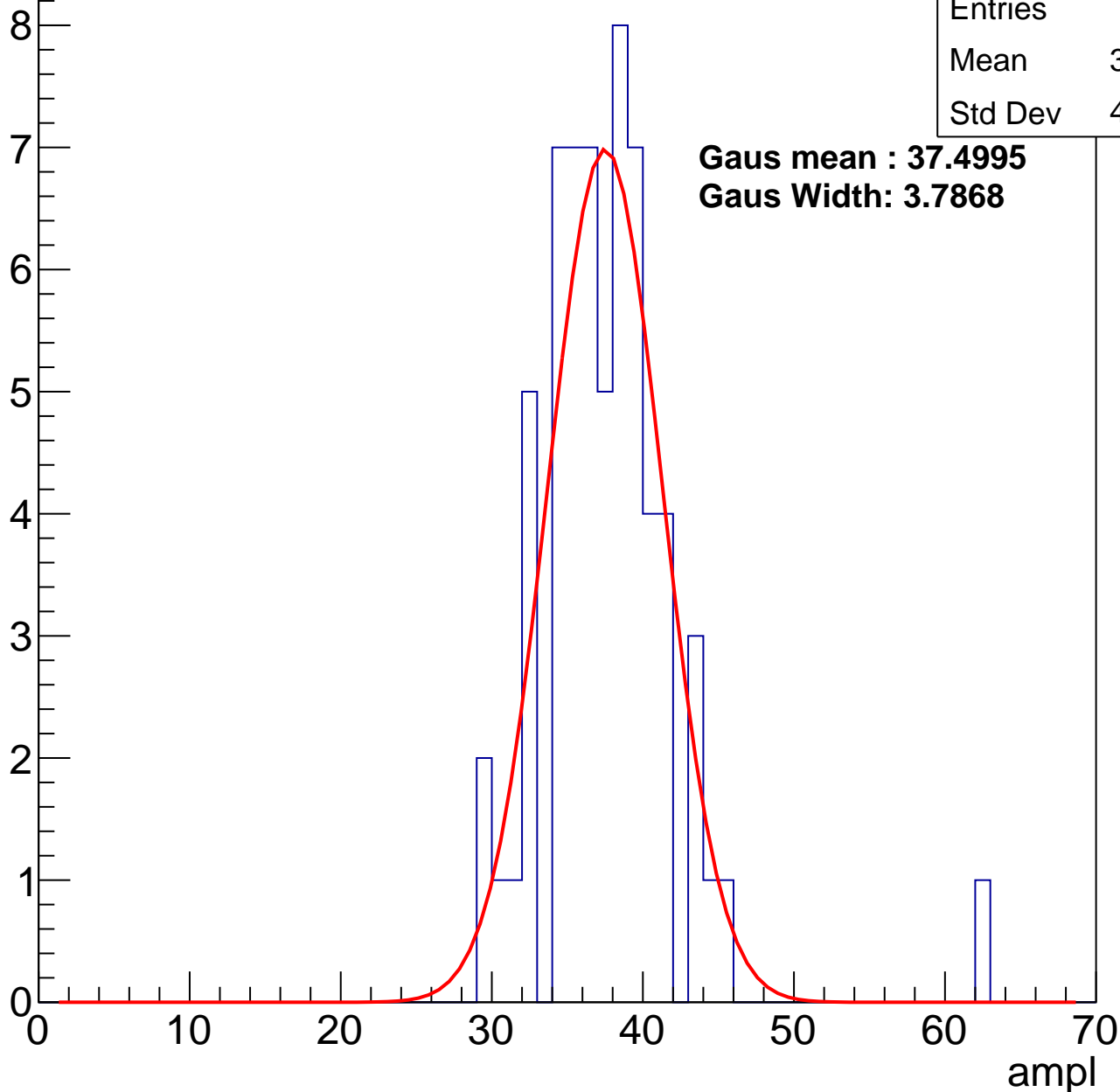
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.19
Std Dev	4.703

Gaus mean : 37.4995

Gaus Width: 3.7868



B1L101S, U1-ch15, adc2

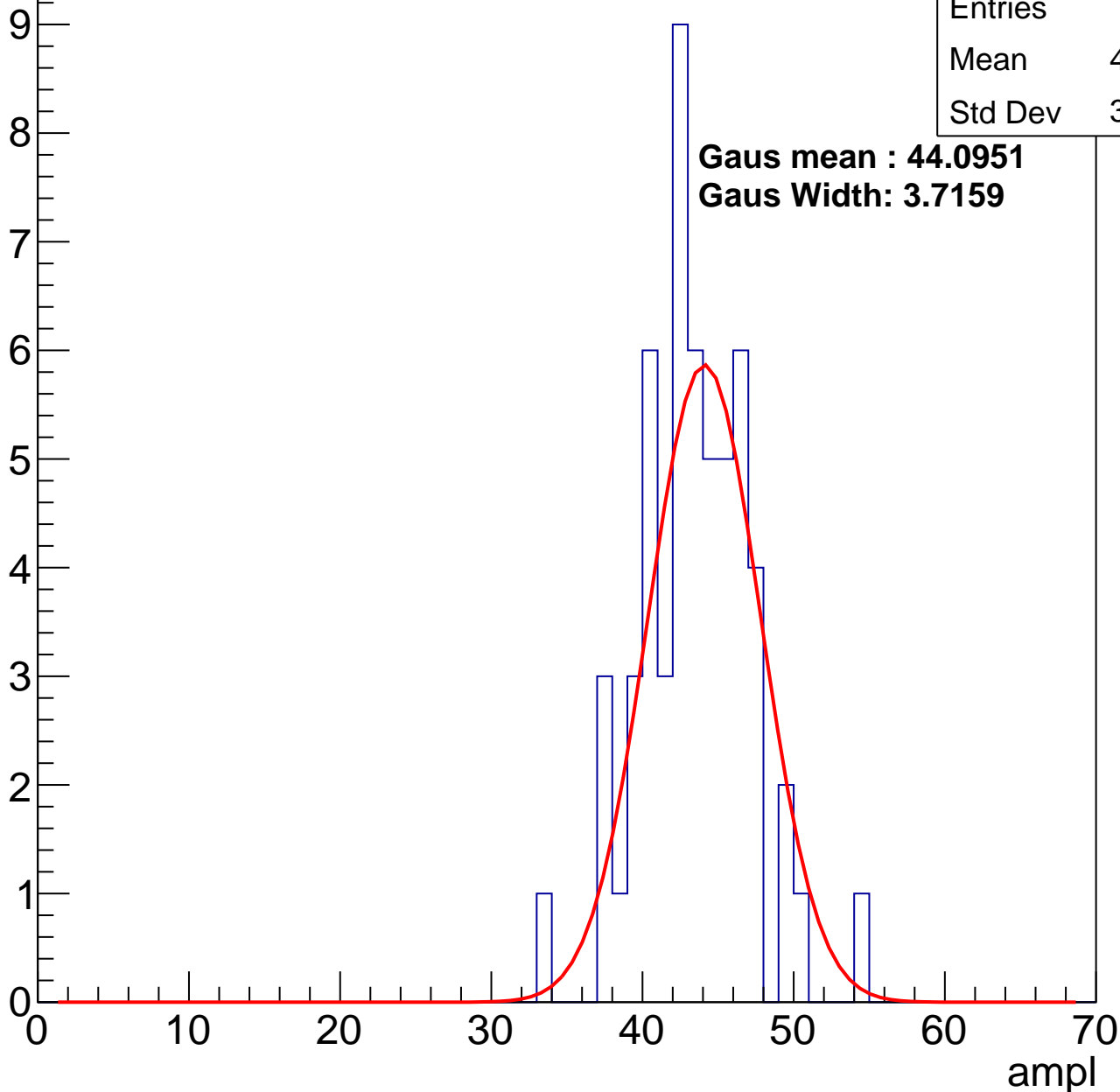
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.02
Std Dev	3.638

Gaus mean : 44.0951

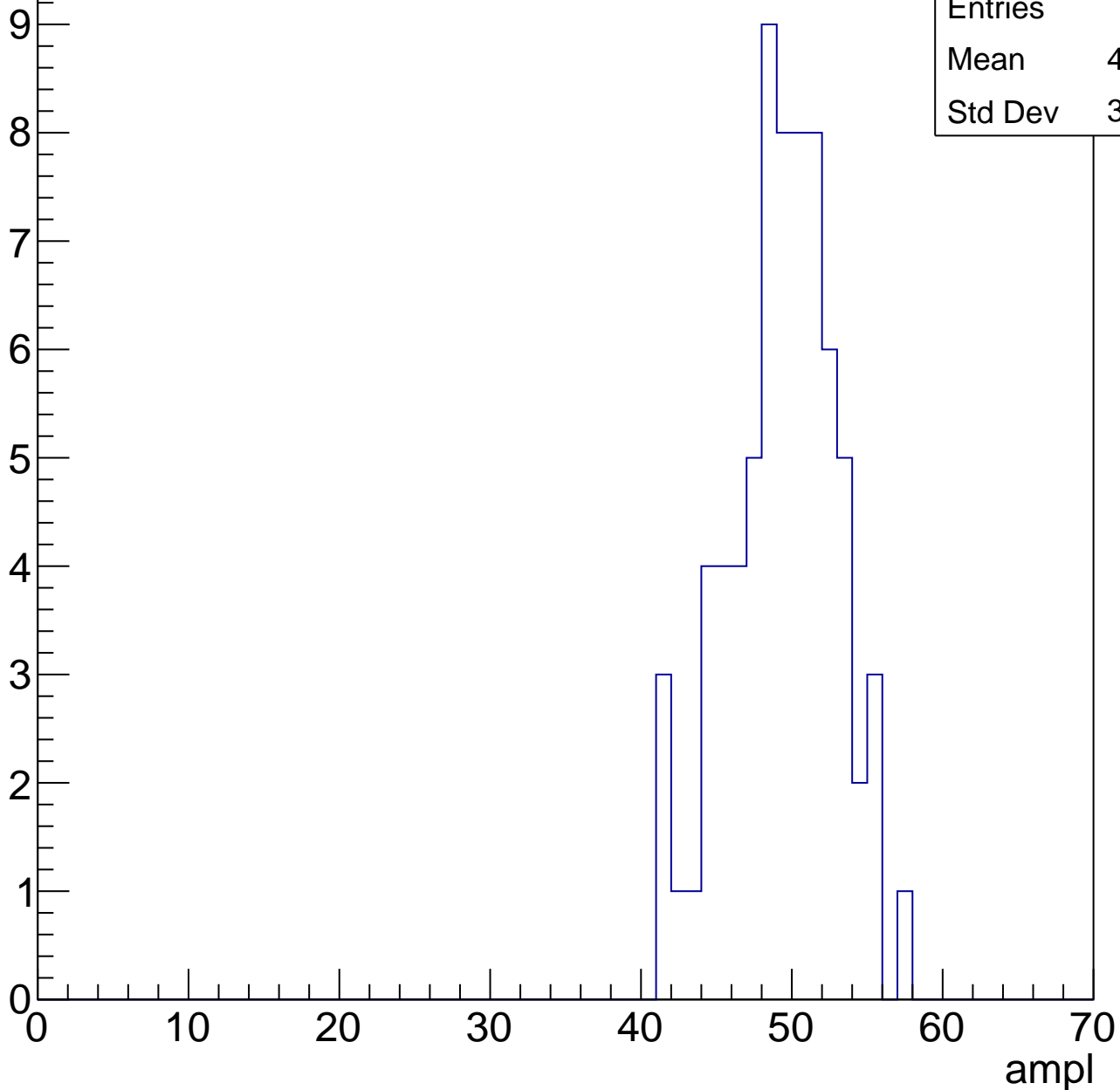
Gaus Width: 3.7159



B1L101S, U1-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

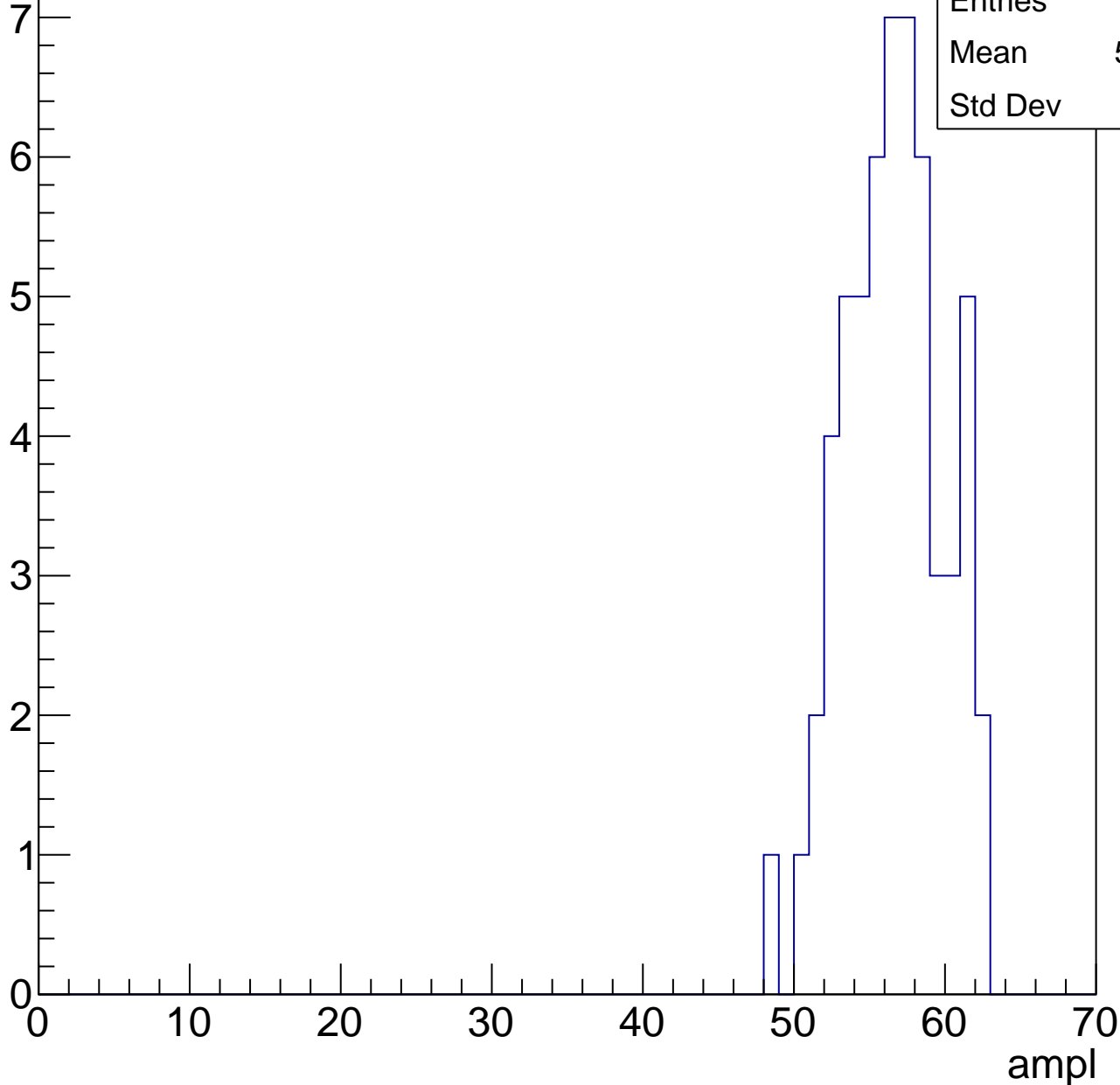
Entry



B1L101S, U1-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

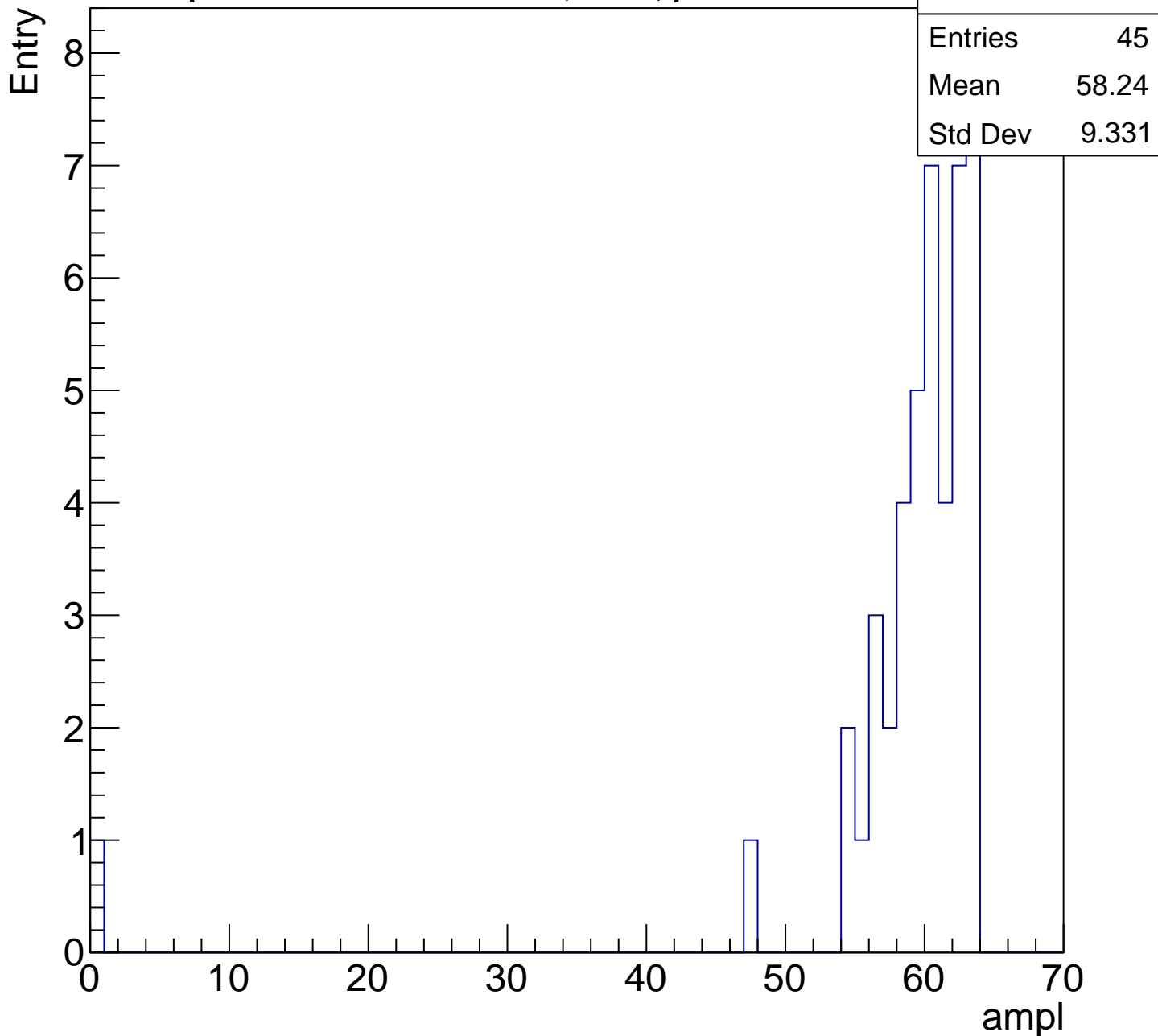
Entry



Entries	57
Mean	56.11
Std Dev	3.21

B1L101S, U1-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.675

ampl

0 10 20 30 40 50 60 70

B1L101S, U1-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch16, adc0

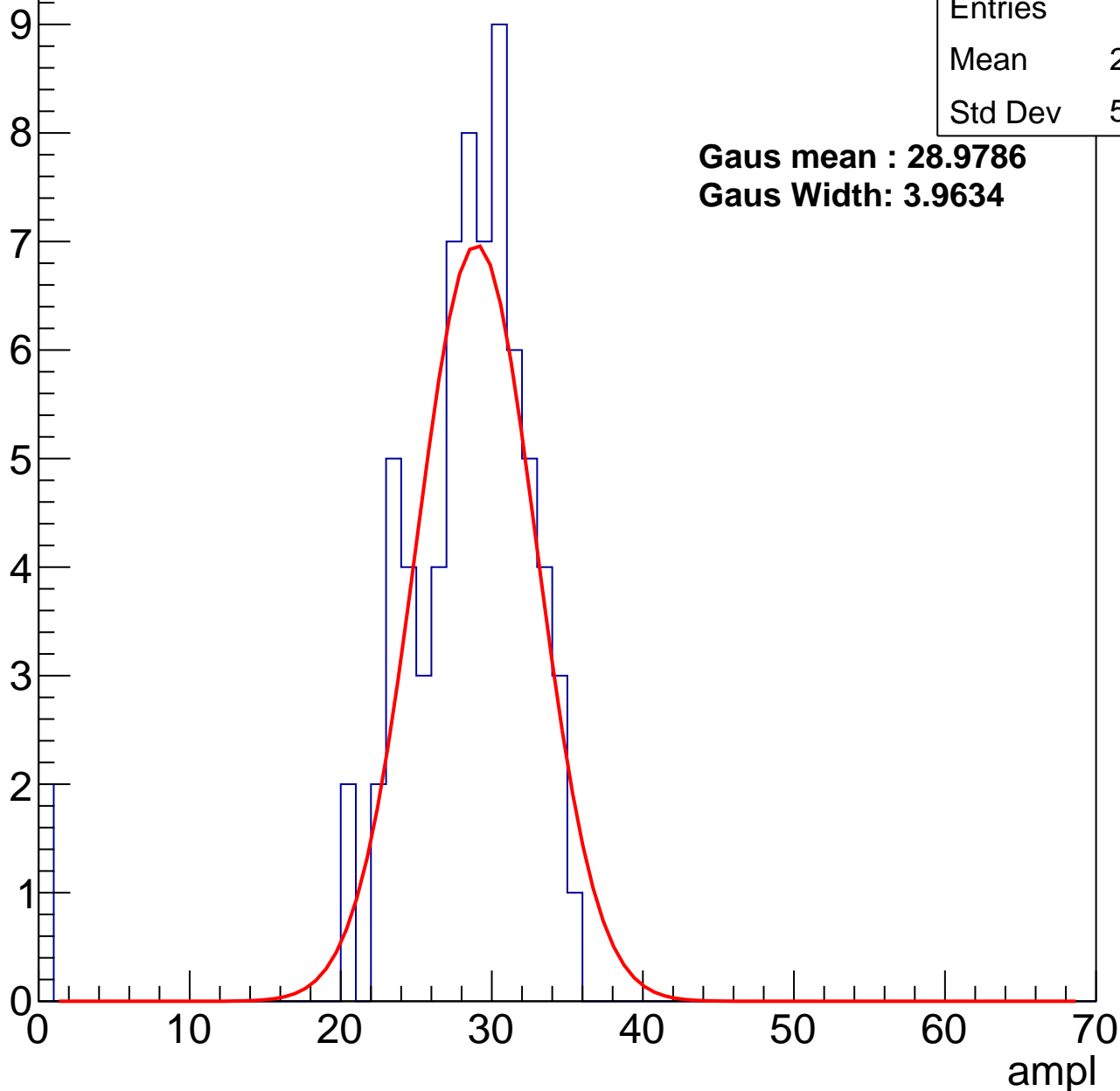
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.43
Std Dev	5.795

Gaus mean : 28.9786

Gaus Width: 3.9634



B1L101S, U1-ch16, adc1

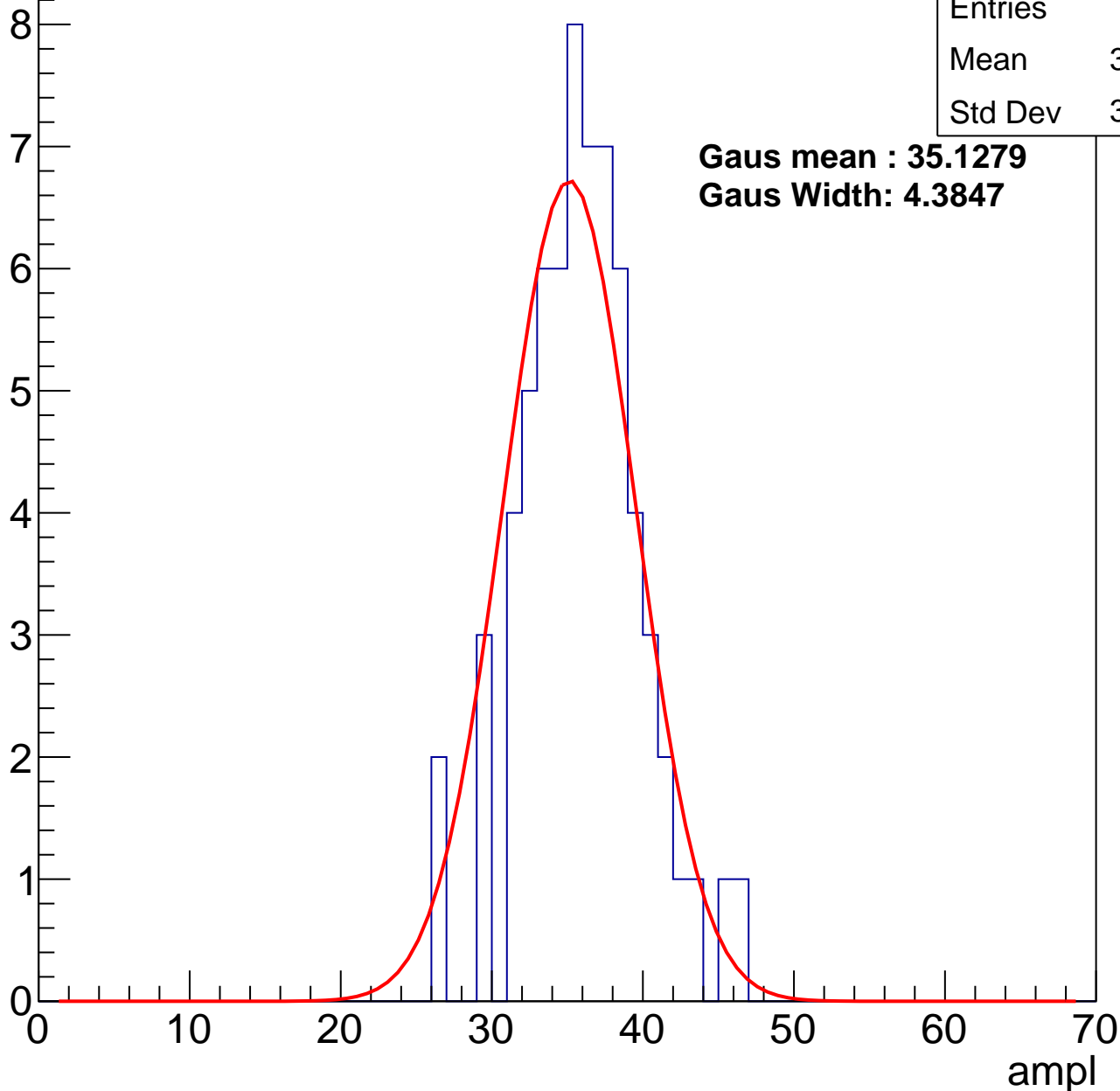
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.49
Std Dev	3.915

Gaus mean : 35.1279

Gaus Width: 4.3847



B1L101S, U1-ch16, adc2

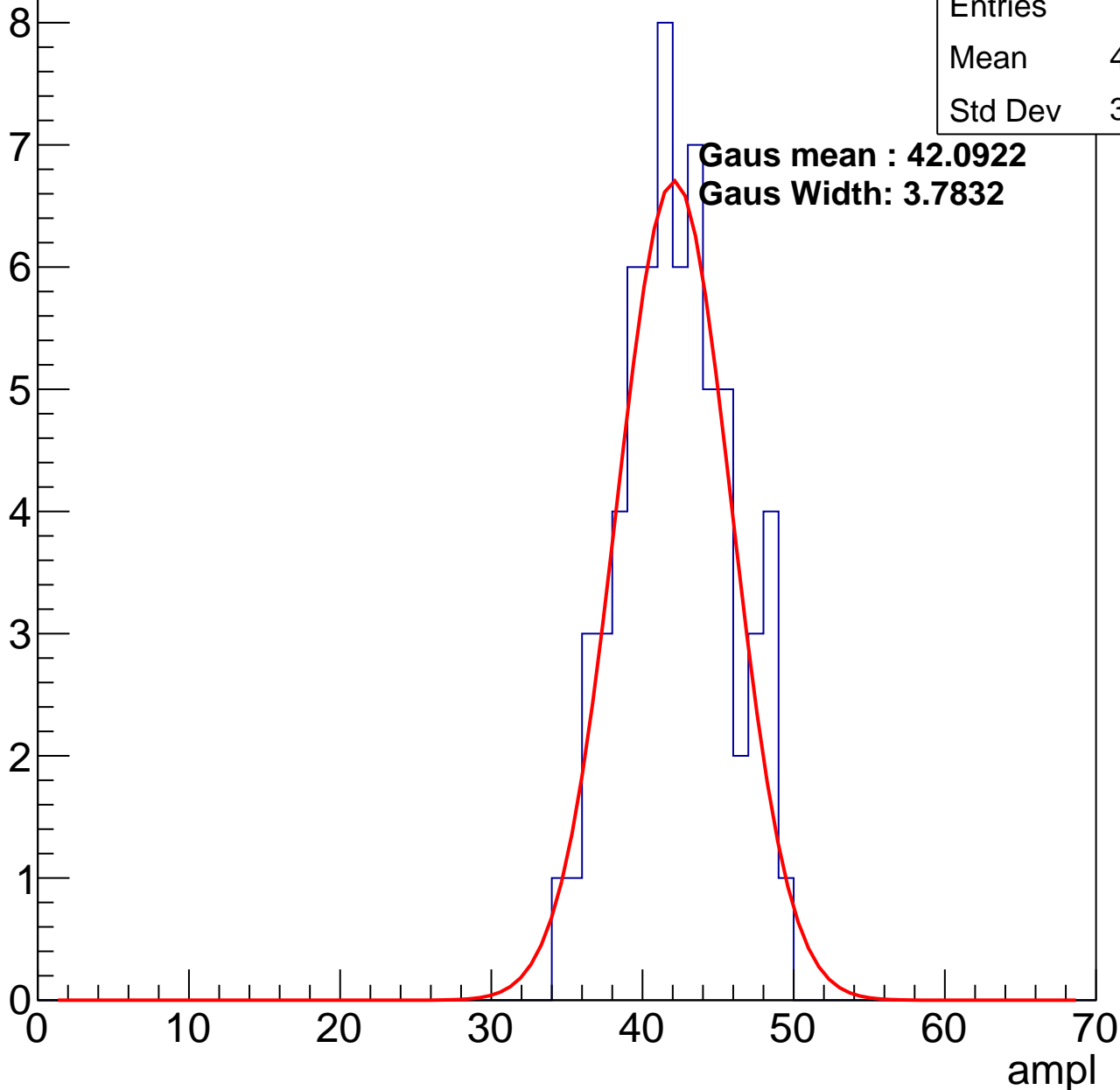
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.75
Std Dev	3.539

Gaus mean : 42.0922

Gaus Width: 3.7832

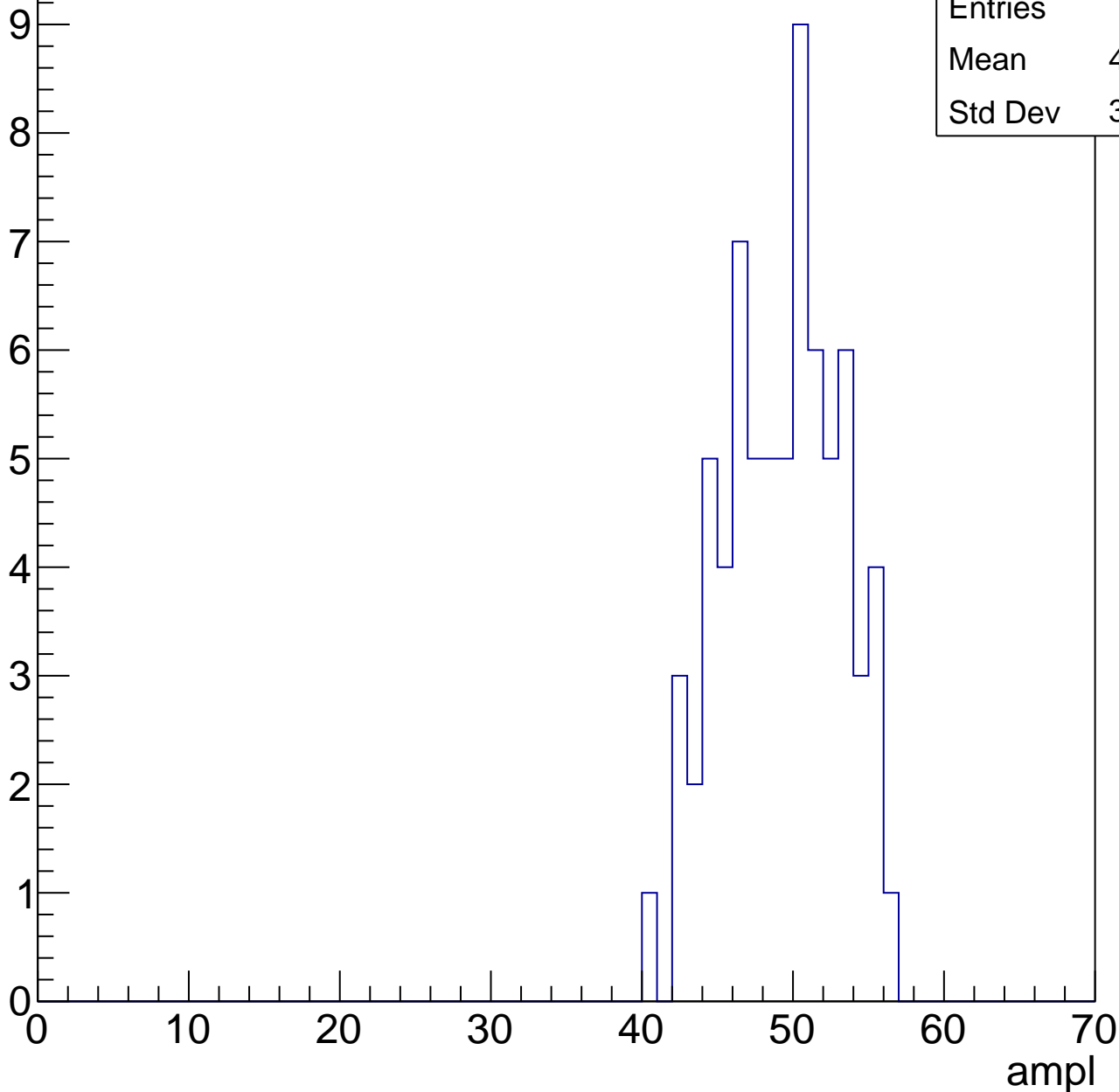


B1L101S, U1-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	48.82
Std Dev	3.784

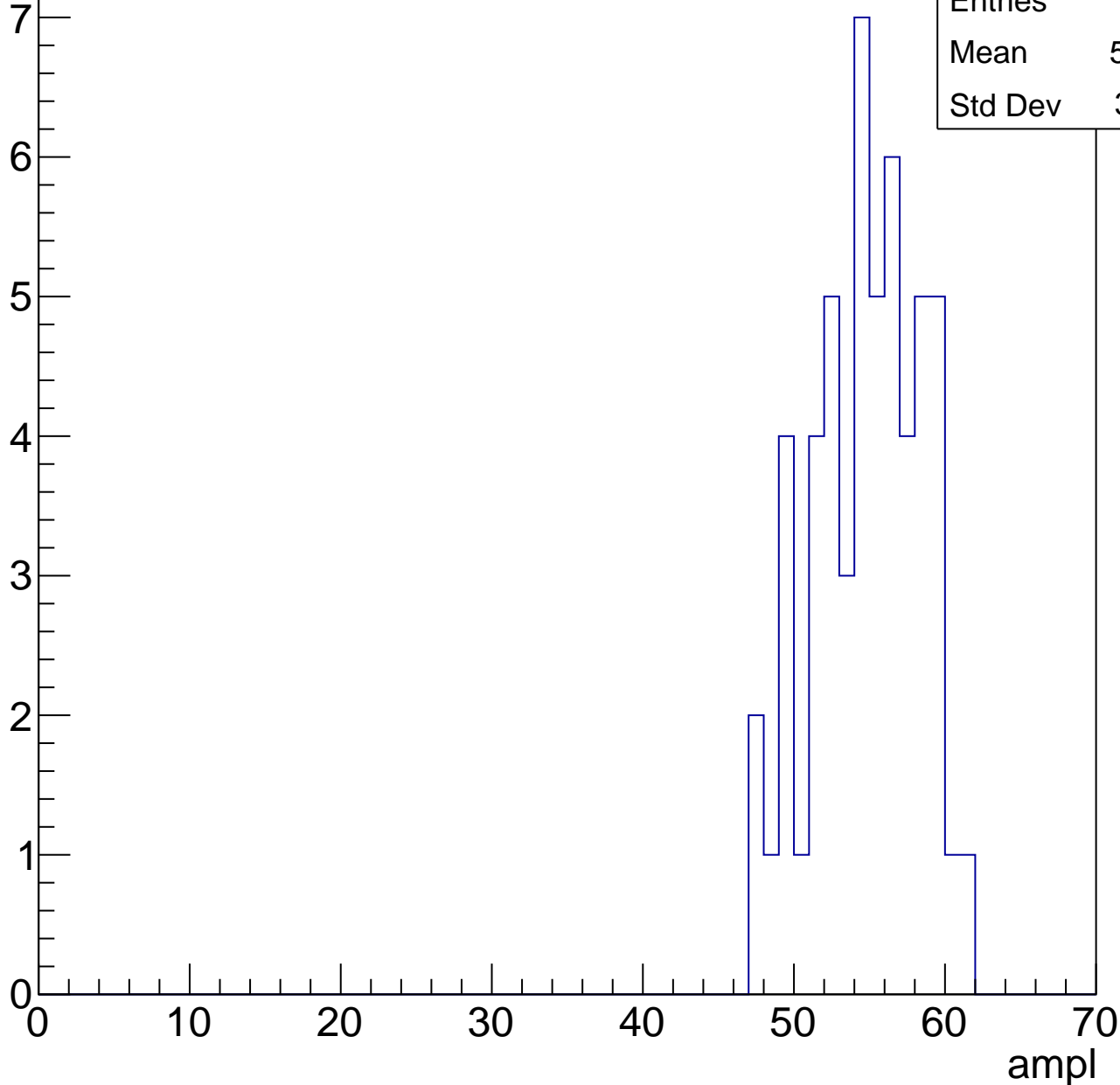


B1L101S, U1-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.33
Std Dev	3.491

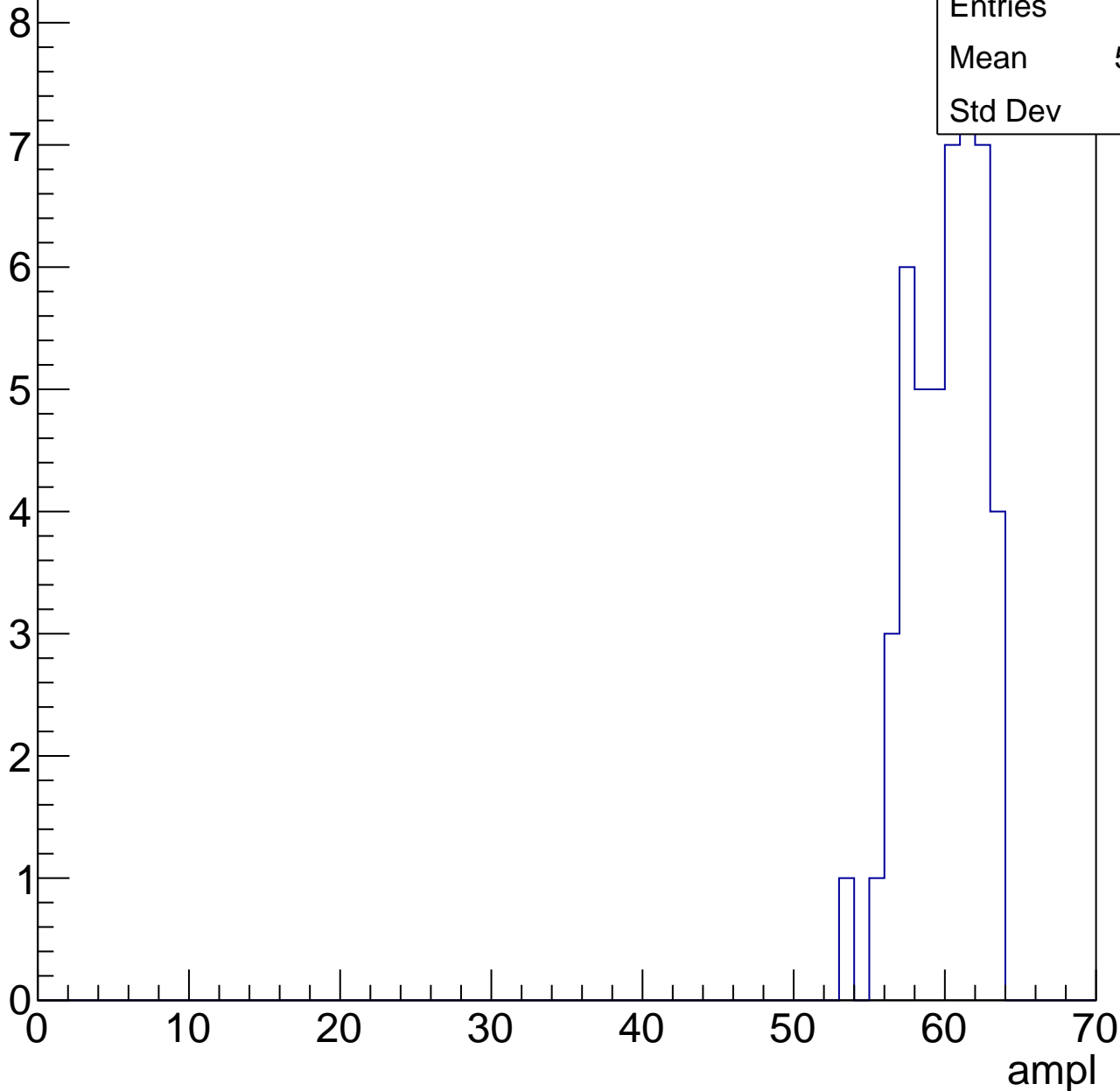


B1L101S, U1-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59.51
Std Dev	2.36

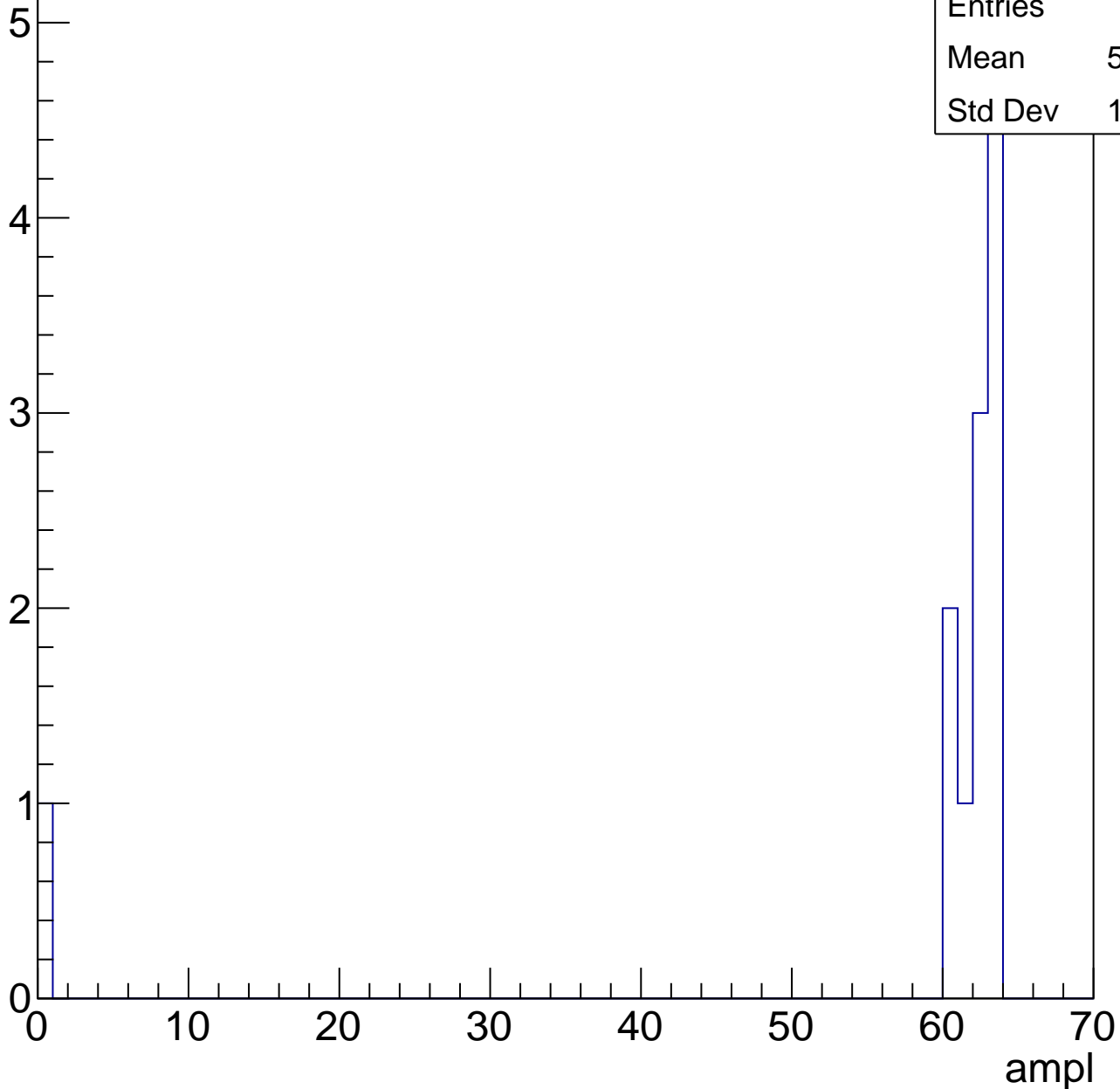


B1L101S, U1-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

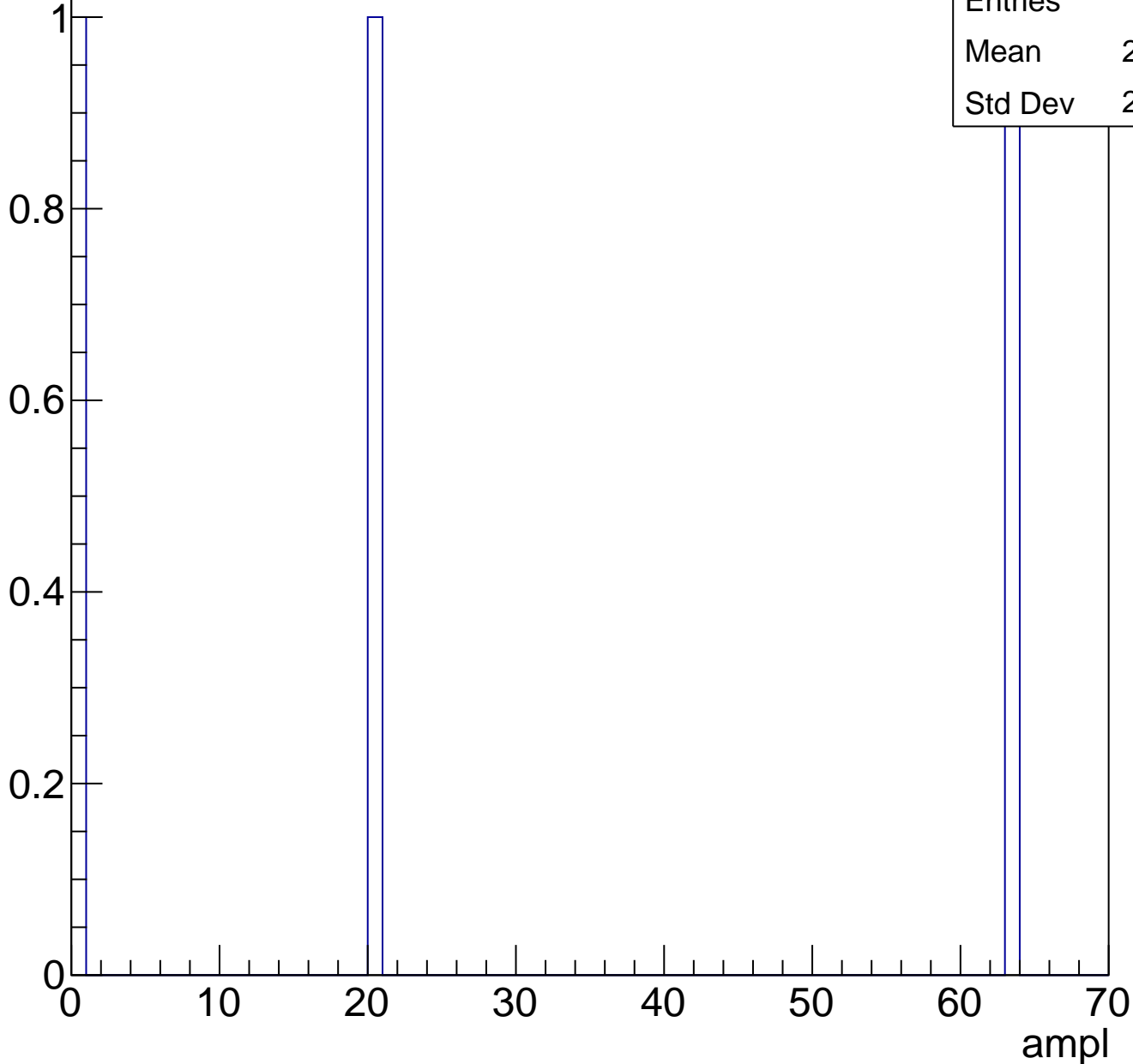
Entries	12
Mean	56.83
Std Dev	17.17



B1L101S, U1-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch17, adc0

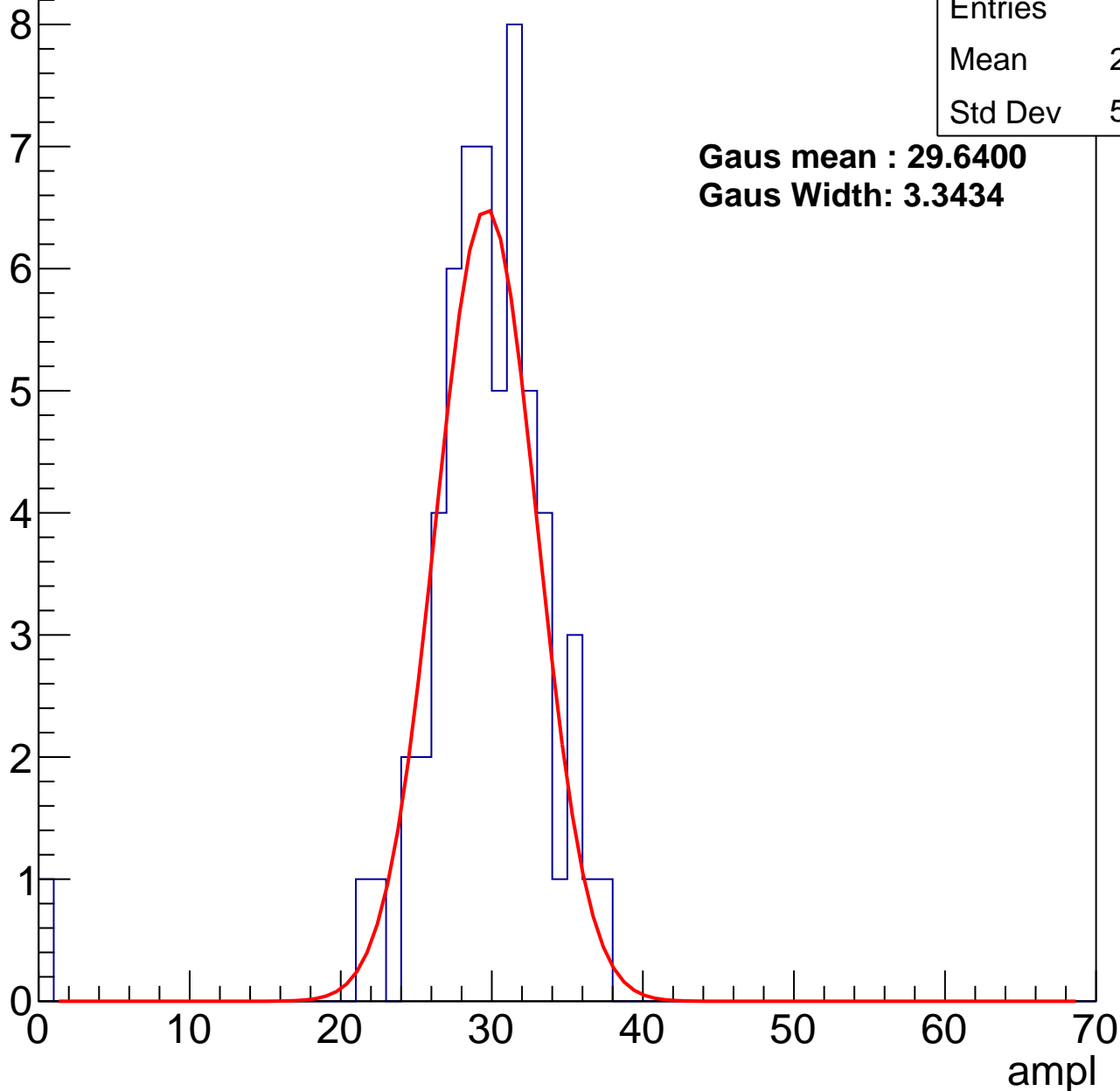
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.95
Std Dev	5.034

Gaus mean : 29.6400

Gaus Width: 3.3434



B1L101S, U1-ch17, adc1

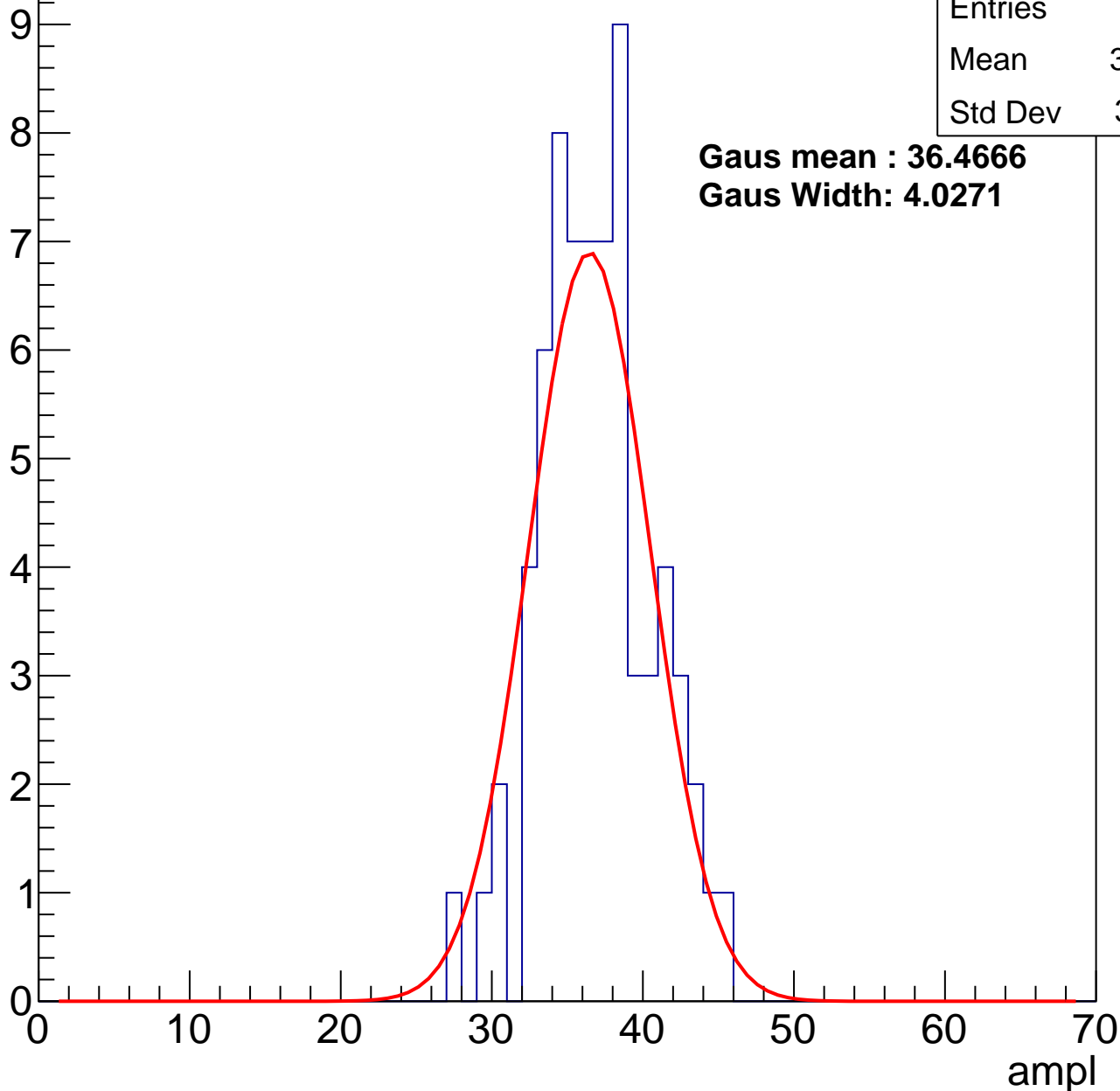
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.43
Std Dev	3.641

Gaus mean : 36.4666

Gaus Width: 4.0271



B1L101S, U1-ch17, adc2

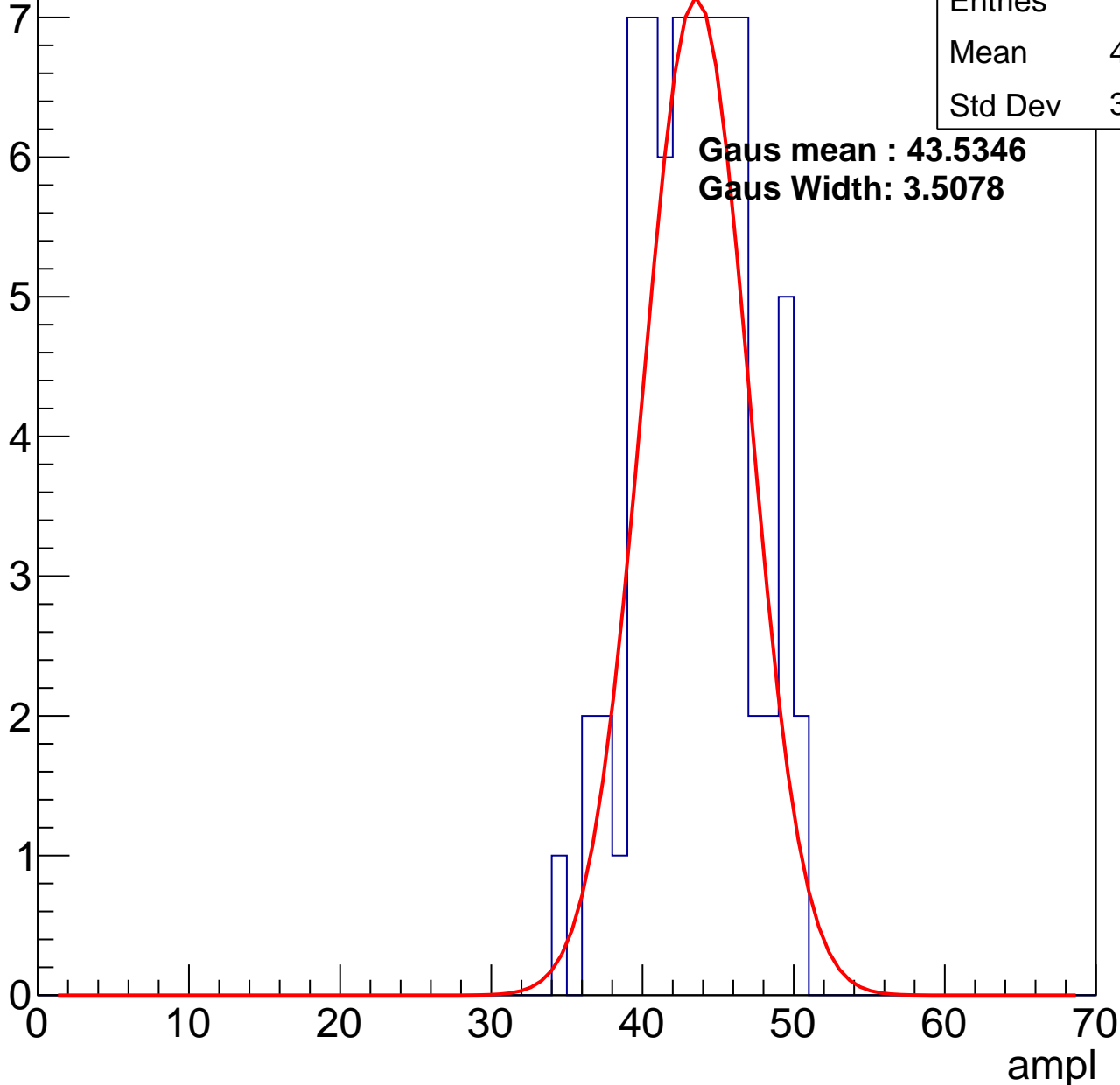
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.94
Std Dev	3.613

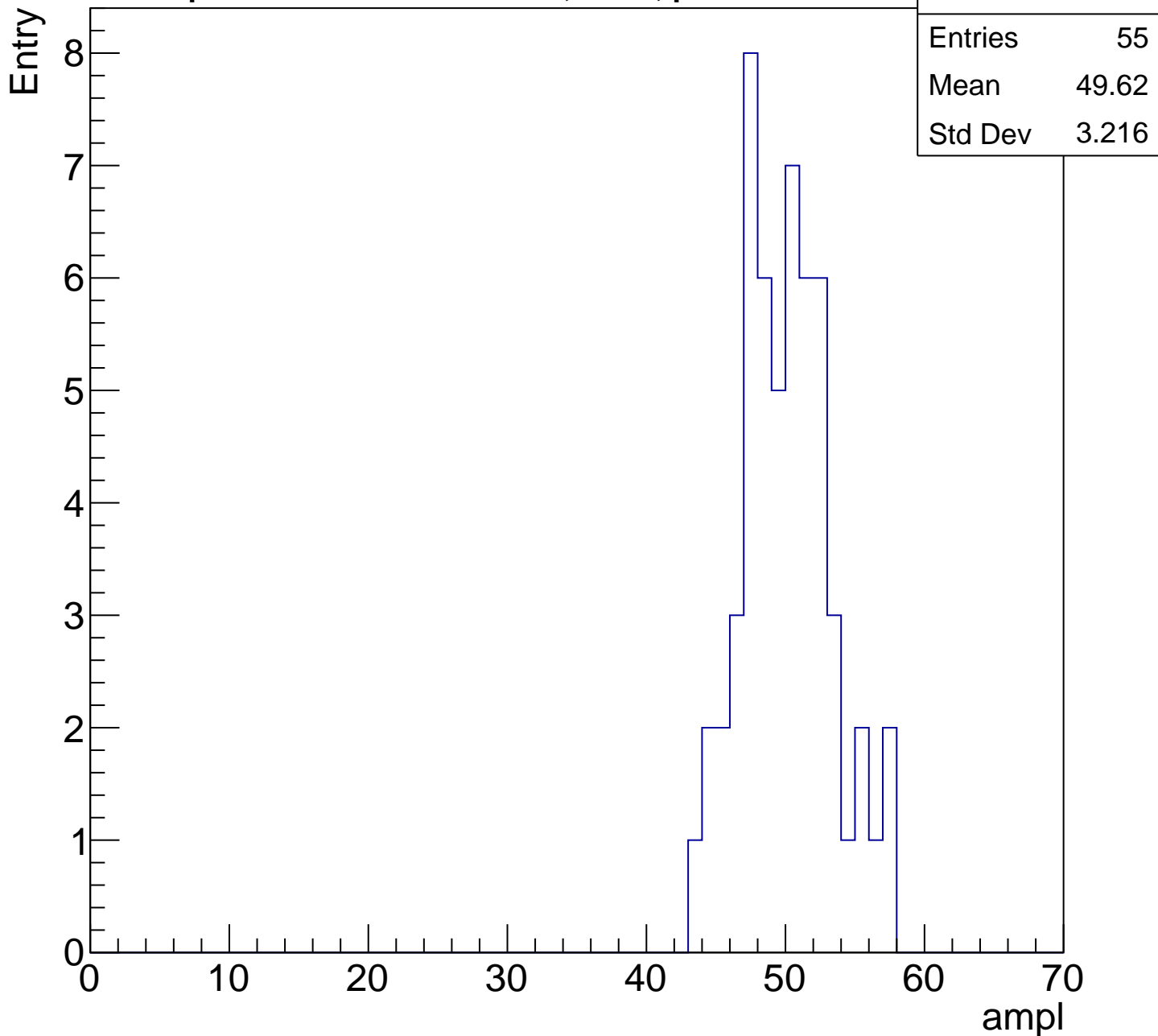
Gaus mean : 43.5346

Gaus Width: 3.5078



B1L101S, U1-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

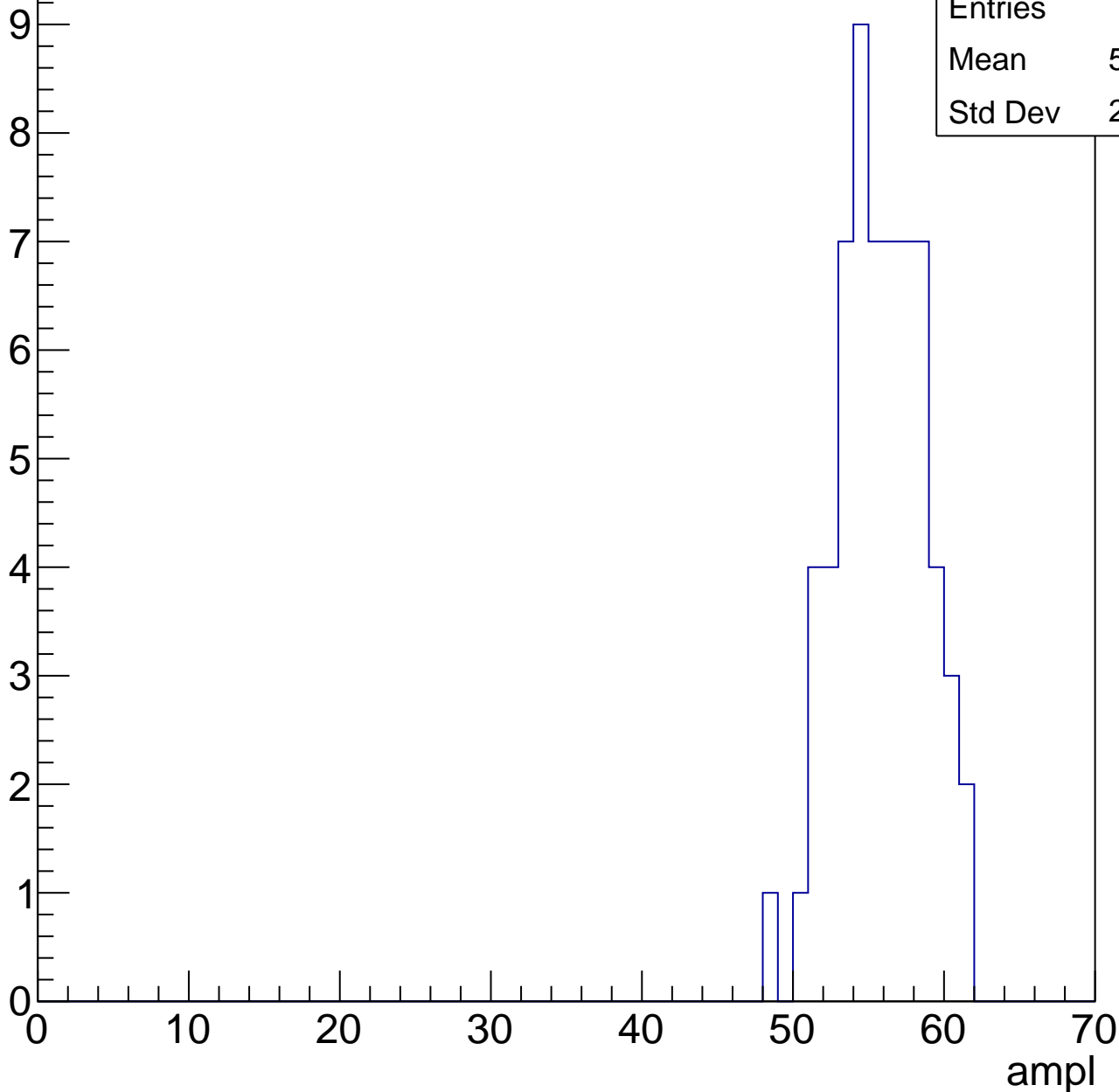


B1L101S, U1-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.35
Std Dev	2.852

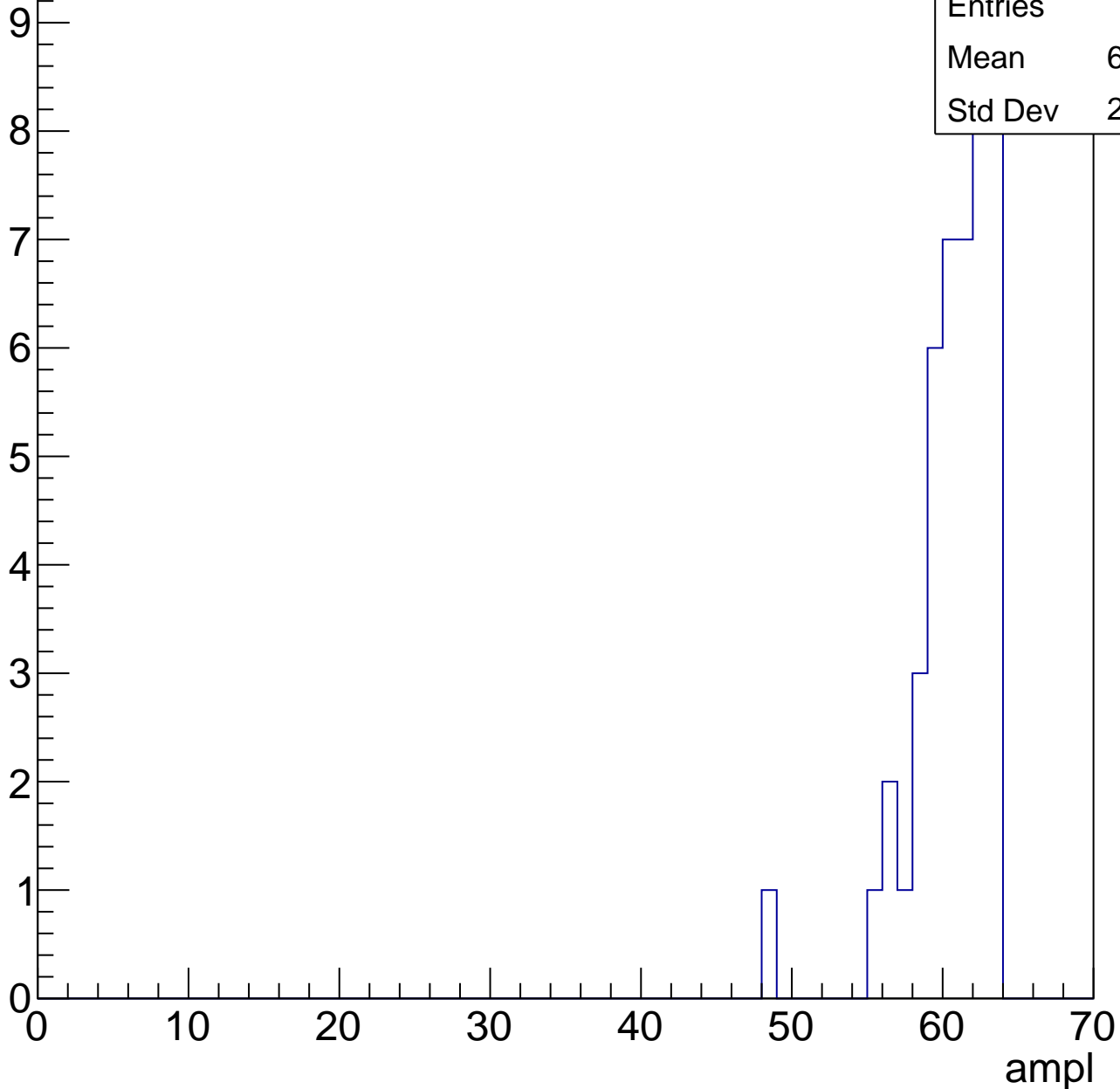


B1L101S, U1-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

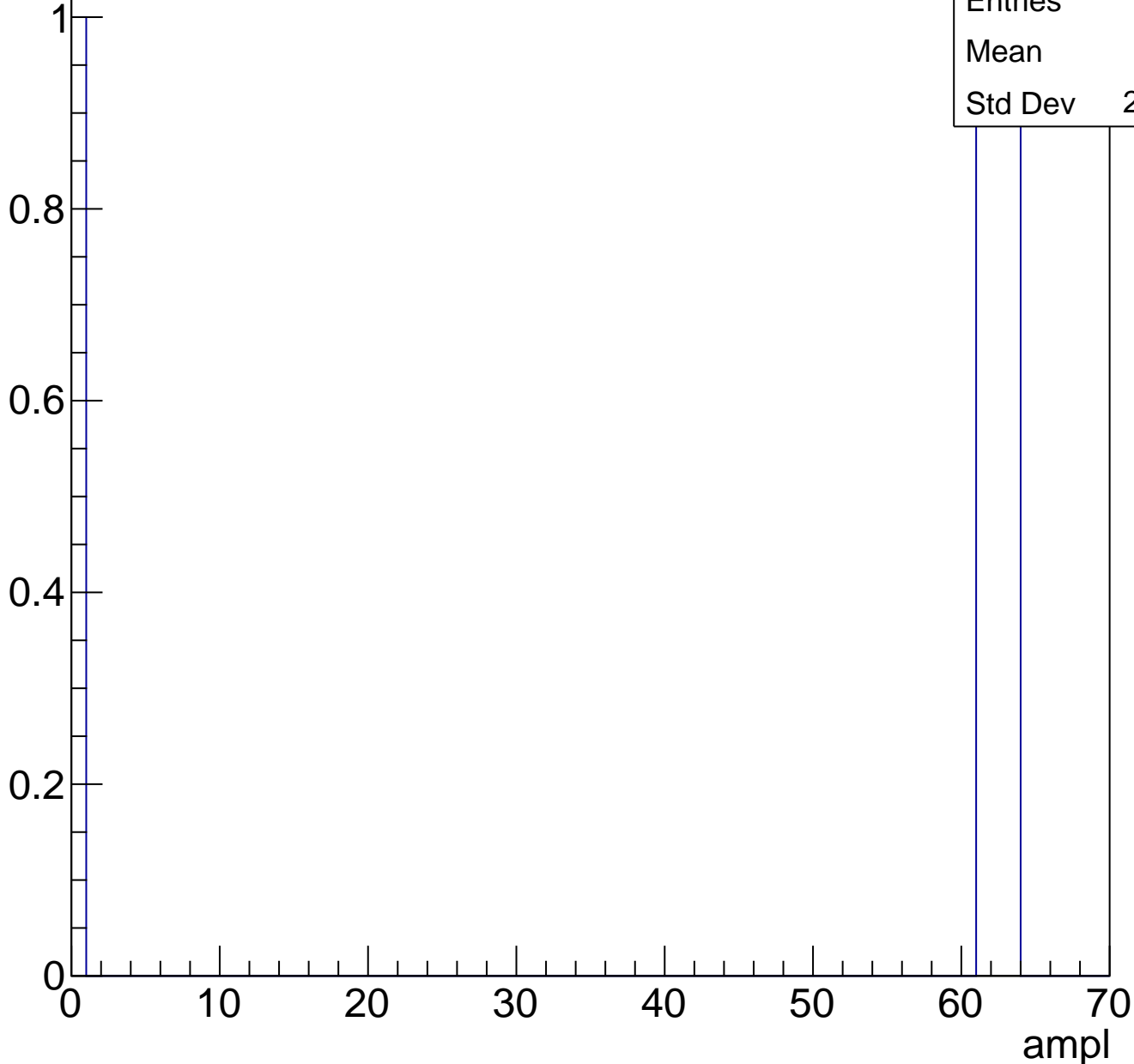
Entries	46
Mean	60.26
Std Dev	2.754



B1L101S, U1-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch18, adc0

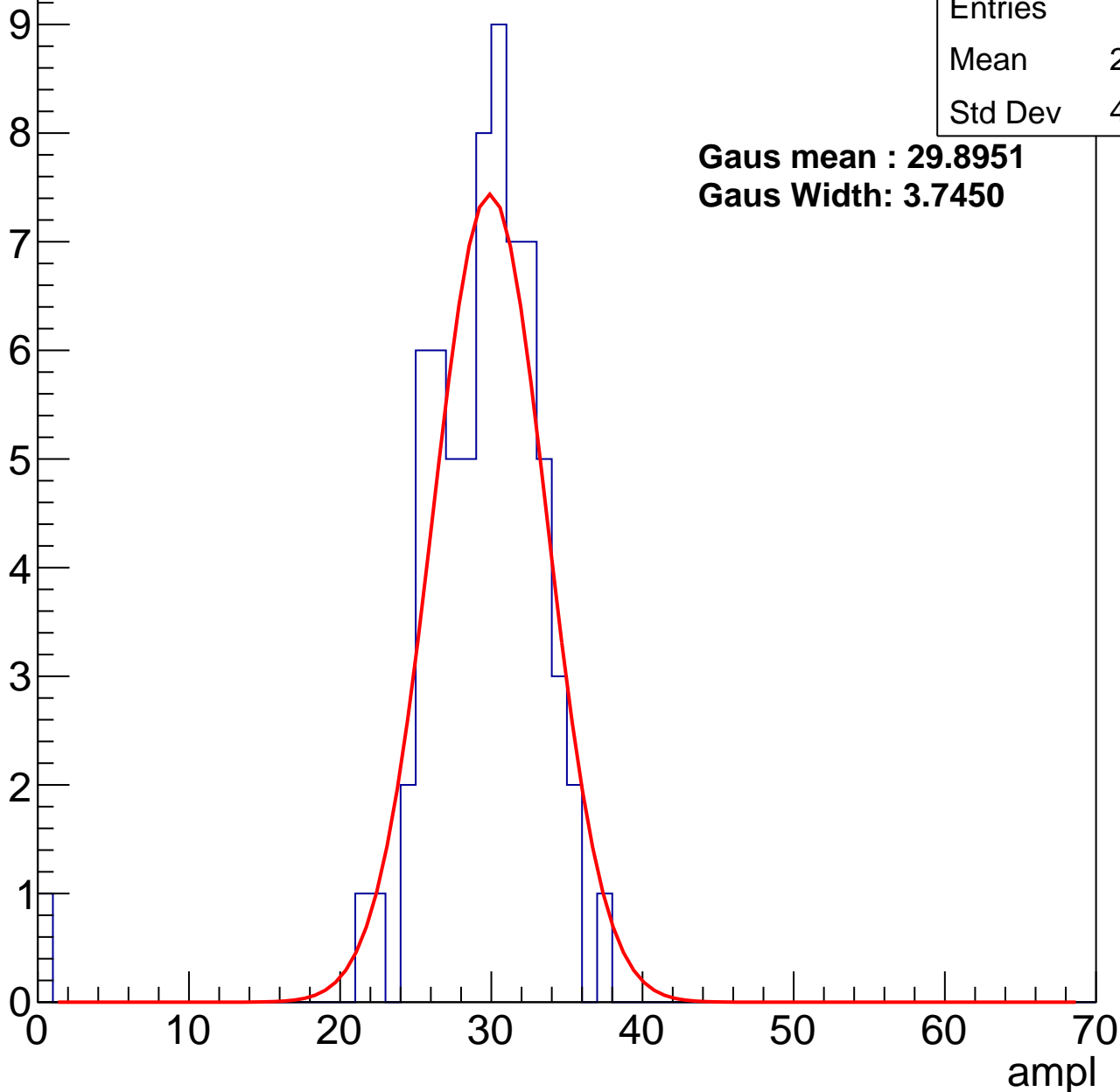
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.83
Std Dev	4.767

Gaus mean : 29.8951

Gaus Width: 3.7450



B1L101S, U1-ch18, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	36.54
Std Dev	3.531

Gaus mean : 37.4048

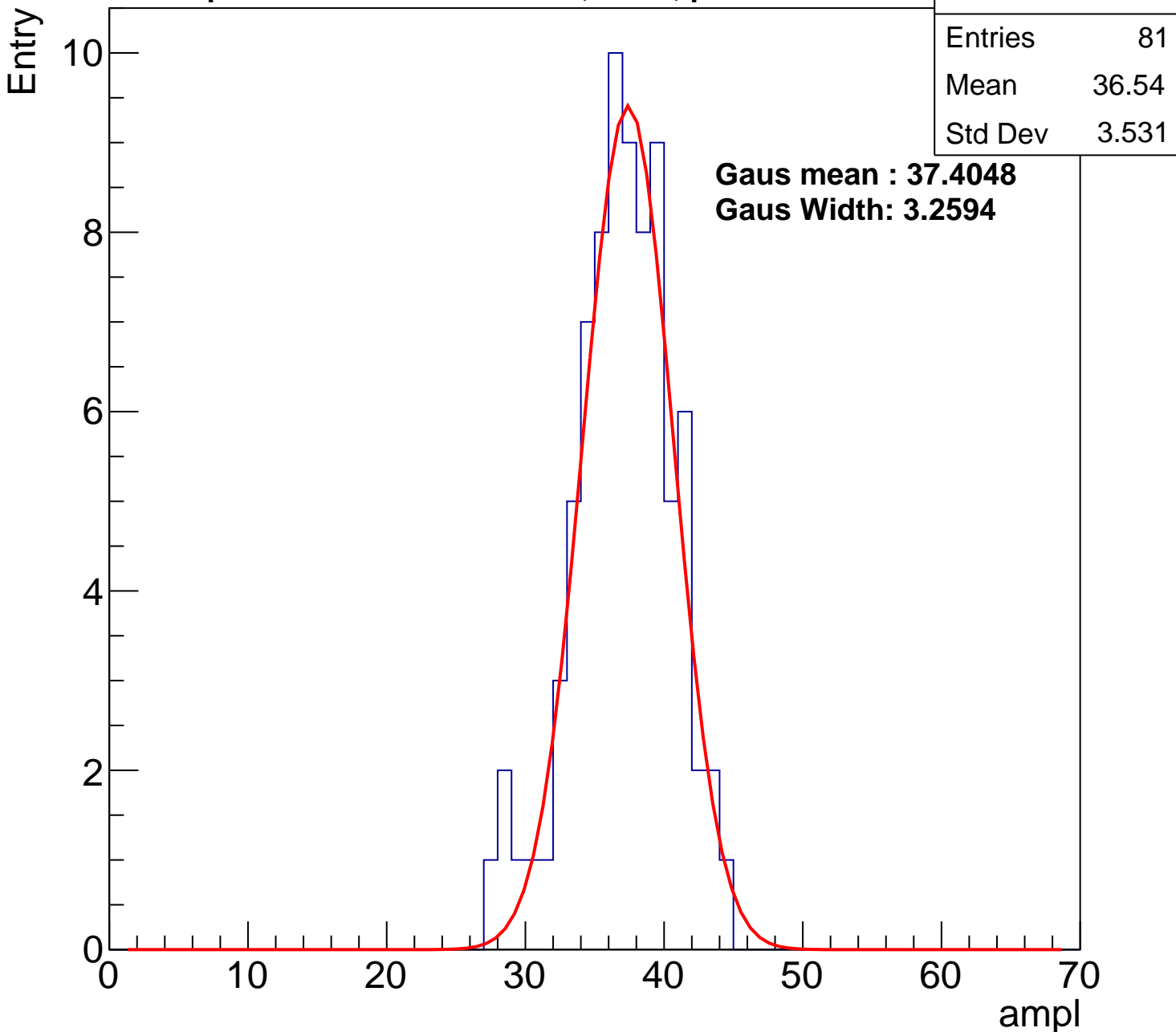
Gaus Width: 3.2594

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

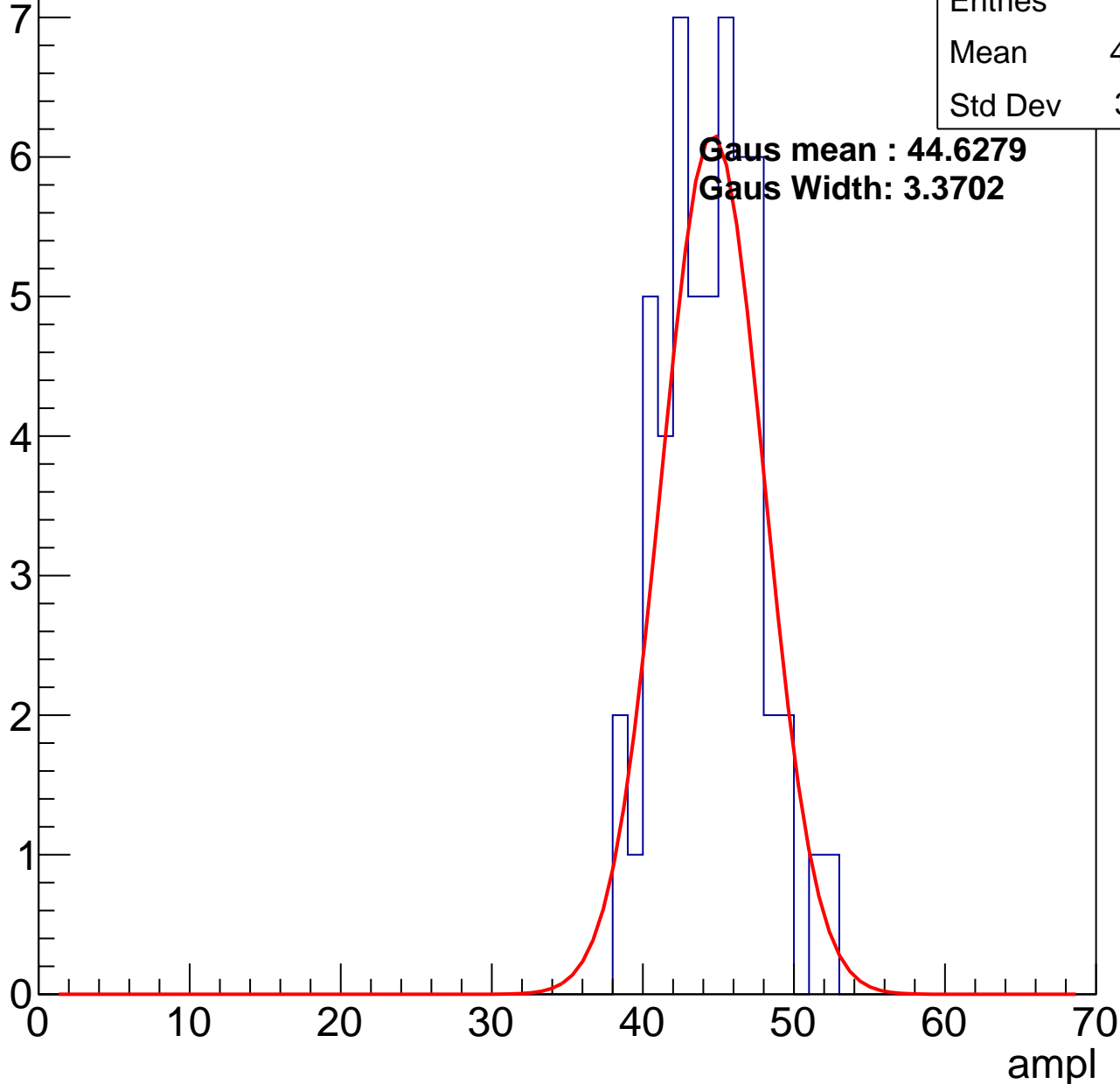


B1L101S, U1-ch18, adc2

calib_packv5_042523_0143.root, FC#0, port D2

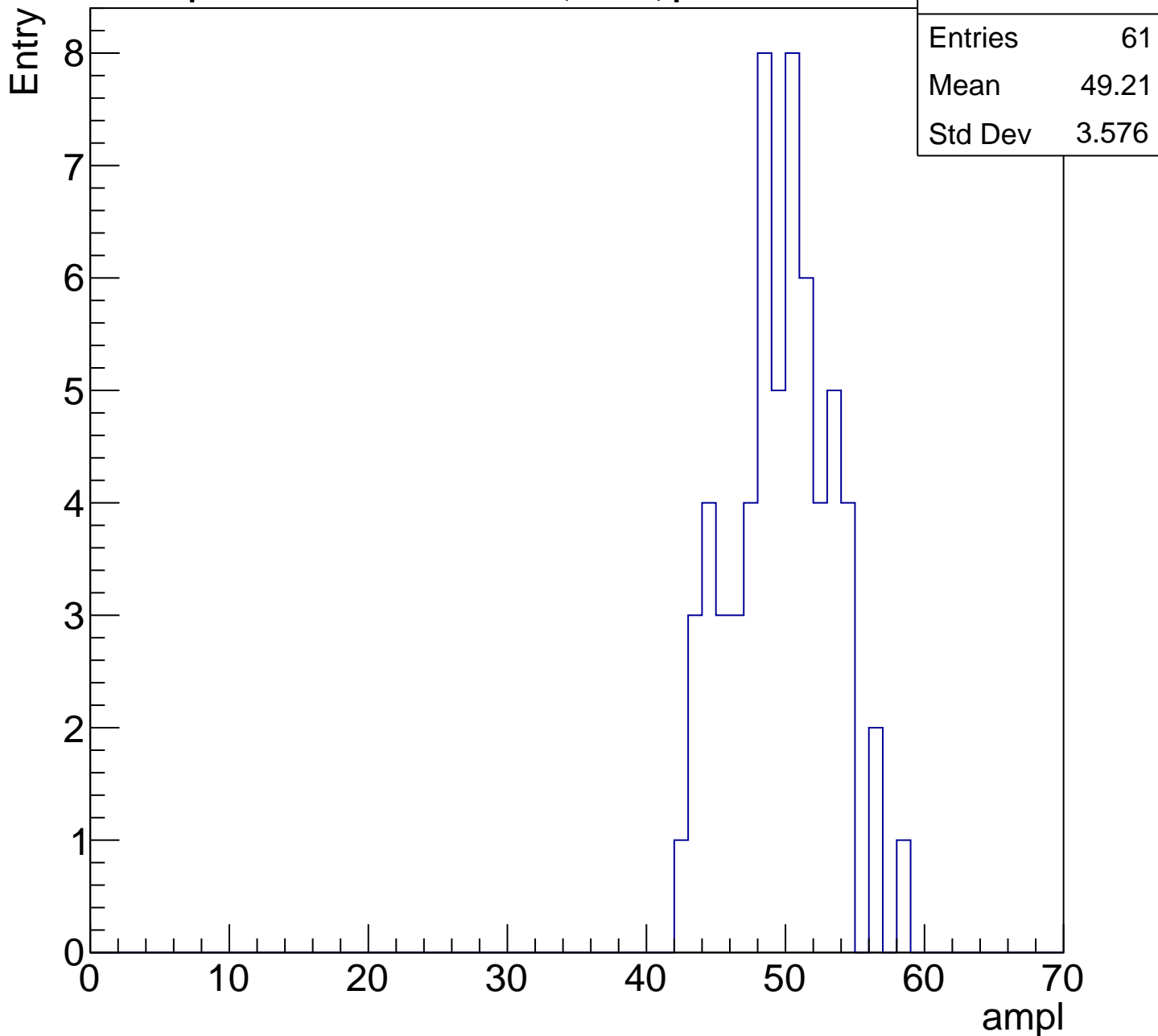
Entry

Entries	54
Mean	44.04
Std Dev	3.121



B1L101S, U1-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

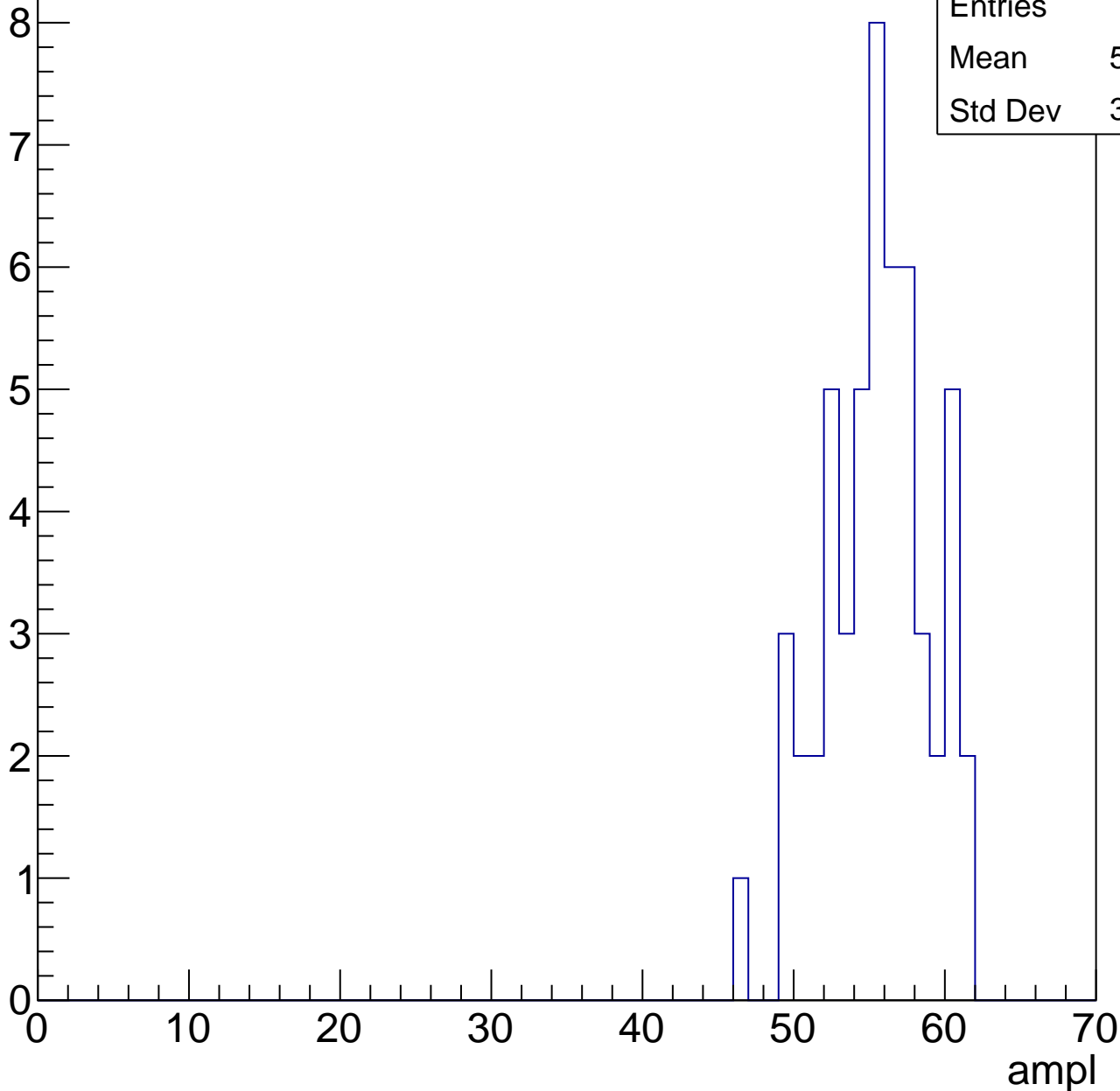


B1L101S, U1-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.02
Std Dev	3.417

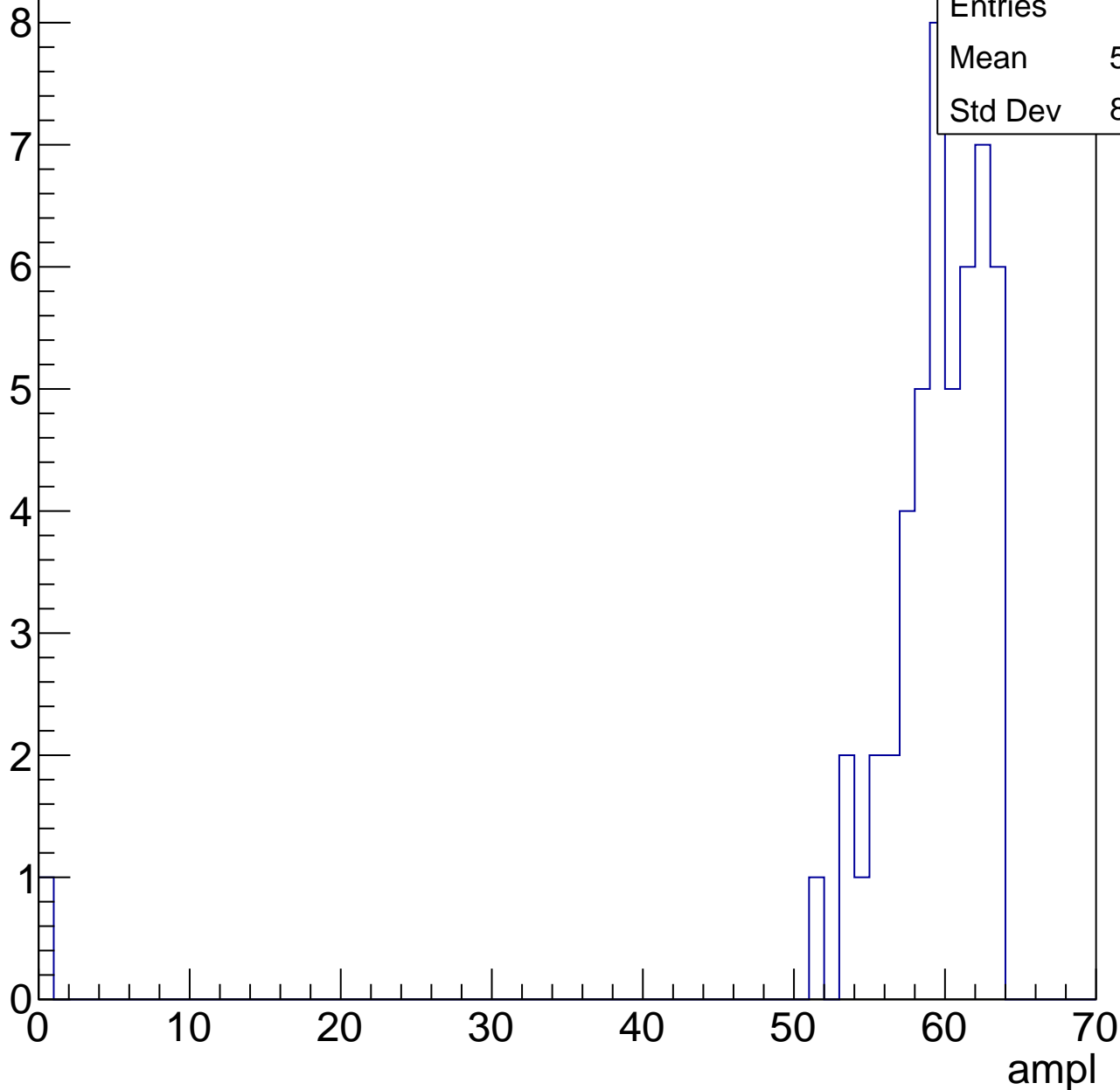


B1L101S, U1-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

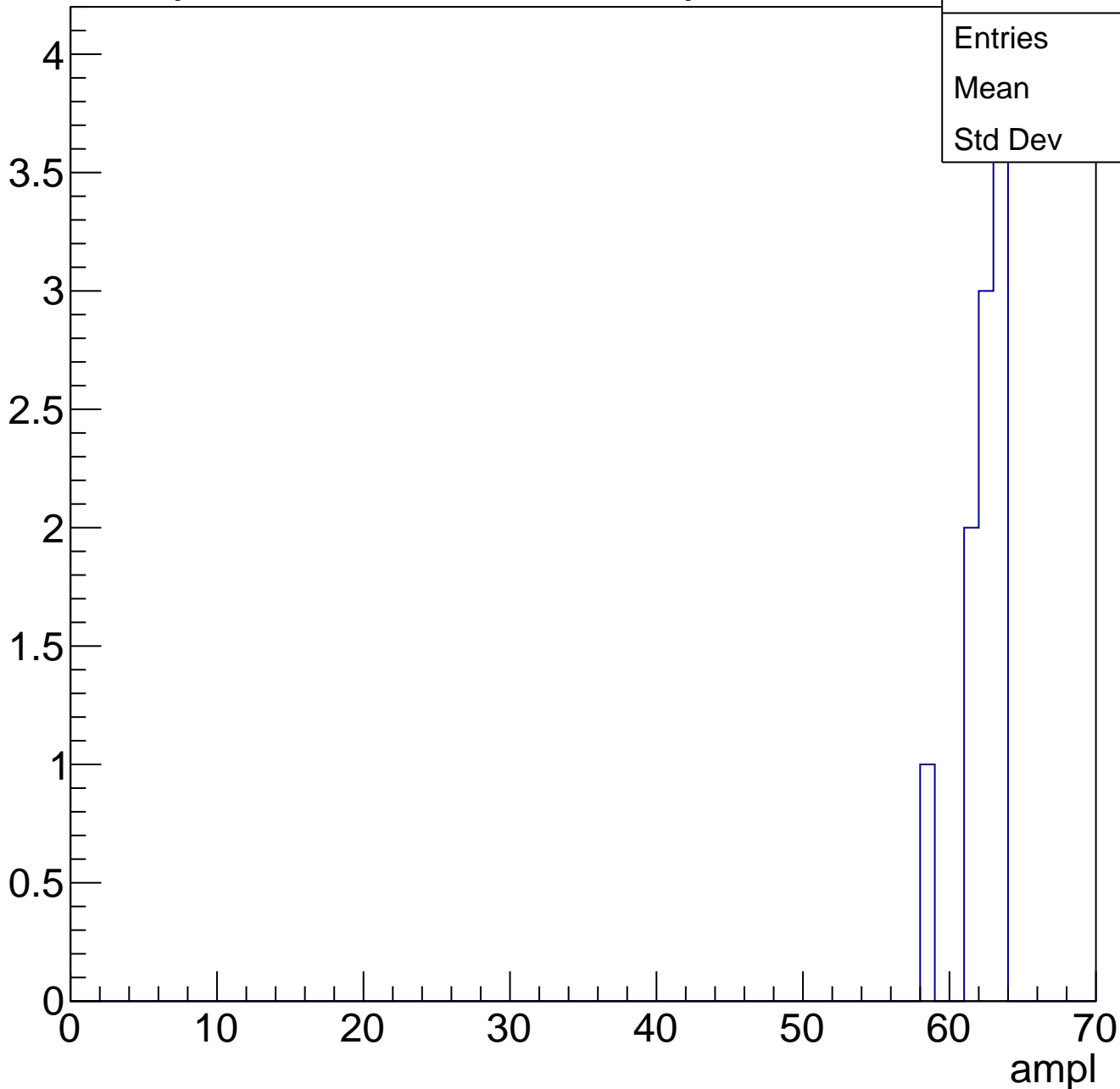
Entries	50
Mean	58.02
Std Dev	8.778



B1L101S, U1-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch19, adc0

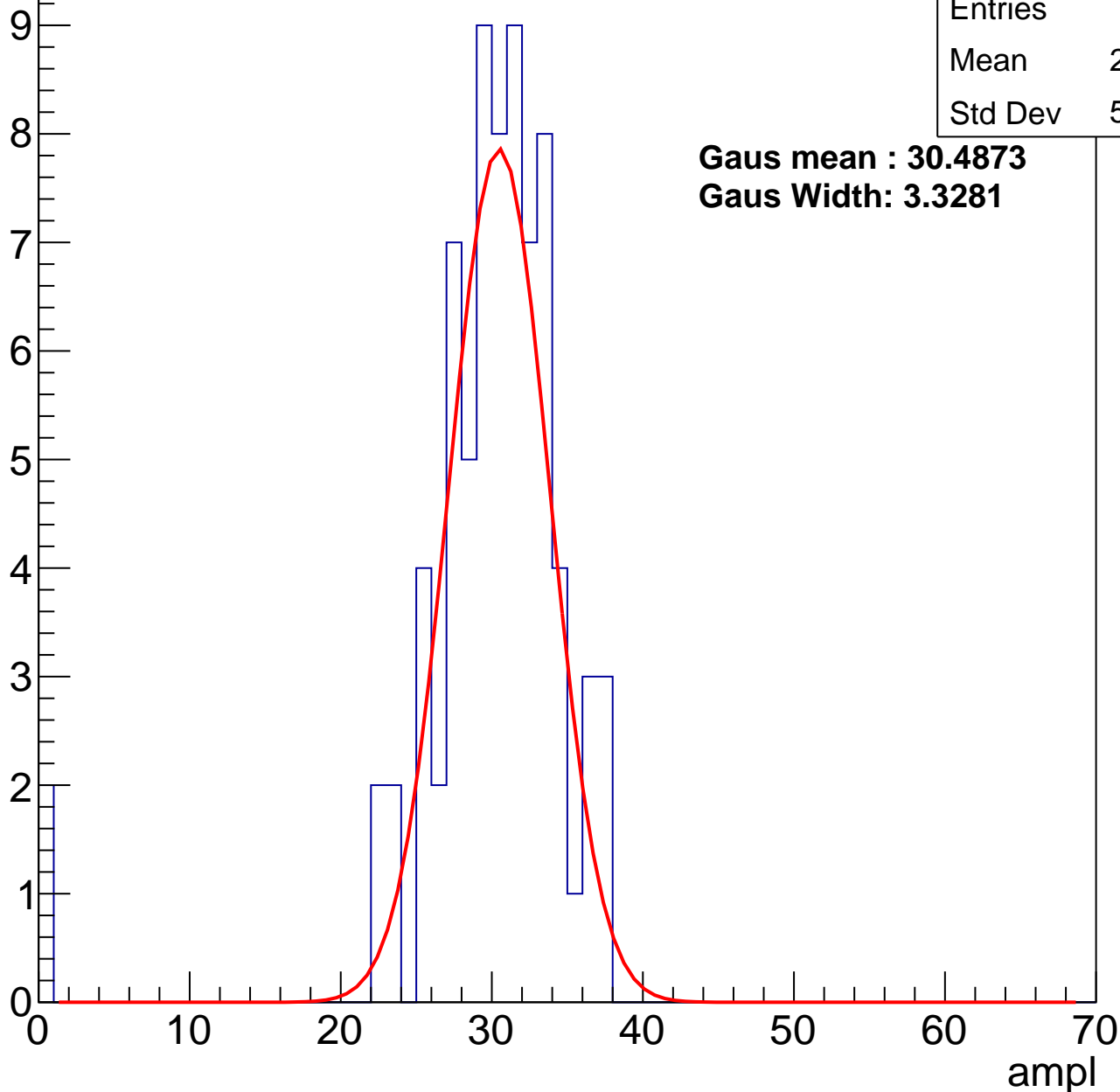
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.33
Std Dev	5.926

Gaus mean : 30.4873

Gaus Width: 3.3281



B1L101S, U1-ch19, adc1

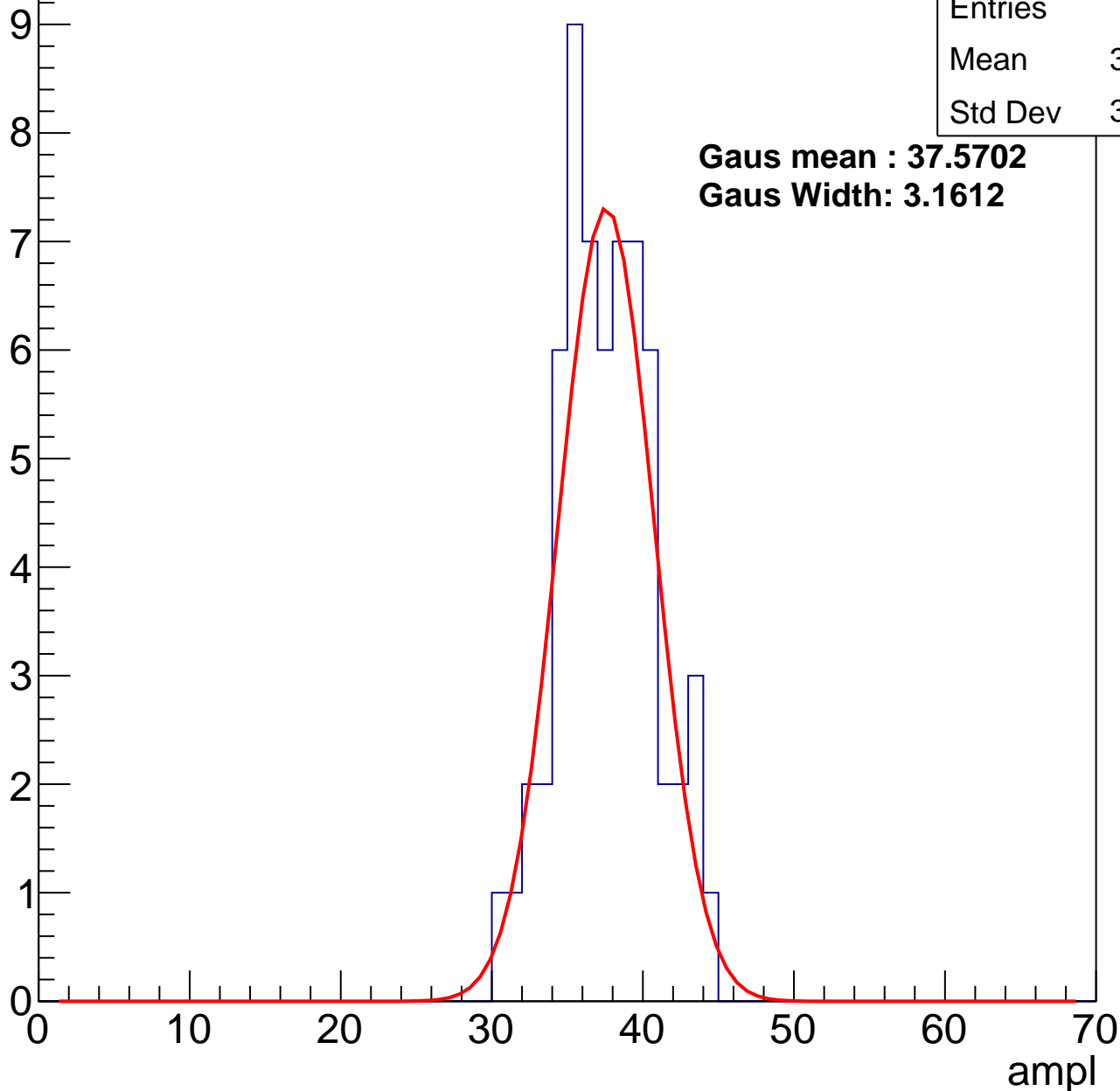
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.13
Std Dev	3.087

Gaus mean : 37.5702

Gaus Width: 3.1612



B1L101S, U1-ch19, adc2

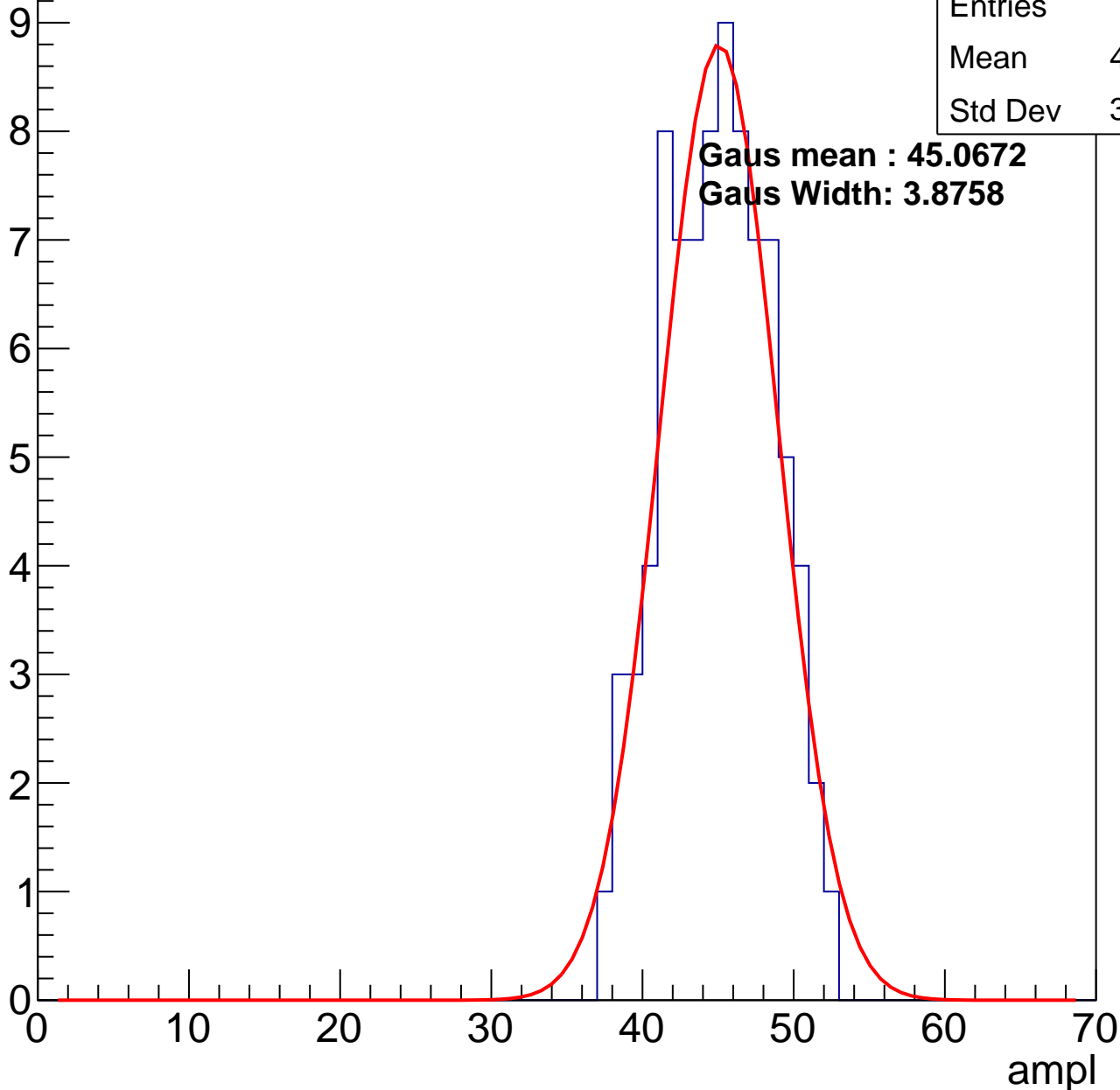
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	44.52
Std Dev	3.497

Gaus mean : 45.0672

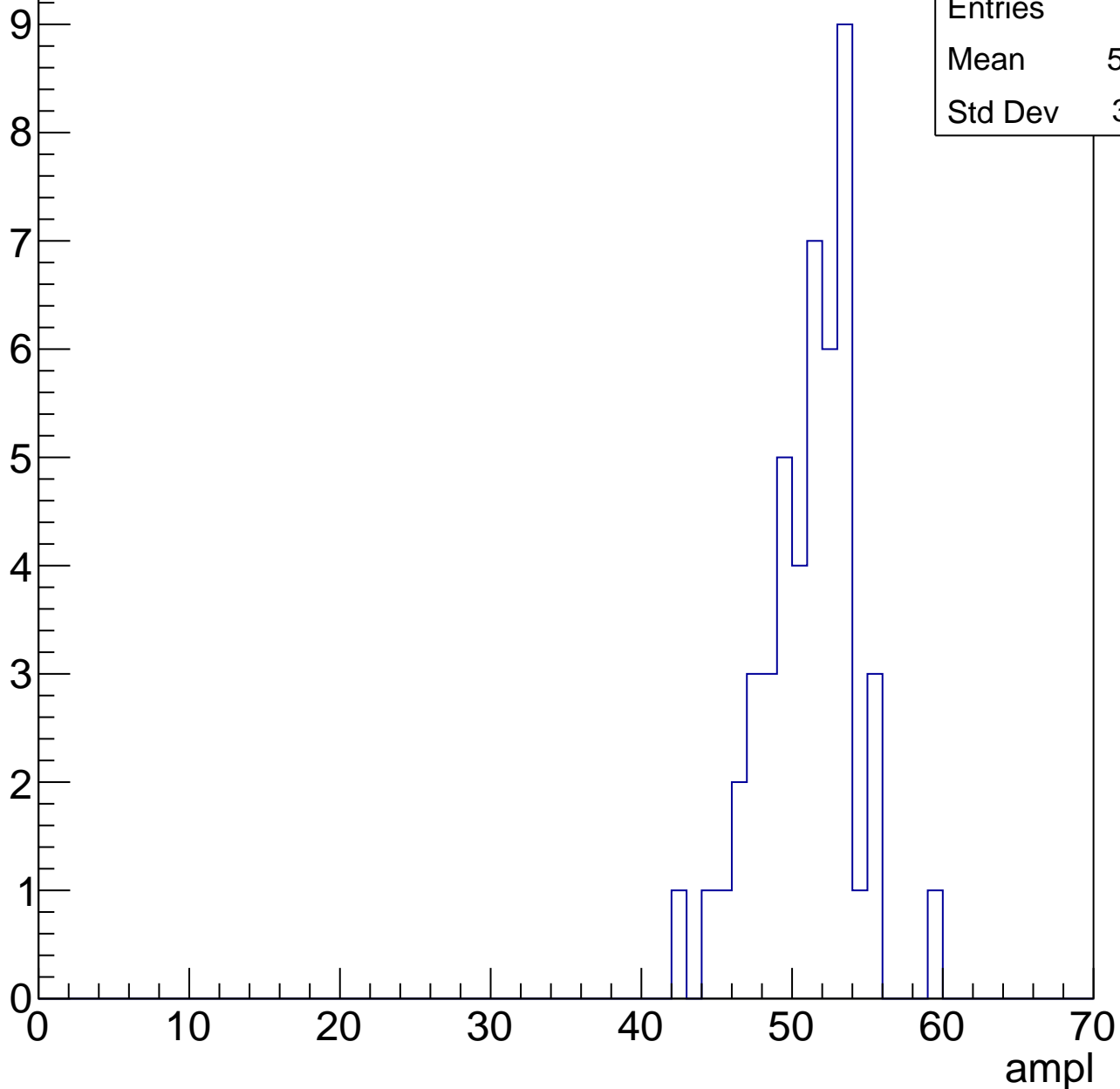
Gaus Width: 3.8758



B1L101S, U1-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

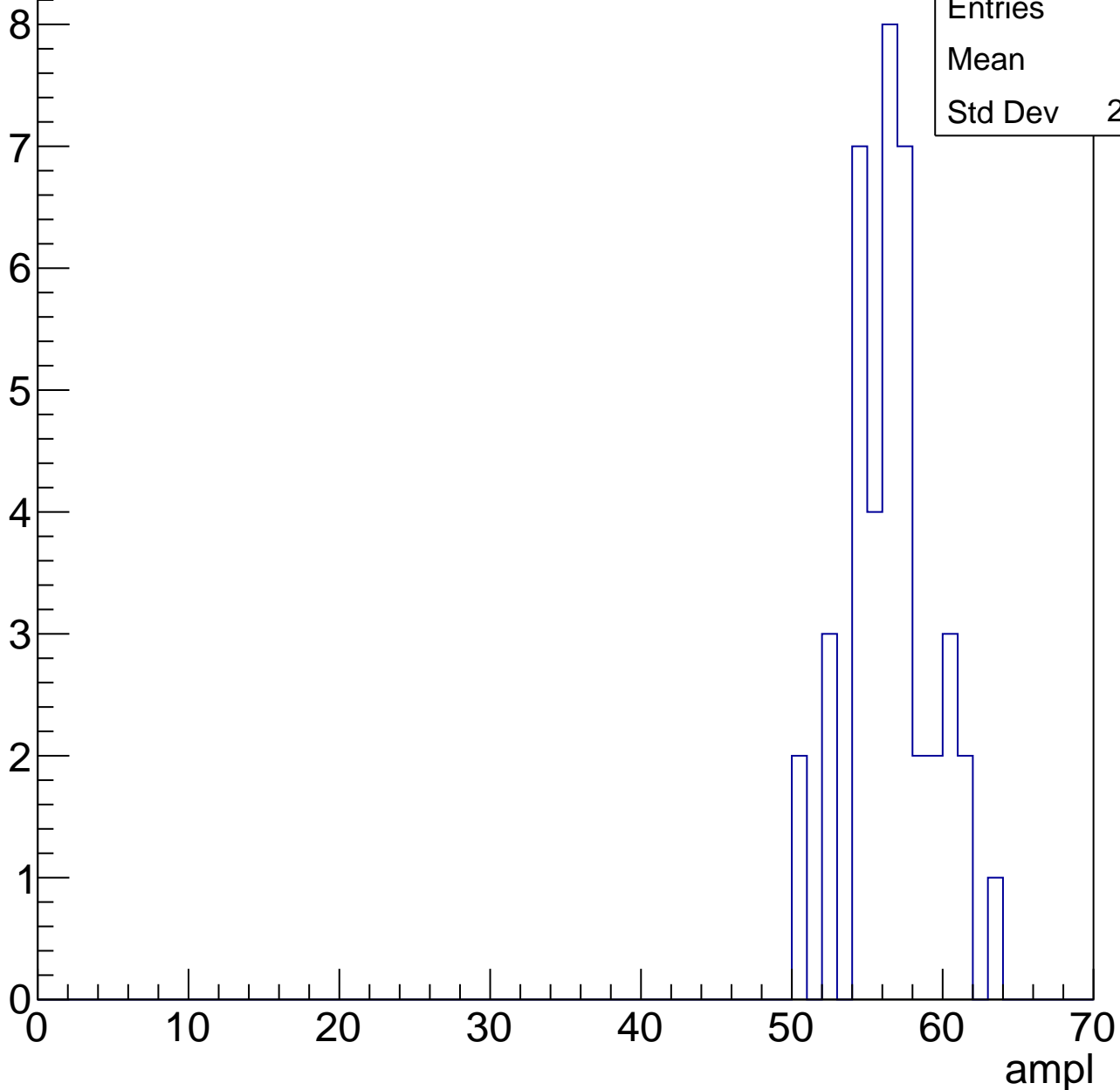


B1L101S, U1-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	56.1
Std Dev	2.844

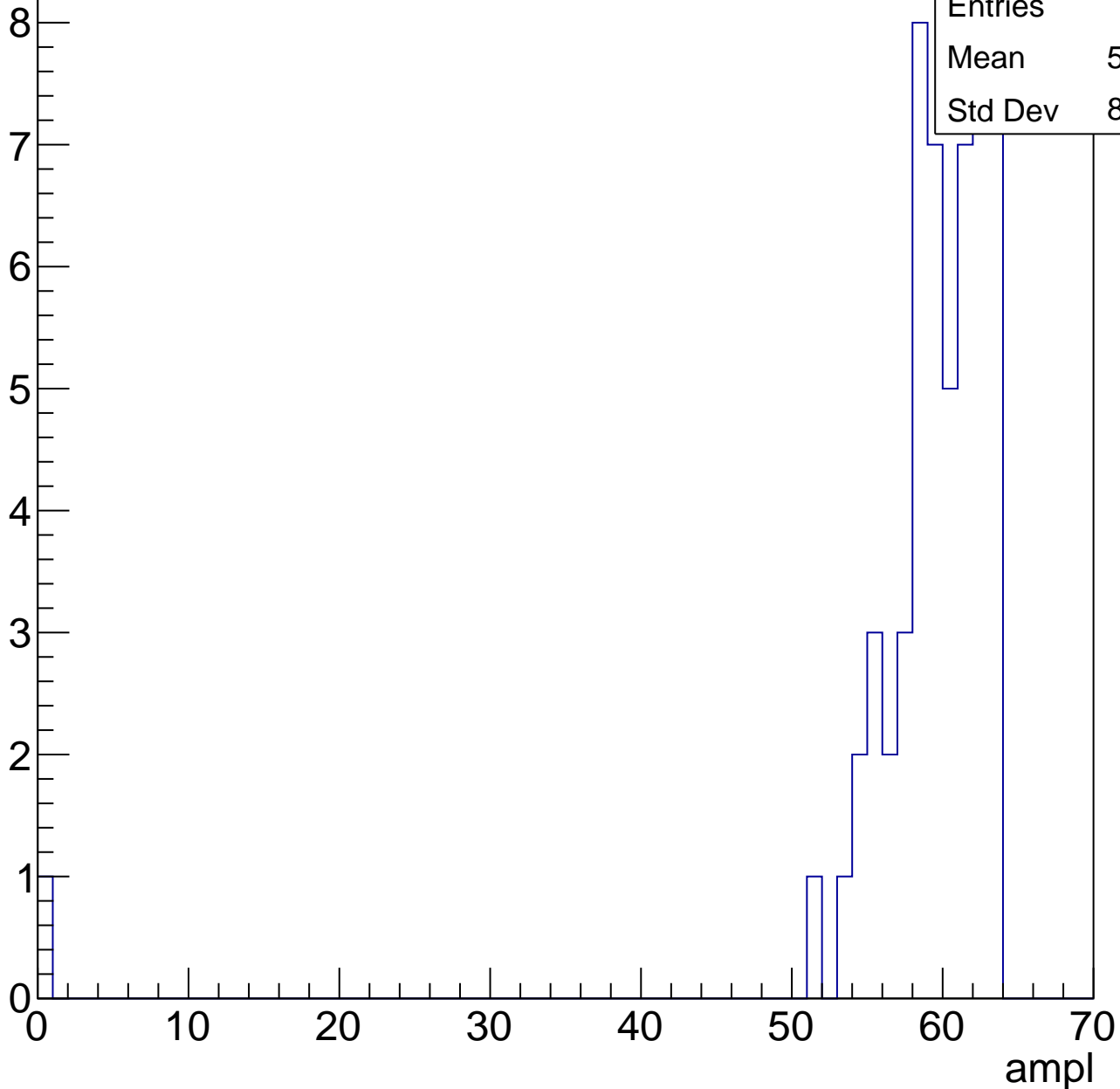


B1L101S, U1-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	58.29
Std Dev	8.368



B1L101S, U1-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

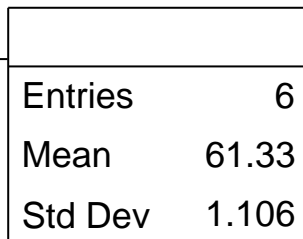
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.106

0 10 20 30 40 50 60 70

ampl



B1L101S, U1-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch20, adc0

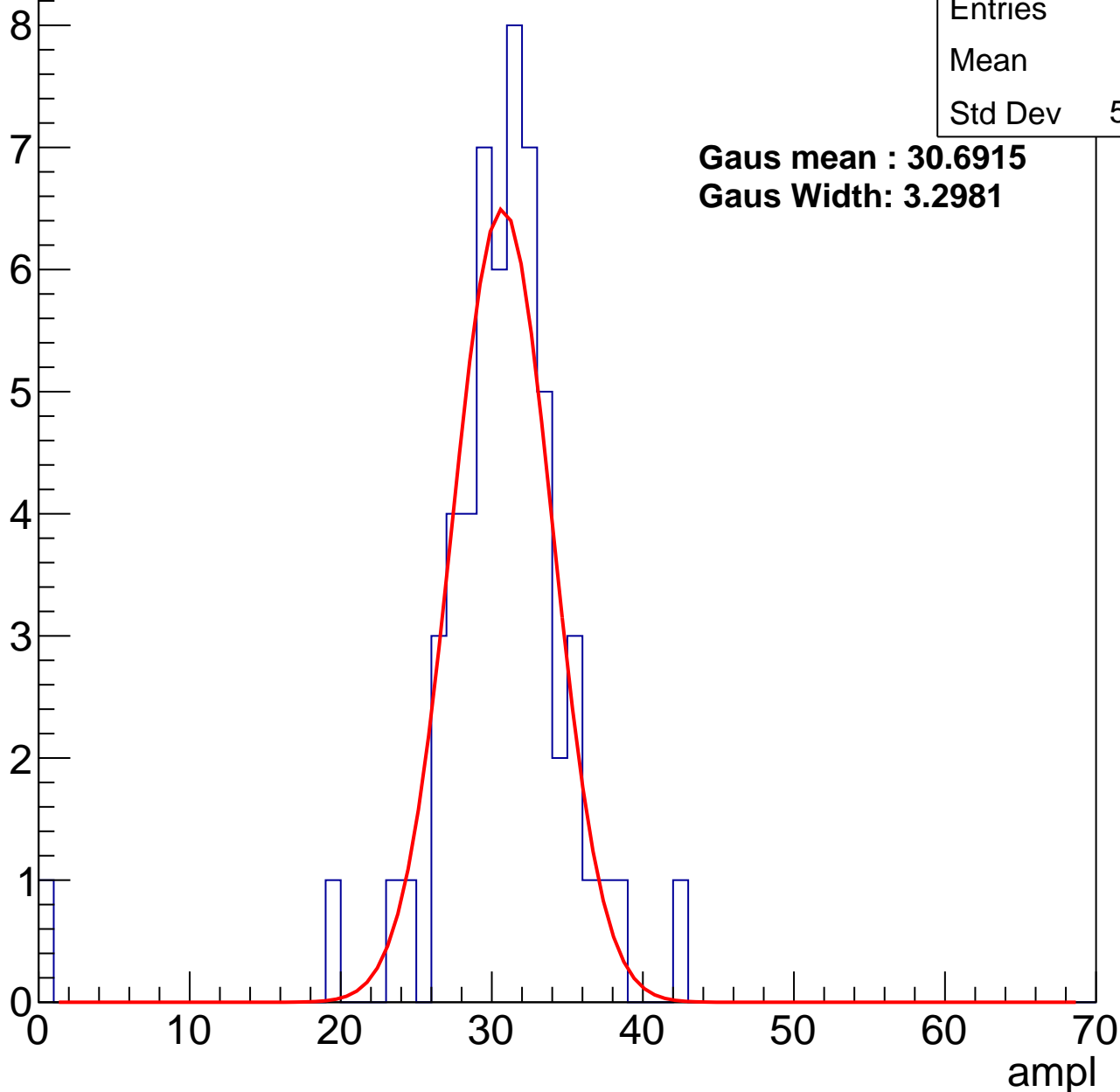
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	30
Std Dev	5.452

Gaus mean : 30.6915

Gaus Width: 3.2981



B1L101S, U1-ch20, adc1

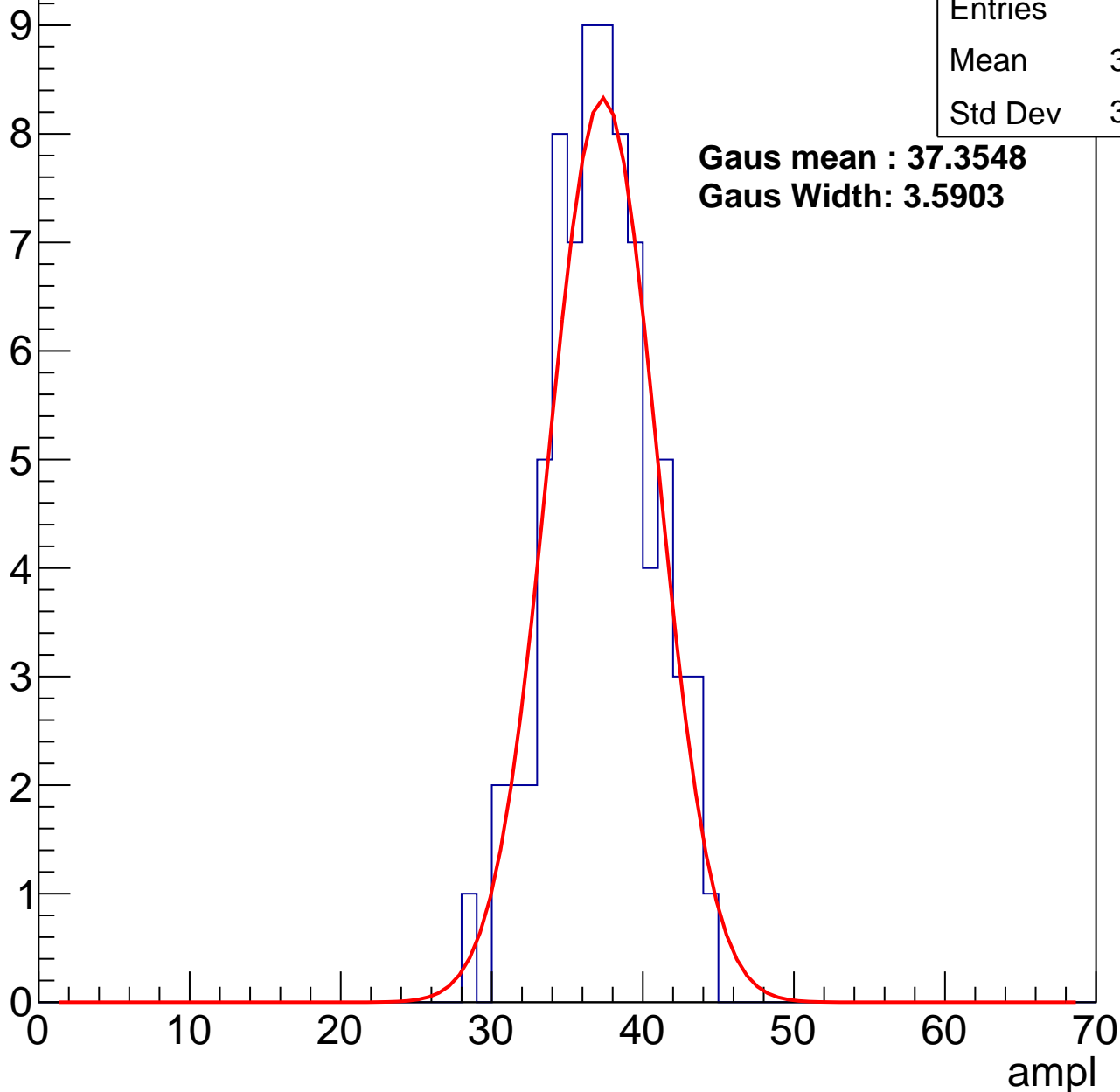
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.76
Std Dev	3.383

Gaus mean : 37.3548

Gaus Width: 3.5903



B1L101S, U1-ch20, adc2

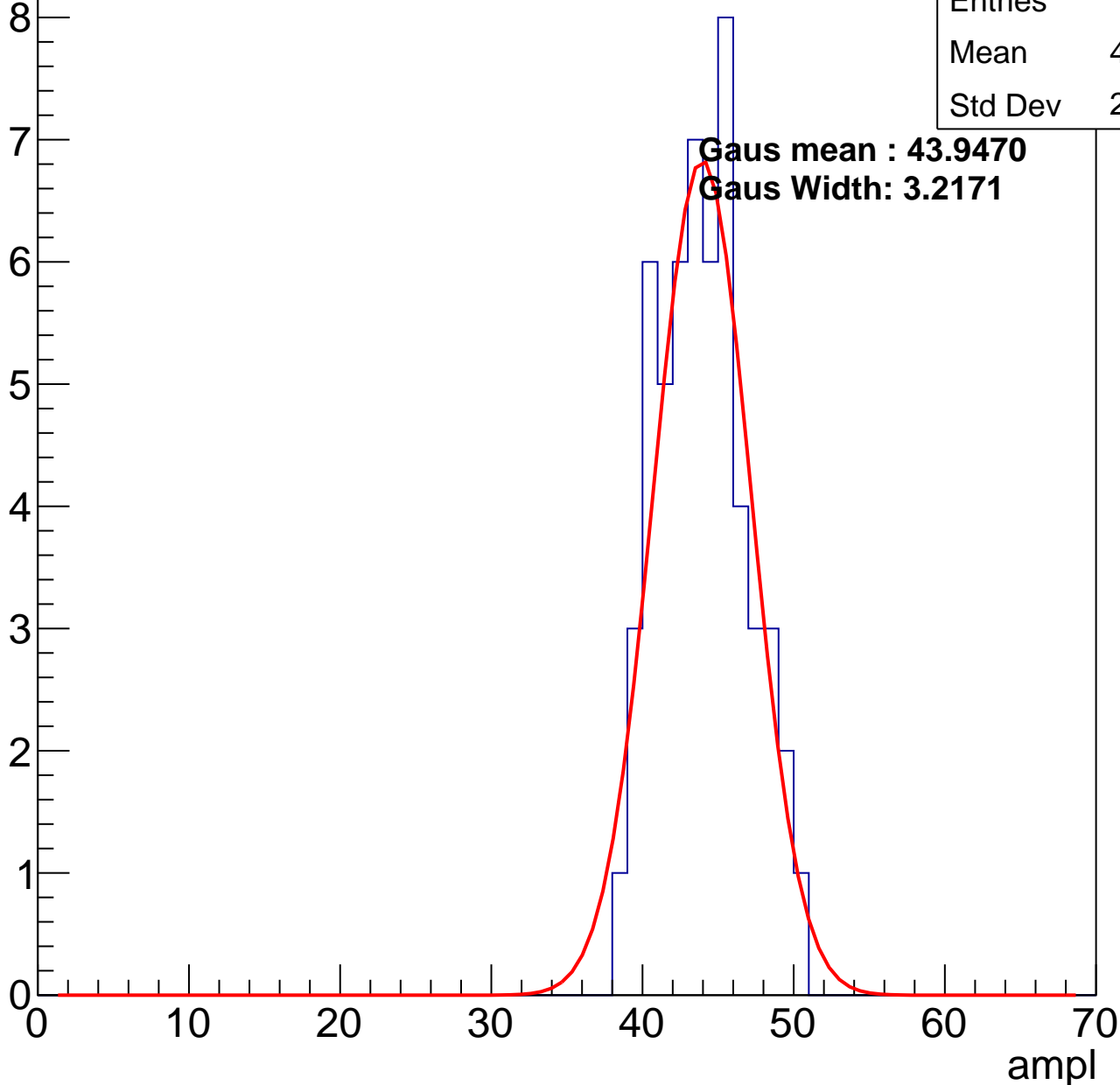
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.53
Std Dev	2.878

Gaus mean : 43.9470

Gaus Width: 3.2171

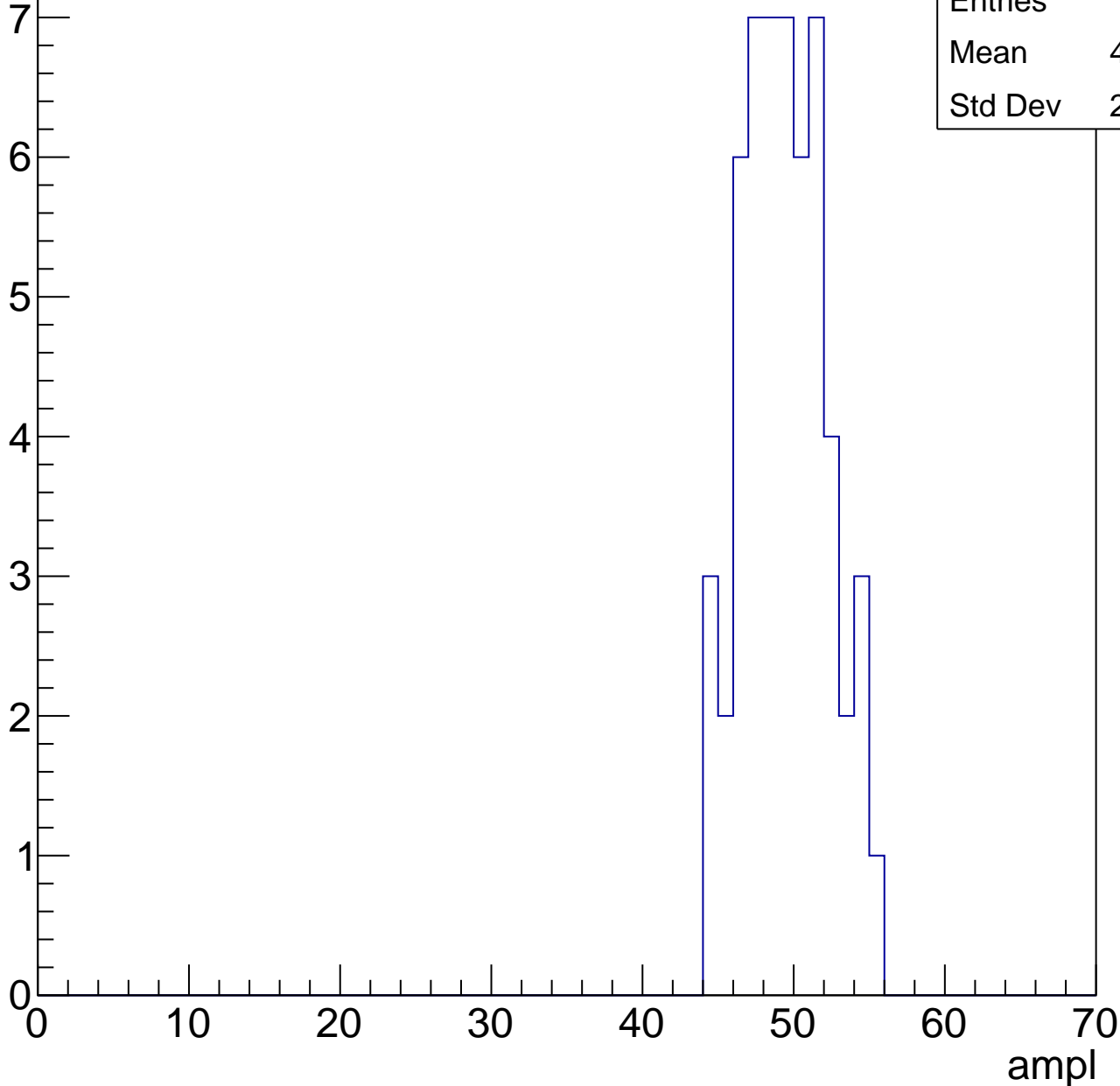


B1L101S, U1-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

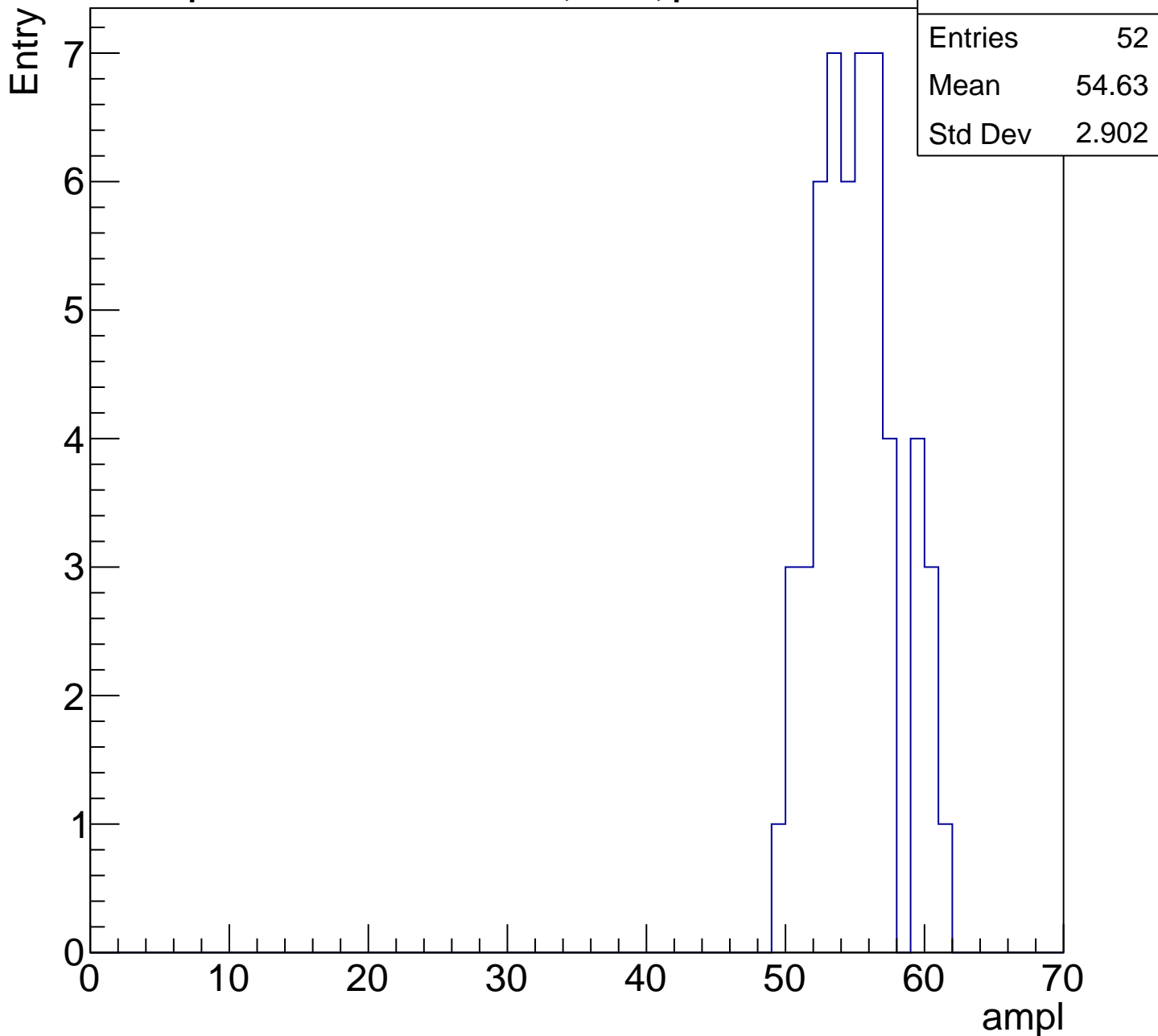
Entry

Entries	55
Mean	48.98
Std Dev	2.727



B1L101S, U1-ch20, adc4

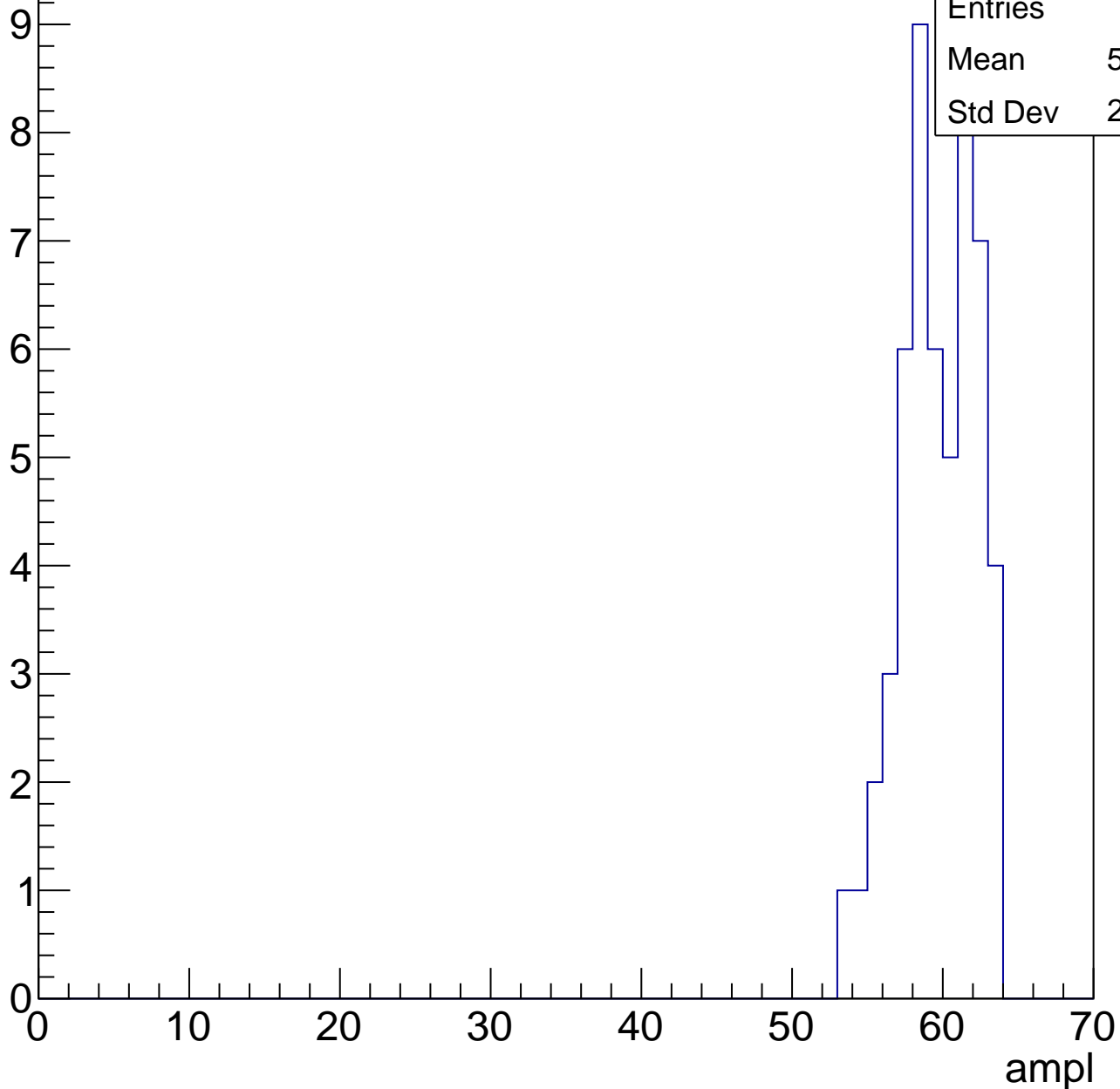
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



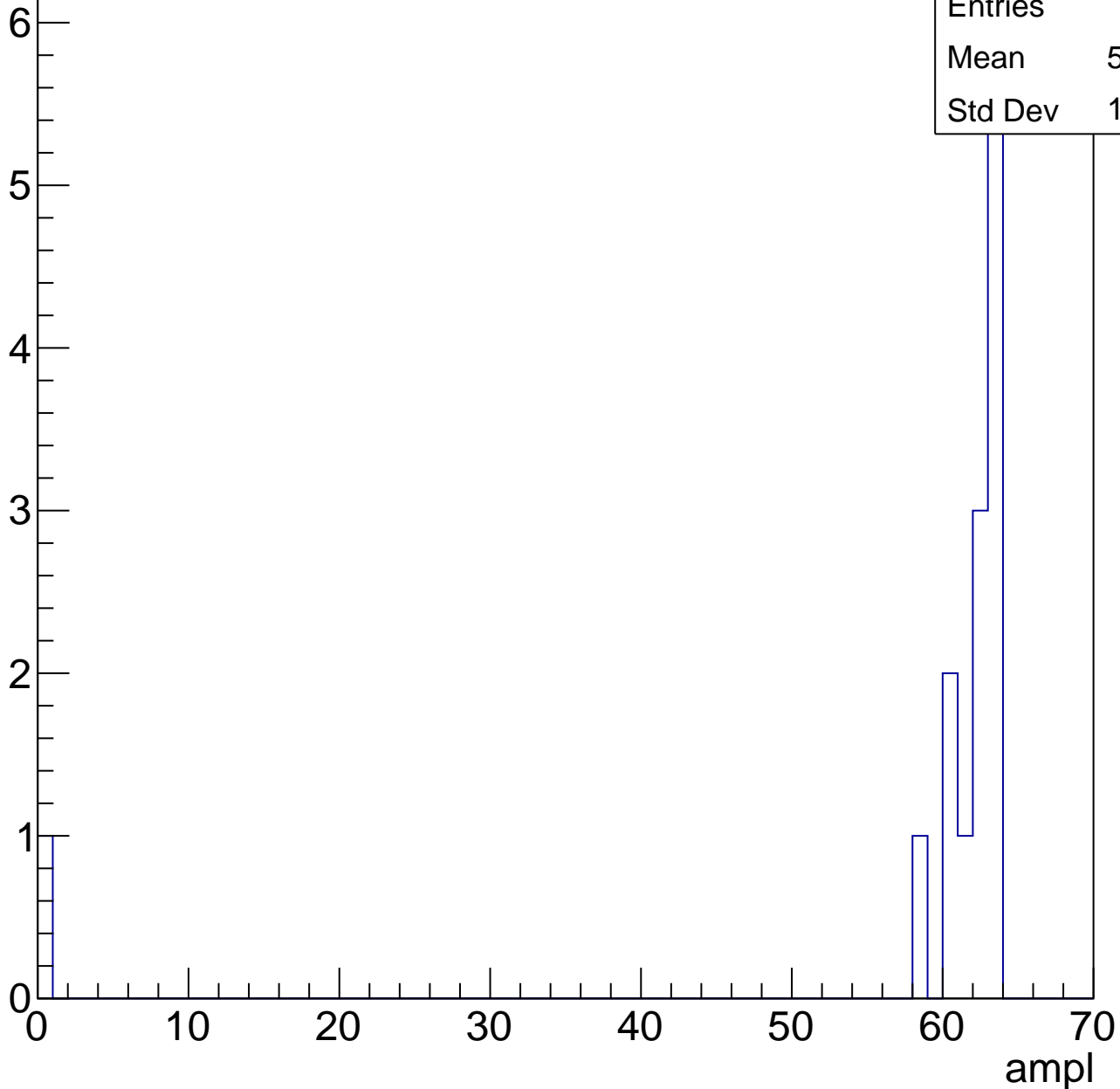
Entries	52
Mean	59.17
Std Dev	2.463

B1L101S, U1-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.36
Std Dev	15.98



B1L101S, U1-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch21, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	28.79
Std Dev	5.704

Gaus mean : 30.2187

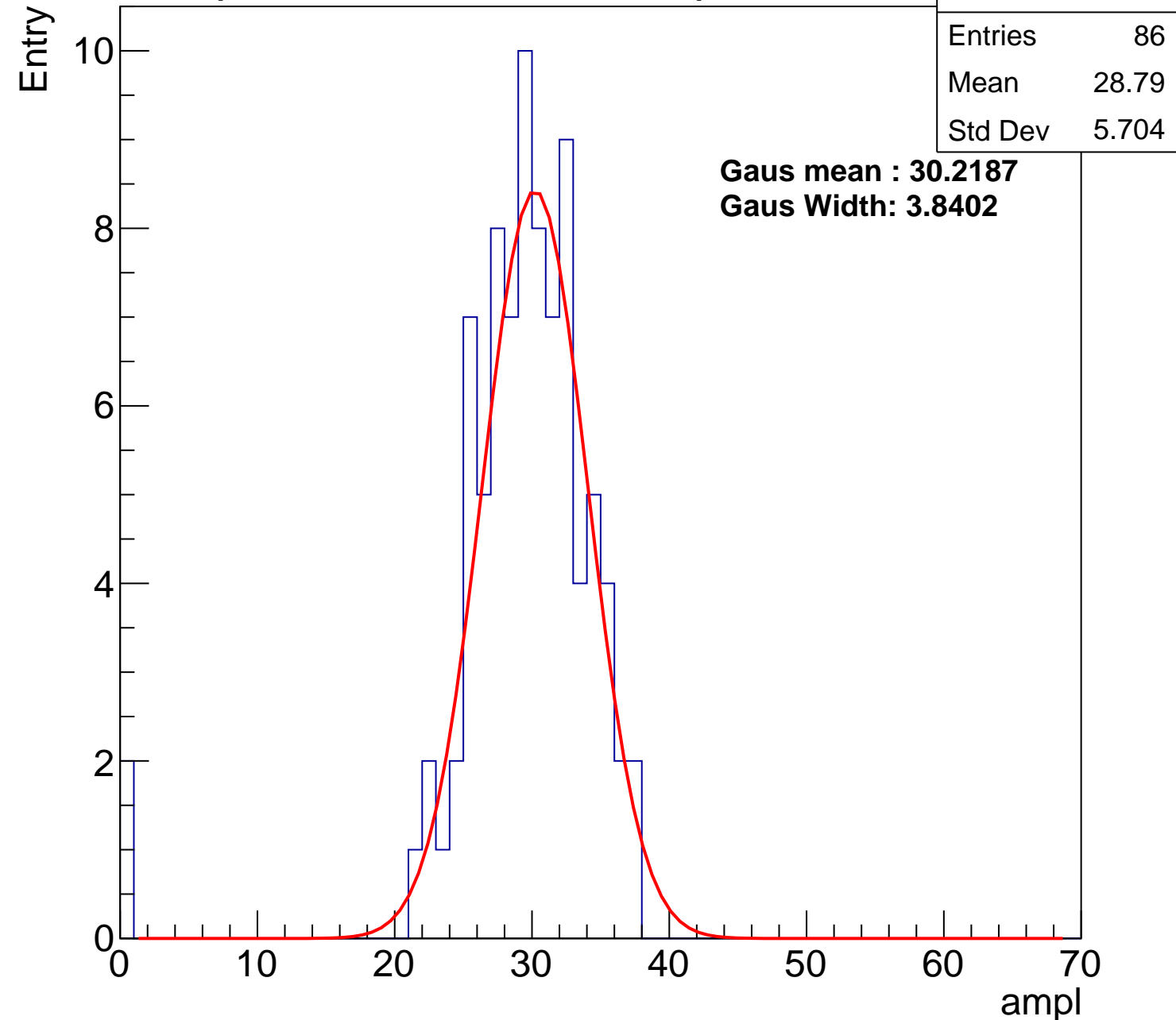
Gaus Width: 3.8402

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch21, adc1

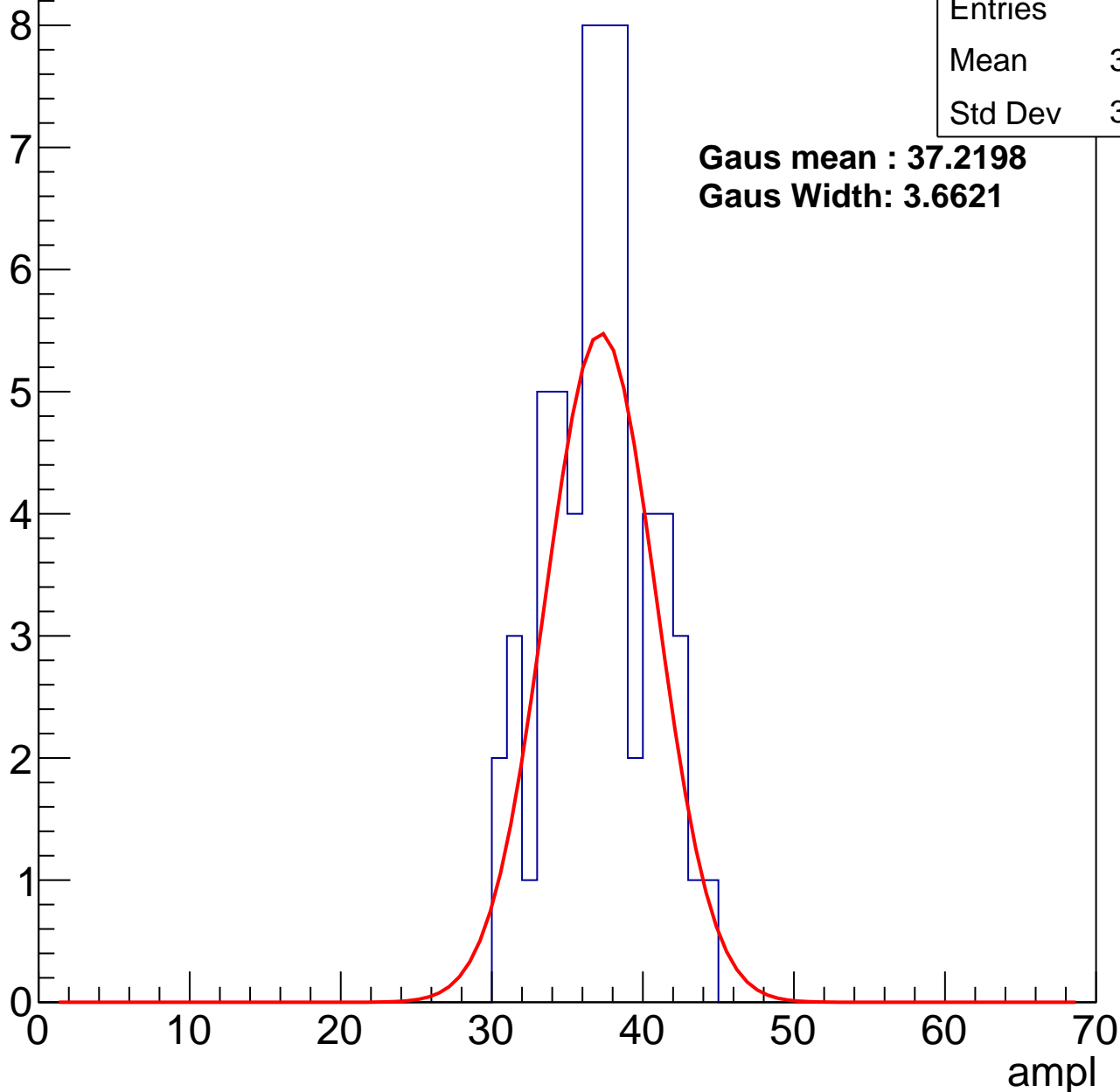
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.66
Std Dev	3.317

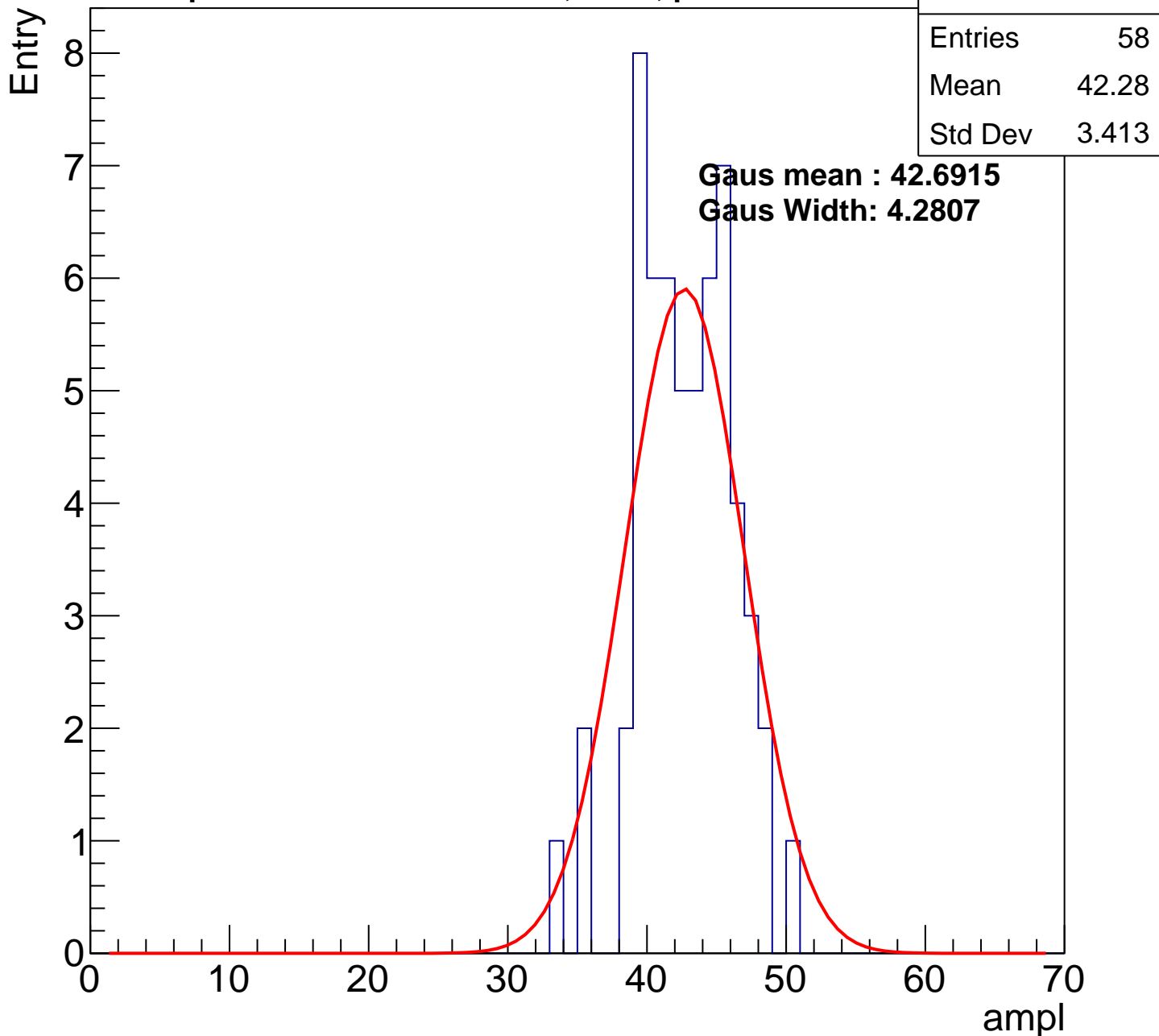
Gaus mean : 37.2198

Gaus Width: 3.6621



B1L101S, U1-ch21, adc2

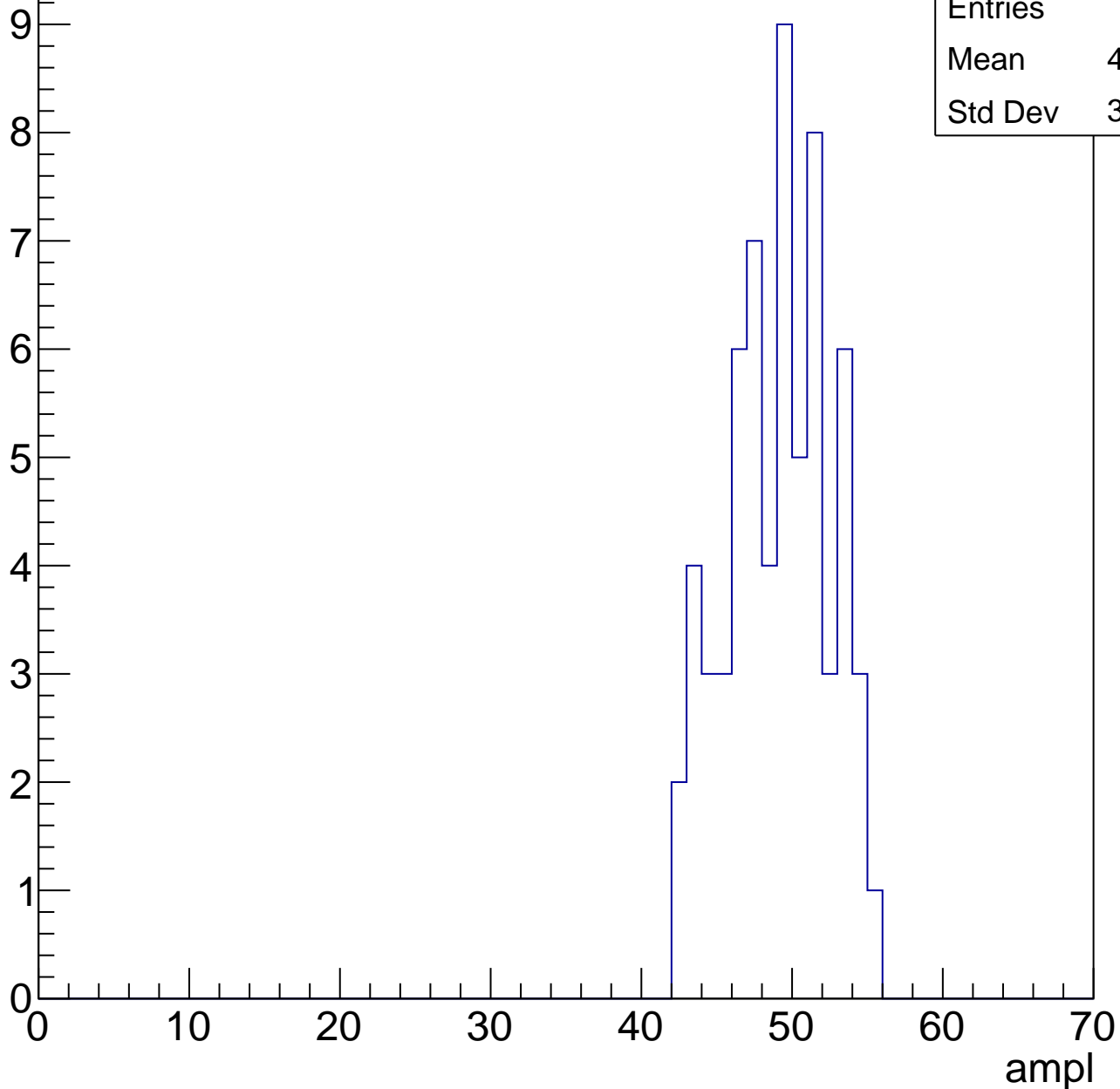
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

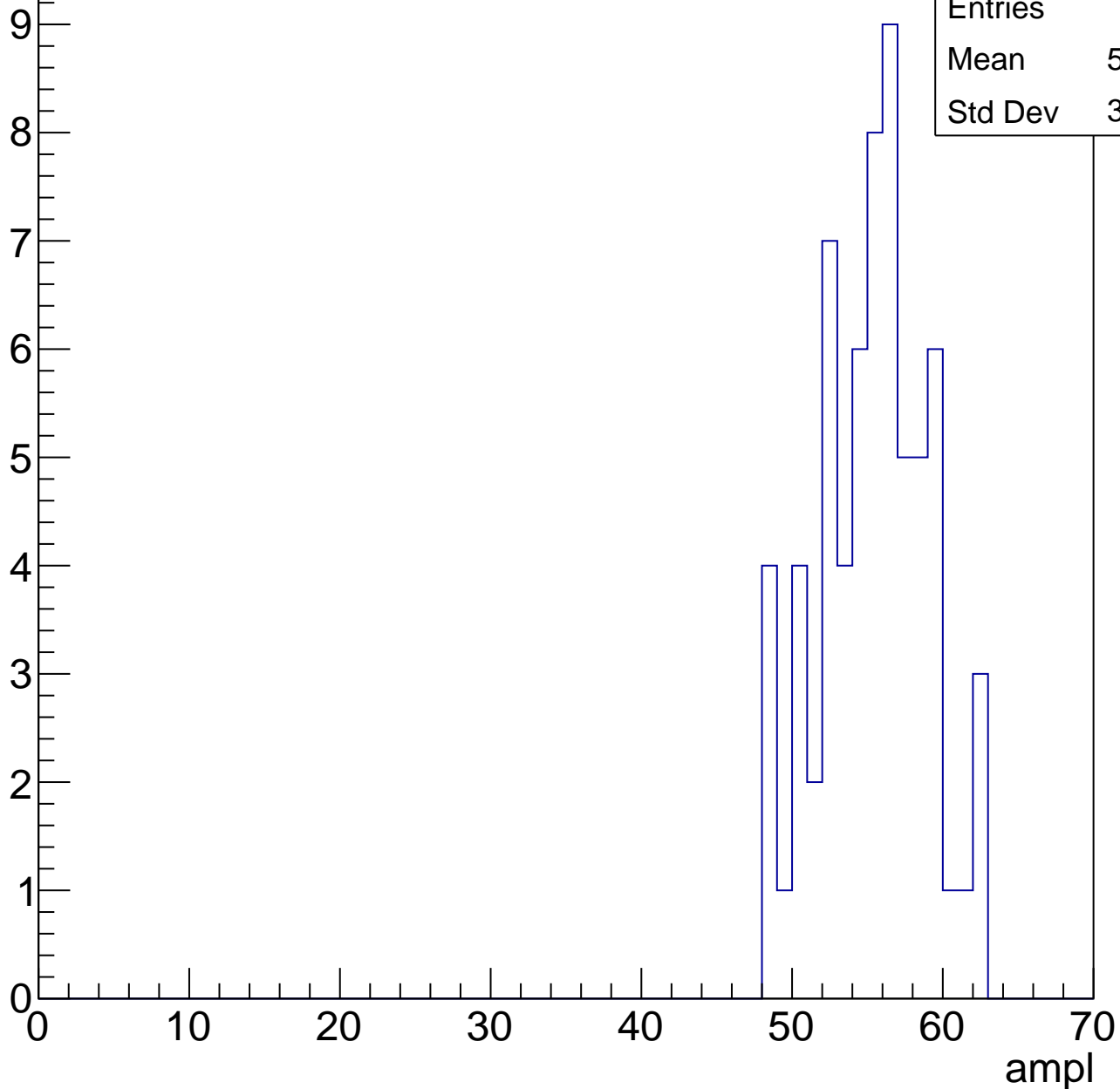
Entry



B1L101S, U1-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

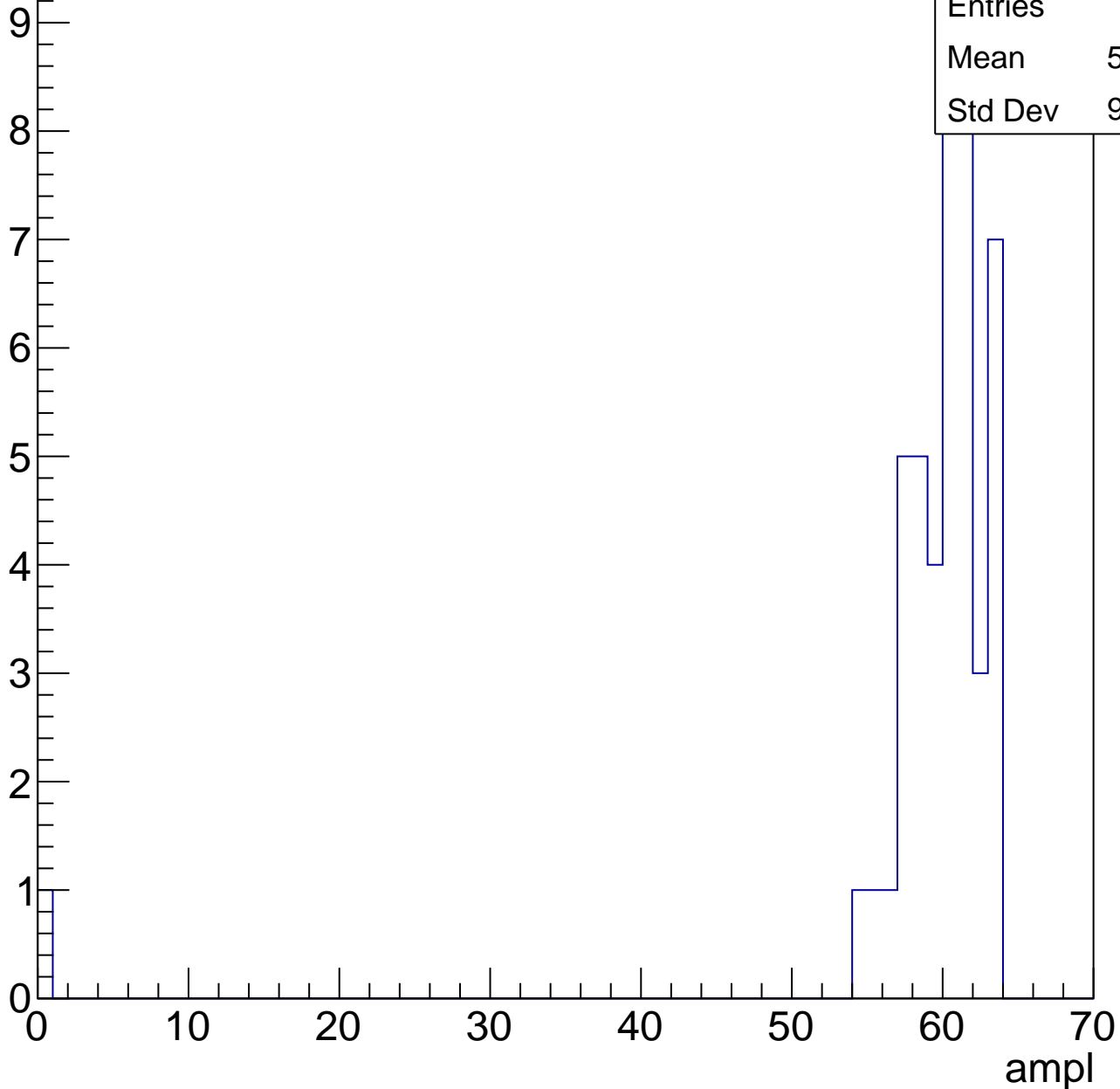


B1L101S, U1-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

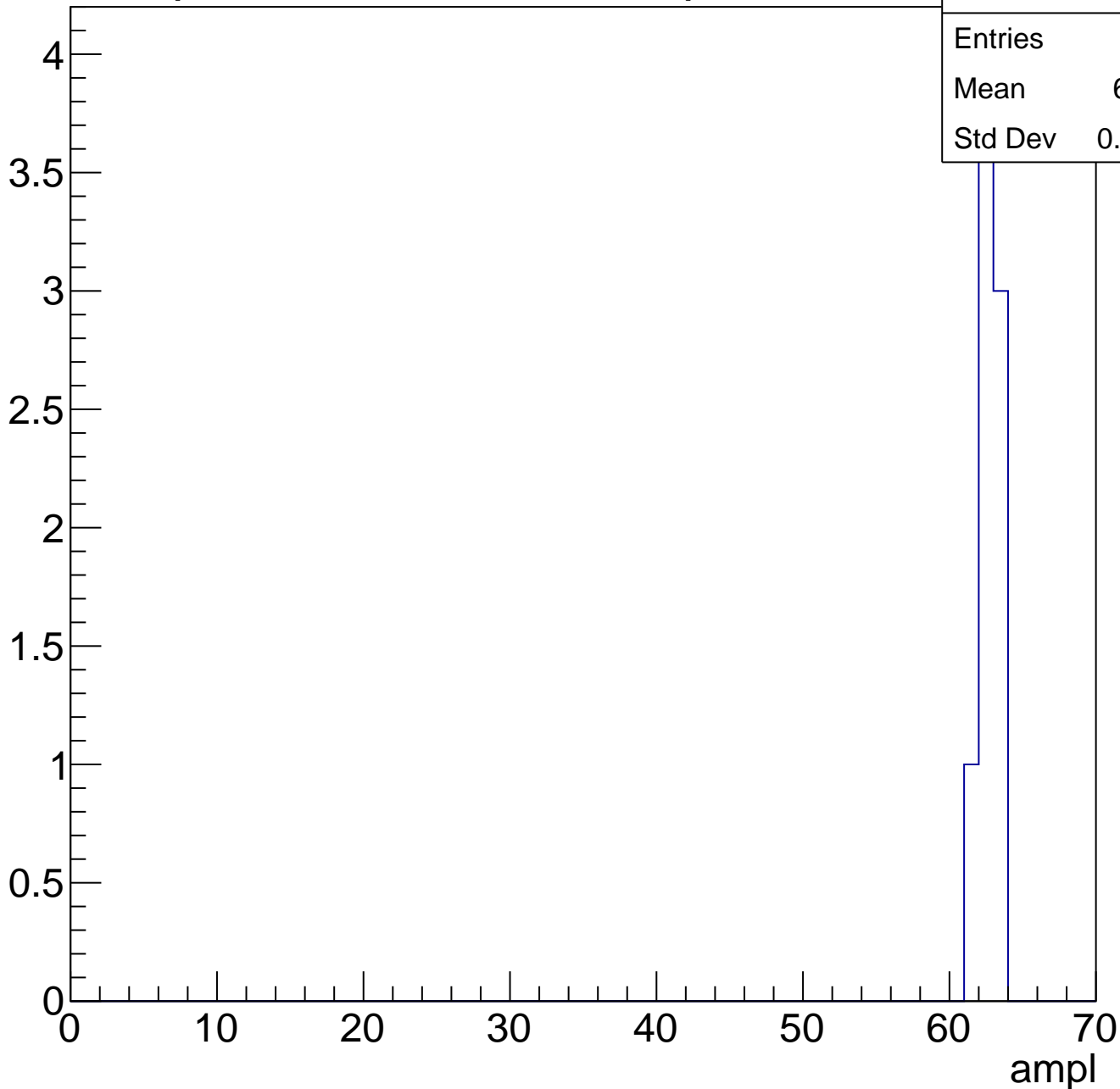
Entries	45
Mean	58.47
Std Dev	9.096



B1L101S, U1-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch22, adc0

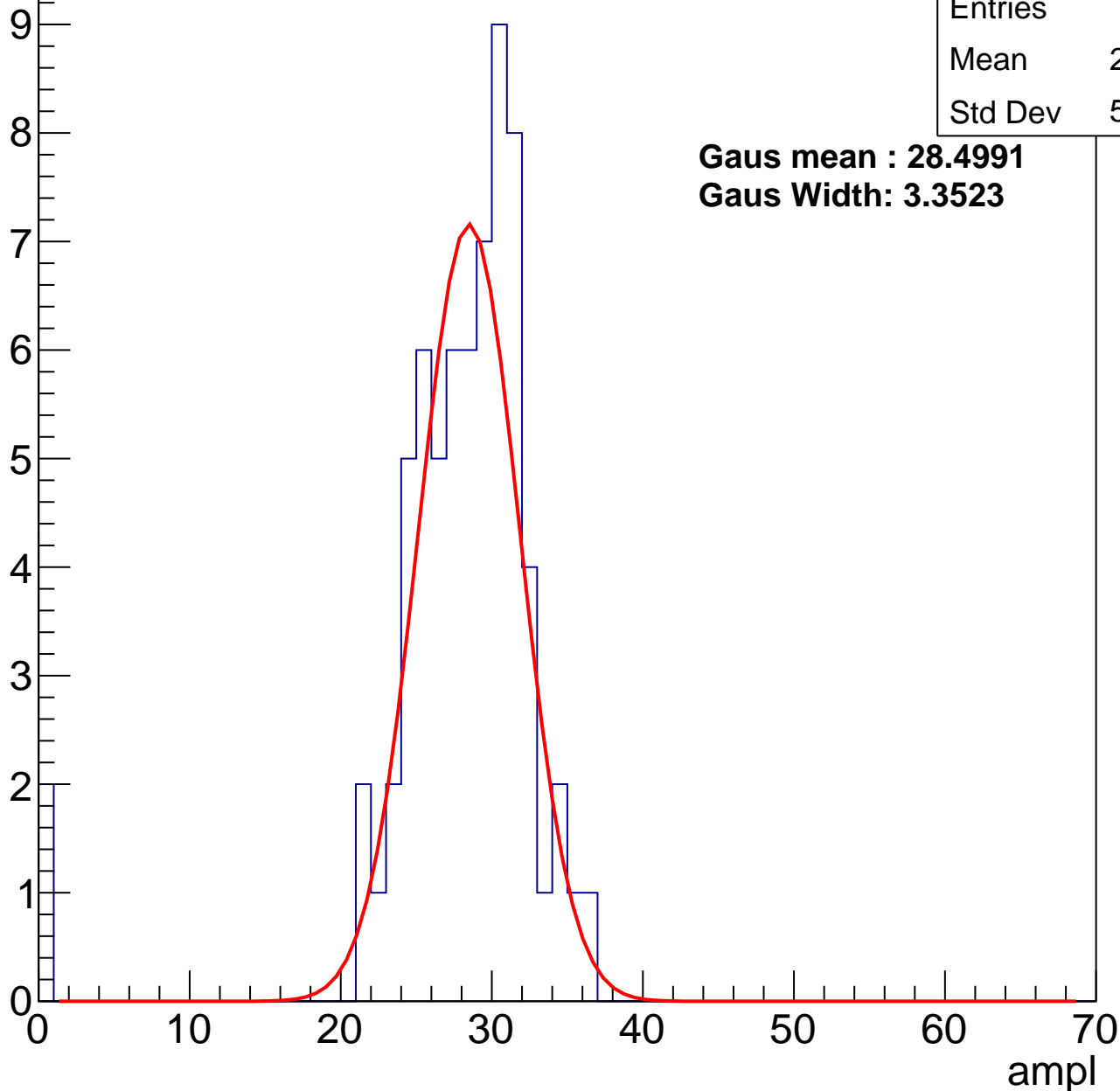
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	27.37
Std Dev	5.783

Gaus mean : 28.4991

Gaus Width: 3.3523



B1L101S, U1-ch22, adc1

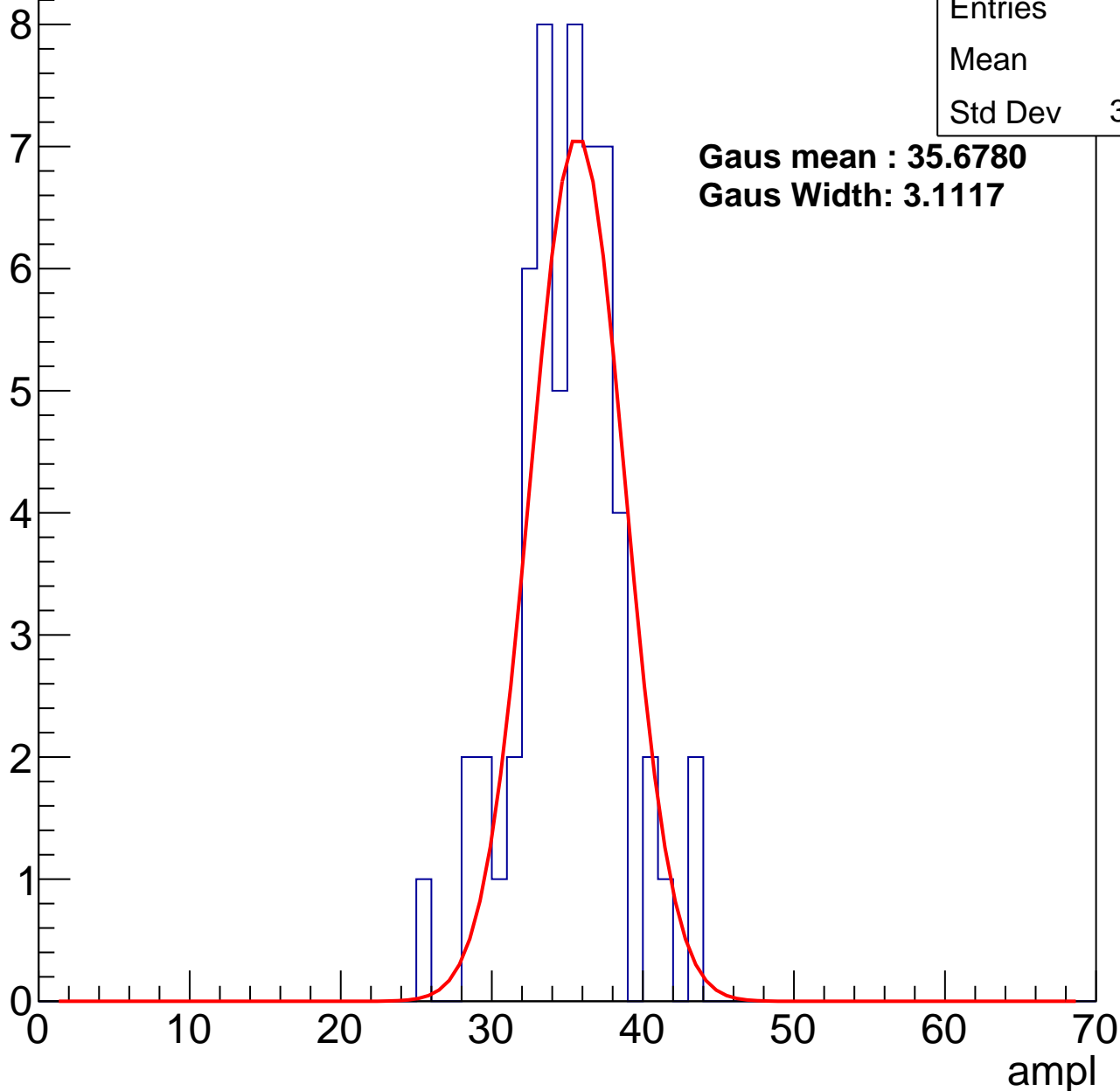
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	34.6
Std Dev	3.459

Gaus mean : 35.6780

Gaus Width: 3.1117



B1L101S, U1-ch22, adc2

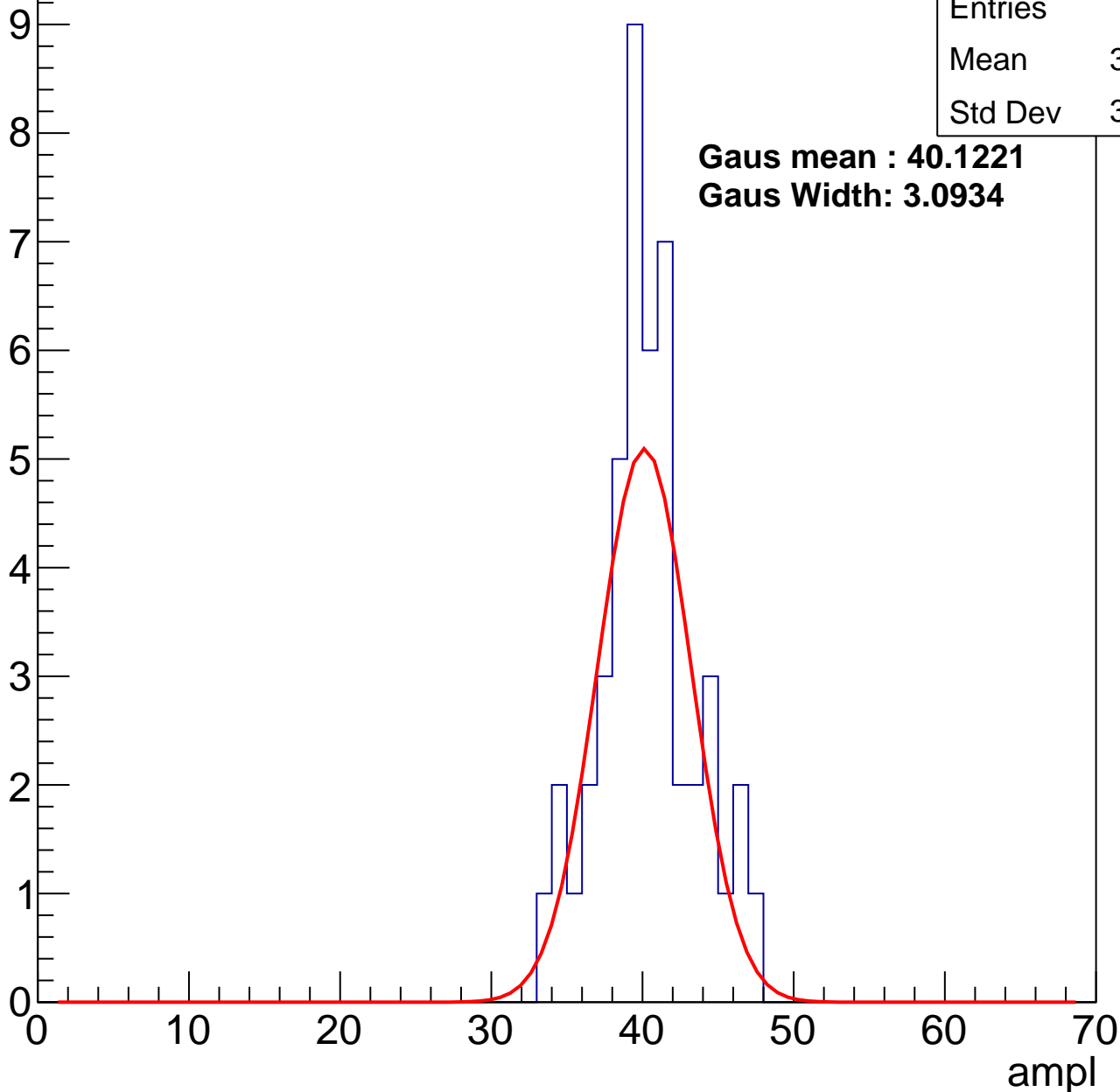
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	39.85
Std Dev	3.128

Gaus mean : 40.1221

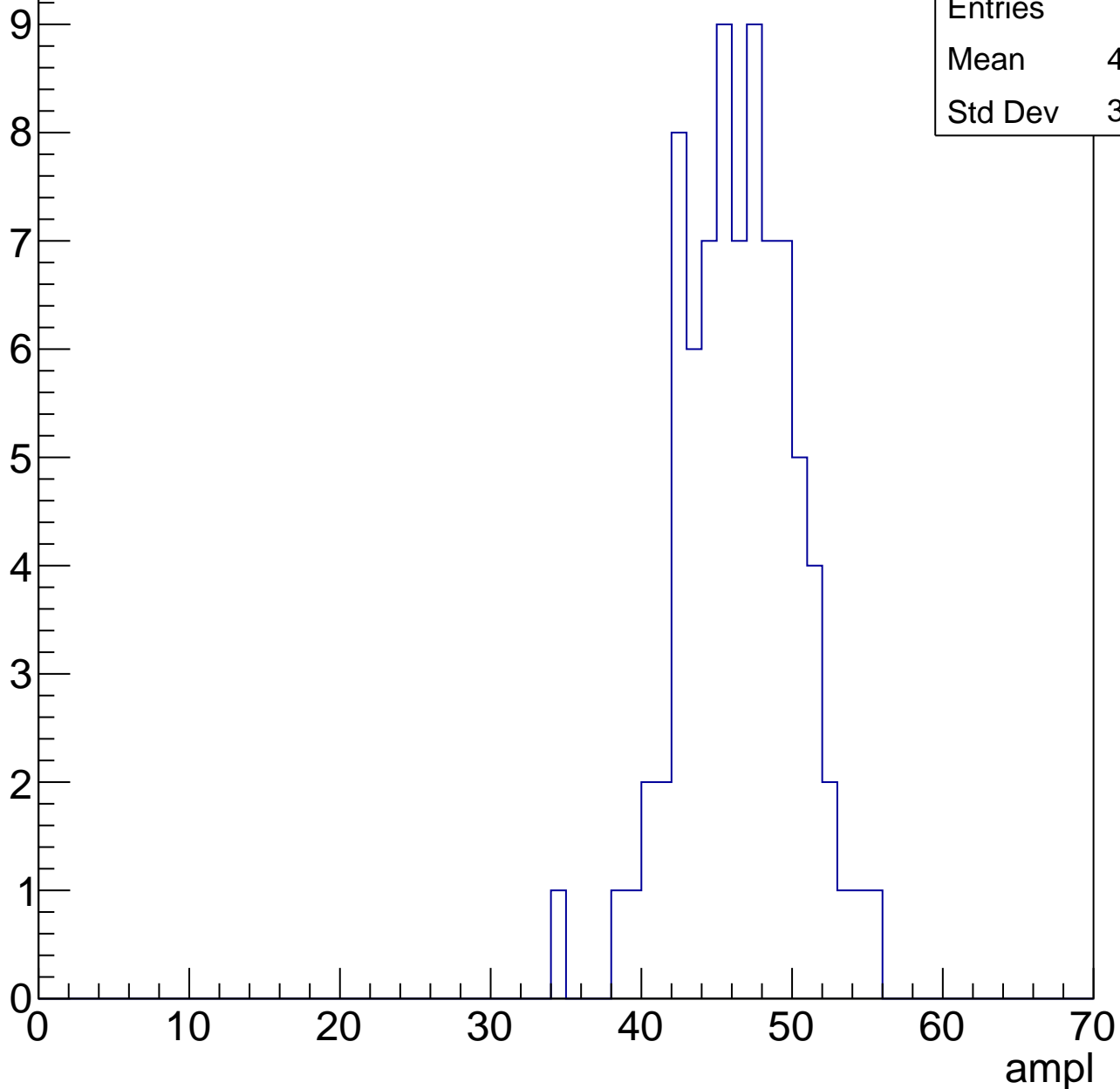
Gaus Width: 3.0934



B1L101S, U1-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

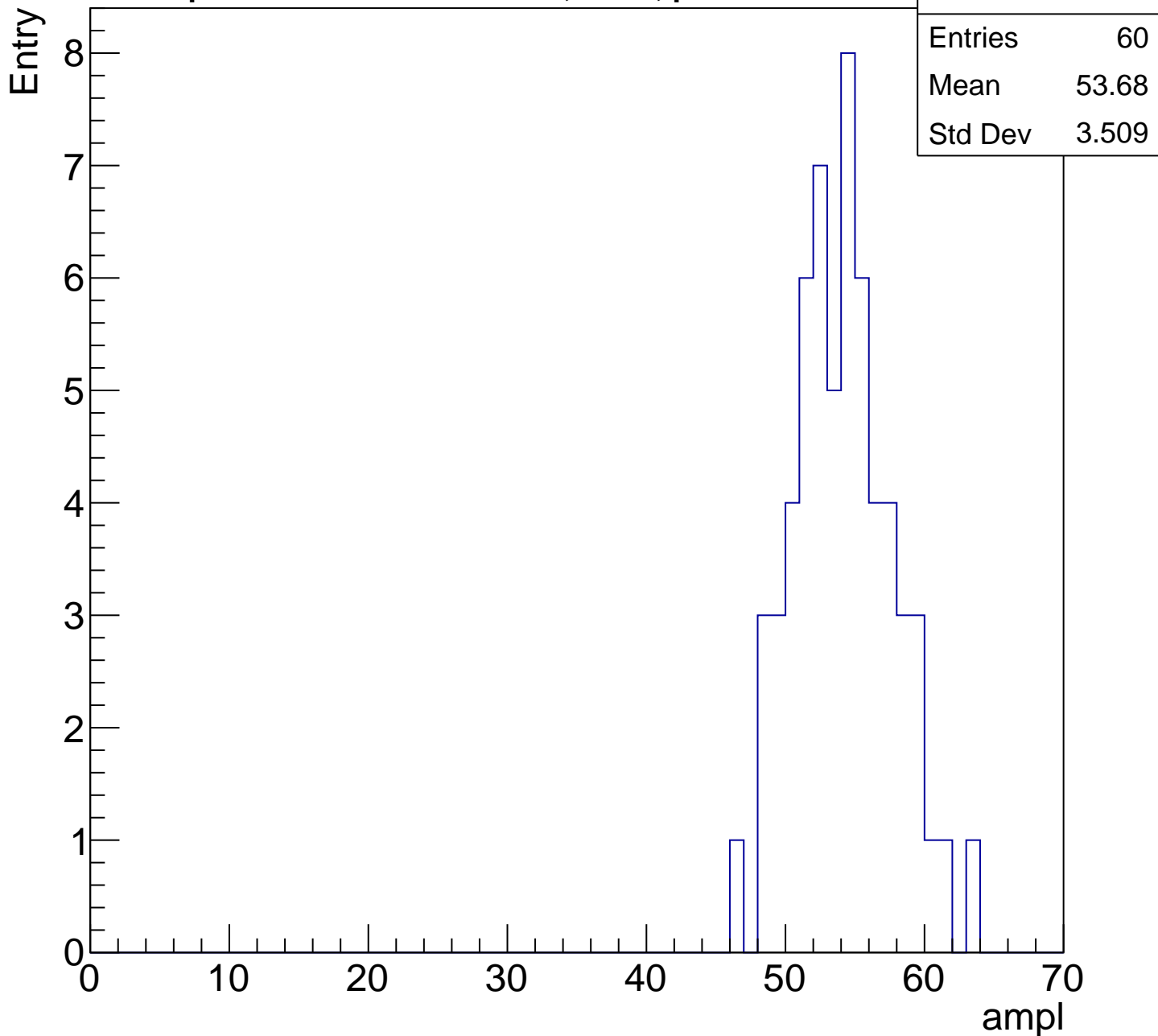
Entry



Entries	81
Mean	45.98
Std Dev	3.758

B1L101S, U1-ch22, adc4

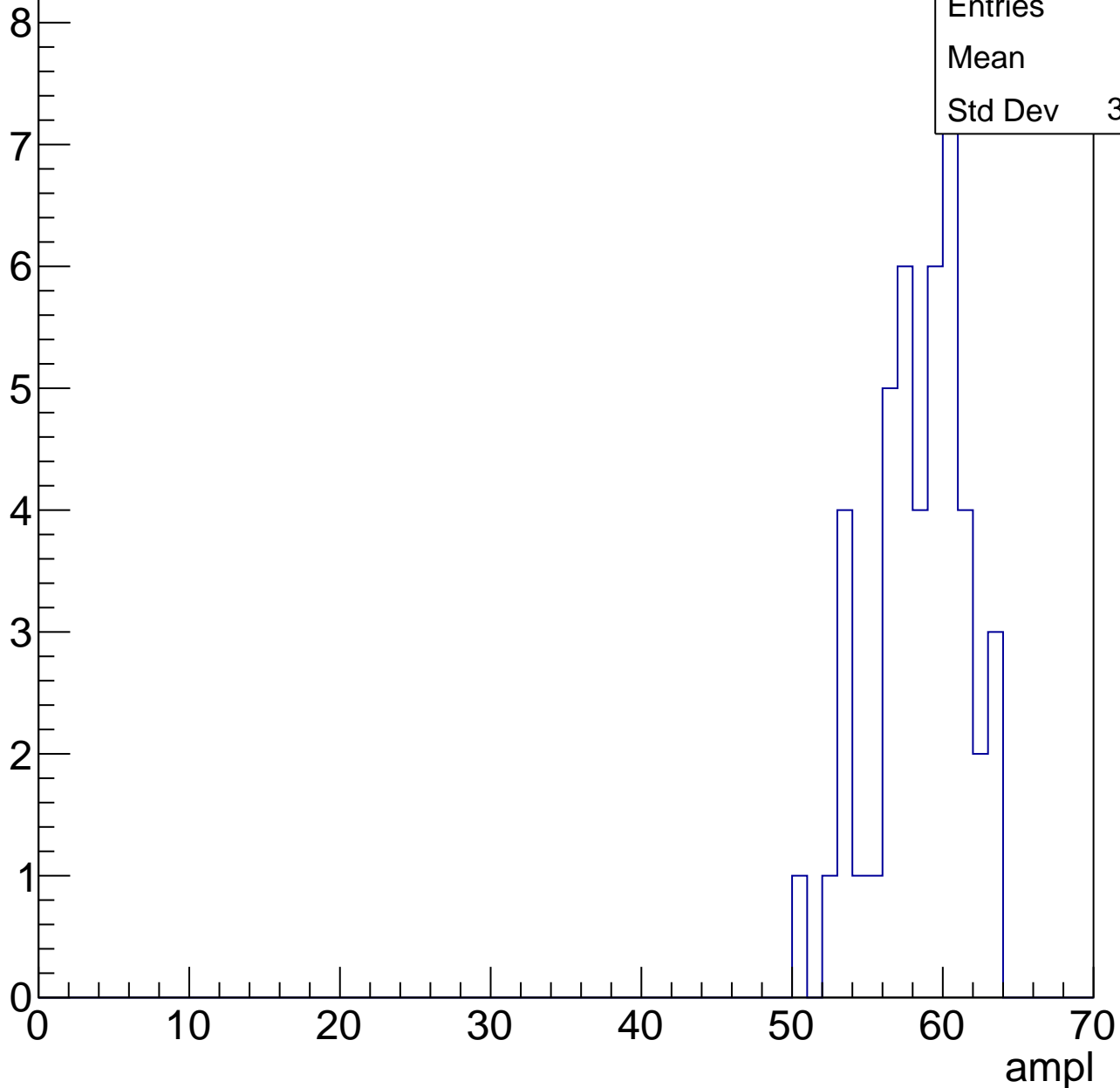
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

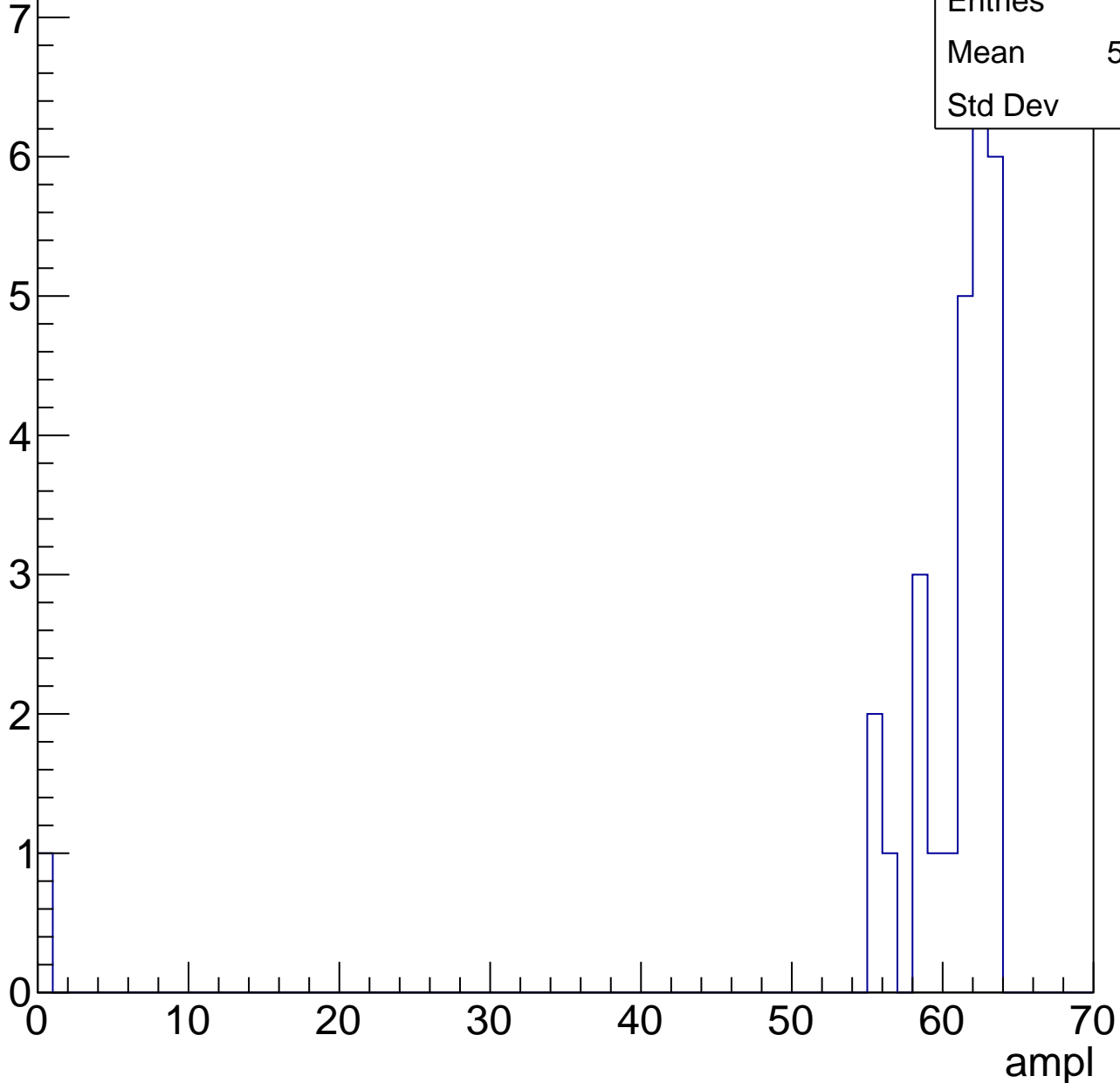


B1L101S, U1-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

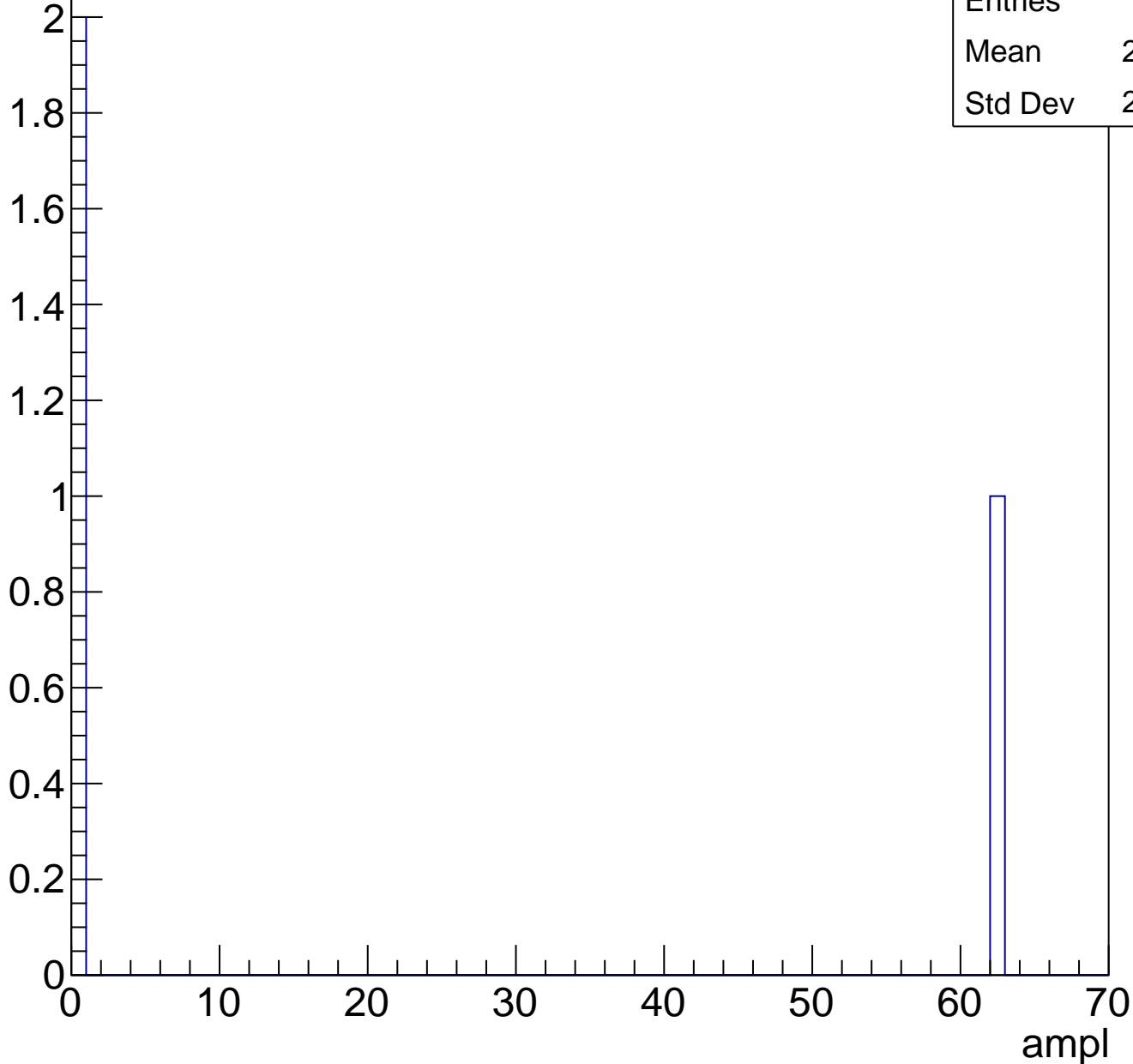
Entries	27
Mean	58.37
Std Dev	11.7



B1L101S, U1-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch23, adc0

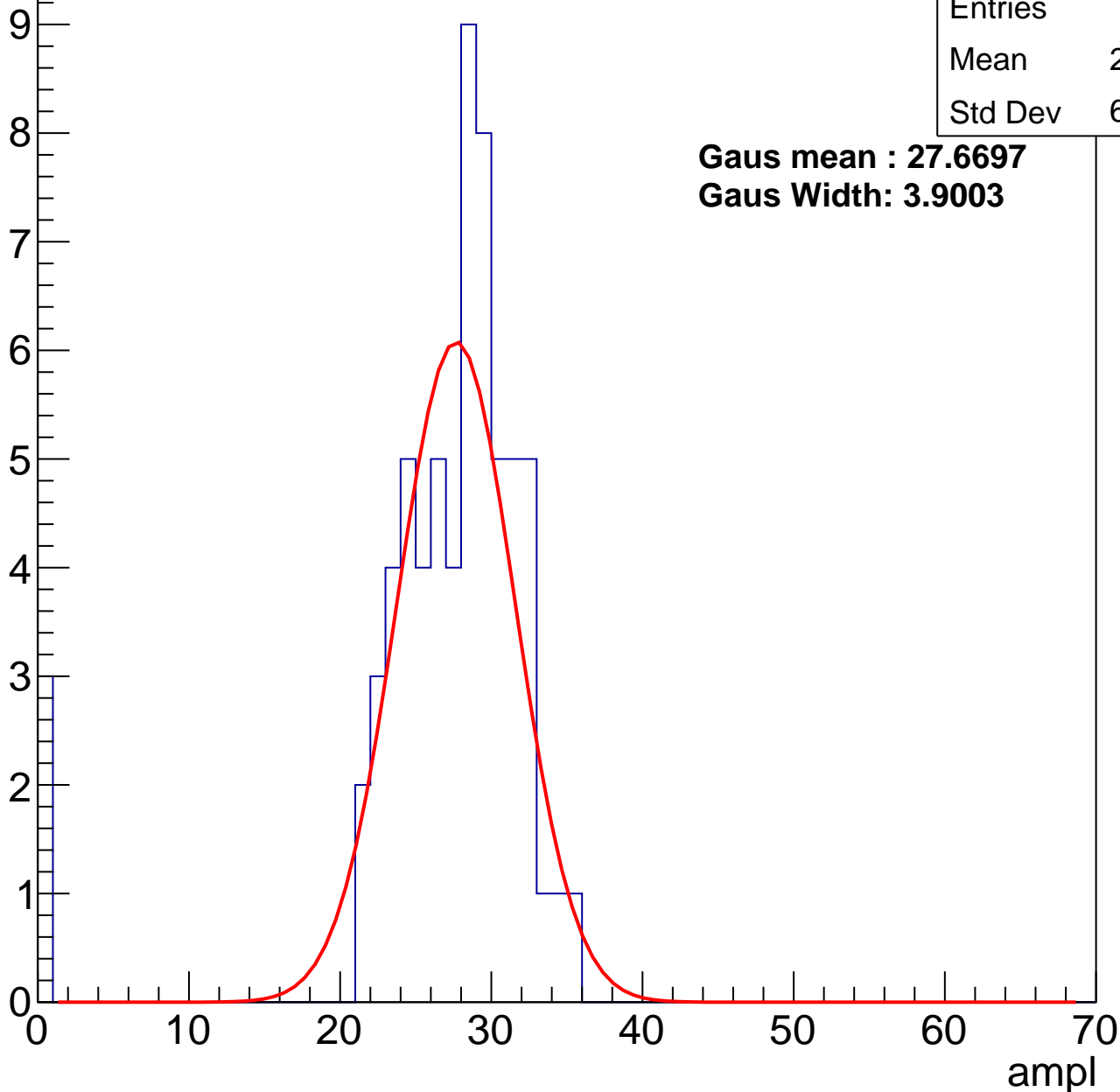
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	26.29
Std Dev	6.647

Gaus mean : 27.6697

Gaus Width: 3.9003



B1L101S, U1-ch23, adc1

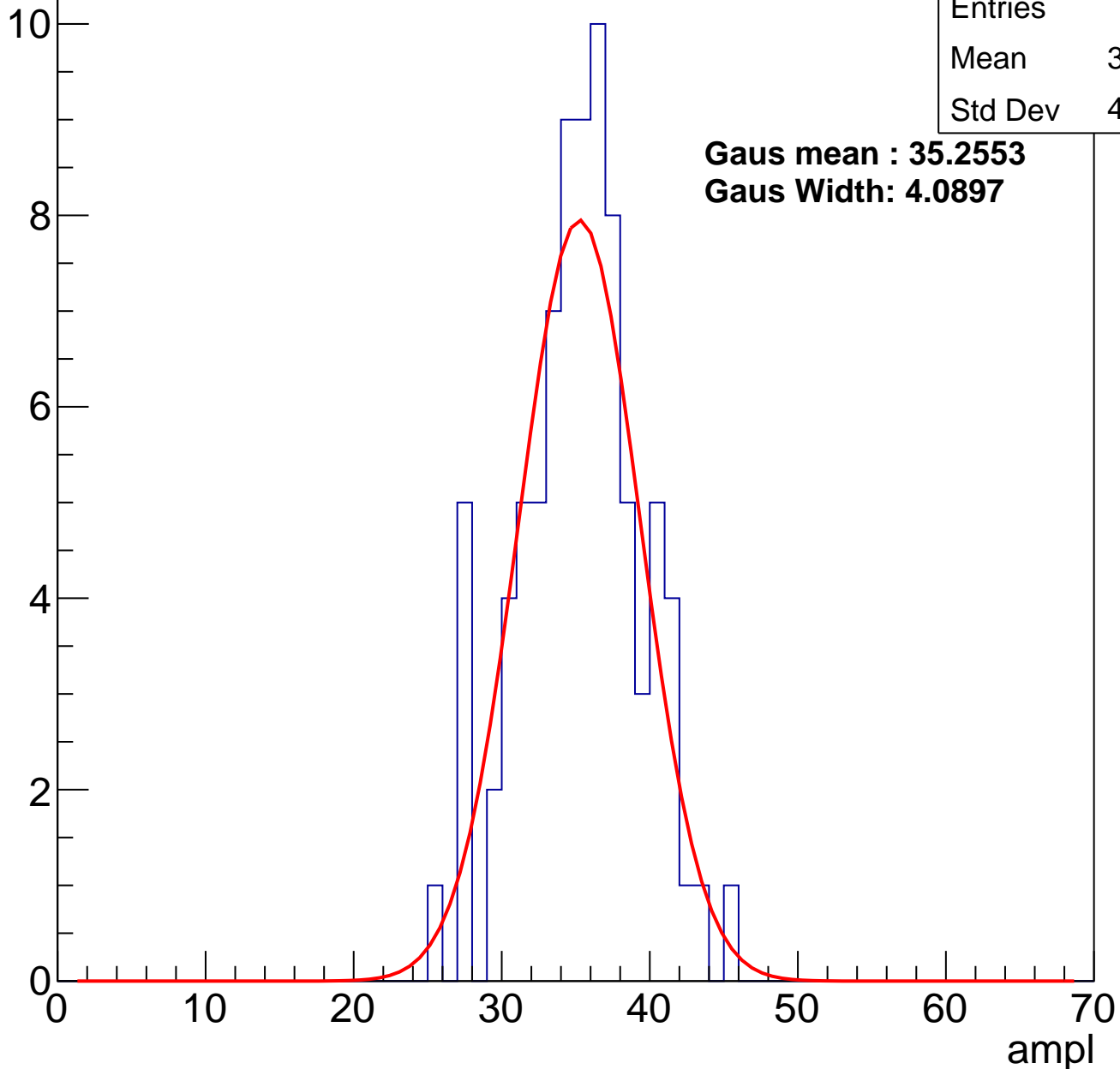
calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	34.85
Std Dev	4.013

Gaus mean : 35.2553

Gaus Width: 4.0897

Entry



B1L101S, U1-ch23, adc2

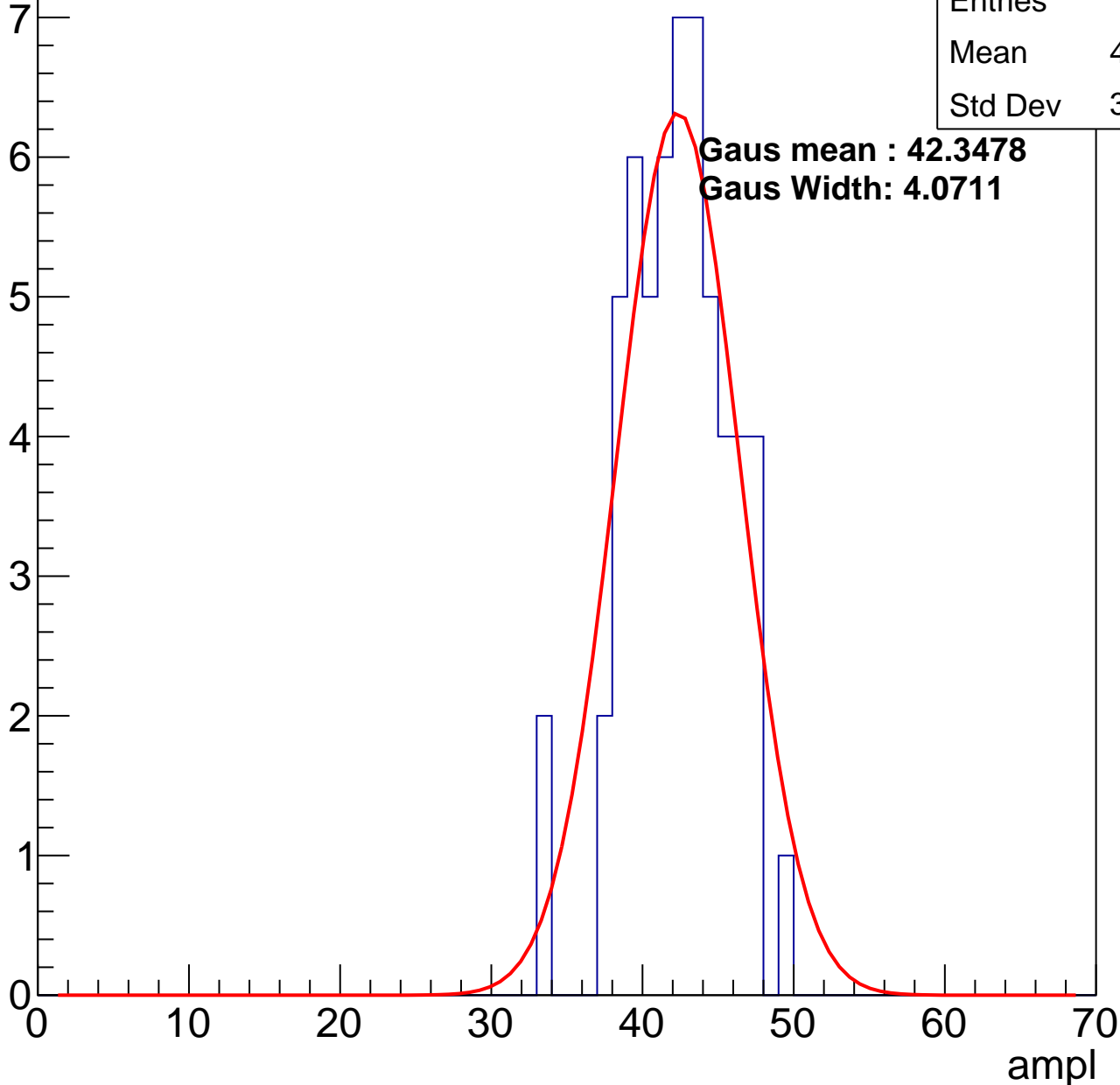
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.83
Std Dev	3.338

Gaus mean : 42.3478

Gaus Width: 4.0711

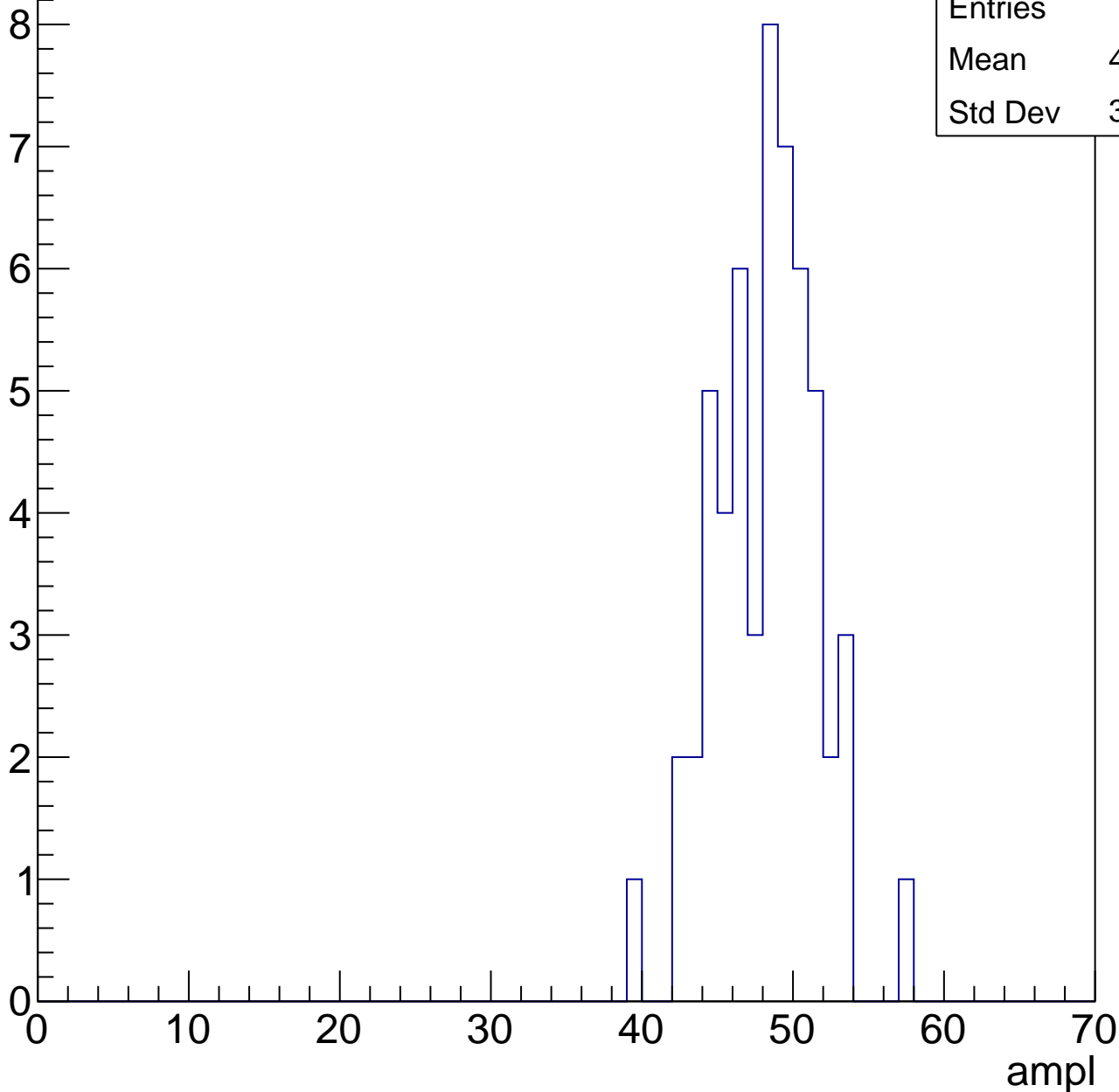


B1L101S, U1-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	47.78
Std Dev	3.323

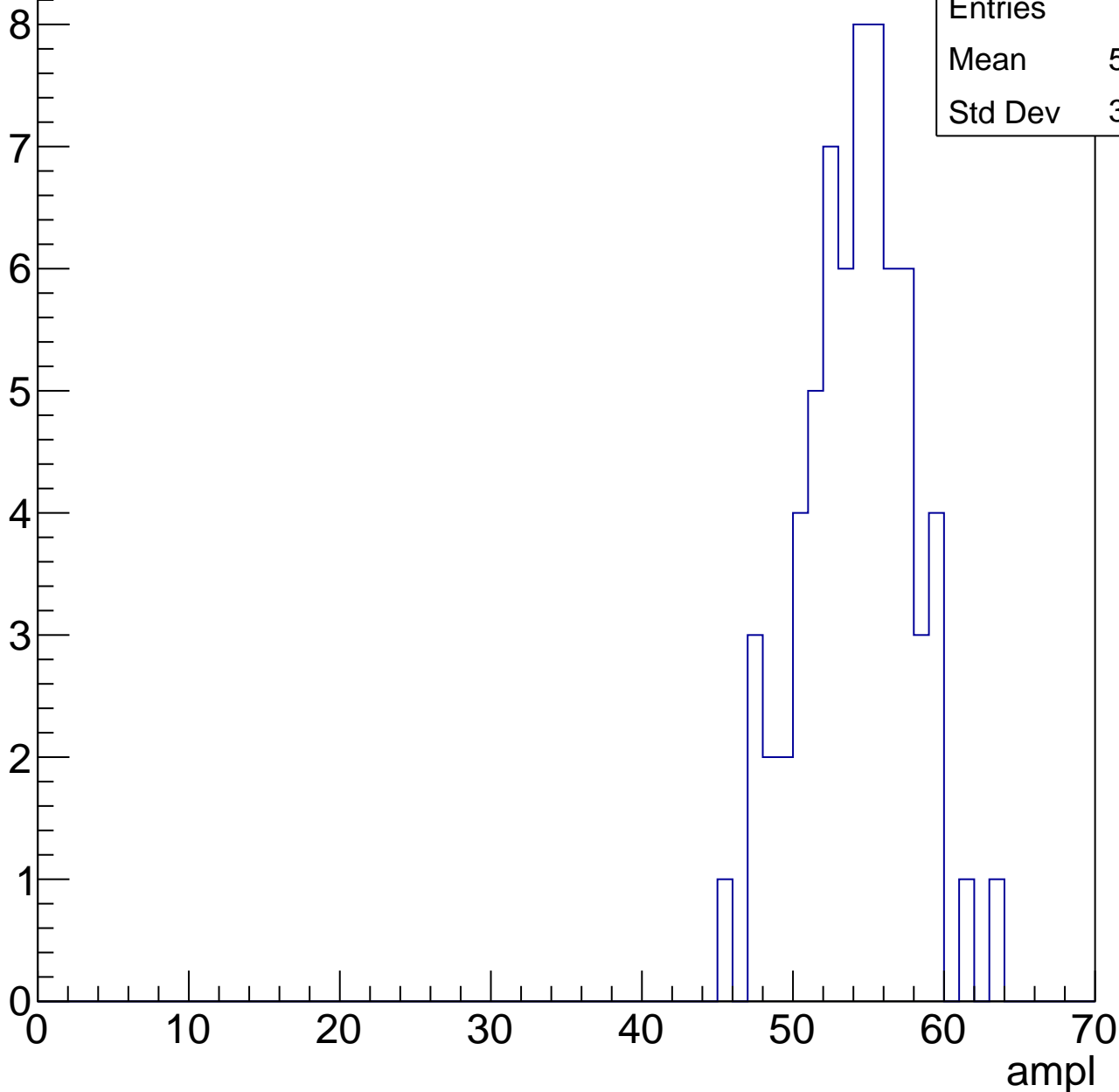


B1L101S, U1-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

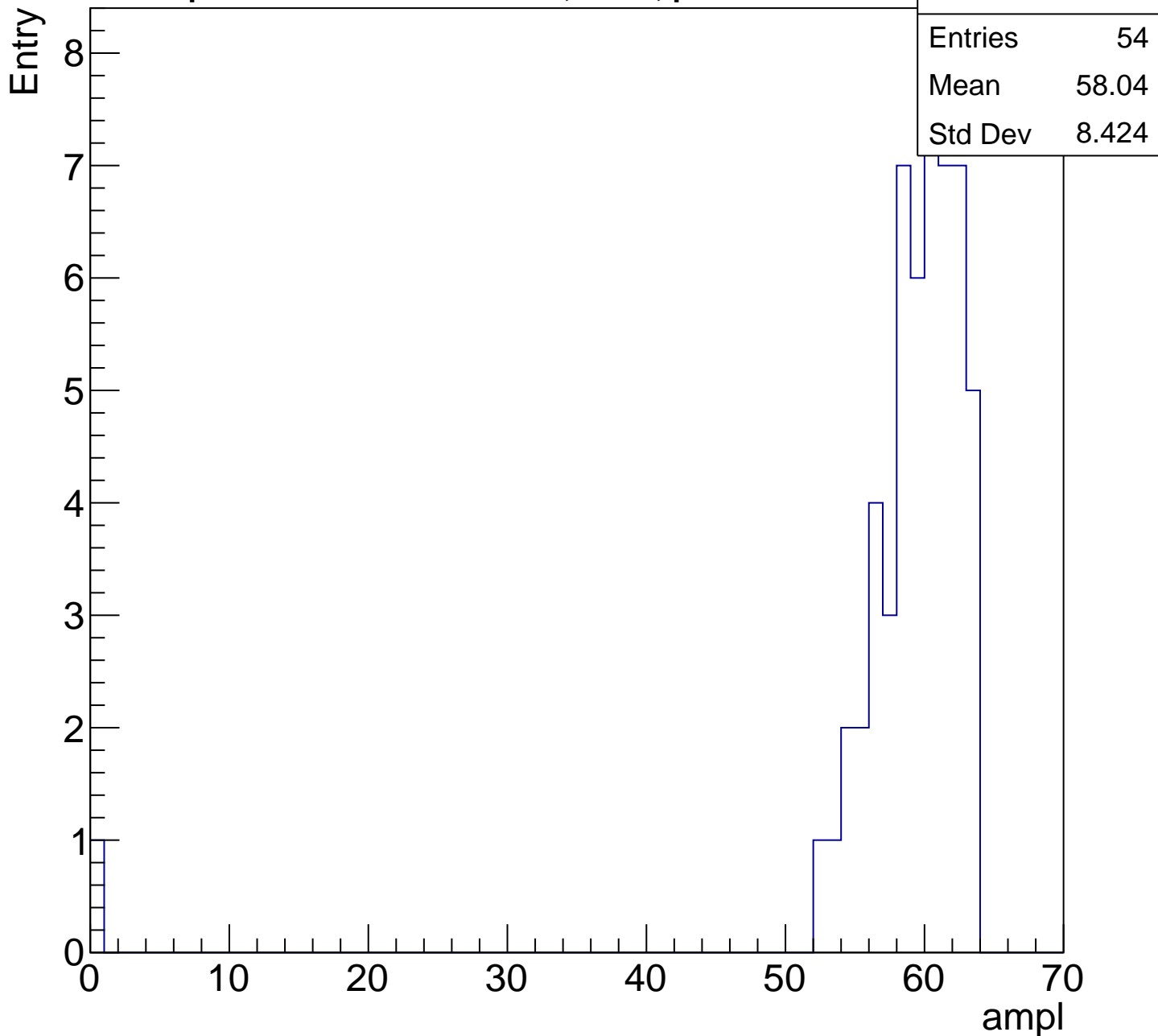
Entry

Entries	67
Mean	53.75
Std Dev	3.567



B1L101S, U1-ch23, adc5

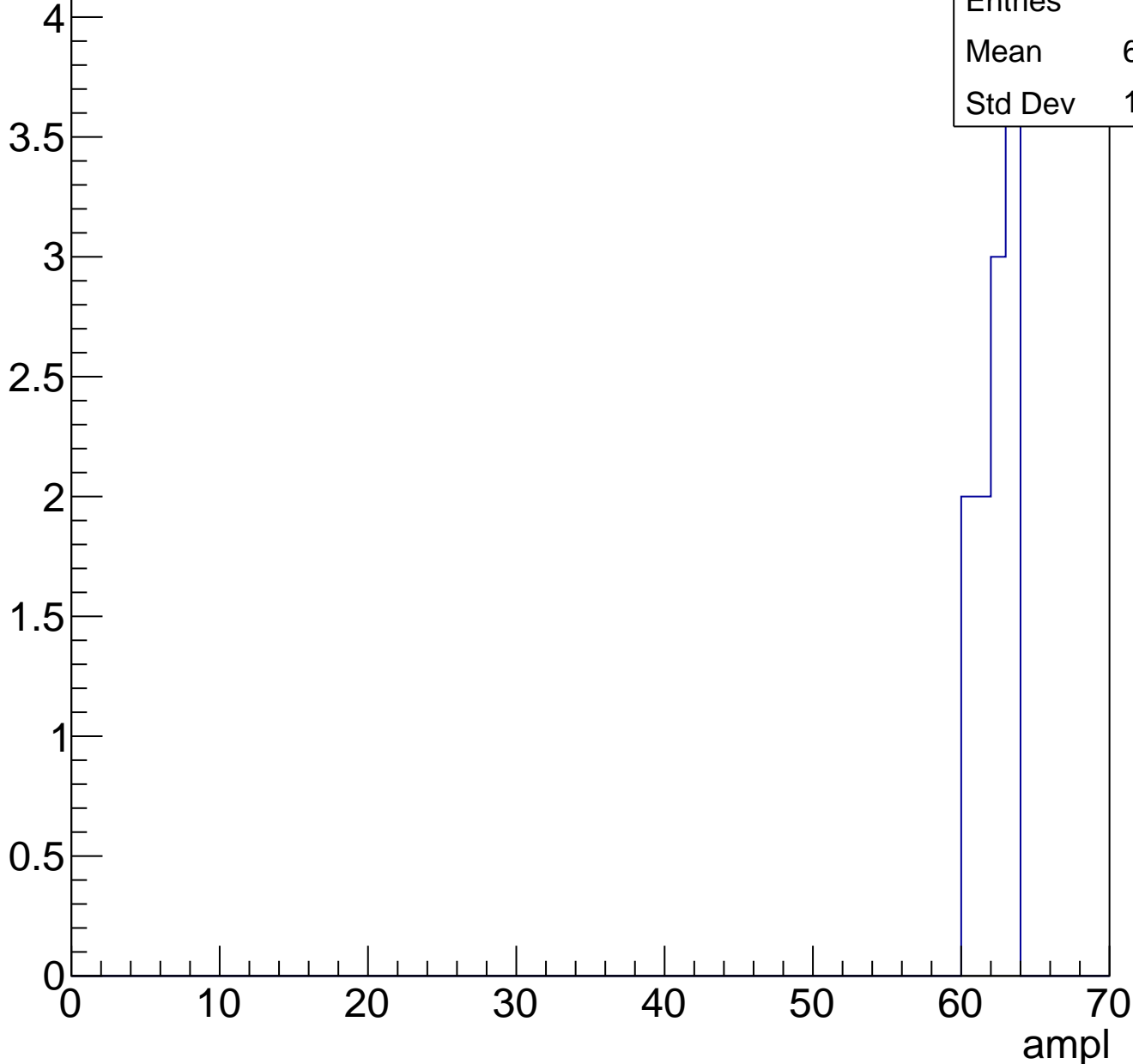
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch24, adc0

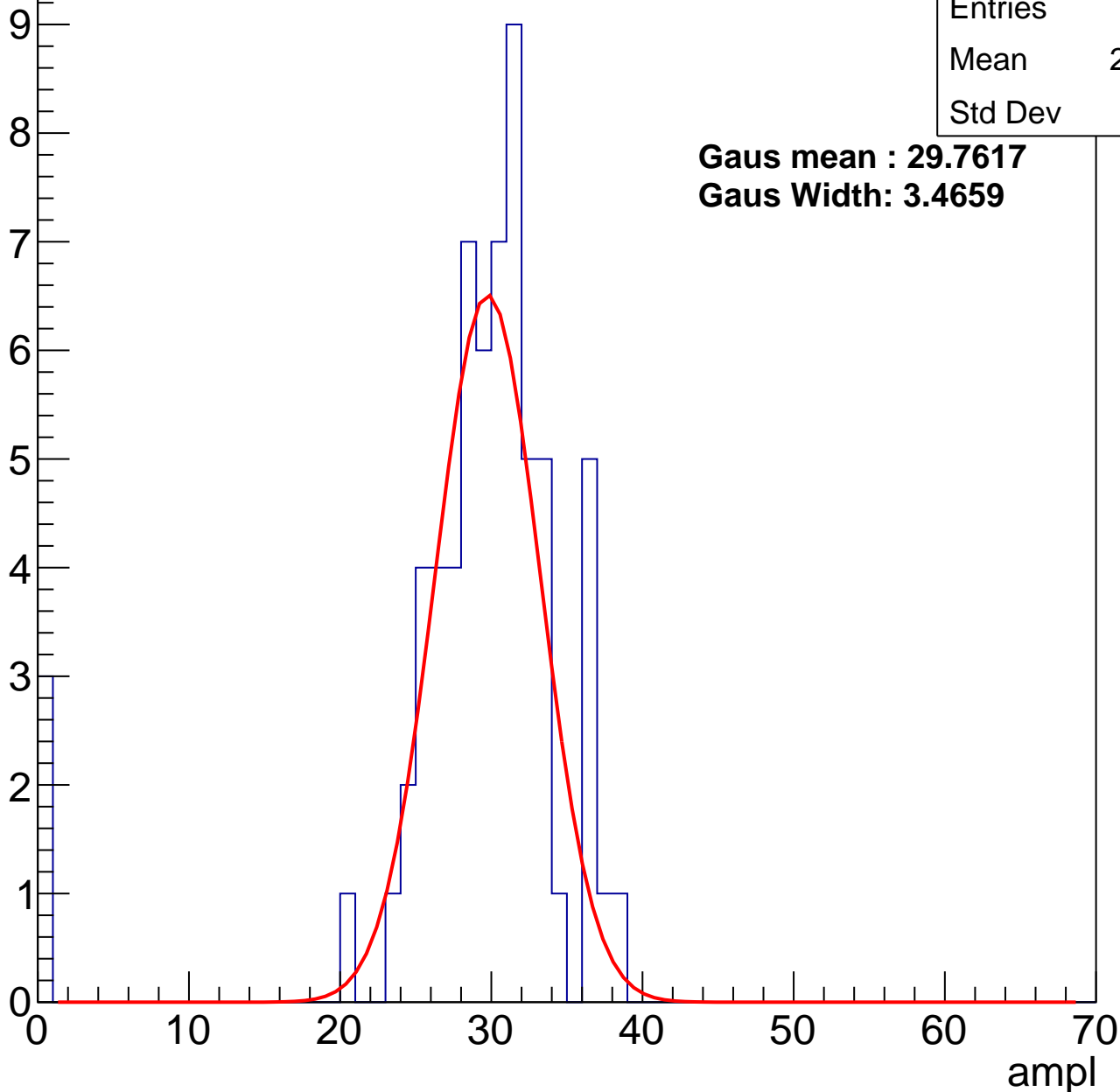
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.42
Std Dev	7.15

Gaus mean : 29.7617

Gaus Width: 3.4659



B1L101S, U1-ch24, adc1

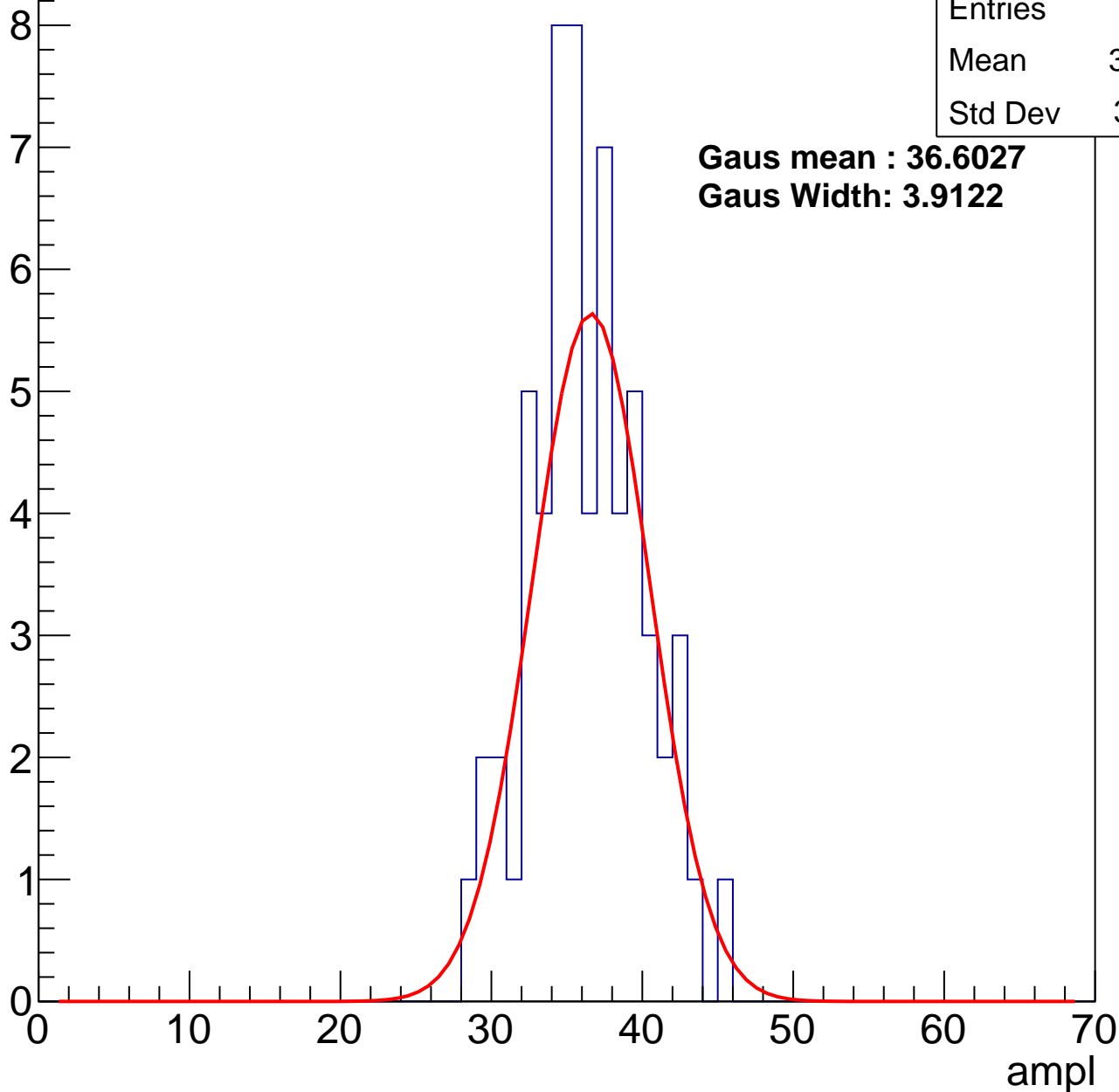
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.85
Std Dev	3.661

Gaus mean : 36.6027

Gaus Width: 3.9122



B1L101S, U1-ch24, adc2

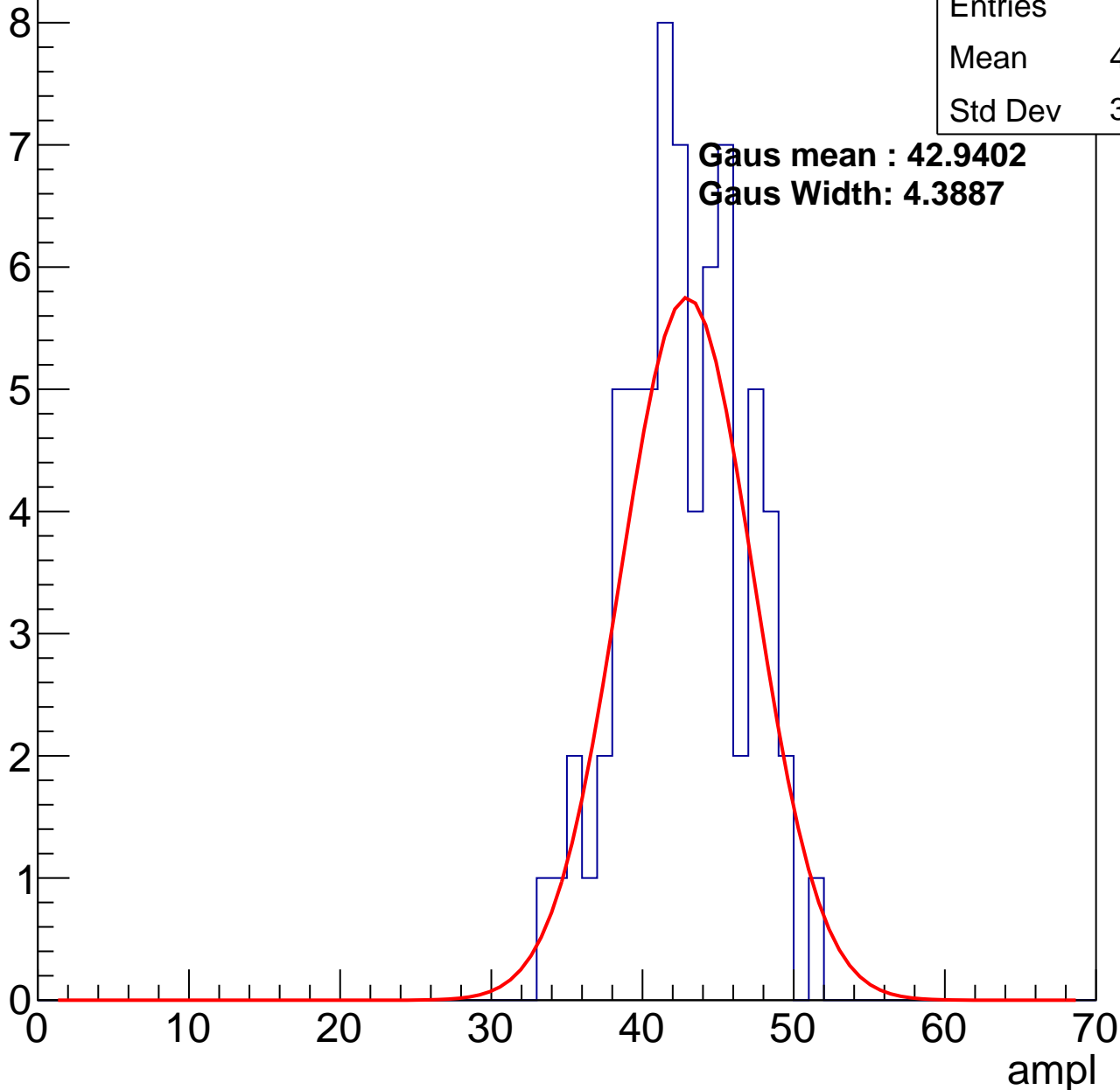
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.25
Std Dev	3.927

Gaus mean : 42.9402

Gaus Width: 4.3887

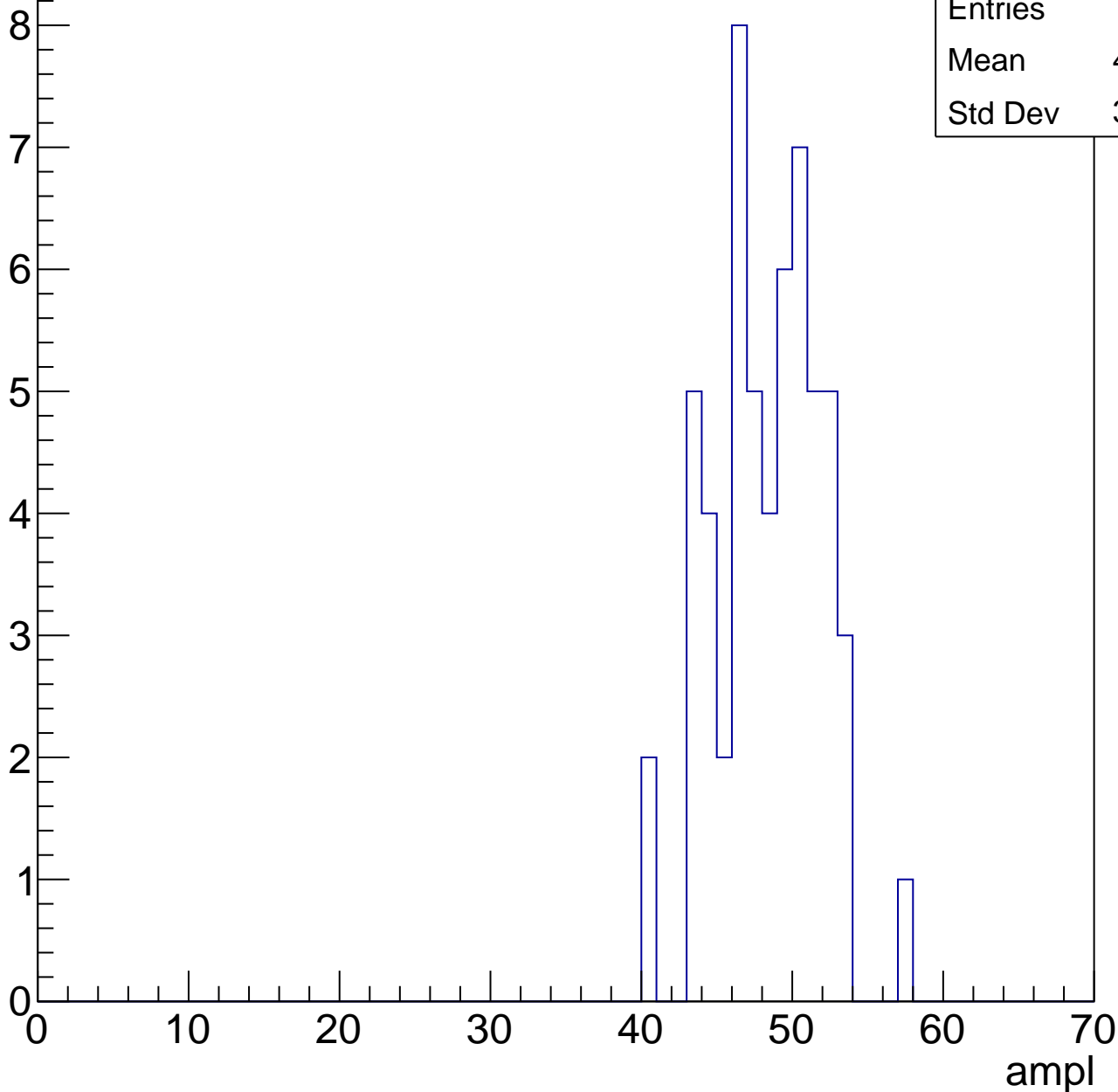


B1L101S, U1-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	47.91
Std Dev	3.471

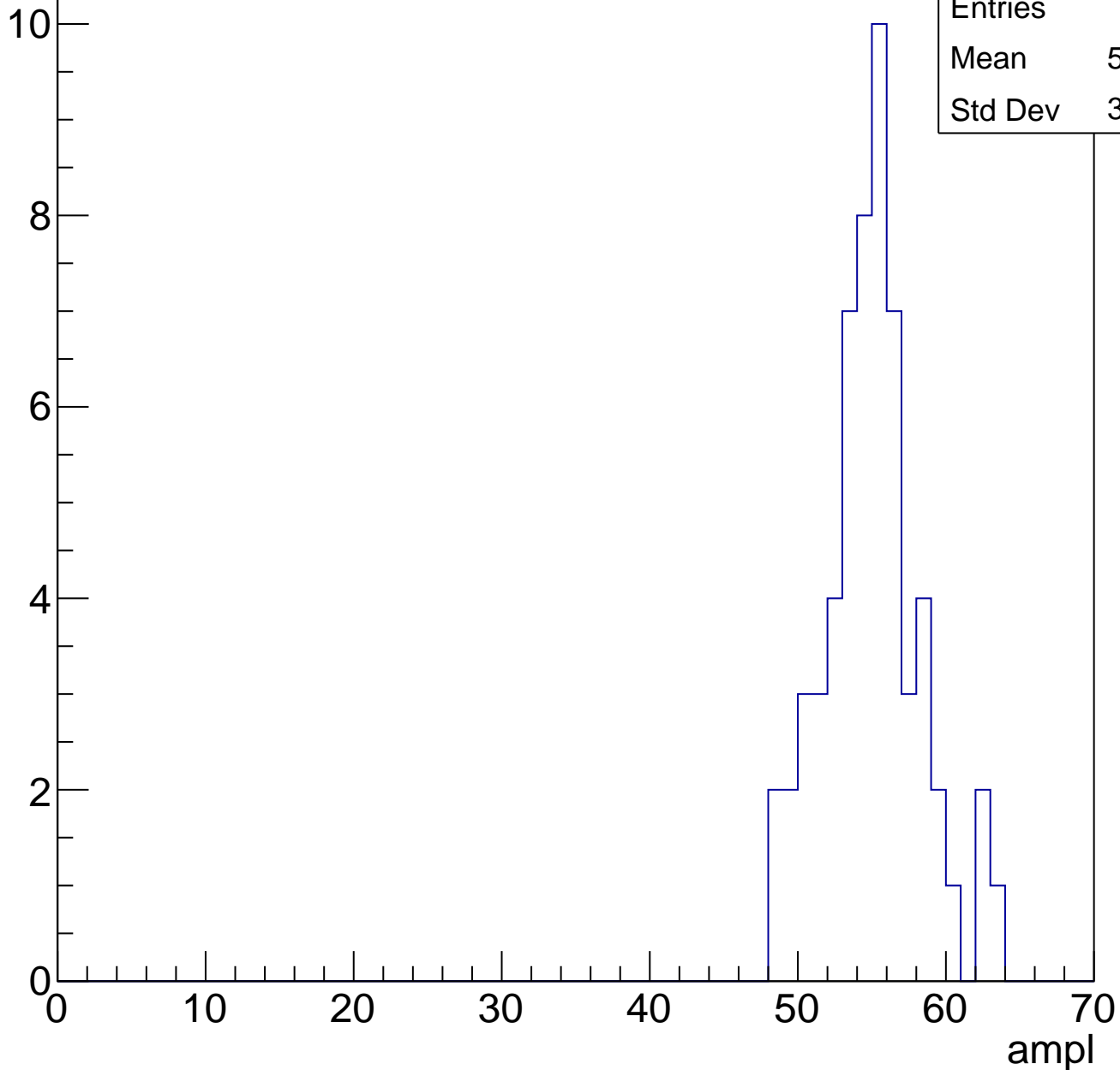


B1L101S, U1-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	54.54
Std Dev	3.259

Entry

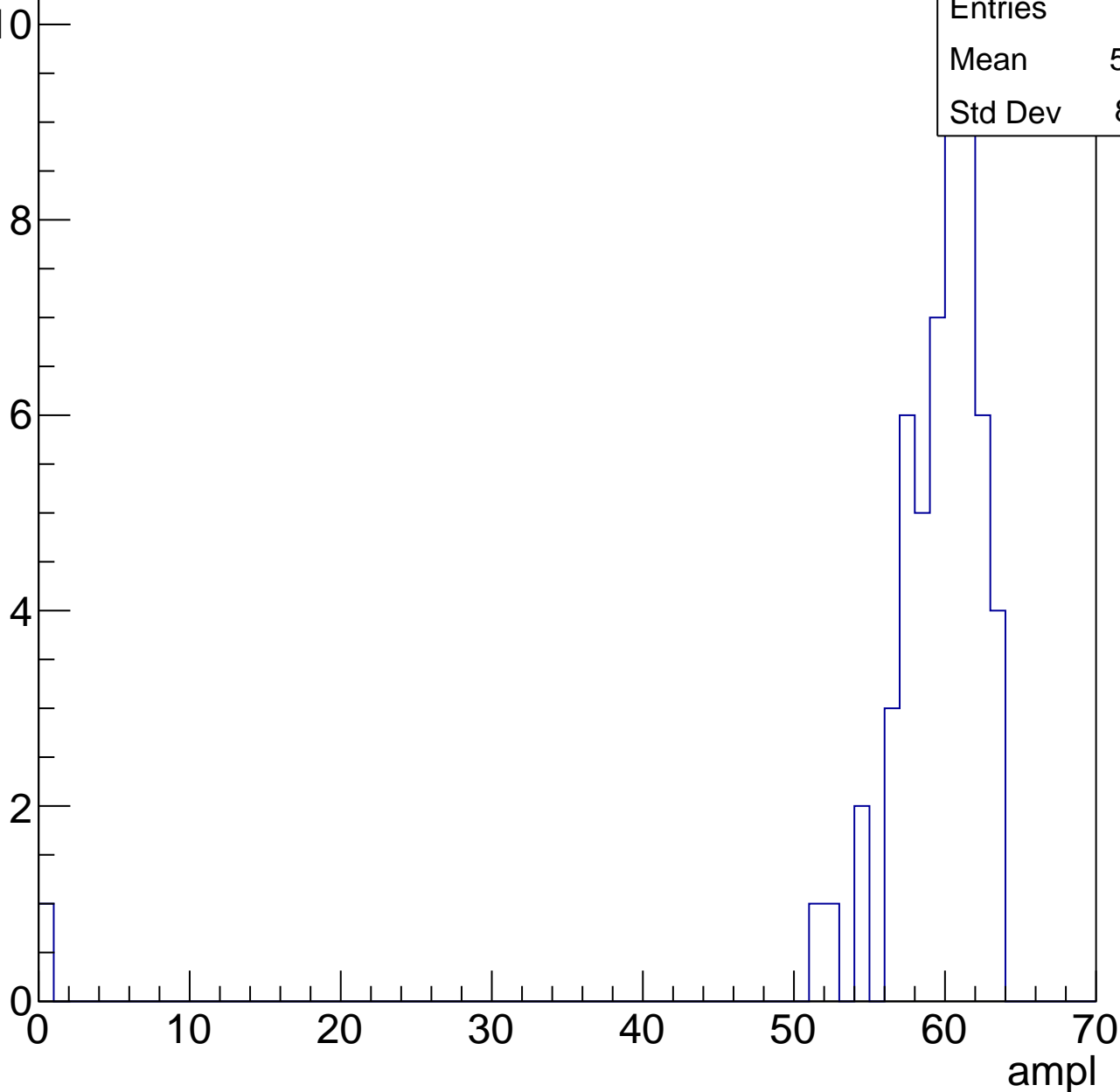


B1L101S, U1-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

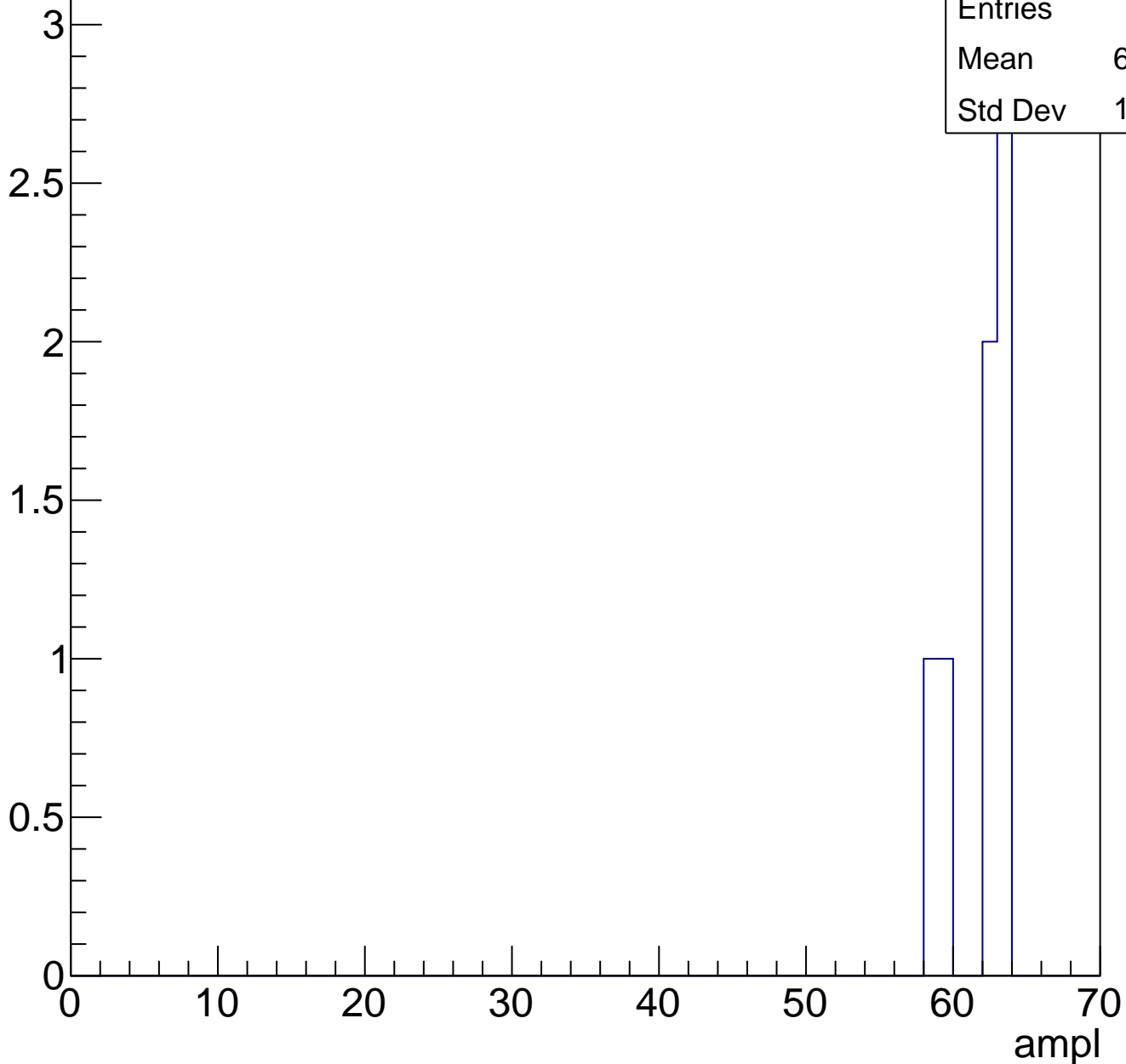
Entries	55
Mean	58.15
Std Dev	8.341



B1L101S, U1-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch25, adc0

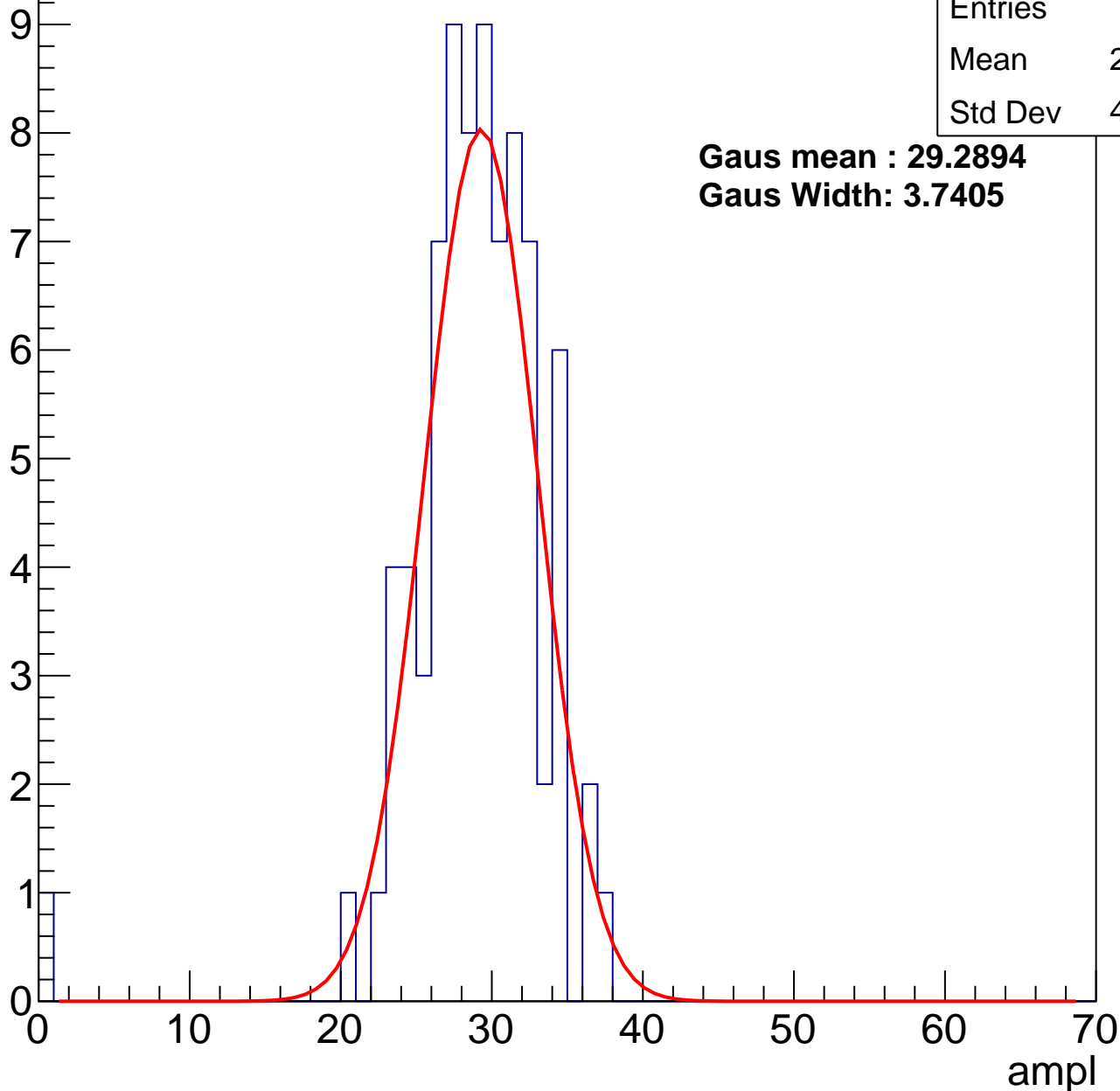
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.45
Std Dev	4.733

Gaus mean : 29.2894

Gaus Width: 3.7405



B1L101S, U1-ch25, adc1

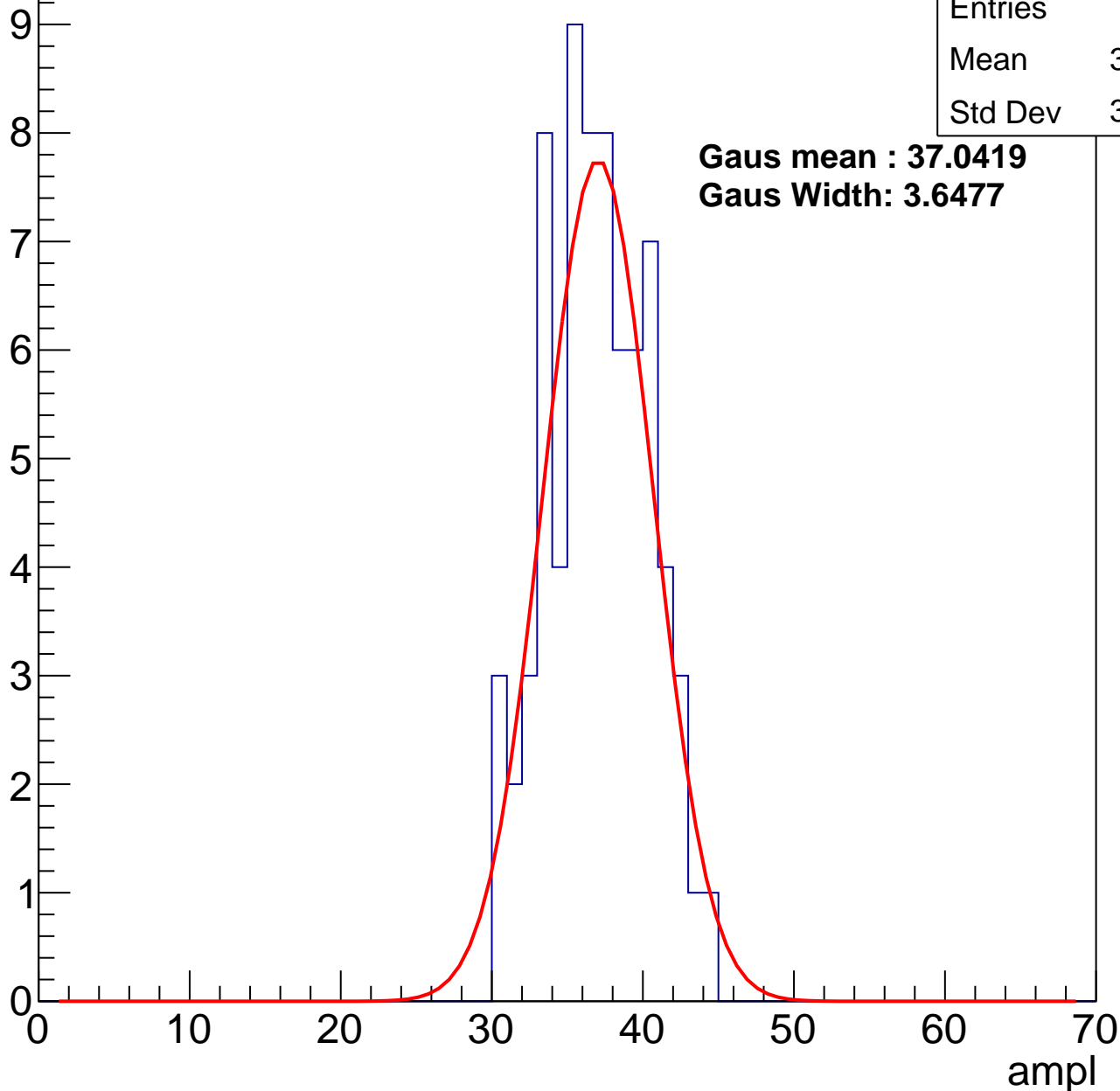
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.52
Std Dev	3.315

Gaus mean : 37.0419

Gaus Width: 3.6477



B1L101S, U1-ch25, adc2

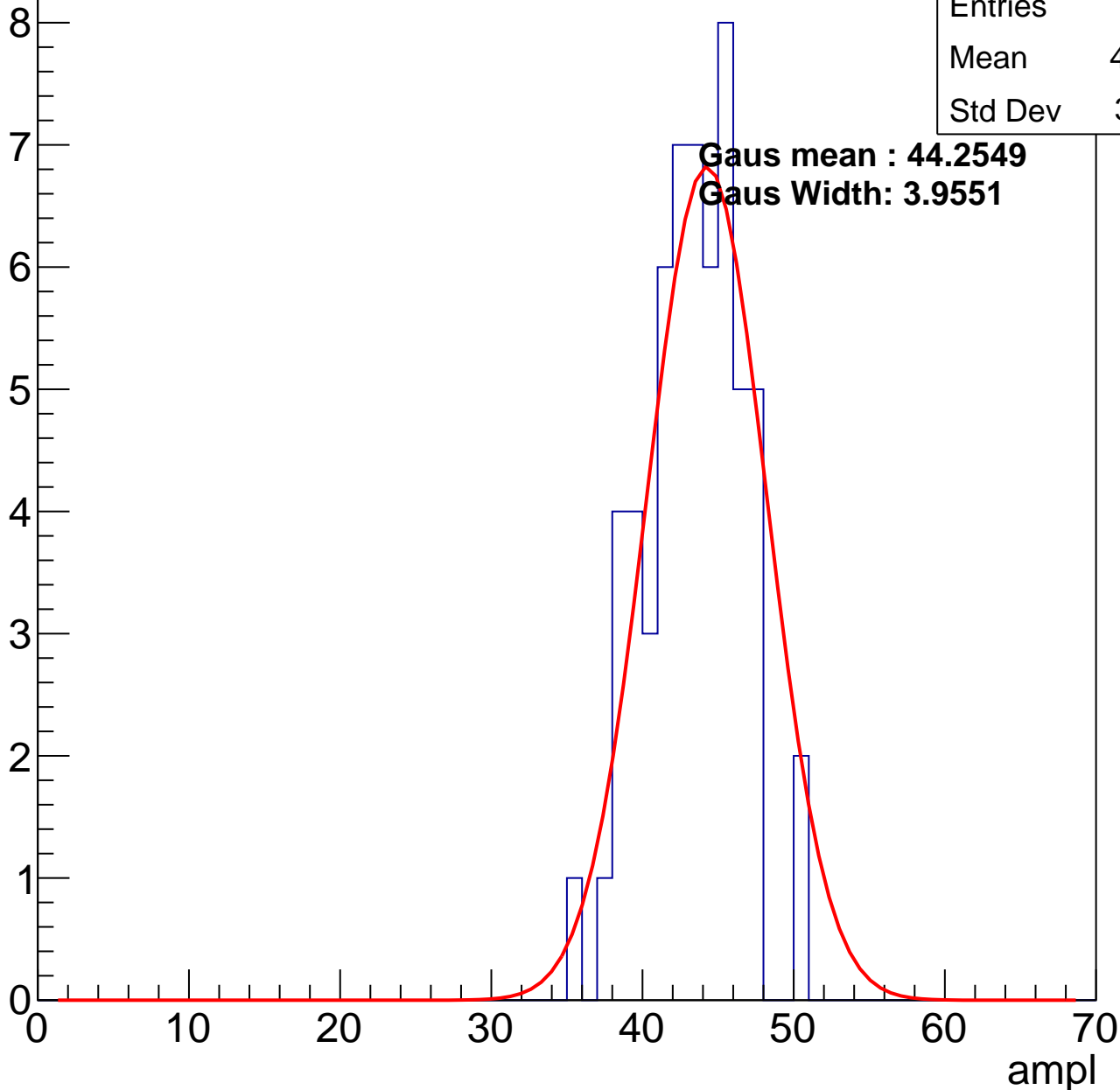
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.88
Std Dev	3.141

Gaus mean : 44.2549

Gaus Width: 3.9551

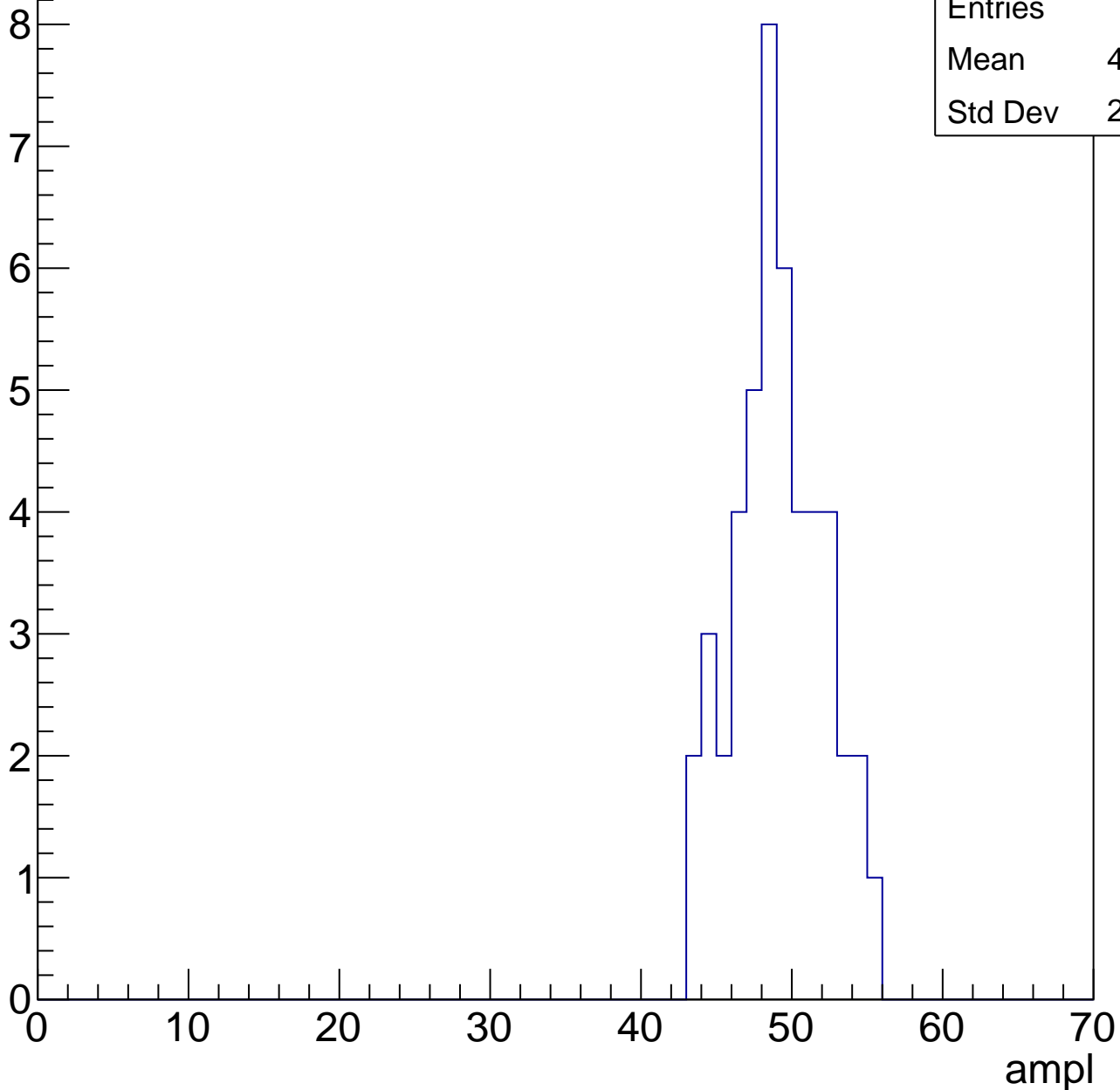


B1L101S, U1-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

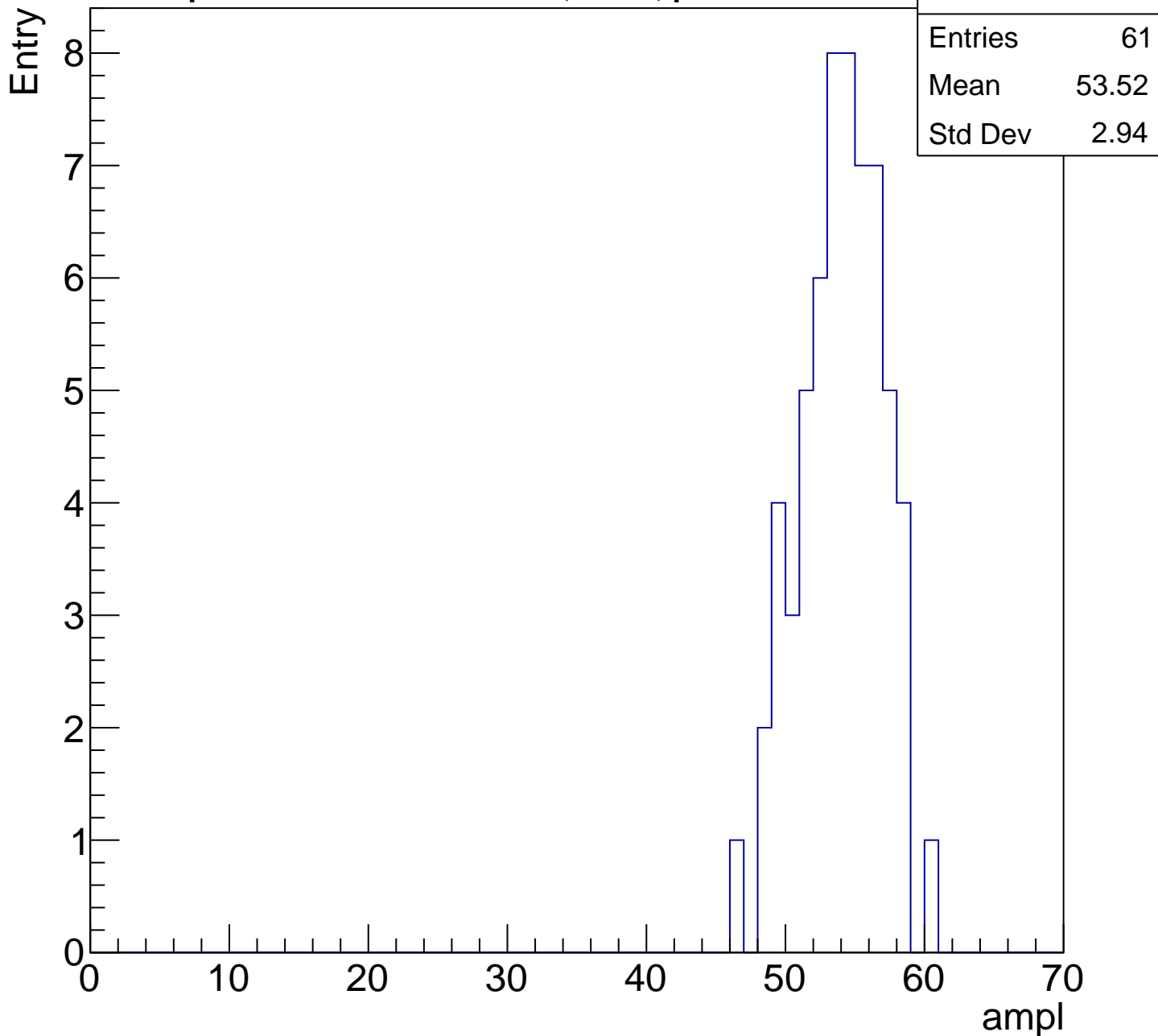
Entry

Entries	47
Mean	48.64
Std Dev	2.957



B1L101S, U1-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

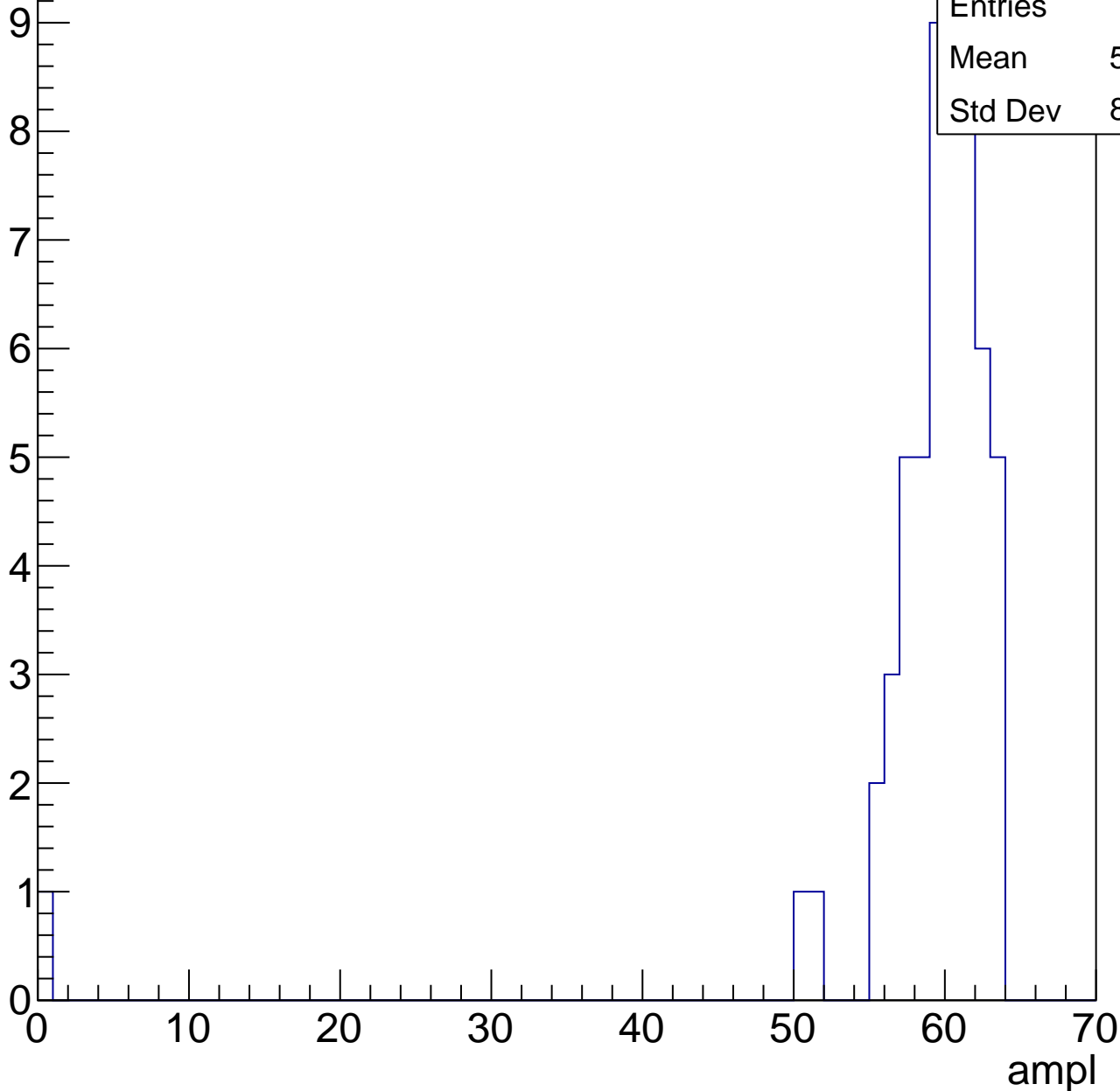


B1L101S, U1-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	58.18
Std Dev	8.365

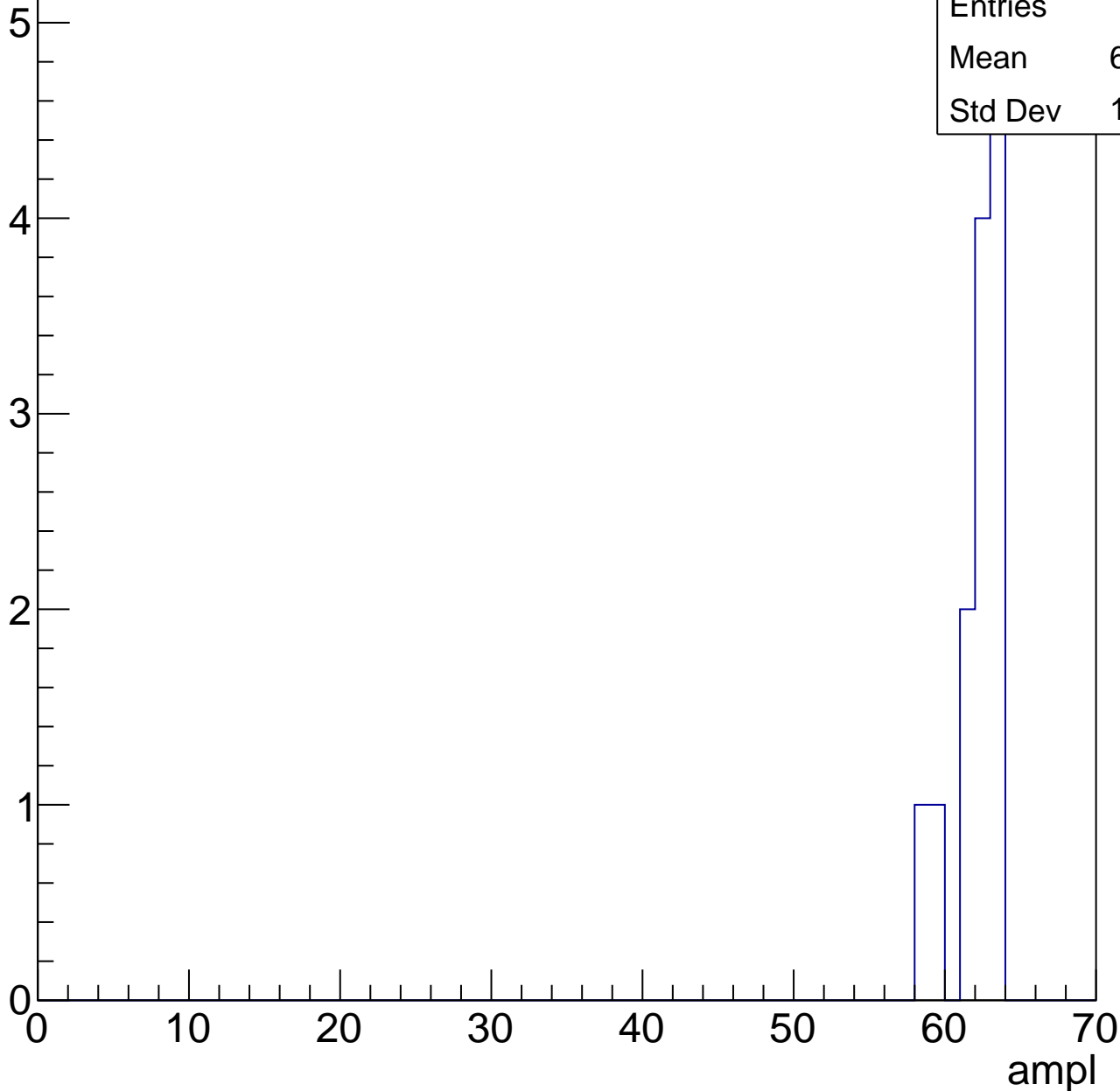


B1L101S, U1-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.69
Std Dev	1.538



B1L101S, U1-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch26, adc0

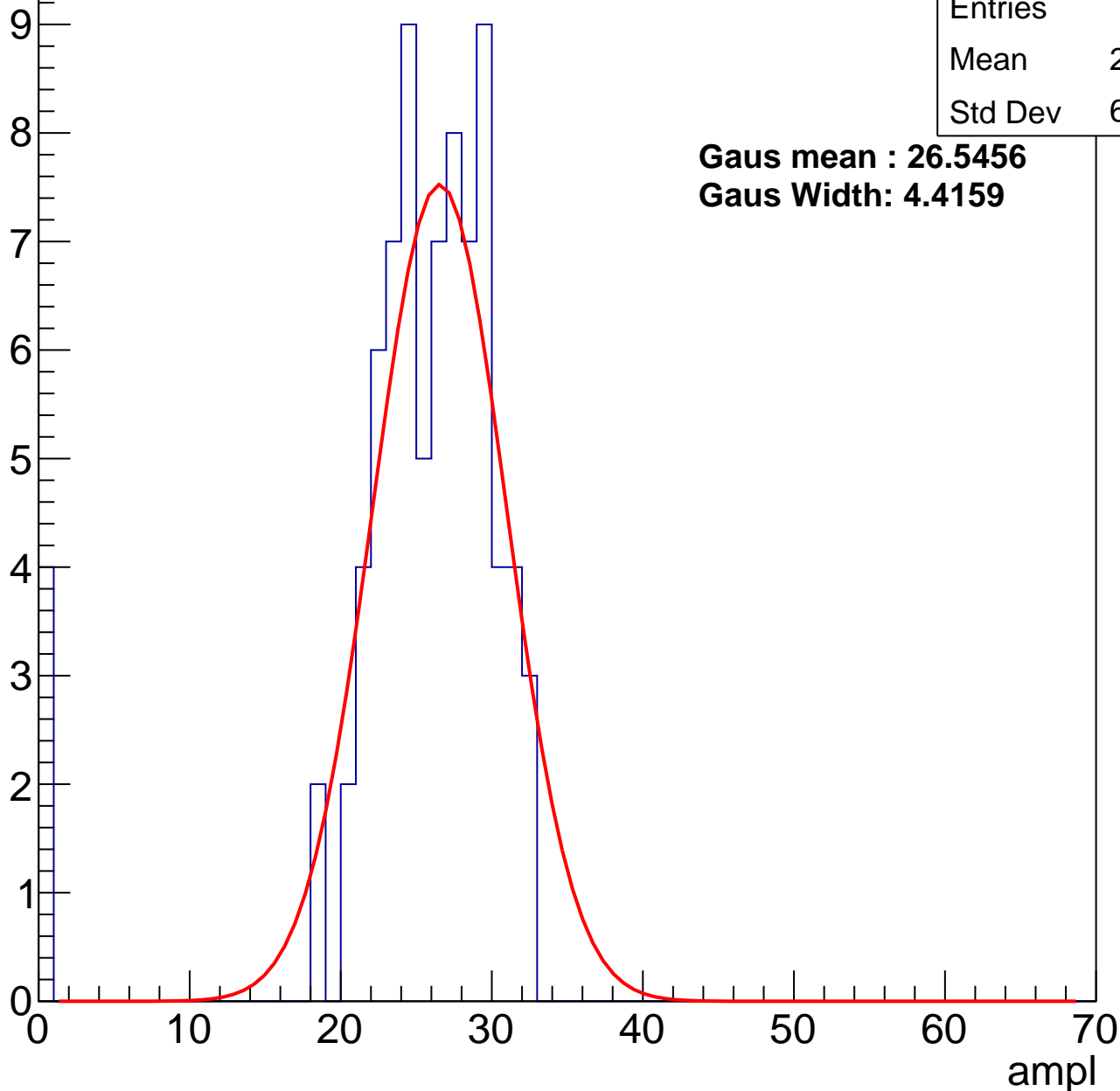
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	24.56
Std Dev	6.502

Gaus mean : 26.5456

Gaus Width: 4.4159



B1L101S, U1-ch26, adc1

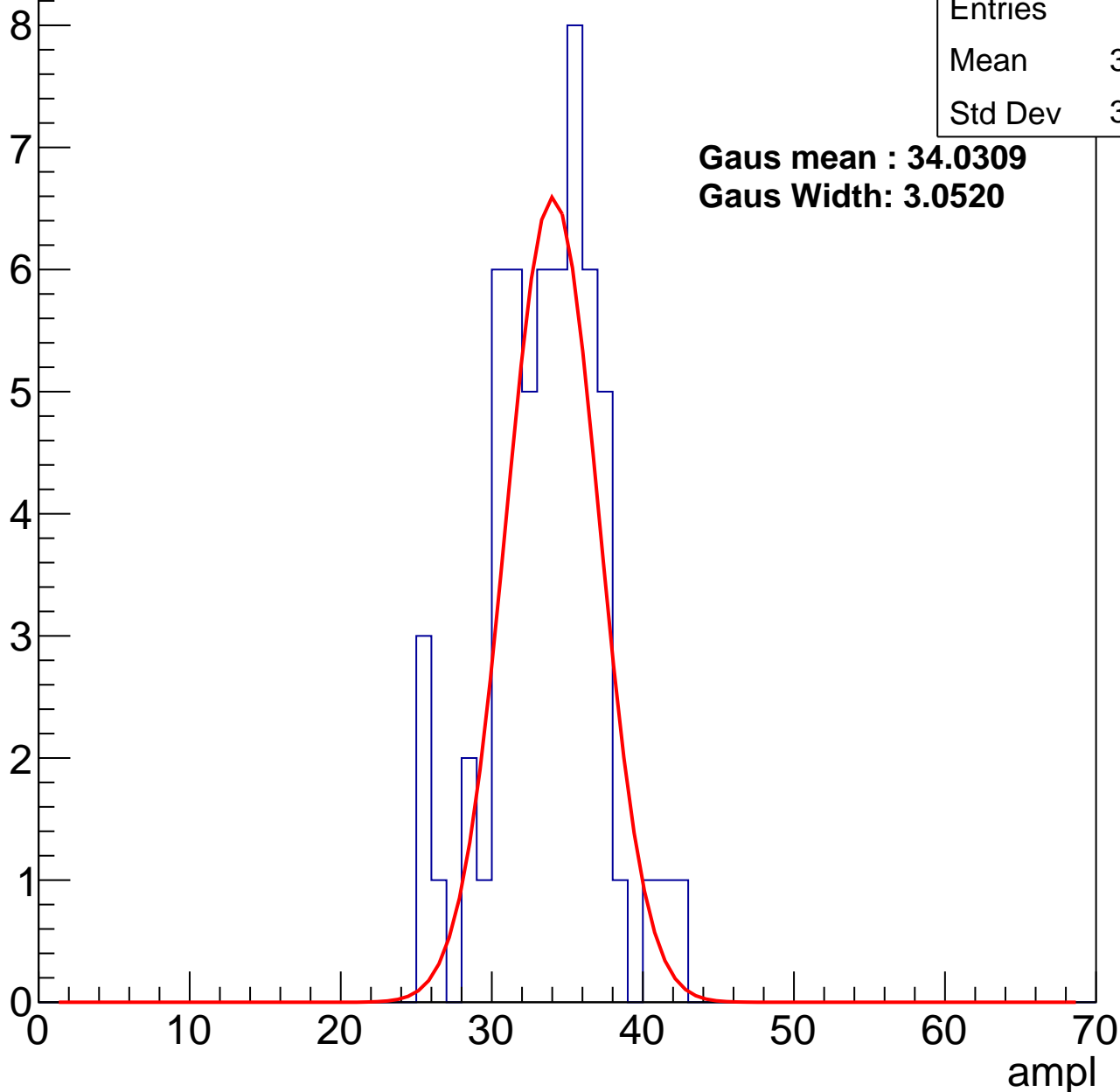
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	33.15
Std Dev	3.635

Gaus mean : 34.0309

Gaus Width: 3.0520



B1L101S, U1-ch26, adc2

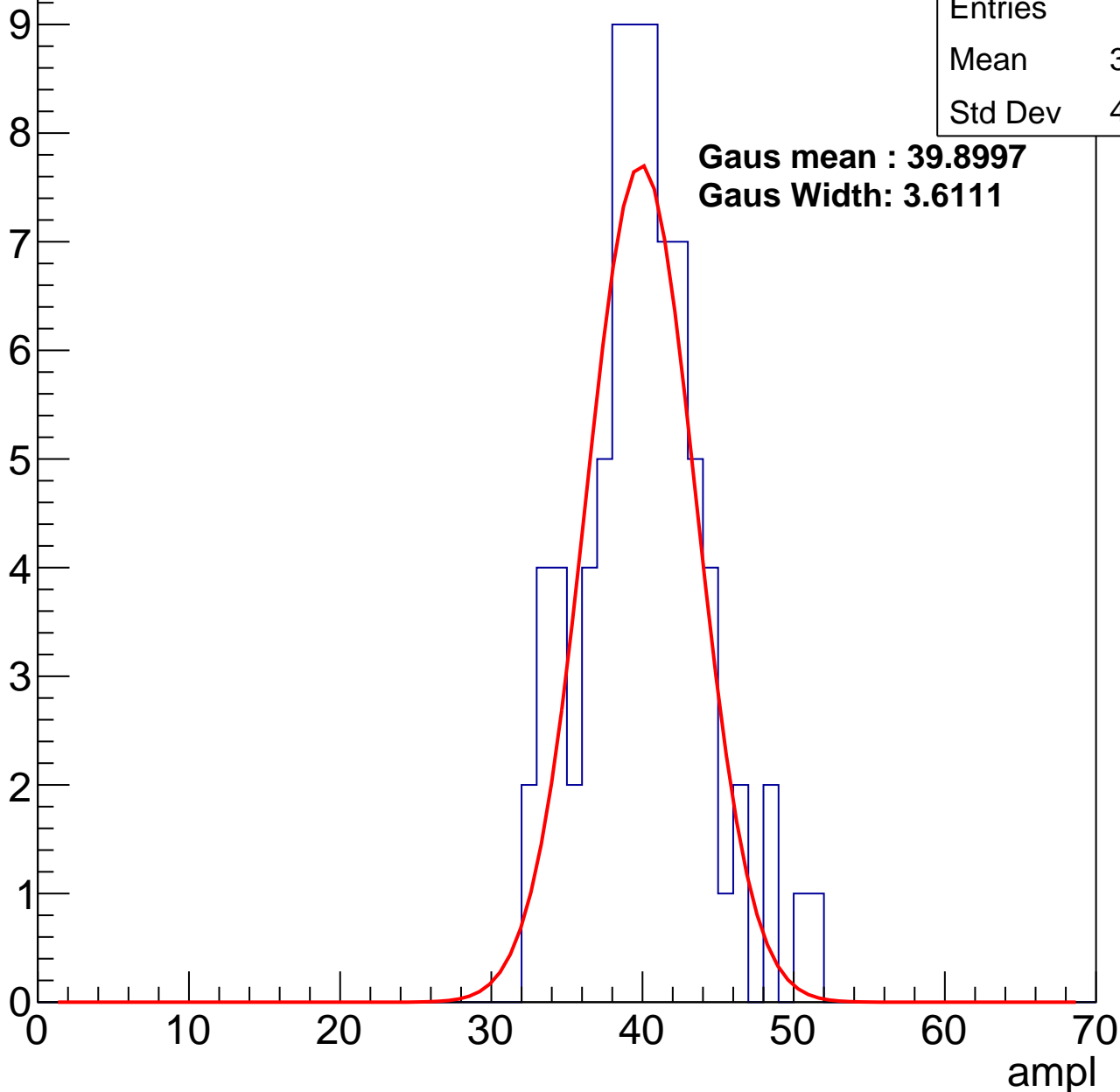
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	39.62
Std Dev	4.004

Gaus mean : 39.8997

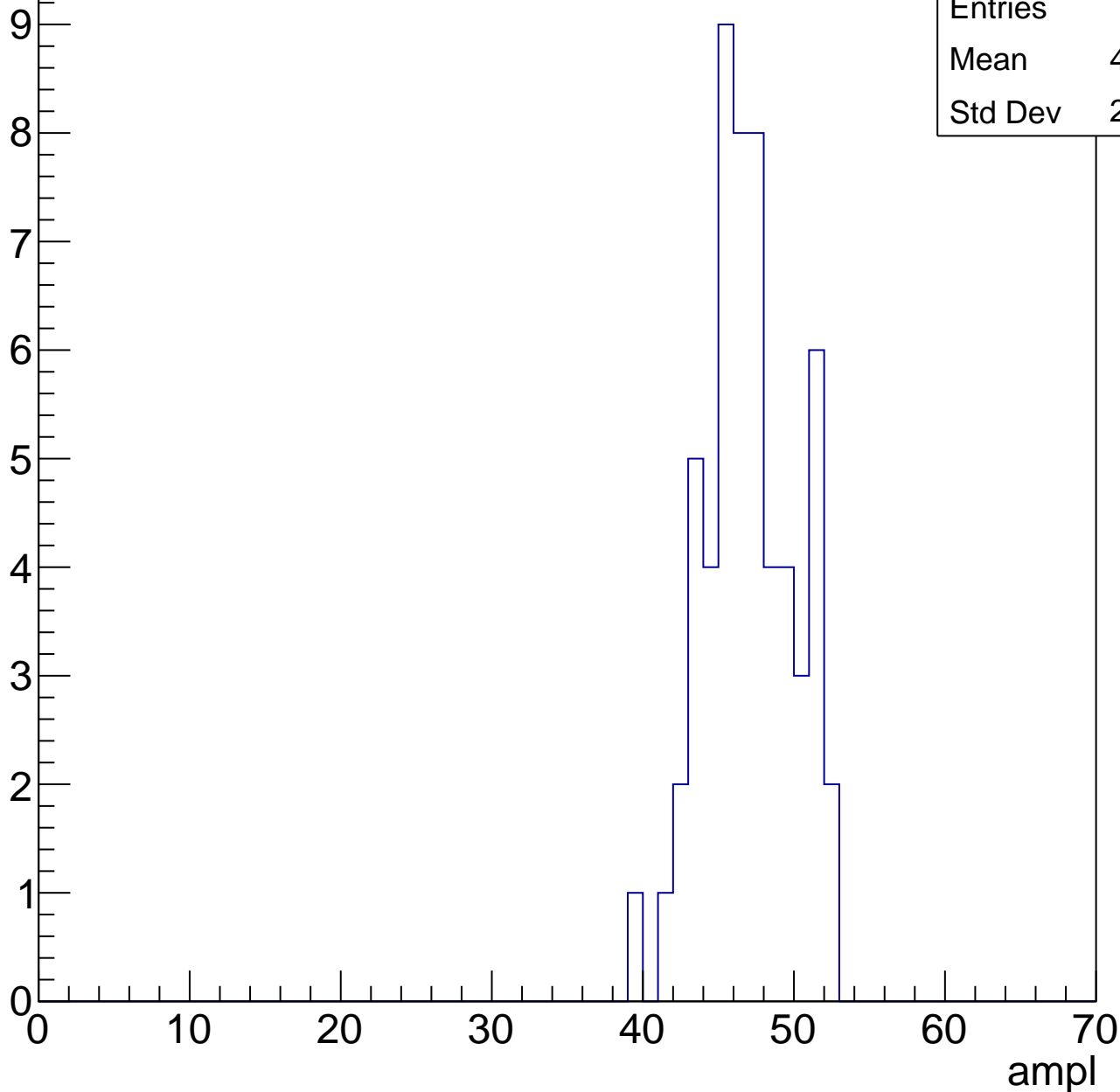
Gaus Width: 3.6111



B1L101S, U1-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

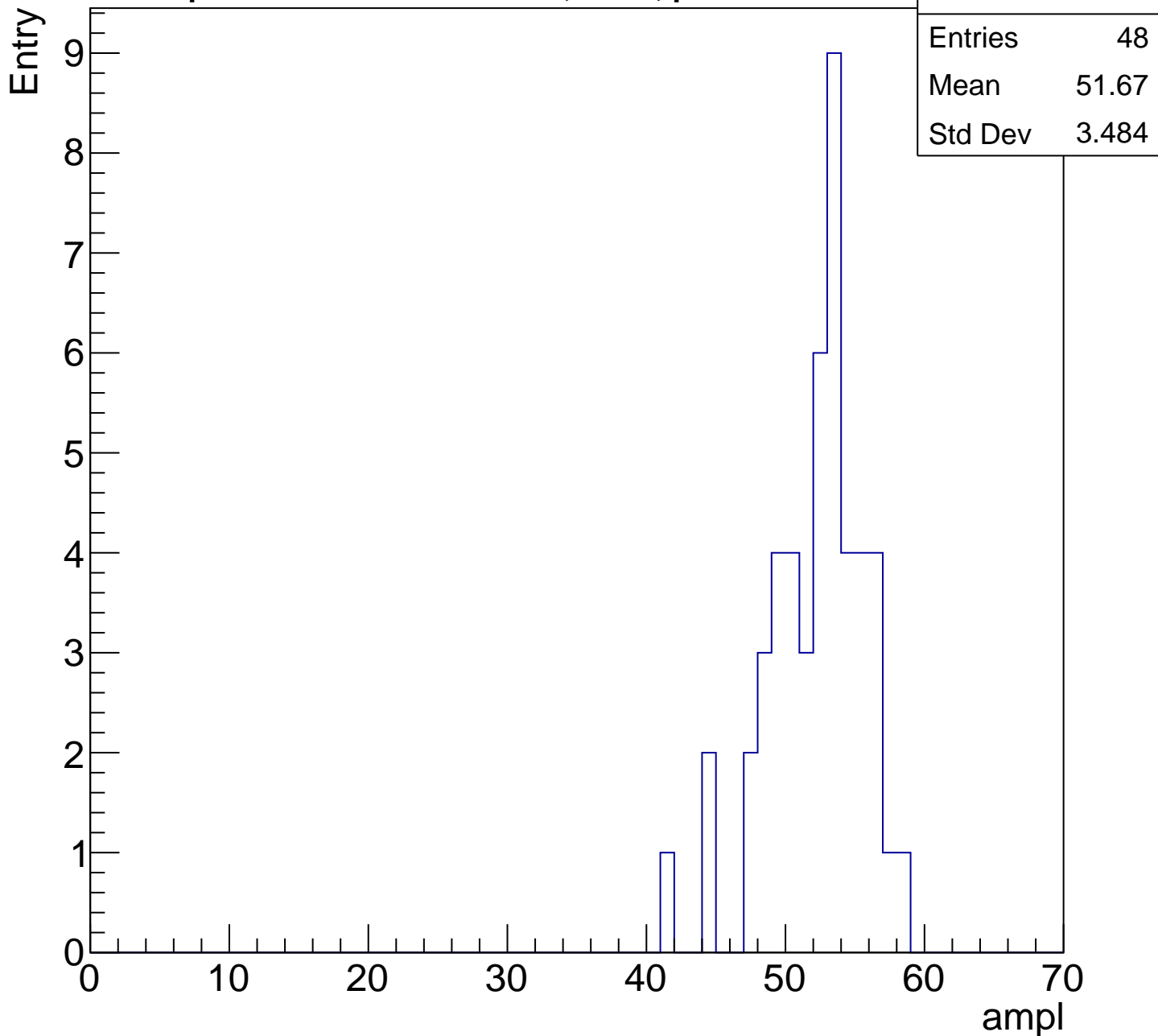
Entry



Entries	57
Mean	46.53
Std Dev	2.933

B1L101S, U1-ch26, adc4

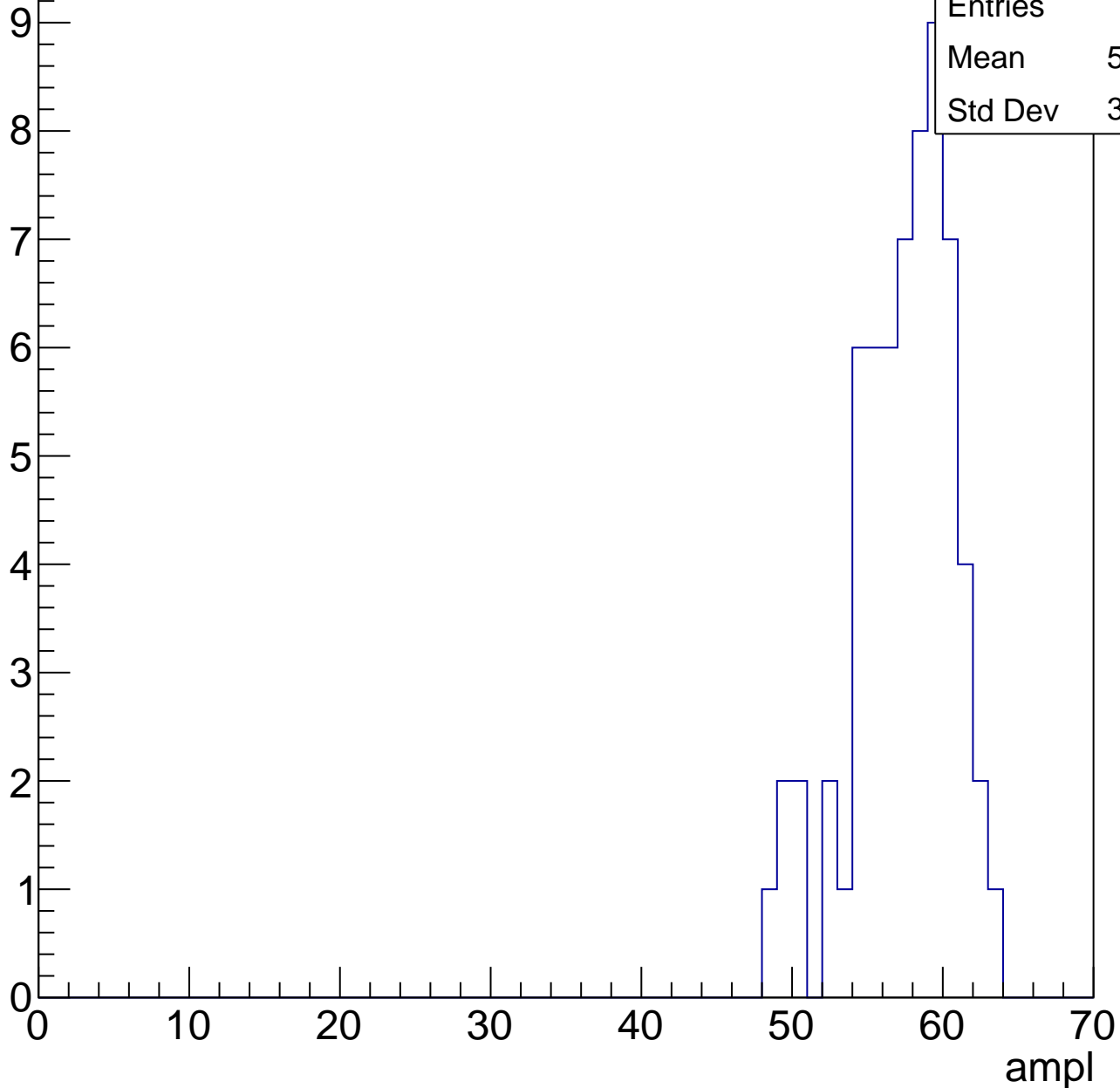
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

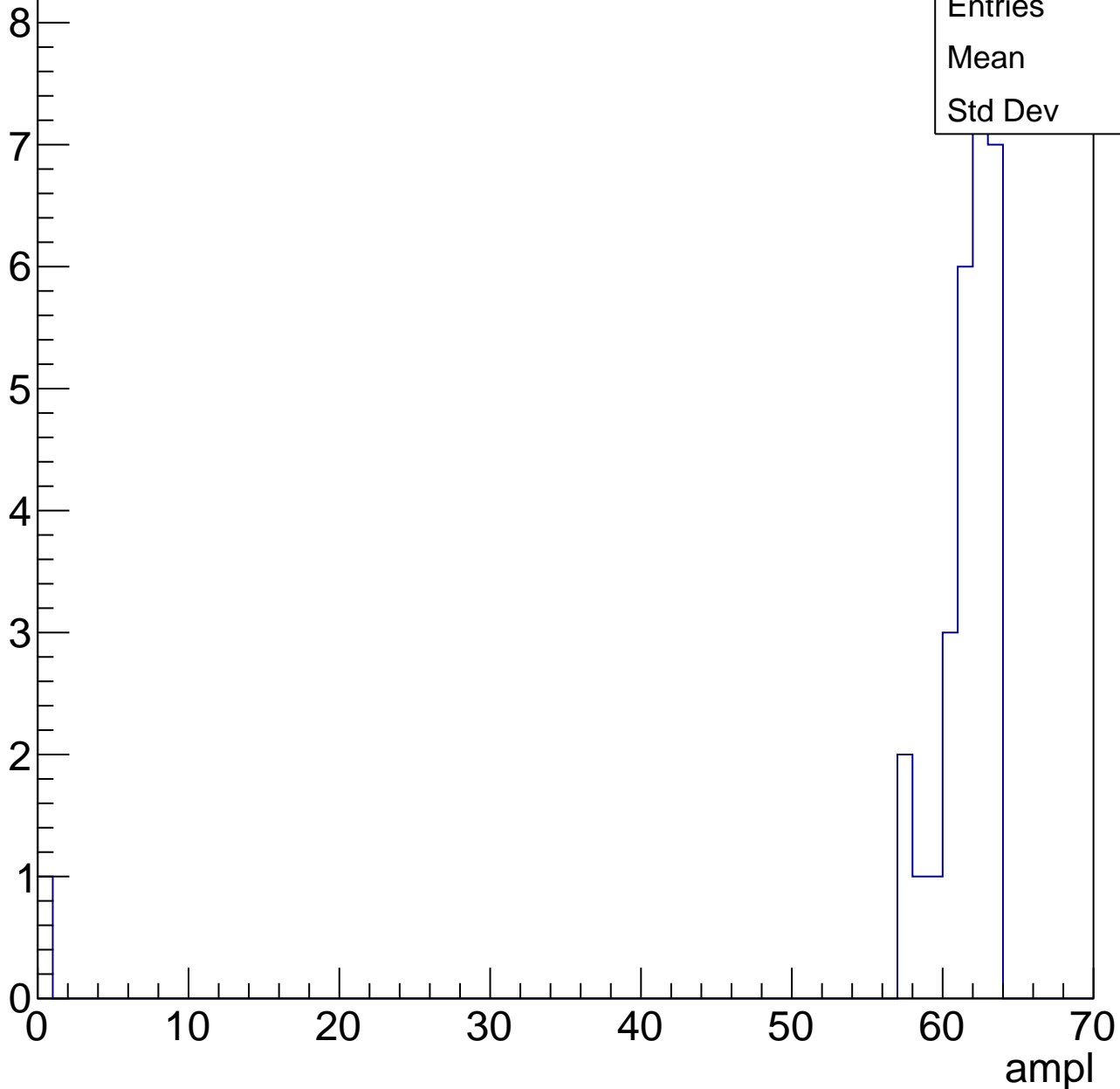


B1L101S, U1-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	59.1
Std Dev	11.3



B1L101S, U1-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch27, adc0

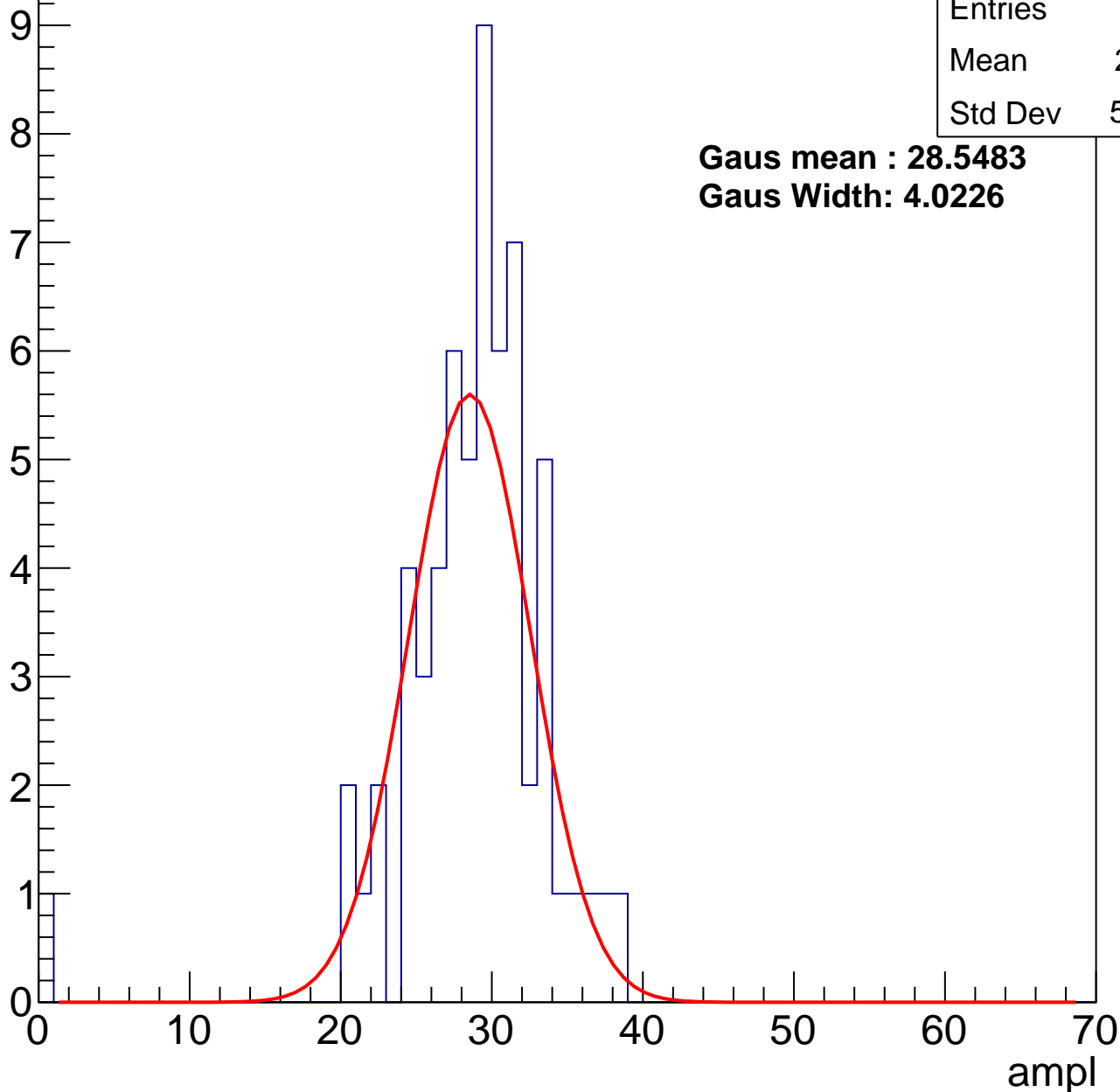
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.21
Std Dev	5.277

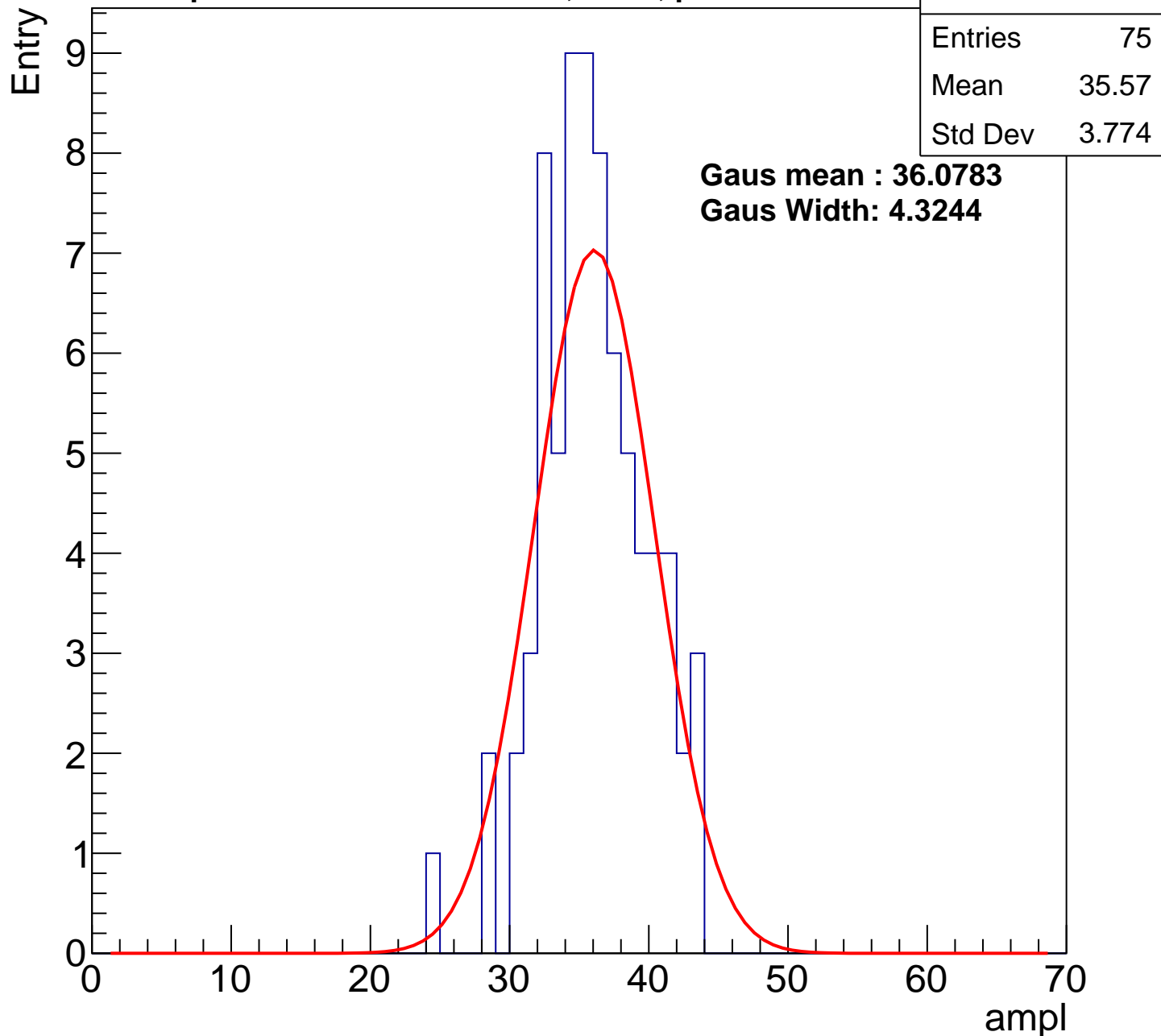
Gaus mean : 28.5483

Gaus Width: 4.0226



B1L101S, U1-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch27, adc2

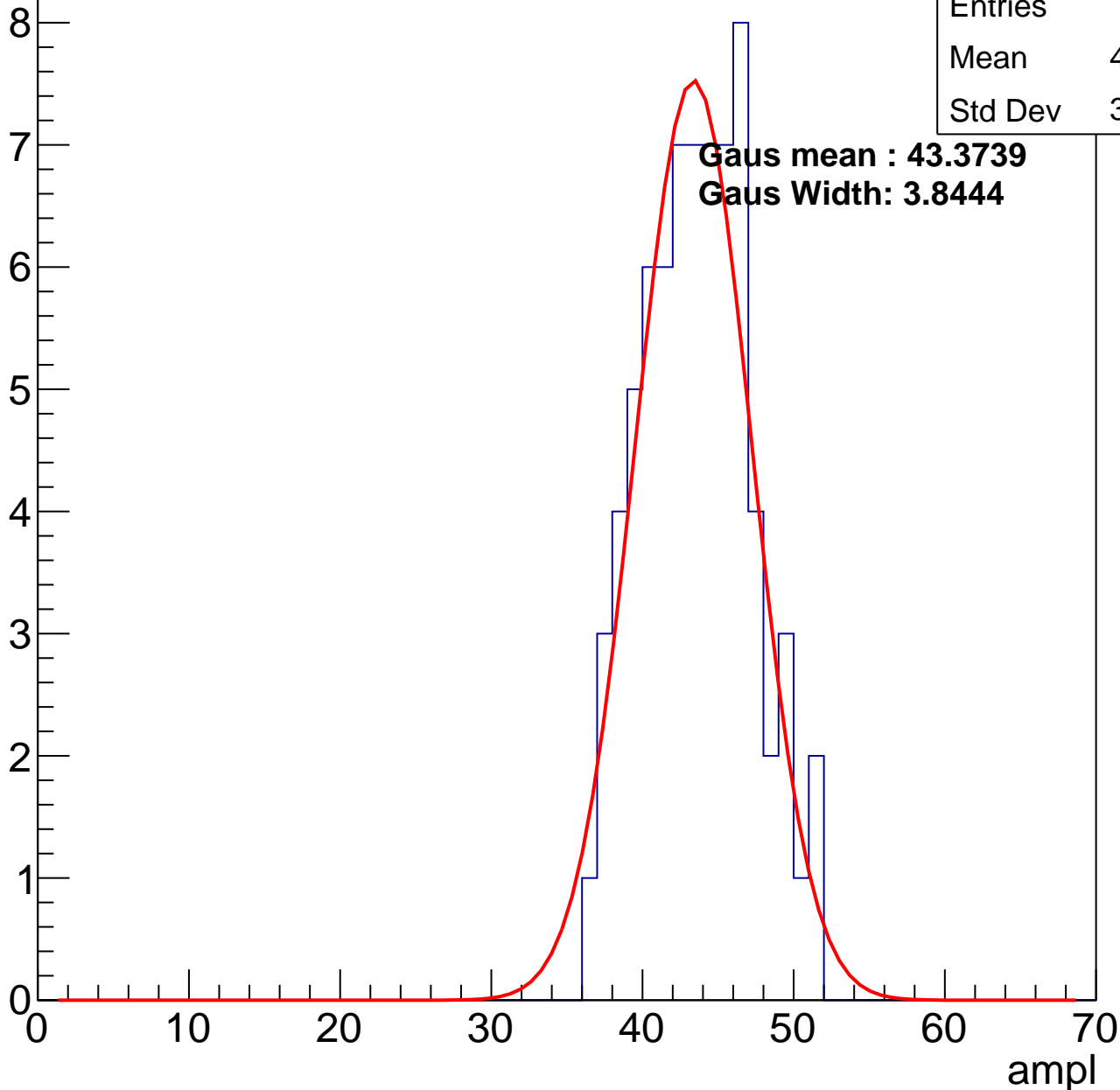
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.14
Std Dev	3.563

Gaus mean : 43.3739

Gaus Width: 3.8444

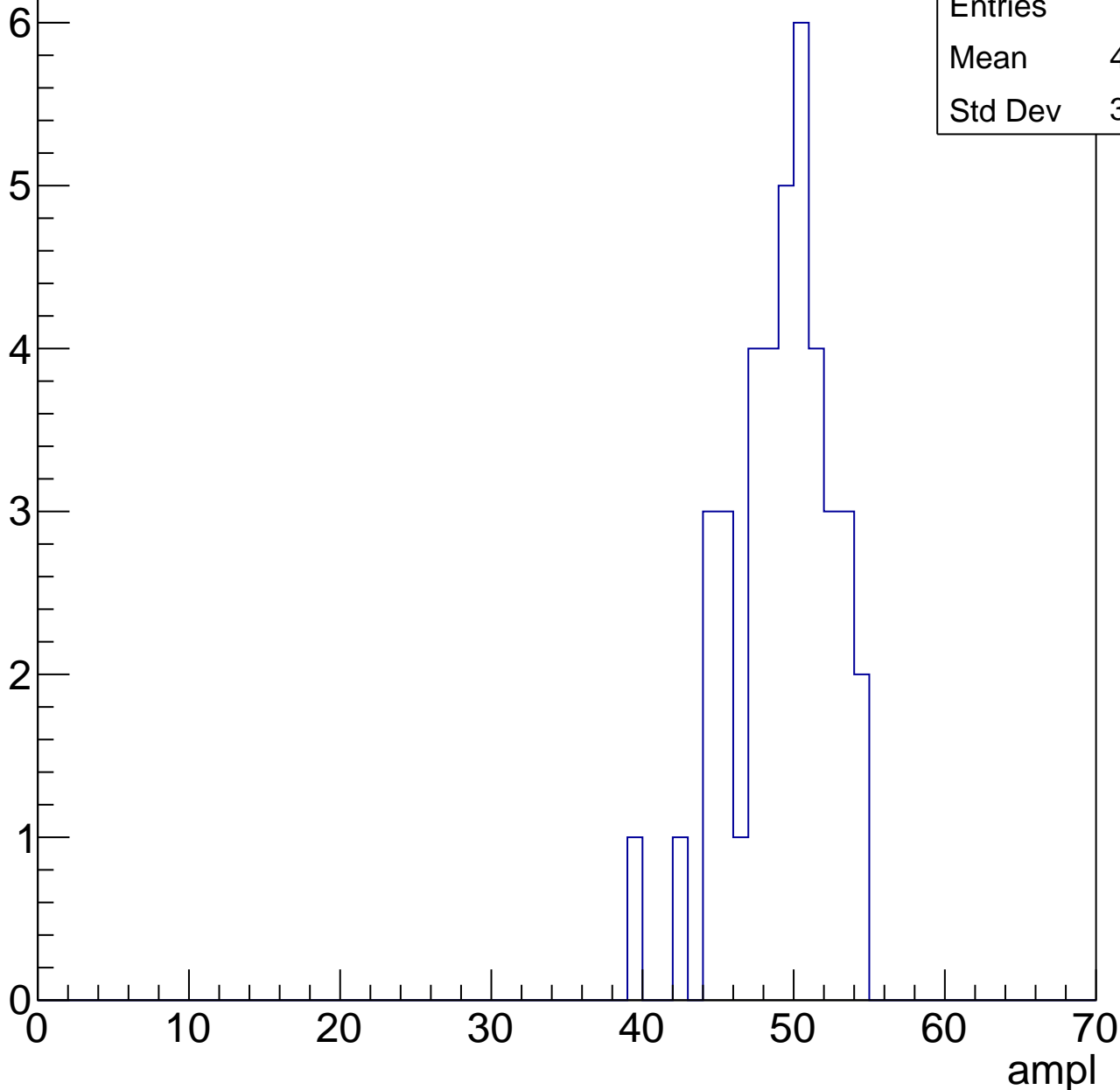


B1L101S, U1-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

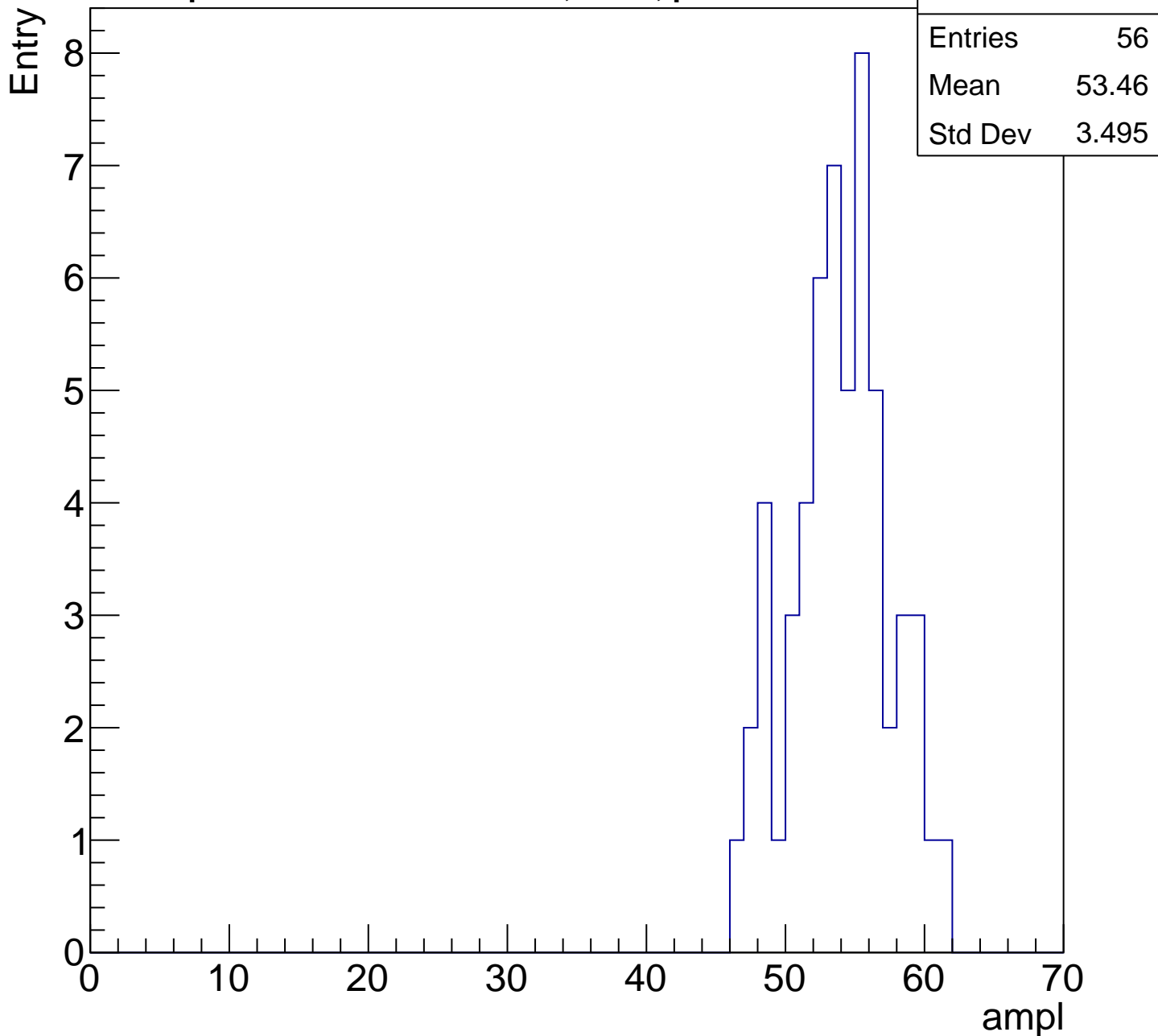
Entry

Entries	40
Mean	48.65
Std Dev	3.328



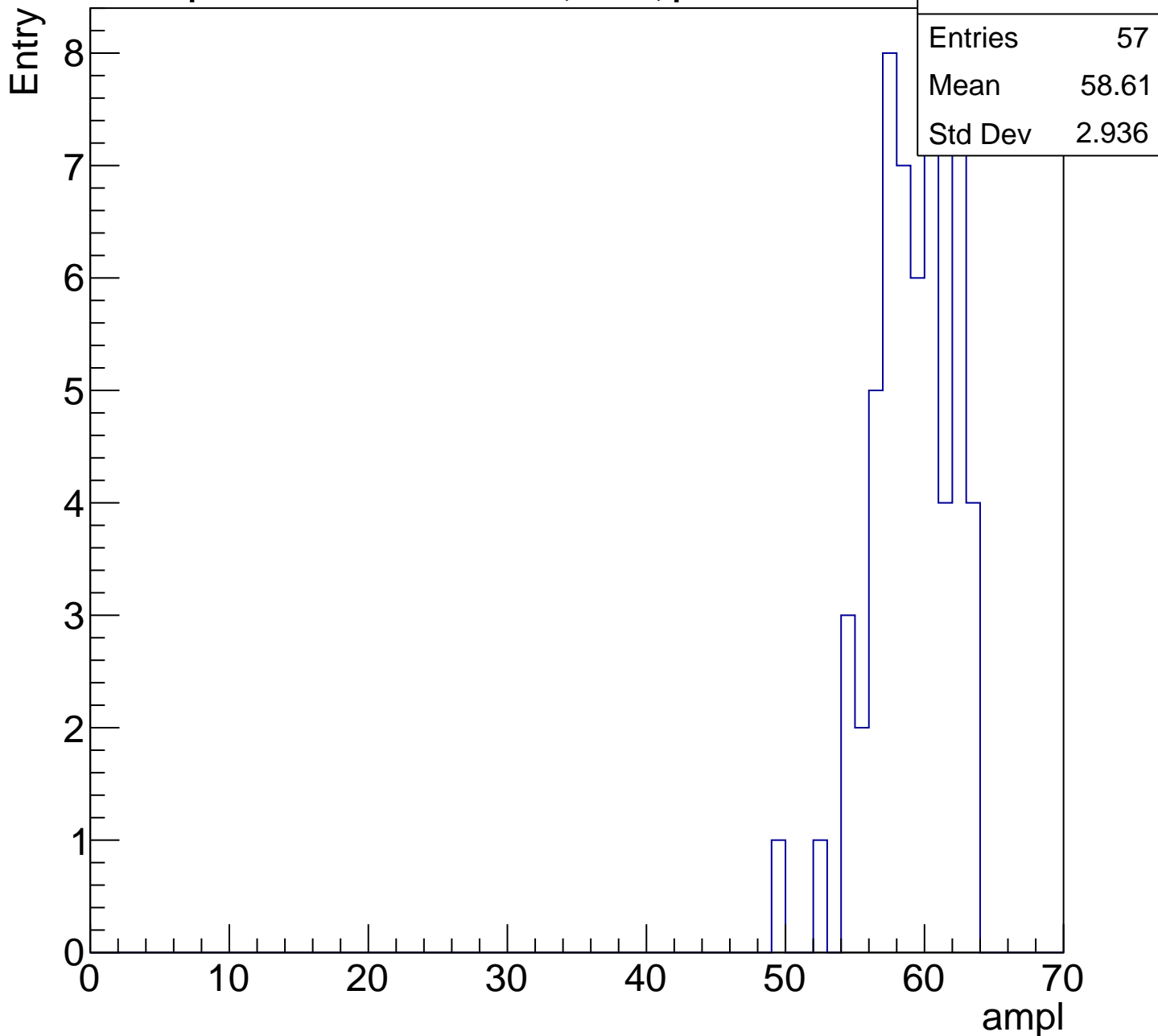
B1L101S, U1-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

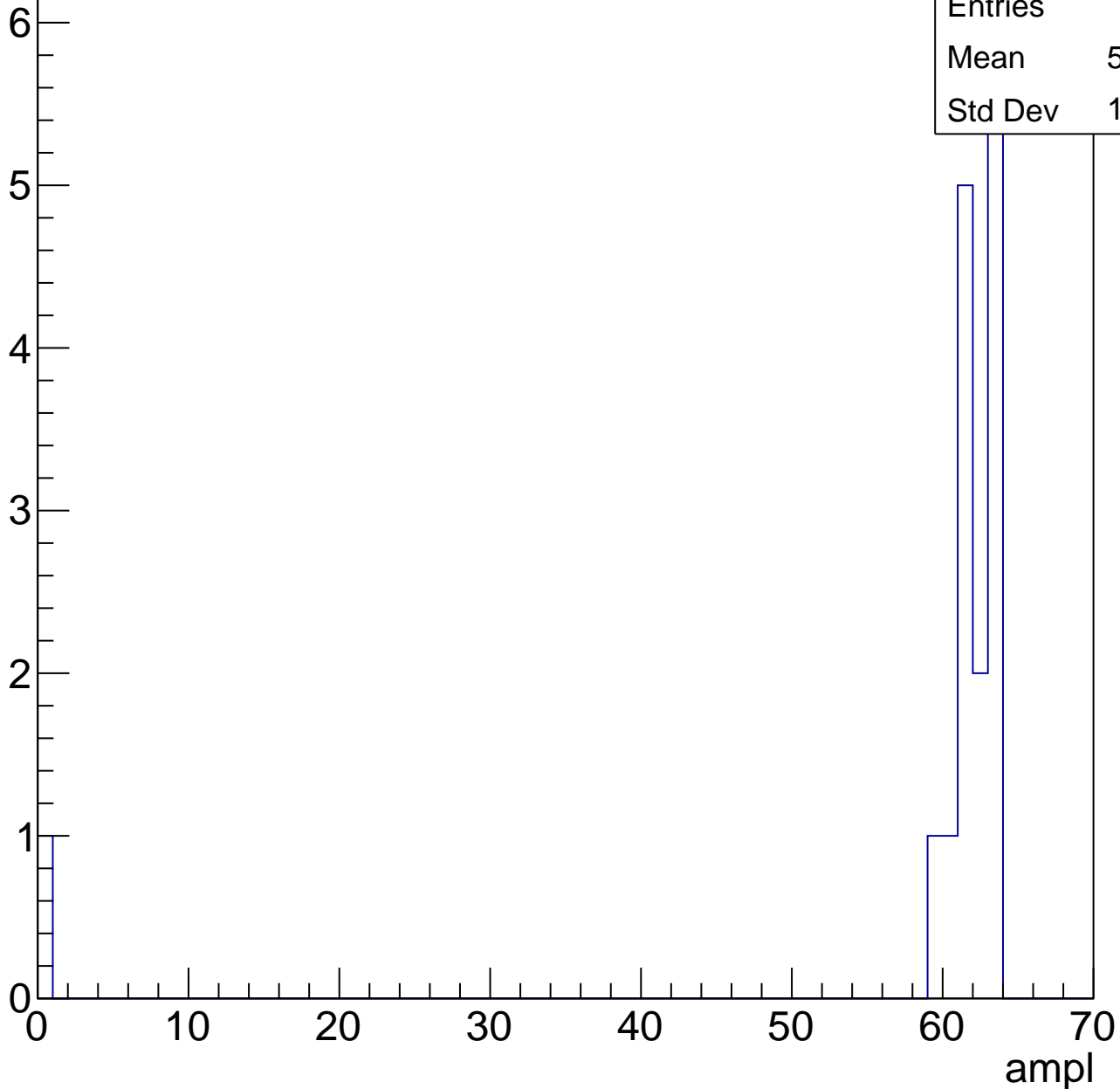


B1L101S, U1-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.88
Std Dev	14.99



B1L101S, U1-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch28, adc0

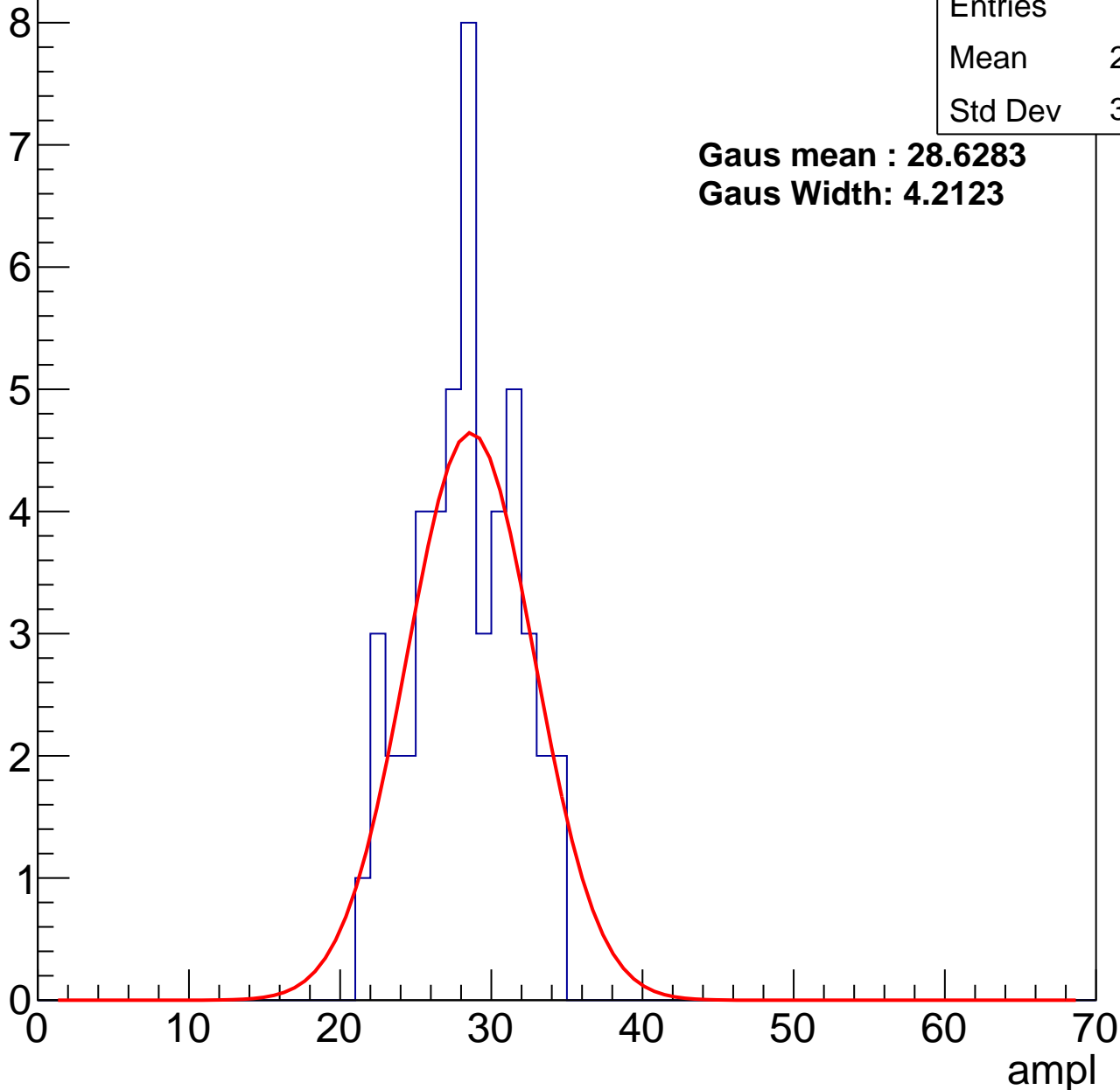
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	27.83
Std Dev	3.319

Gaus mean : 28.6283

Gaus Width: 4.2123



B1L101S, U1-ch28, adc1

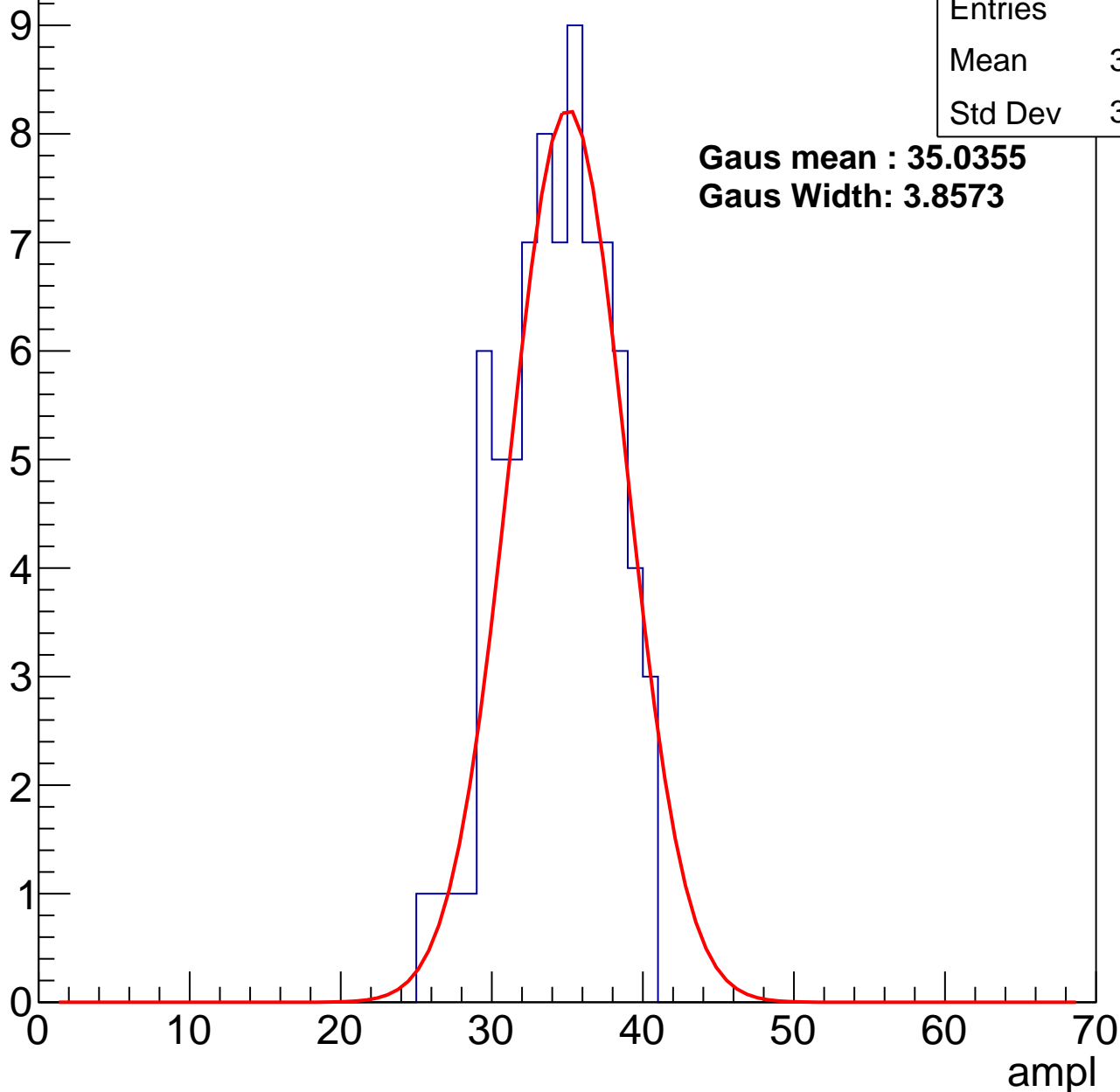
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	33.86
Std Dev	3.478

Gaus mean : 35.0355

Gaus Width: 3.8573



B1L101S, U1-ch28, adc2

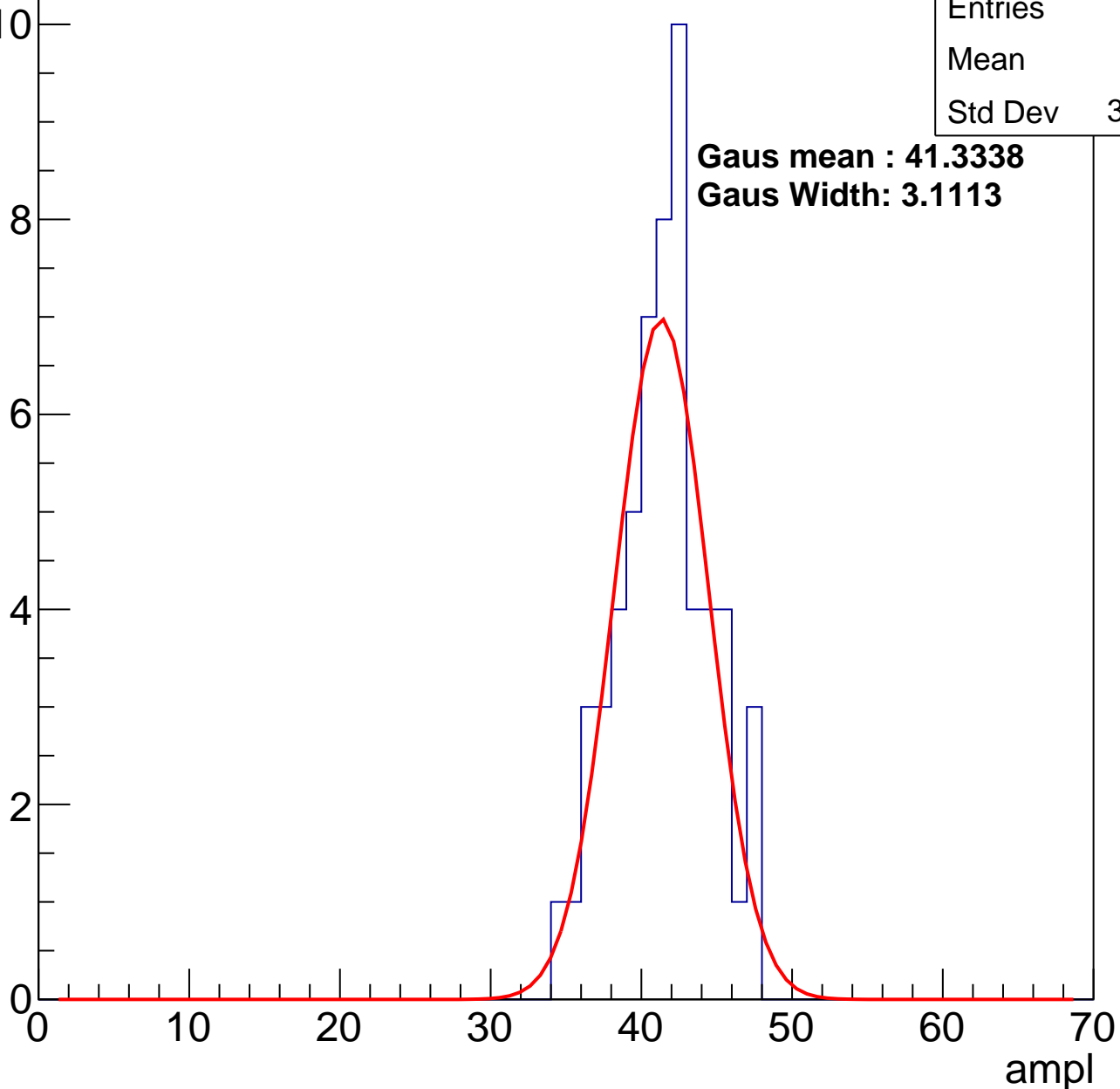
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41
Std Dev	3.023

Gaus mean : 41.3338

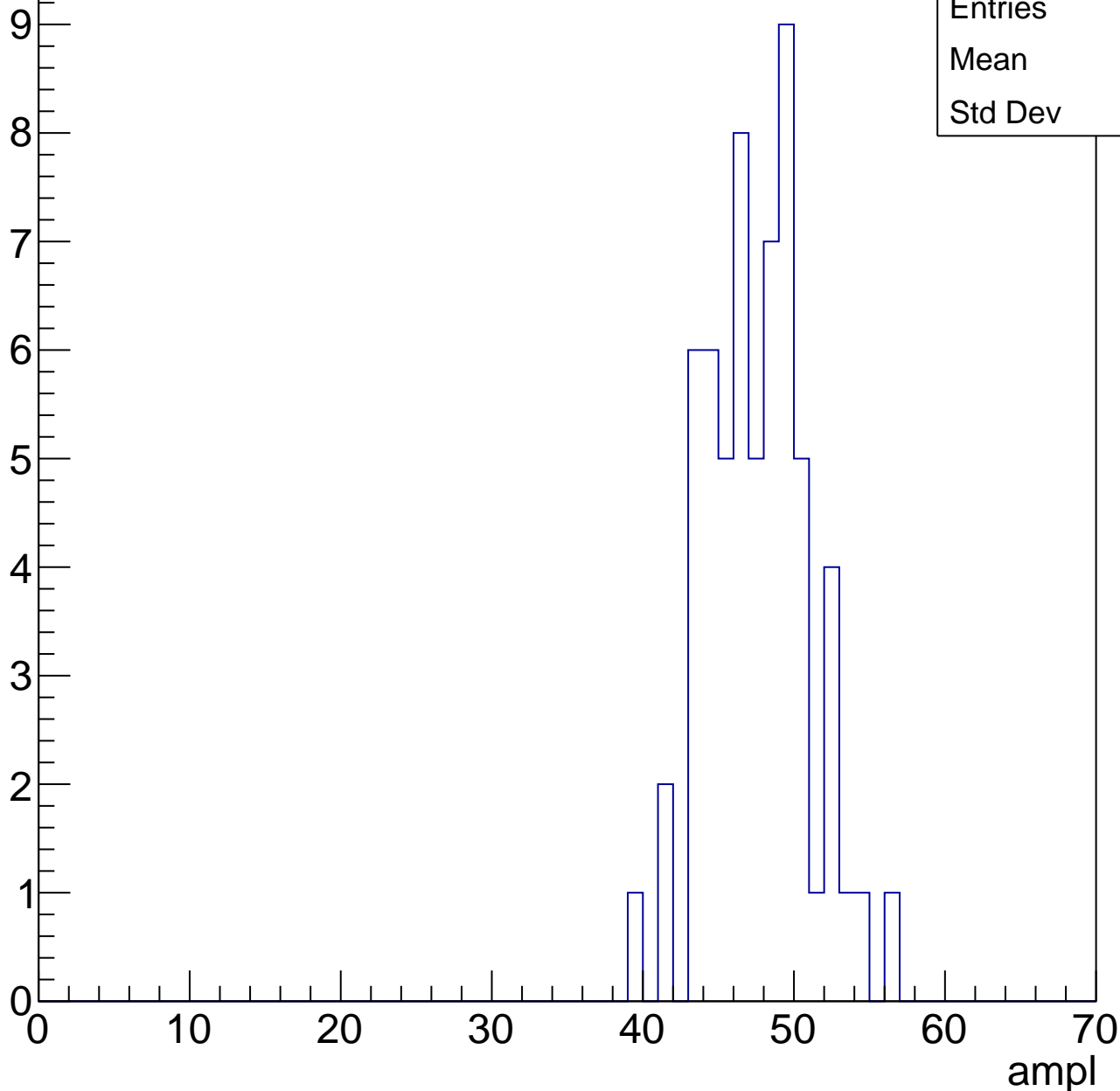
Gaus Width: 3.1113



B1L101S, U1-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



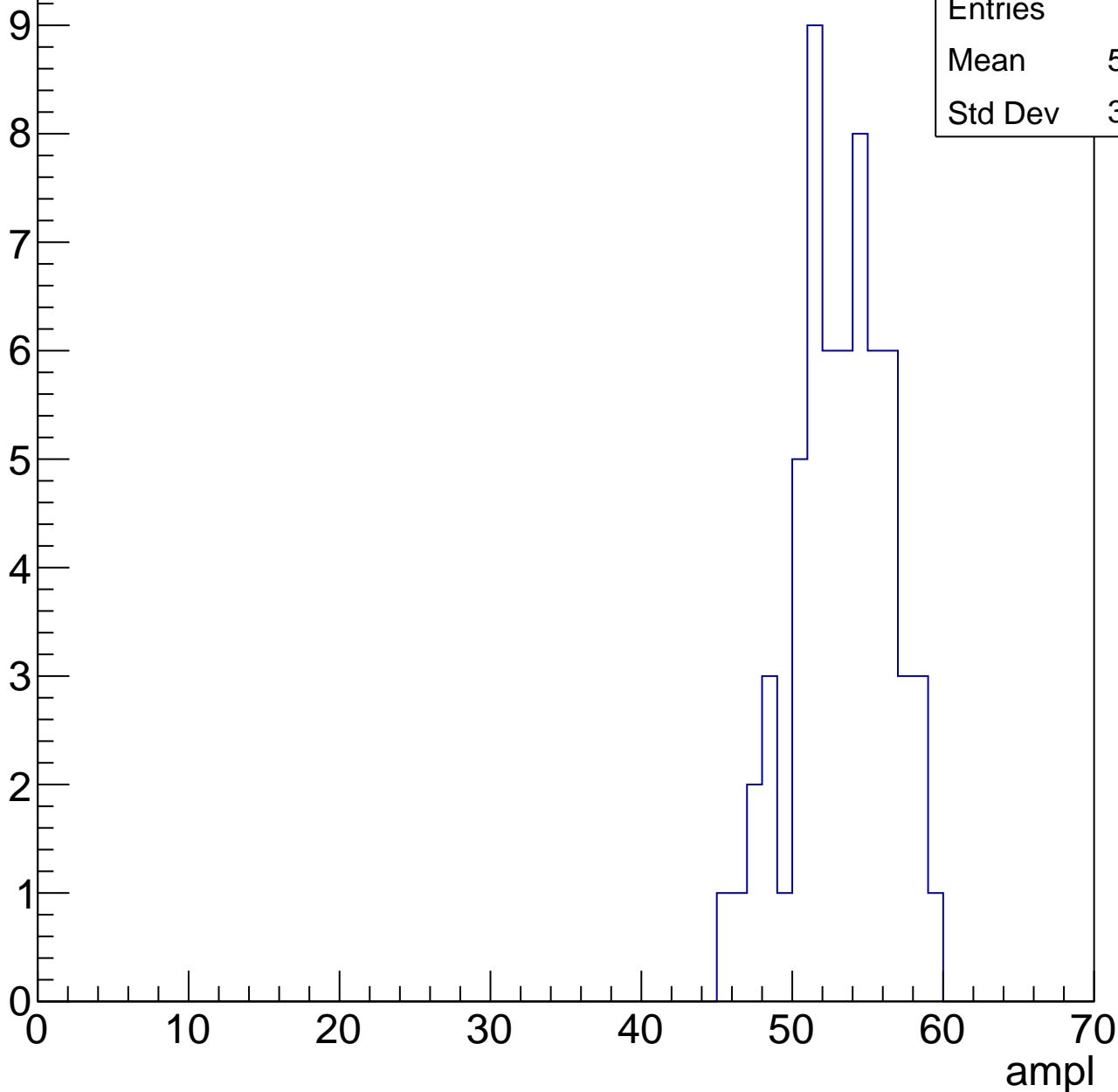
Entries	62
Mean	47.1
Std Dev	3.32

B1L101S, U1-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

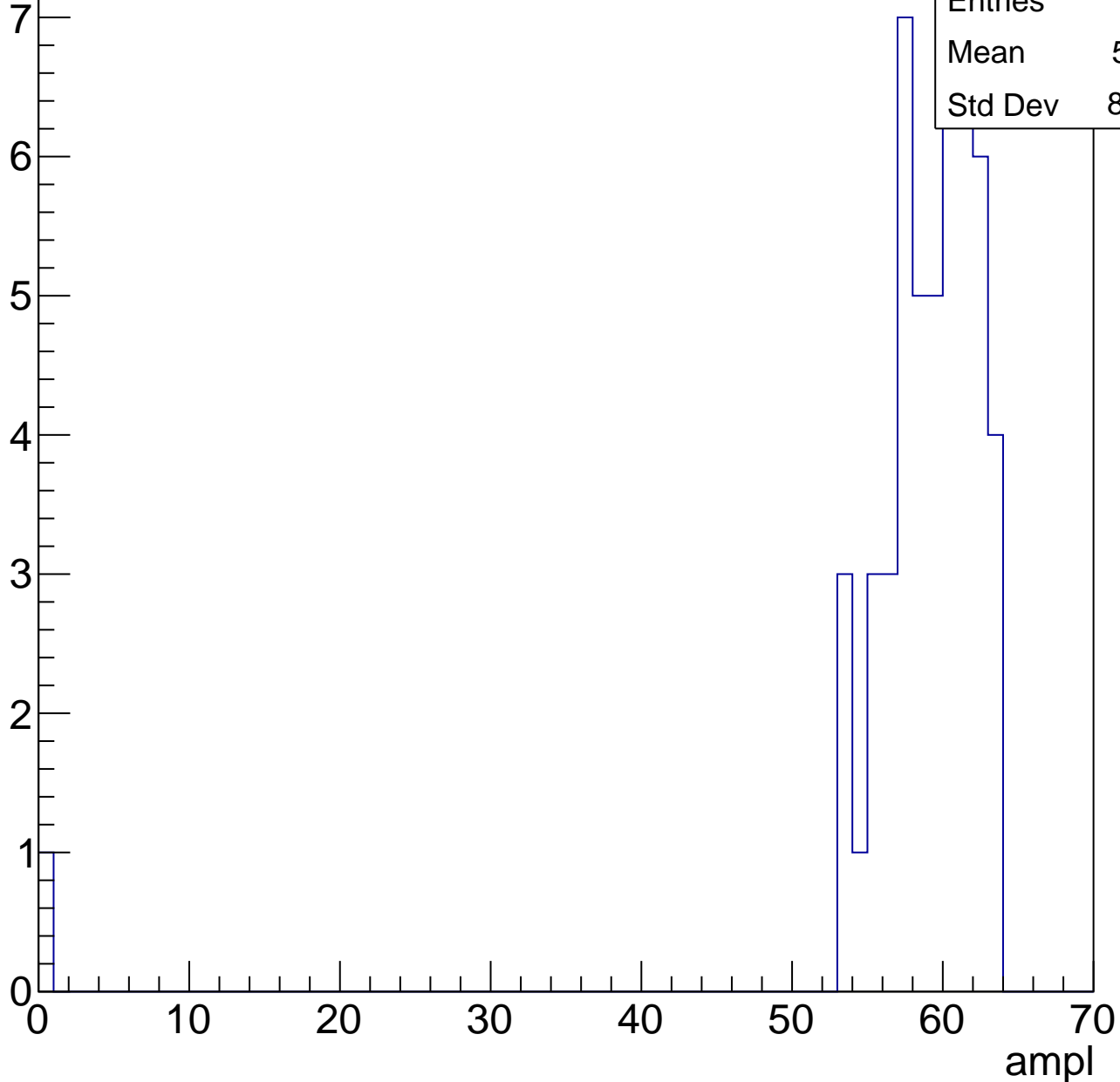
Entries	61
Mean	52.77
Std Dev	3.149



B1L101S, U1-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

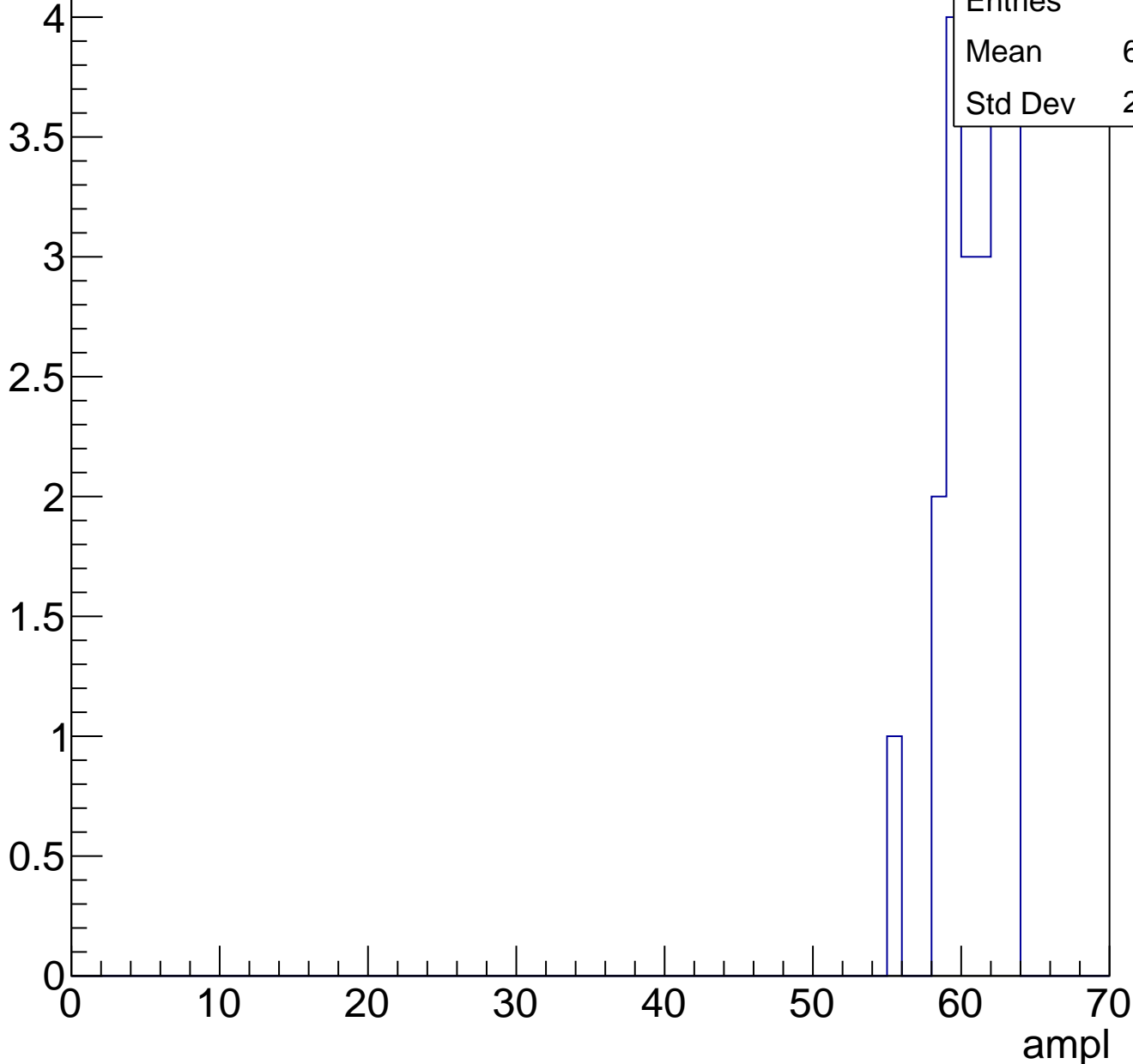
Entry



B1L101S, U1-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

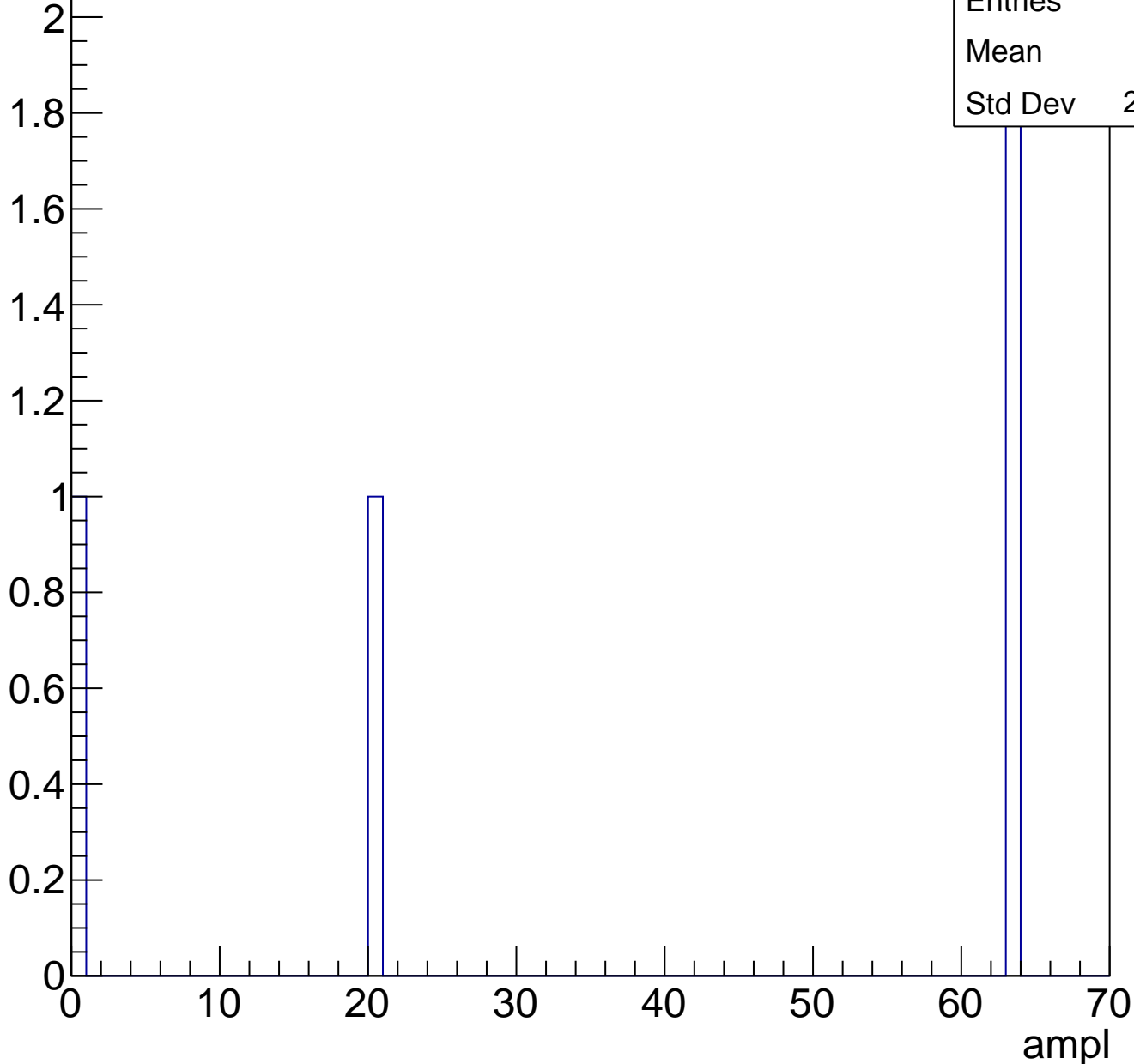
Entry



B1L101S, U1-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch29, adc0

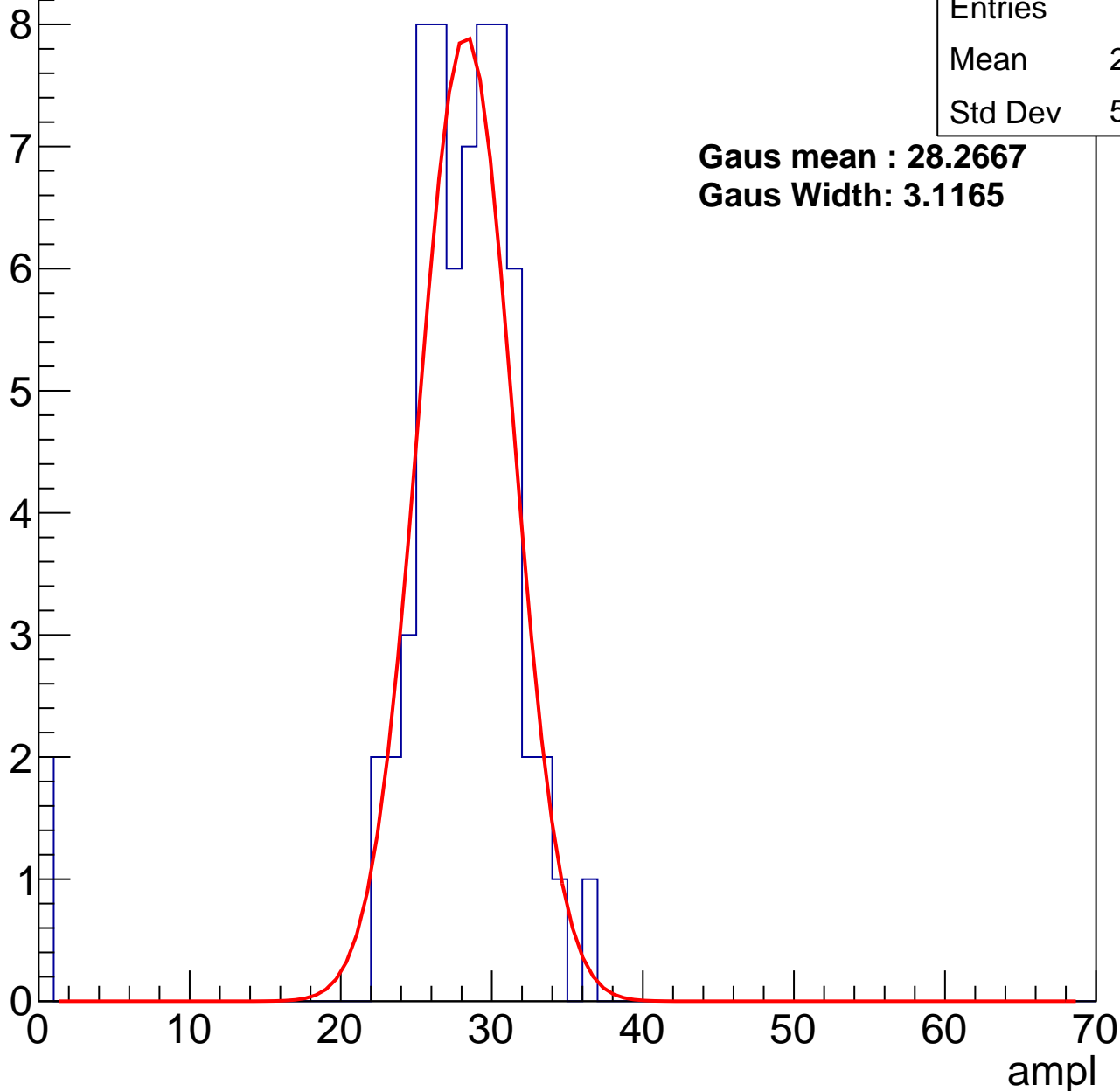
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.06
Std Dev	5.595

Gaus mean : 28.2667

Gaus Width: 3.1165



B1L101S, U1-ch29, adc1

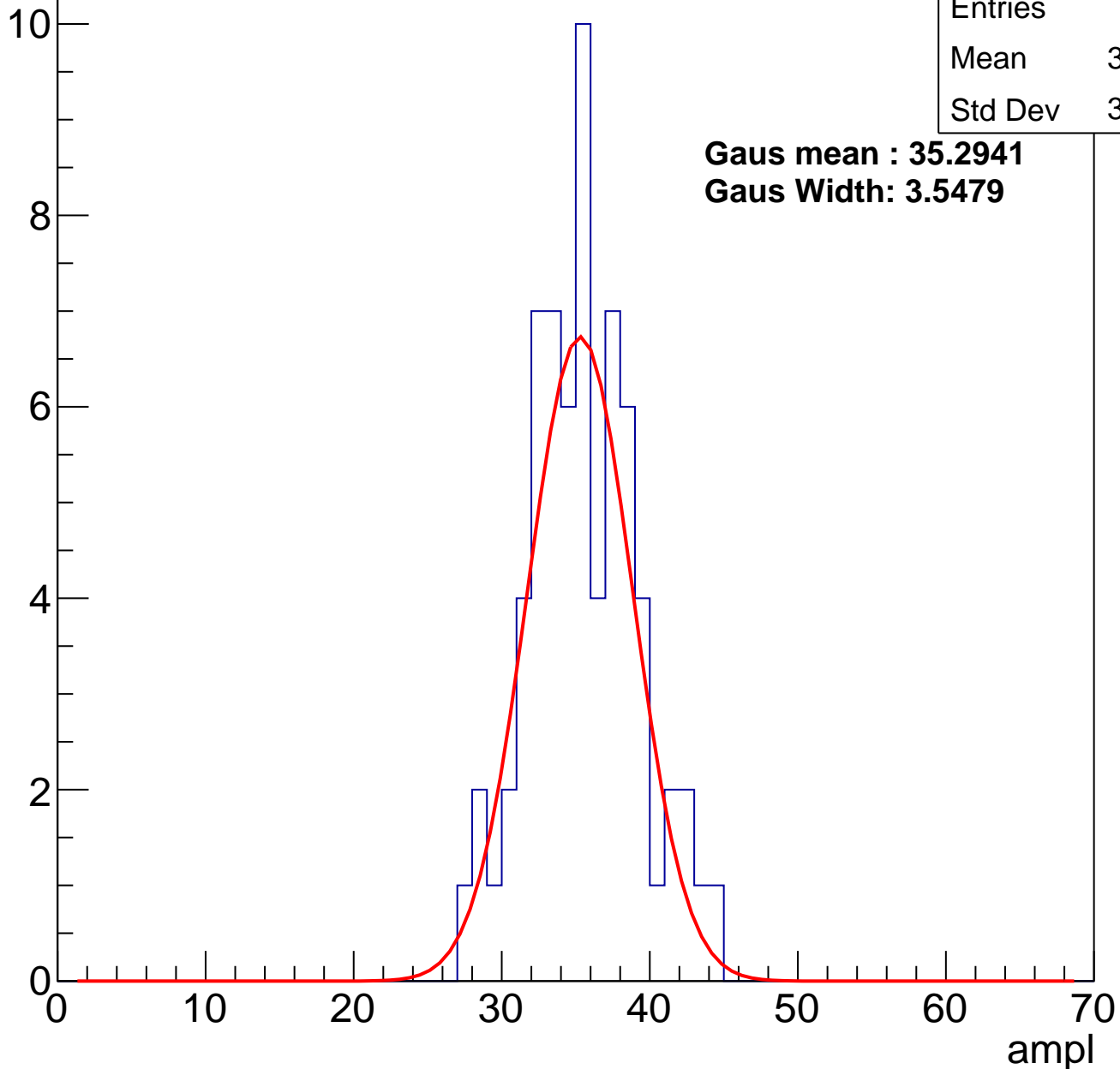
calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	35.07
Std Dev	3.635

Gaus mean : 35.2941

Gaus Width: 3.5479

Entry



B1L101S, U1-ch29, adc2

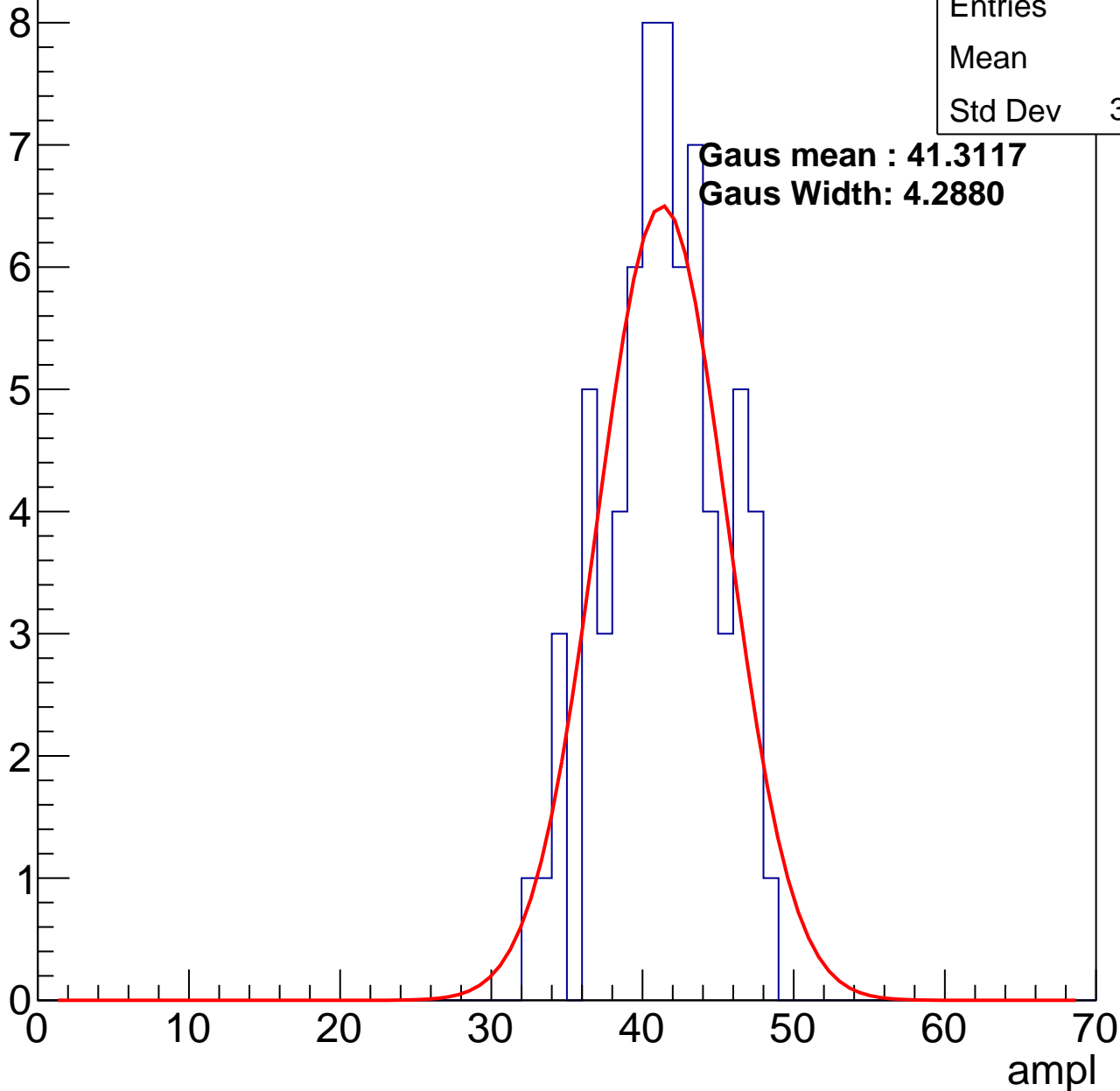
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	40.9
Std Dev	3.746

Gaus mean : 41.3117

Gaus Width: 4.2880

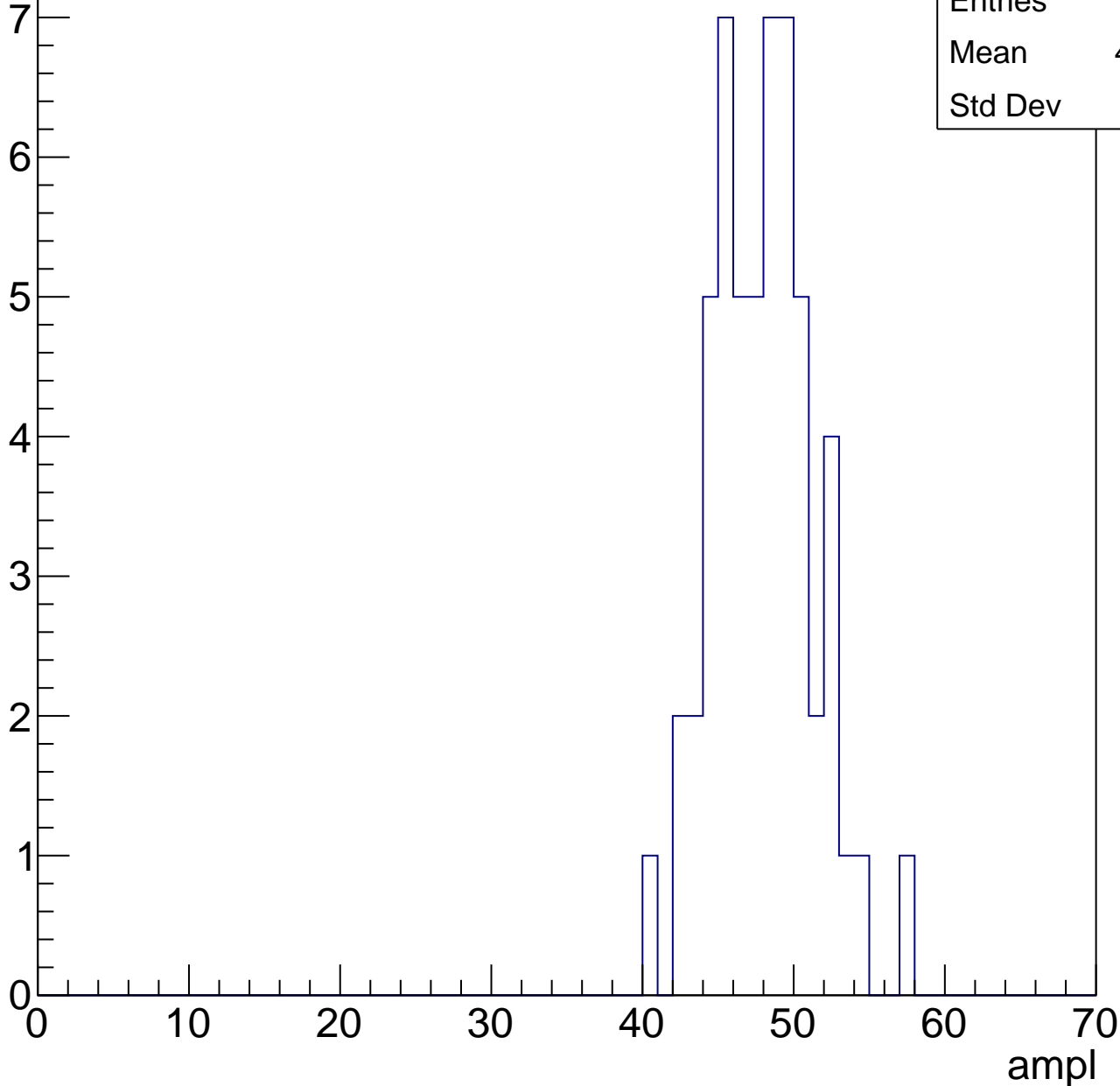


B1L101S, U1-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

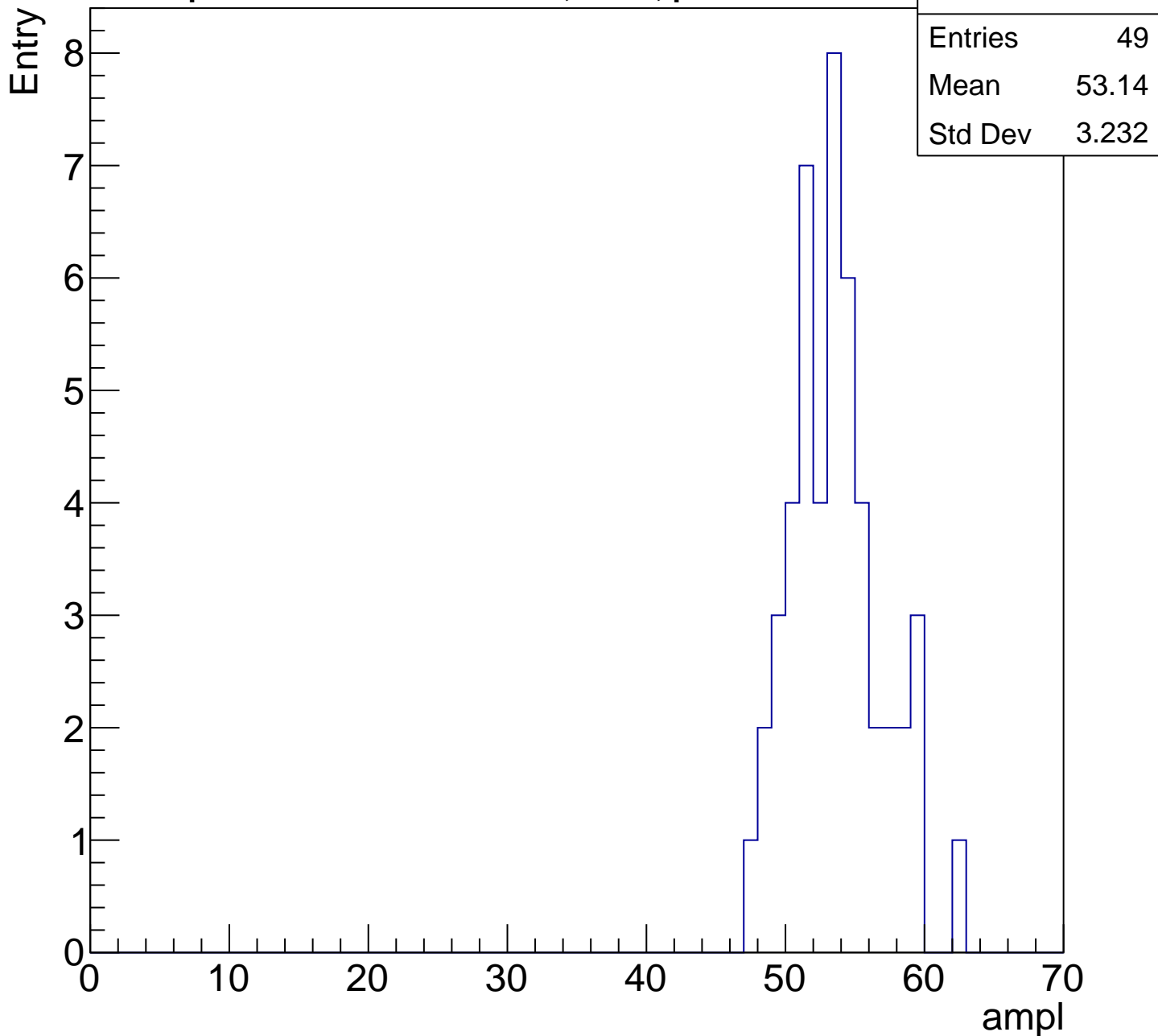
Entry

Entries	55
Mean	47.51
Std Dev	3.28



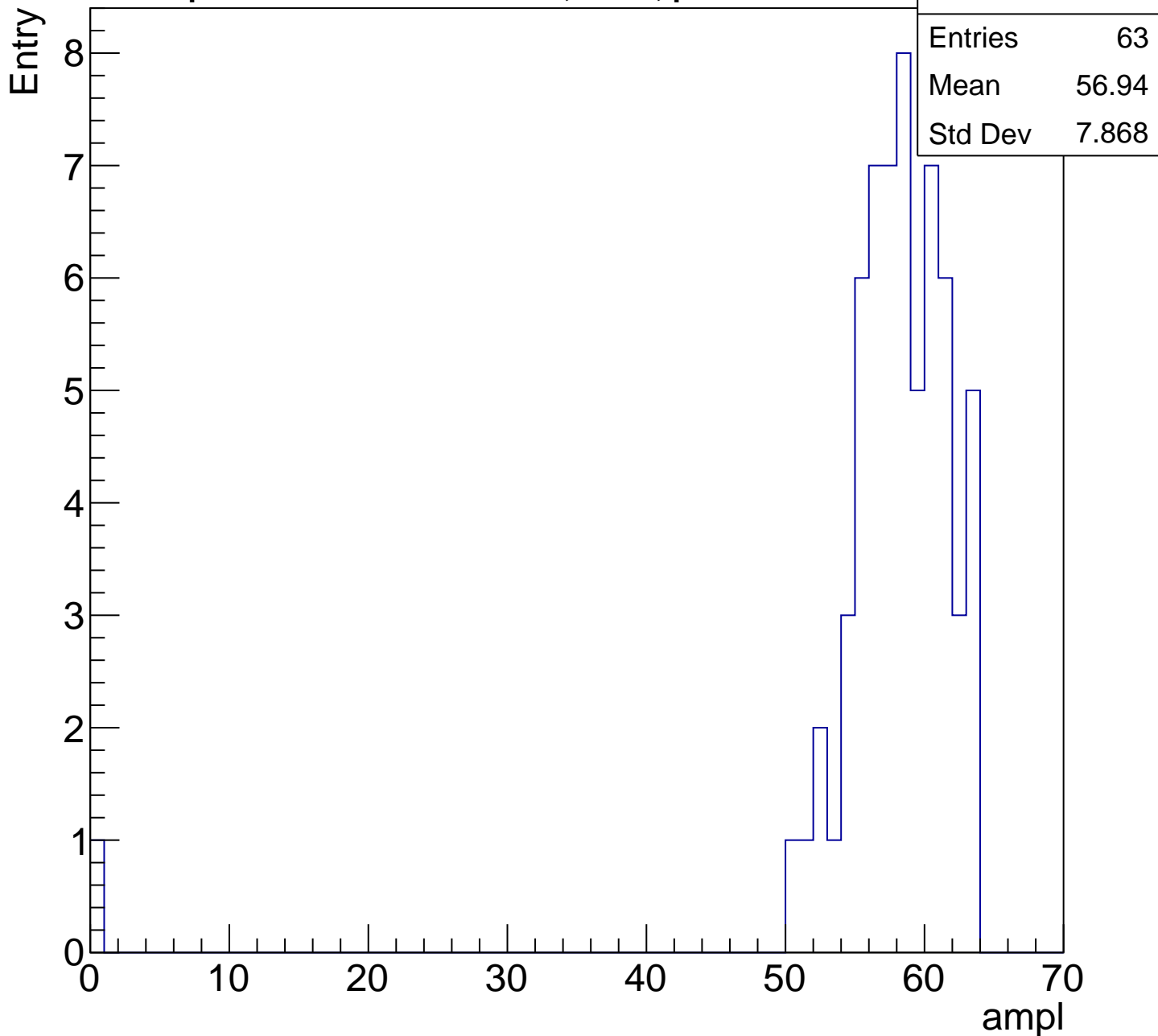
B1L101S, U1-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

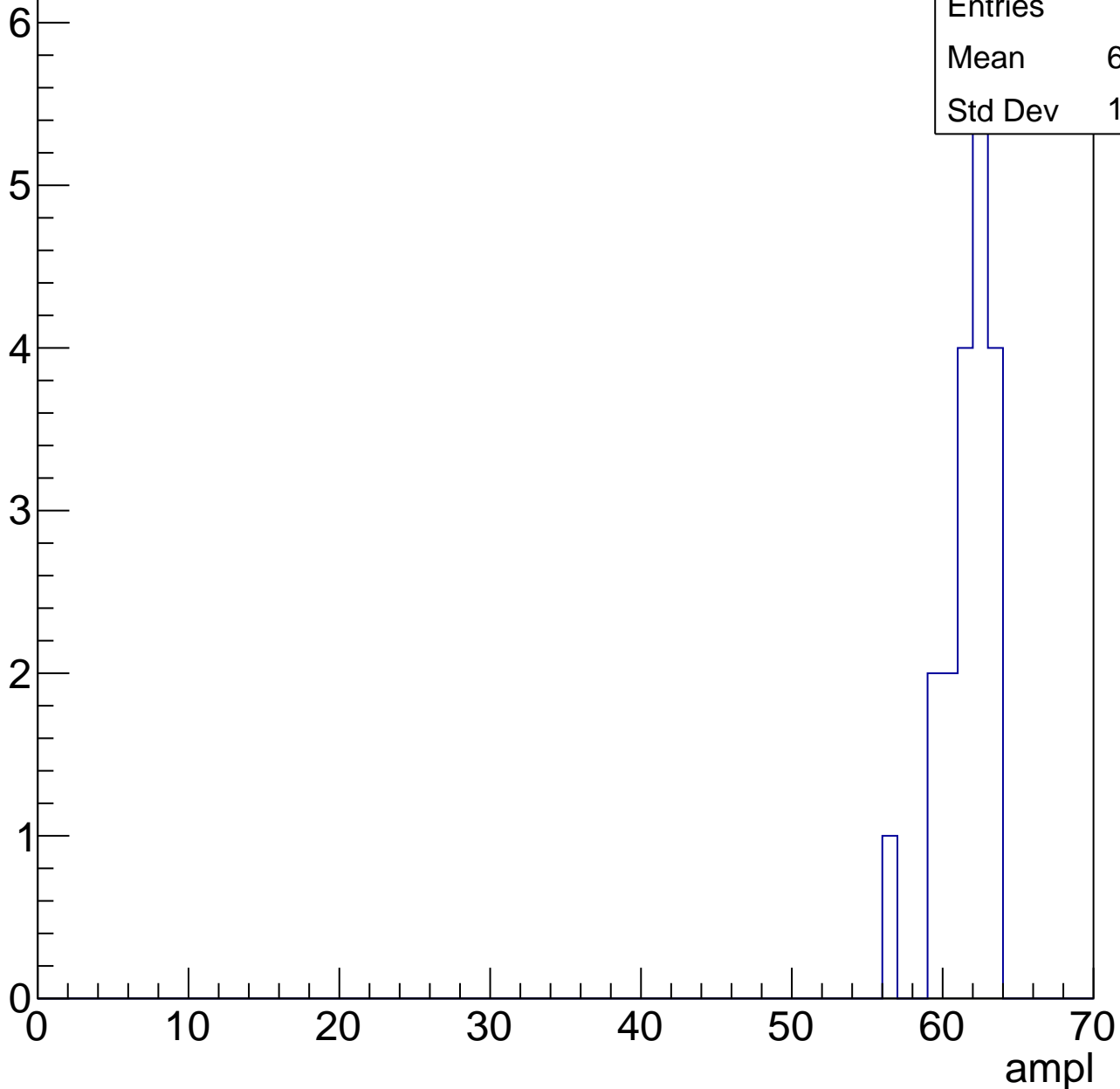


B1L101S, U1-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

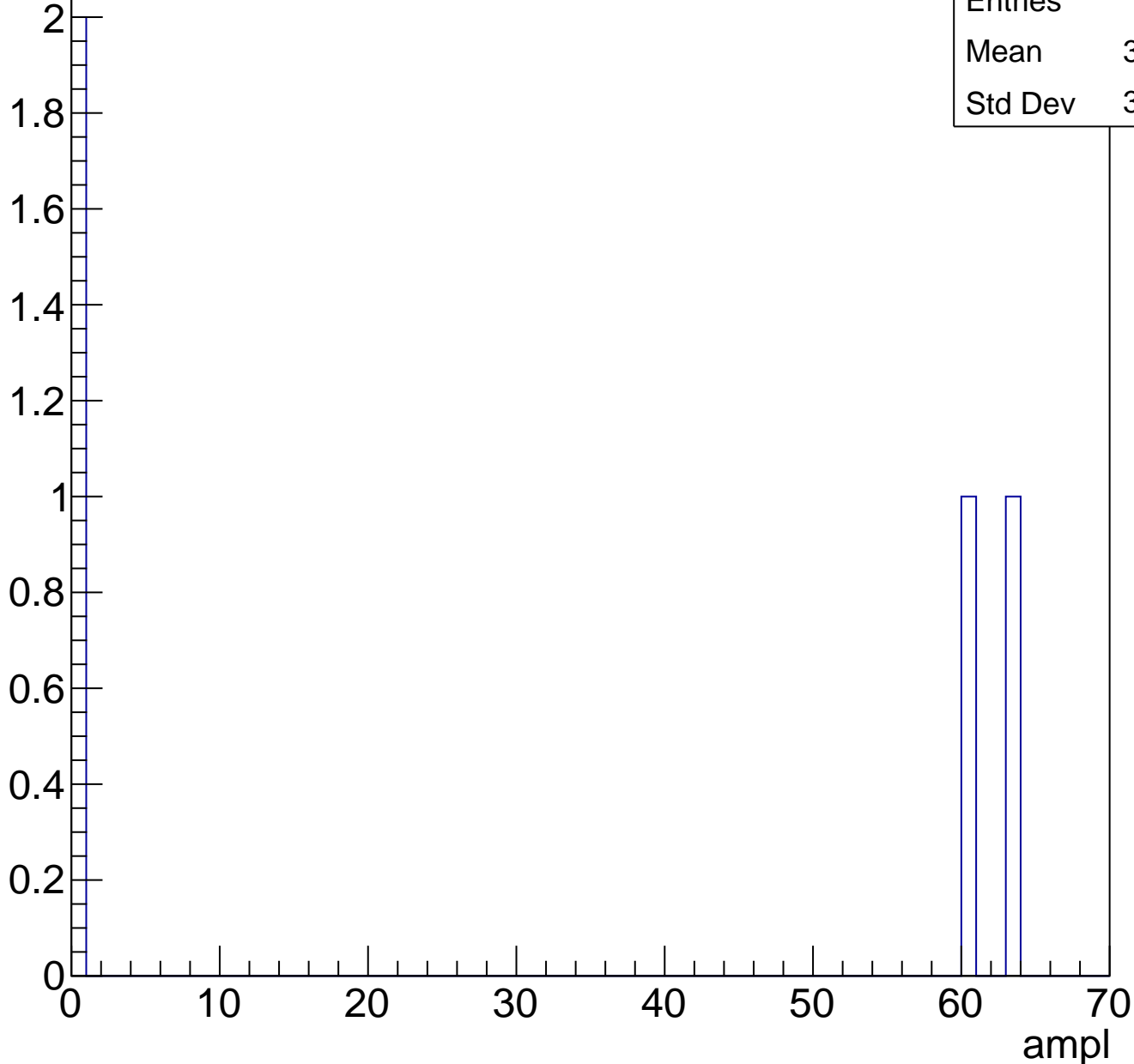
Entries	19
Mean	61.16
Std Dev	1.725



B1L101S, U1-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	30.75
Std Dev	30.77

B1L101S, U1-ch30, adc0

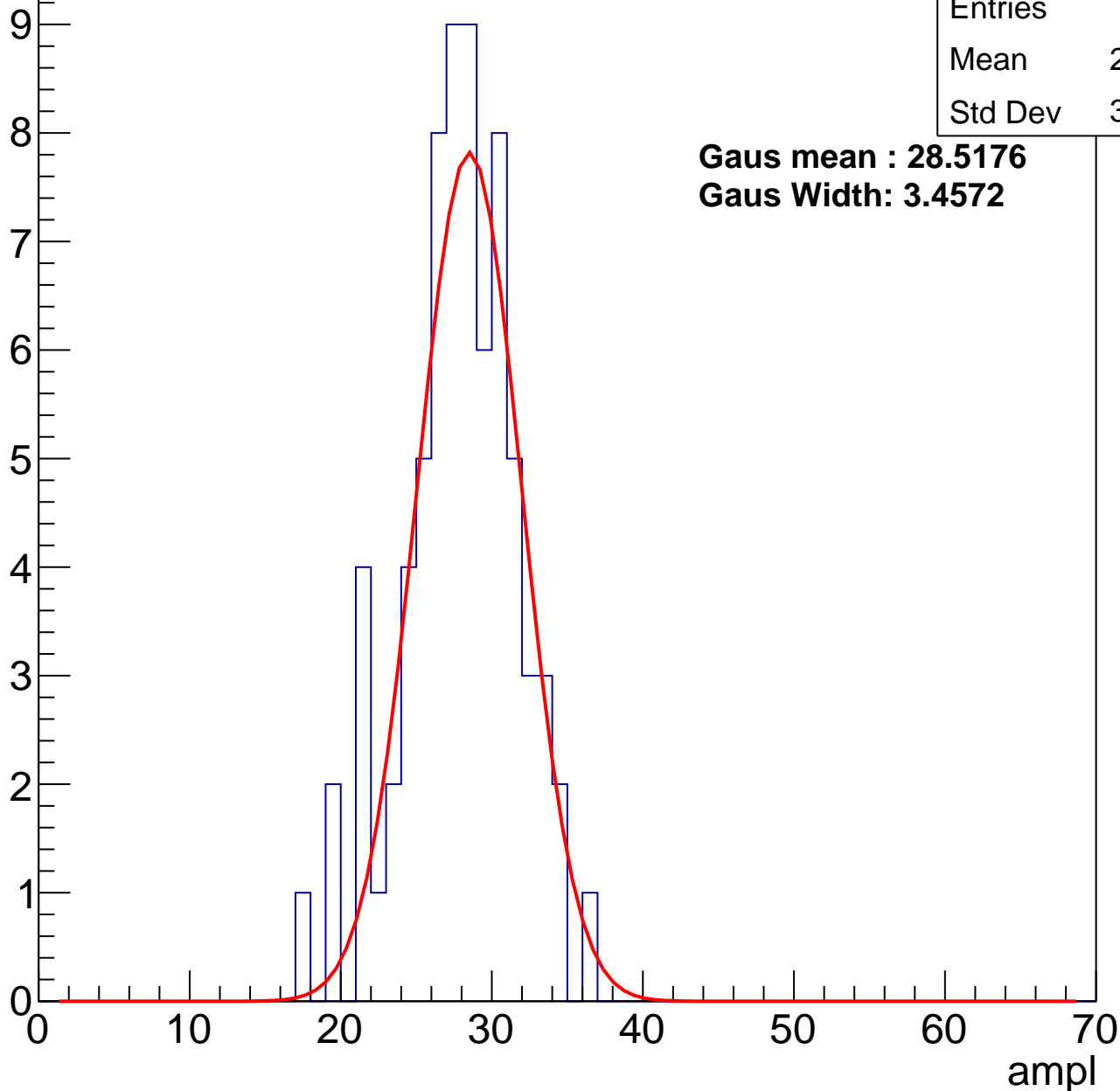
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.38
Std Dev	3.755

Gaus mean : 28.5176

Gaus Width: 3.4572



B1L101S, U1-ch30, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	34.8
Std Dev	3.393

Gaus mean : 35.5718

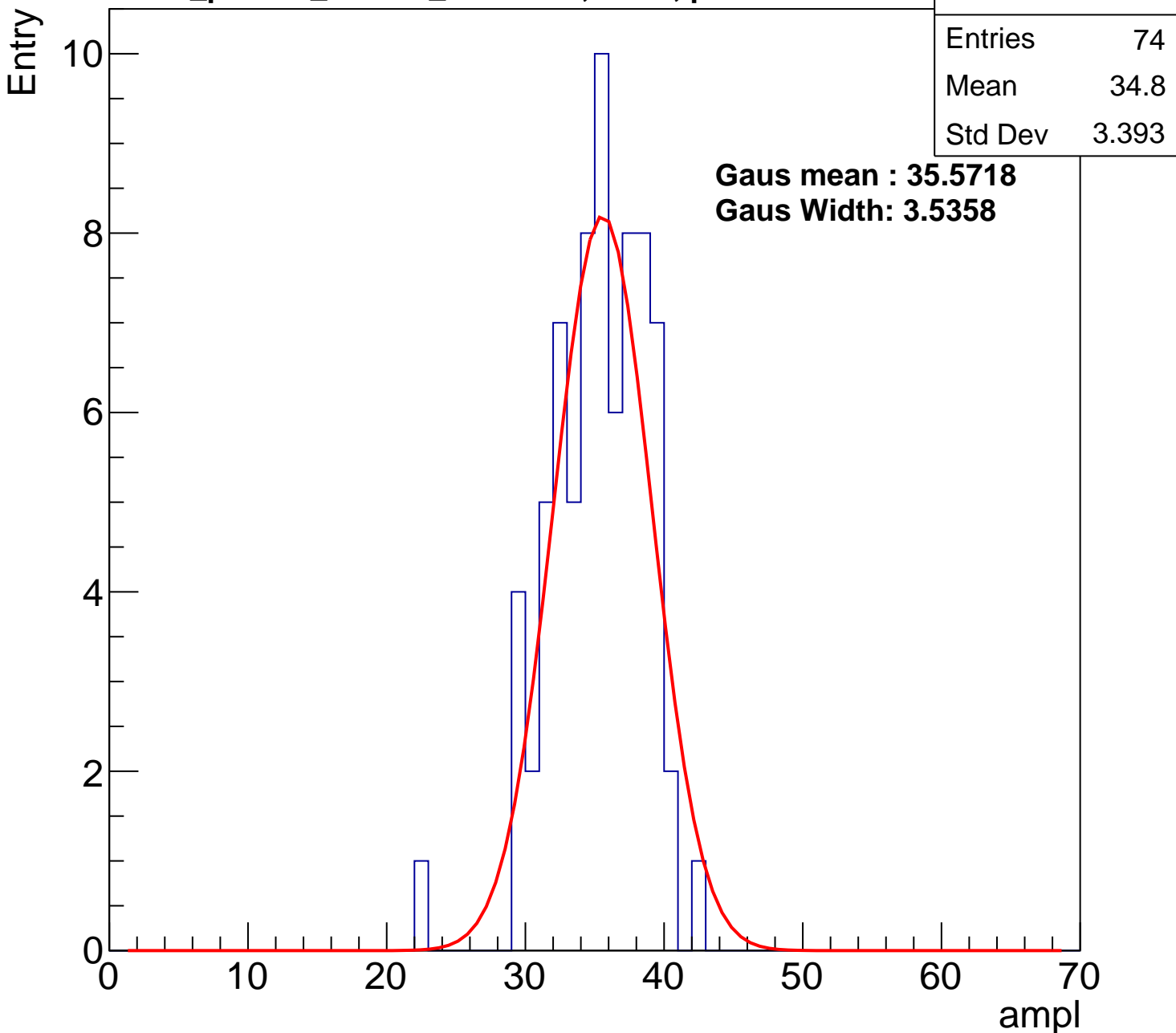
Gaus Width: 3.5358

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch30, adc2

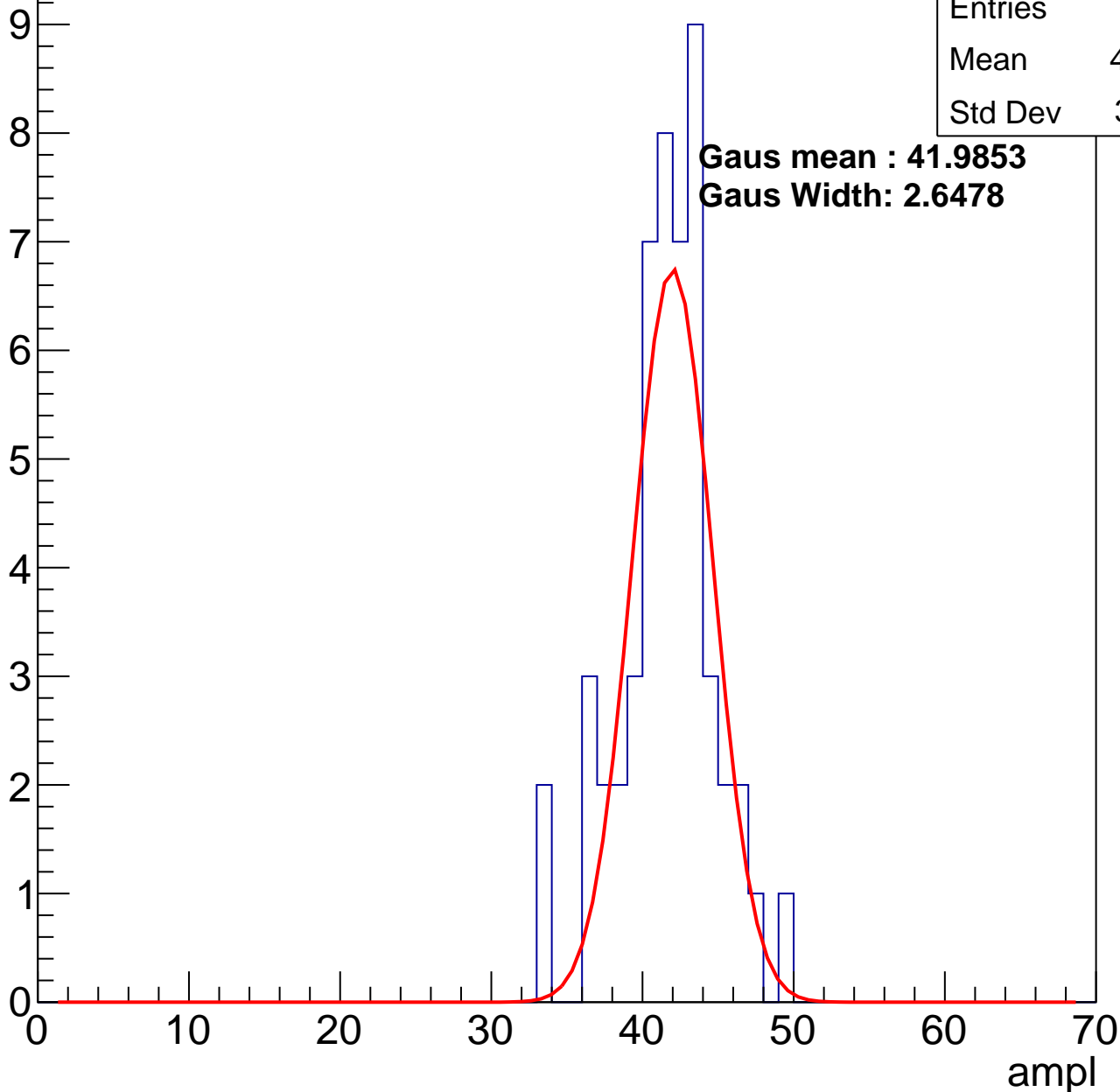
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.15
Std Dev	3.171

Gaus mean : 41.9853

Gaus Width: 2.6478

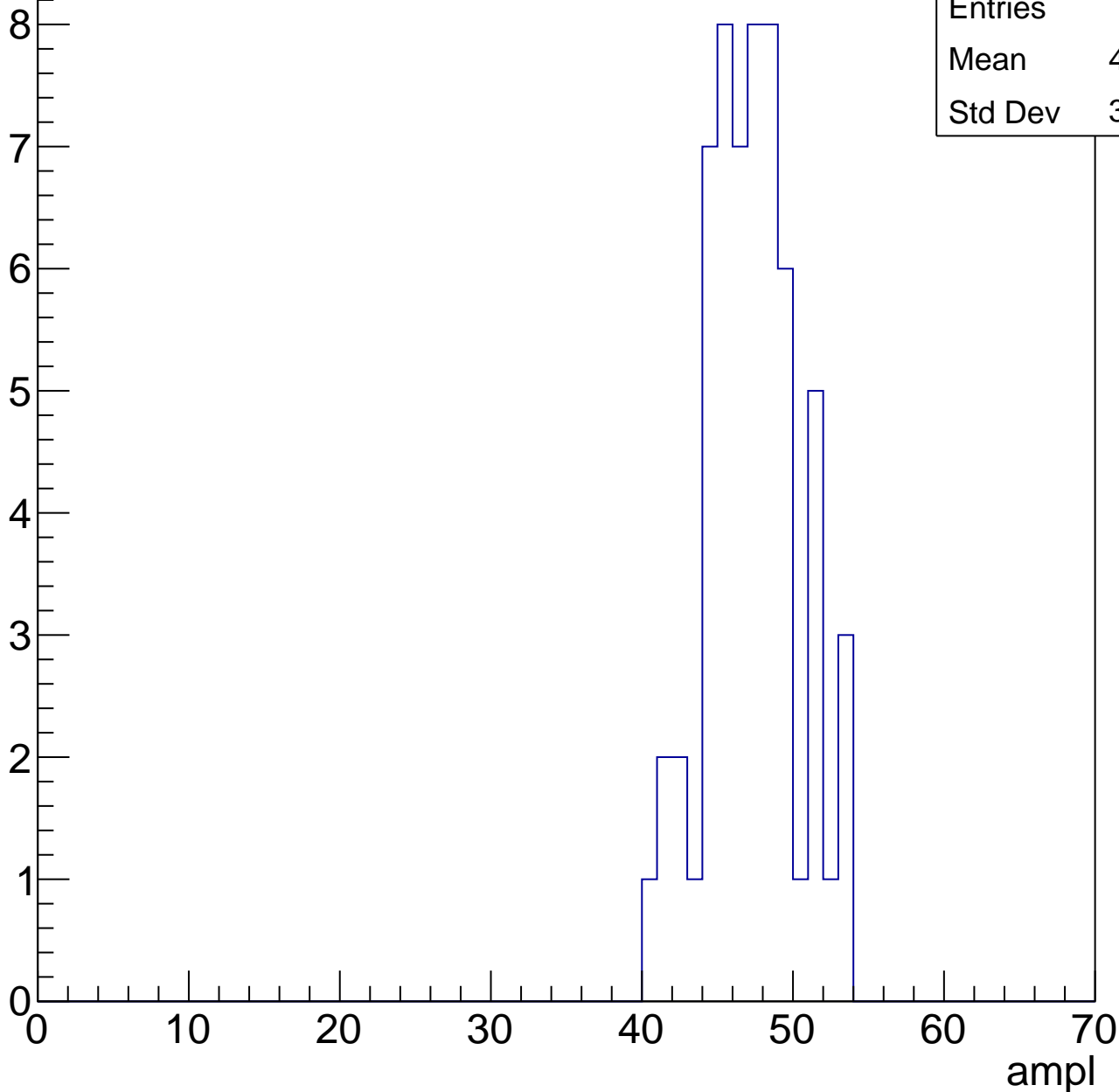


B1L101S, U1-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	46.82
Std Dev	3.003

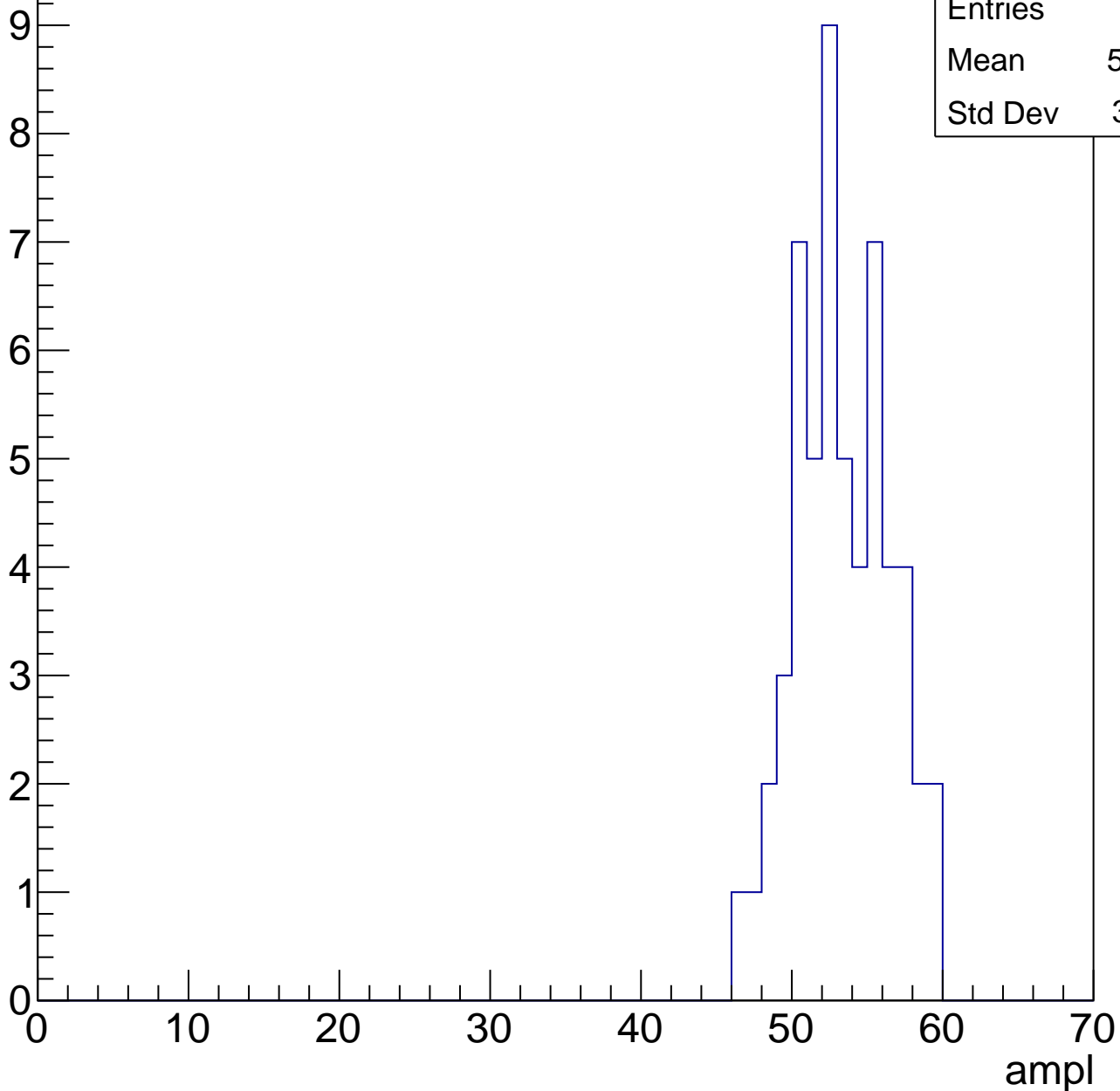


B1L101S, U1-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	52.88
Std Dev	3.071

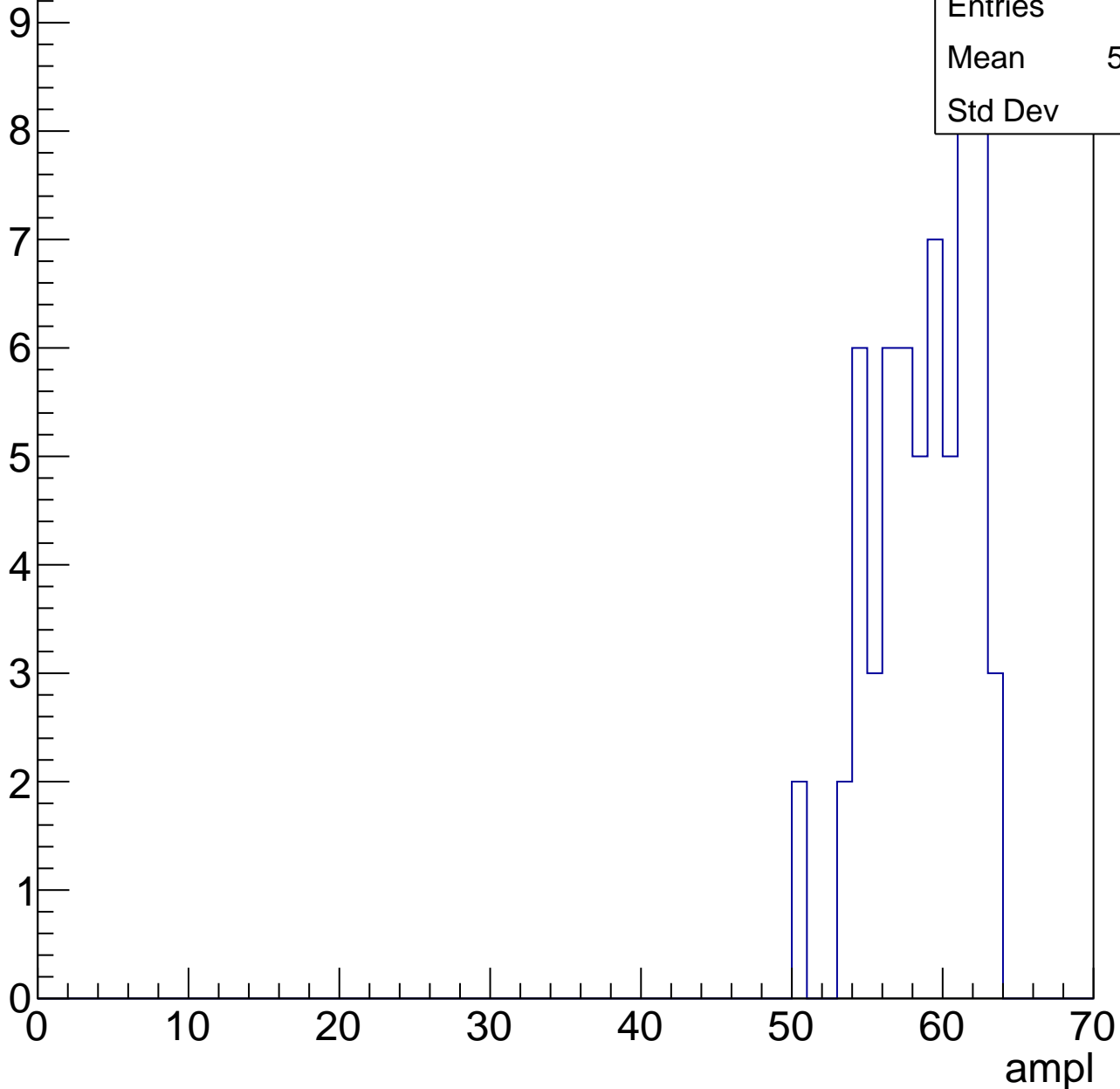


B1L101S, U1-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	58.23
Std Dev	3.21

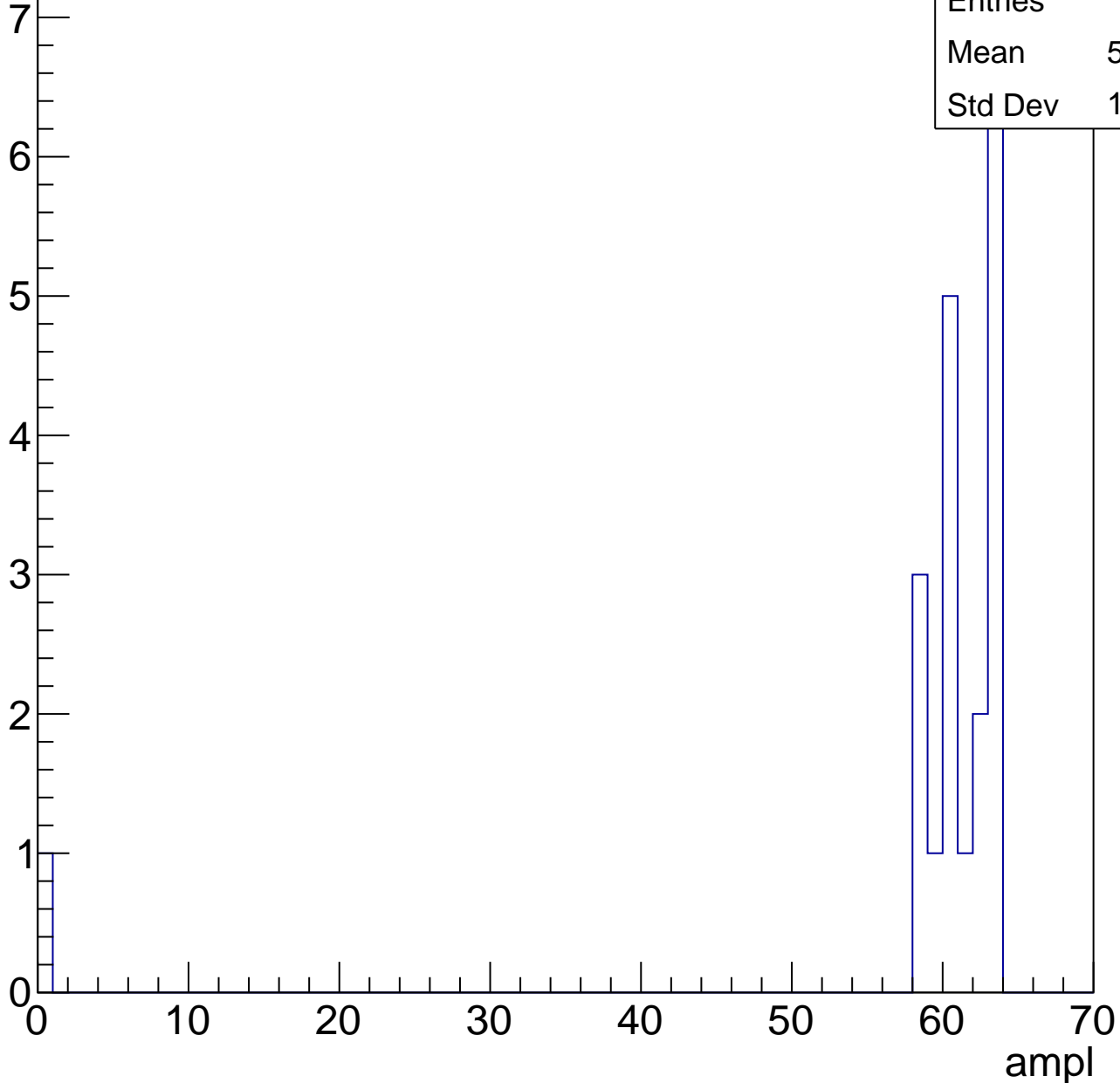


B1L101S, U1-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	57.95
Std Dev	13.42



B1L101S, U1-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch31, adc0

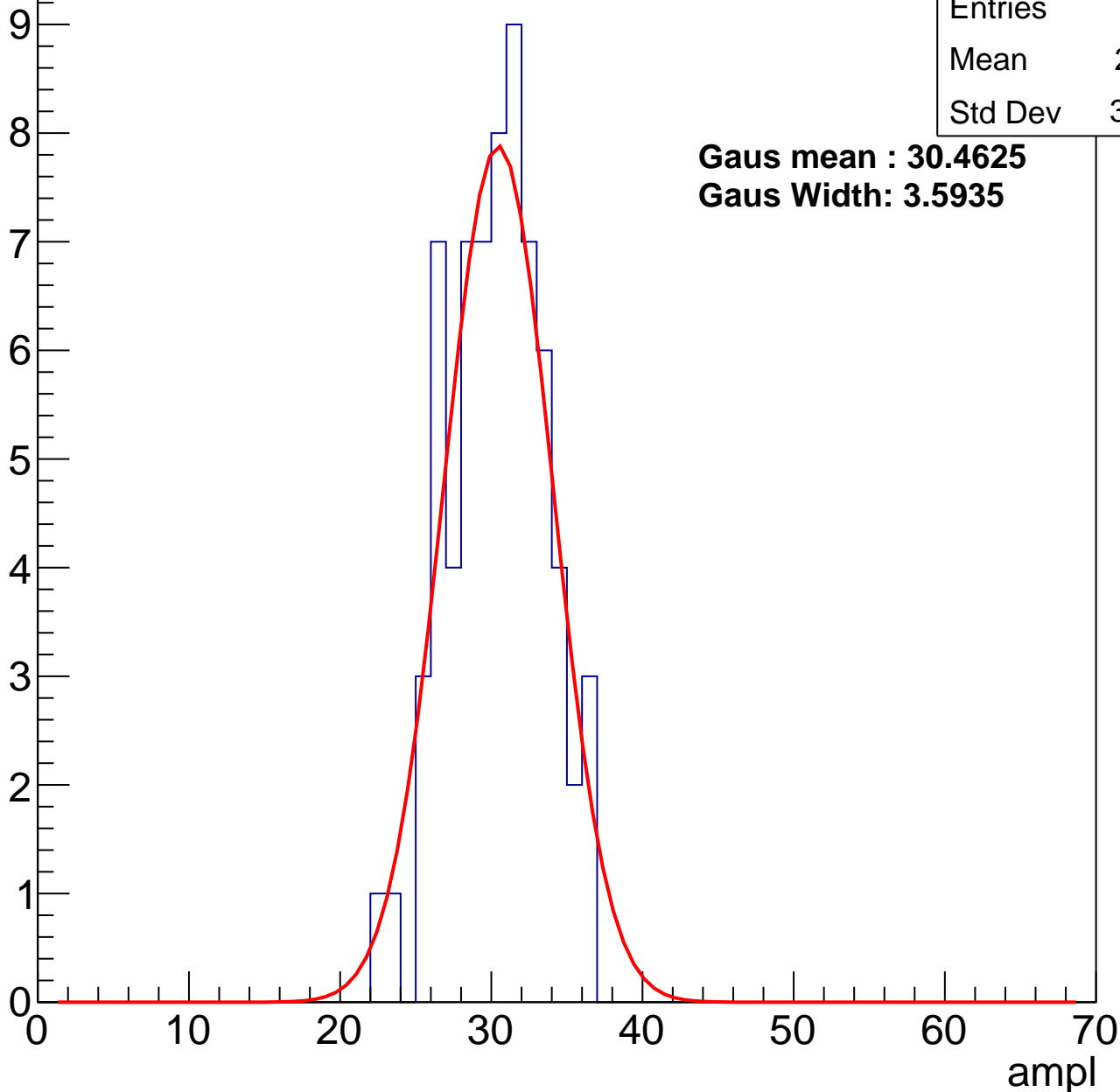
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.91
Std Dev	3.147

Gaus mean : 30.4625

Gaus Width: 3.5935



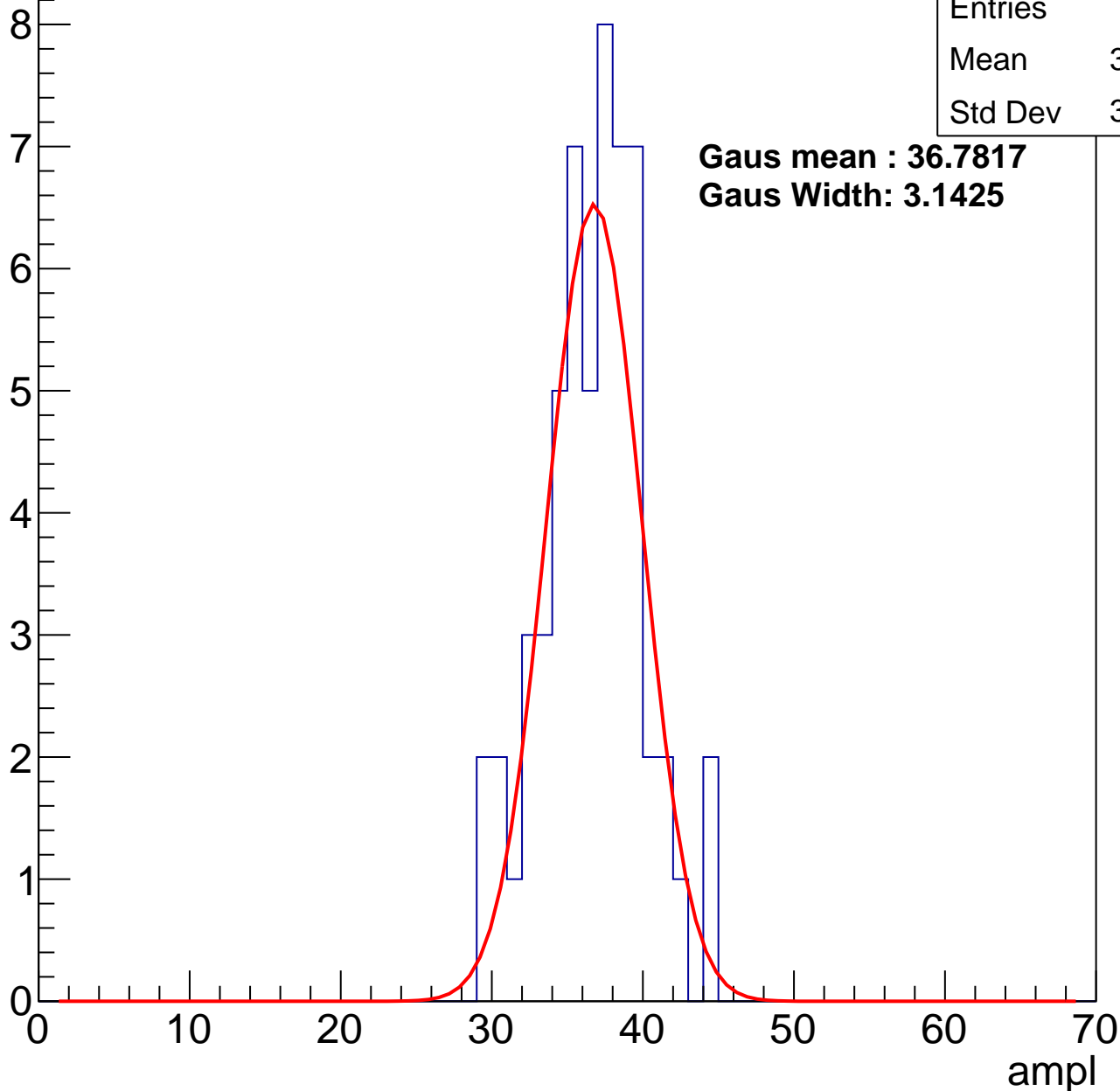
B1L101S, U1-ch31, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.25
Std Dev	3.347

Gaus mean : 36.7817
Gaus Width: 3.1425



B1L101S, U1-ch31, adc2

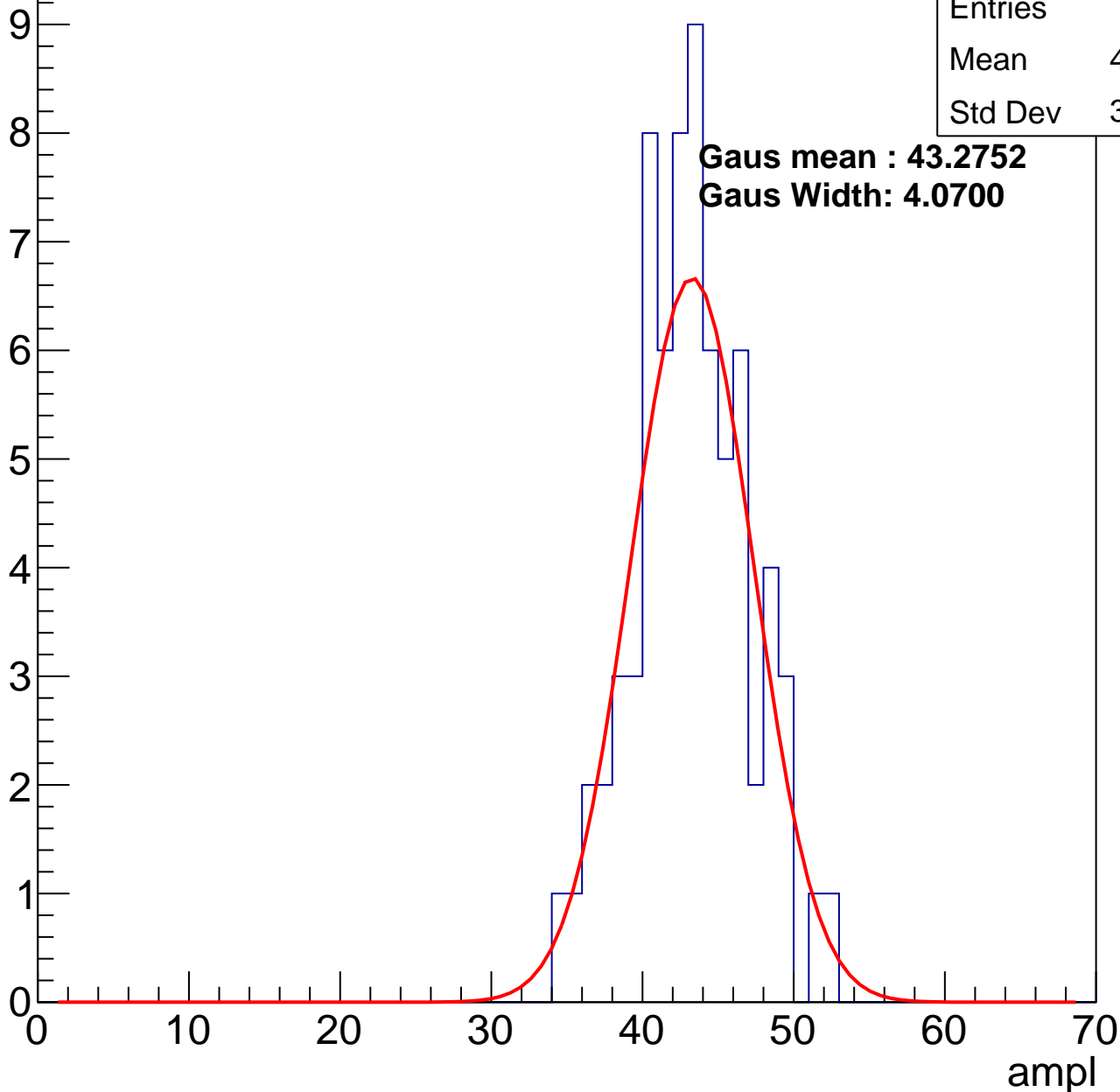
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.76
Std Dev	3.758

Gaus mean : 43.2752

Gaus Width: 4.0700

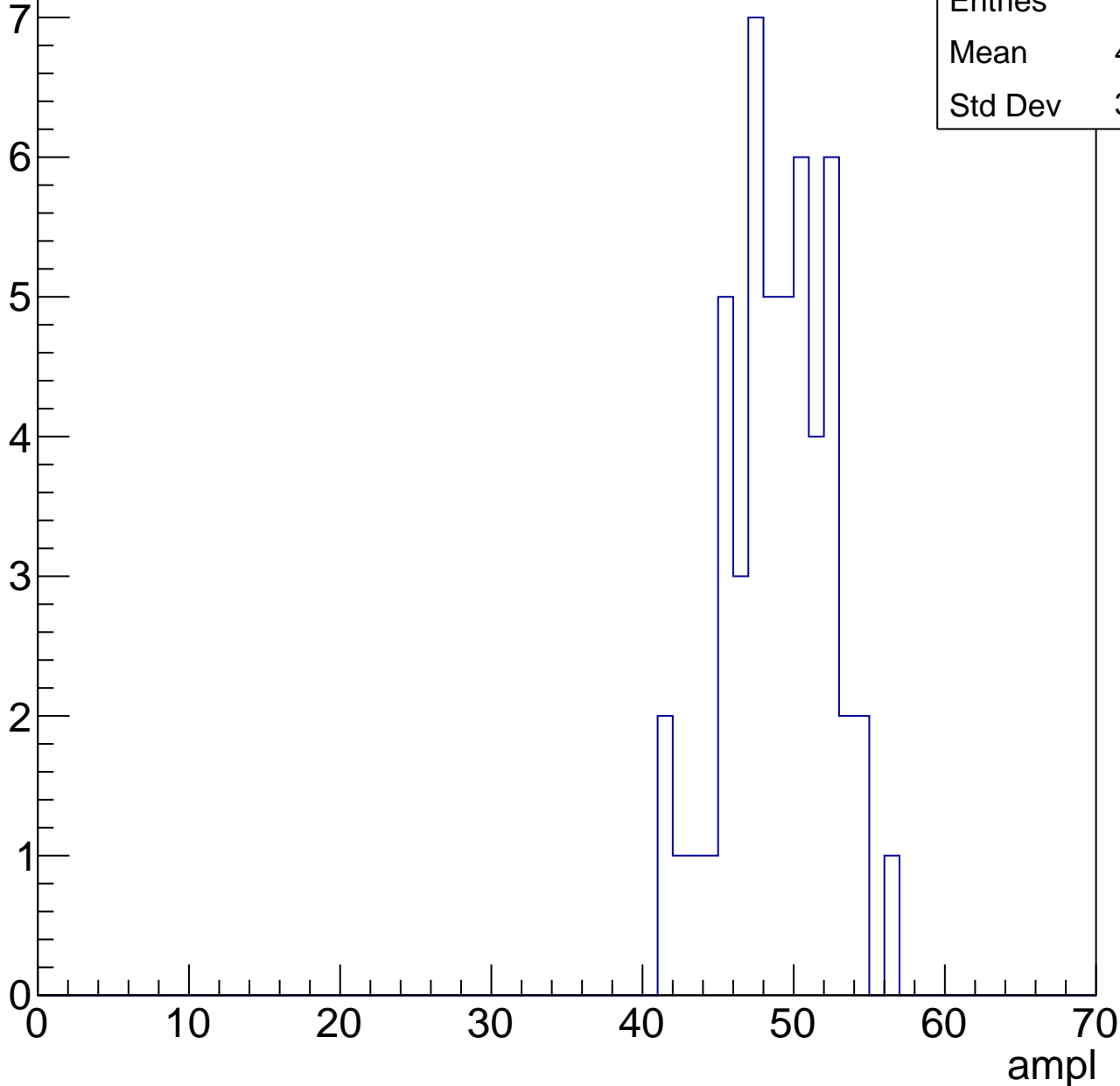


B1L101S, U1-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

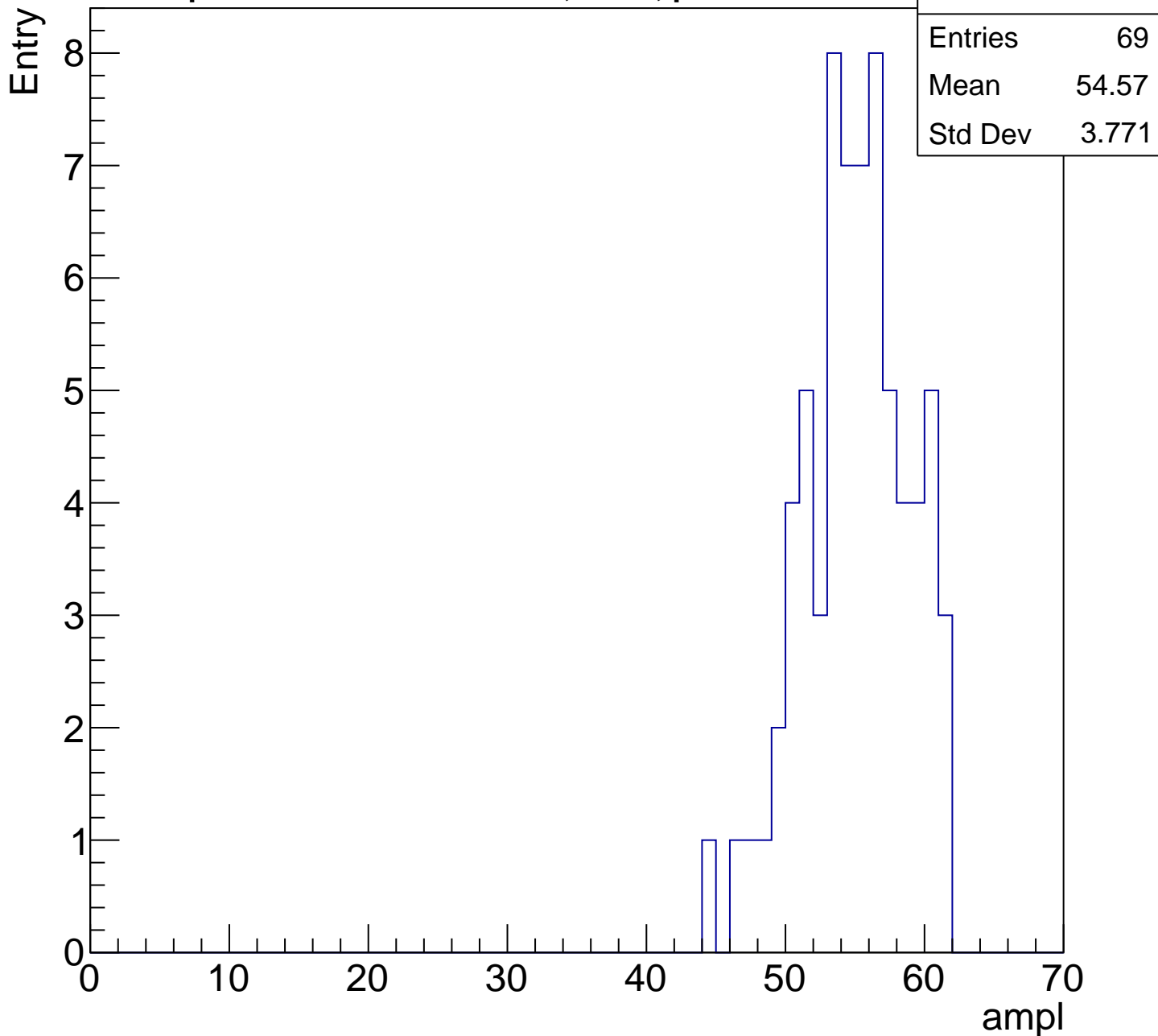
Entry

Entries	51
Mean	48.51
Std Dev	3.351



B1L101S, U1-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

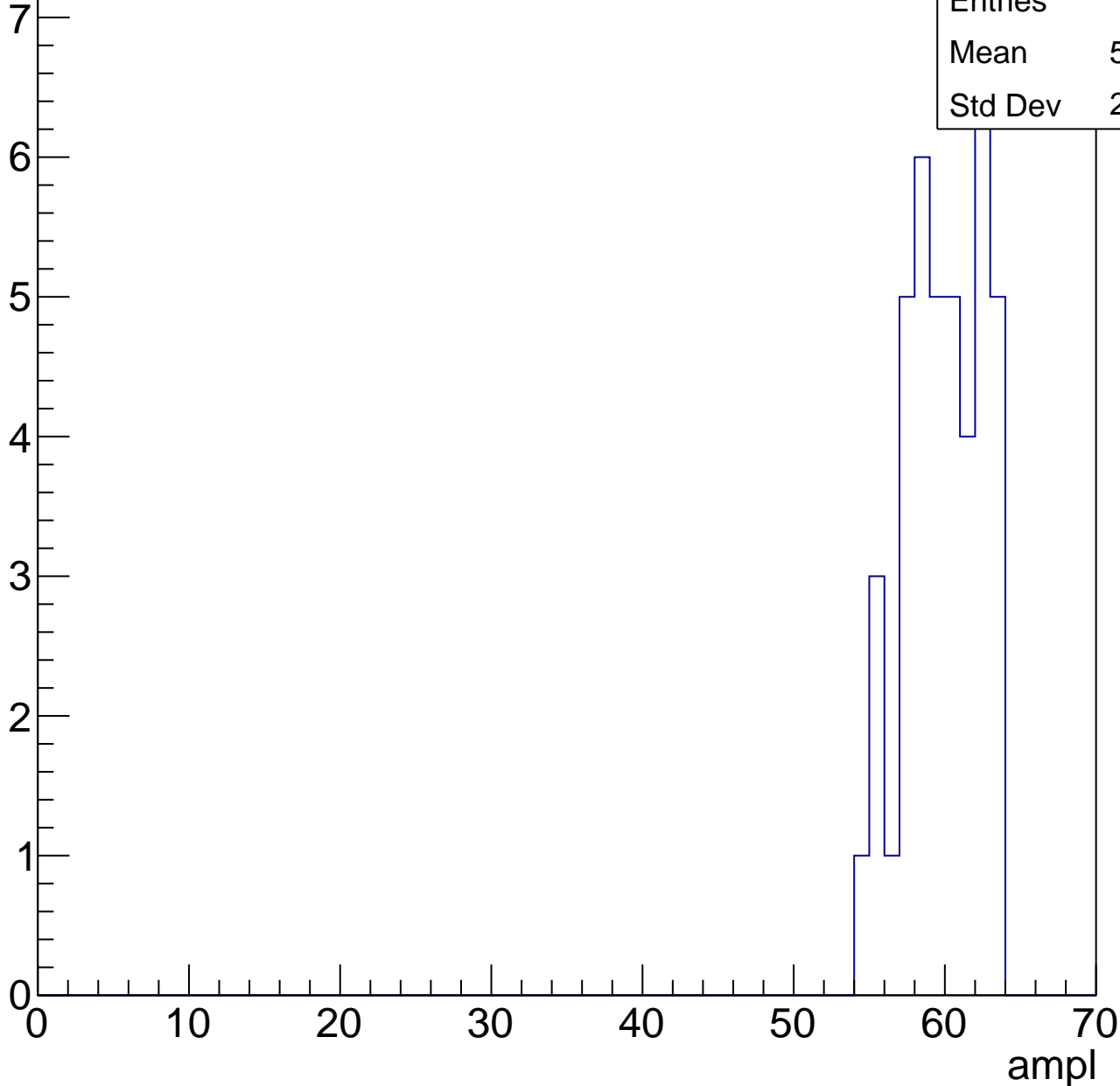


B1L101S, U1-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.43
Std Dev	2.508

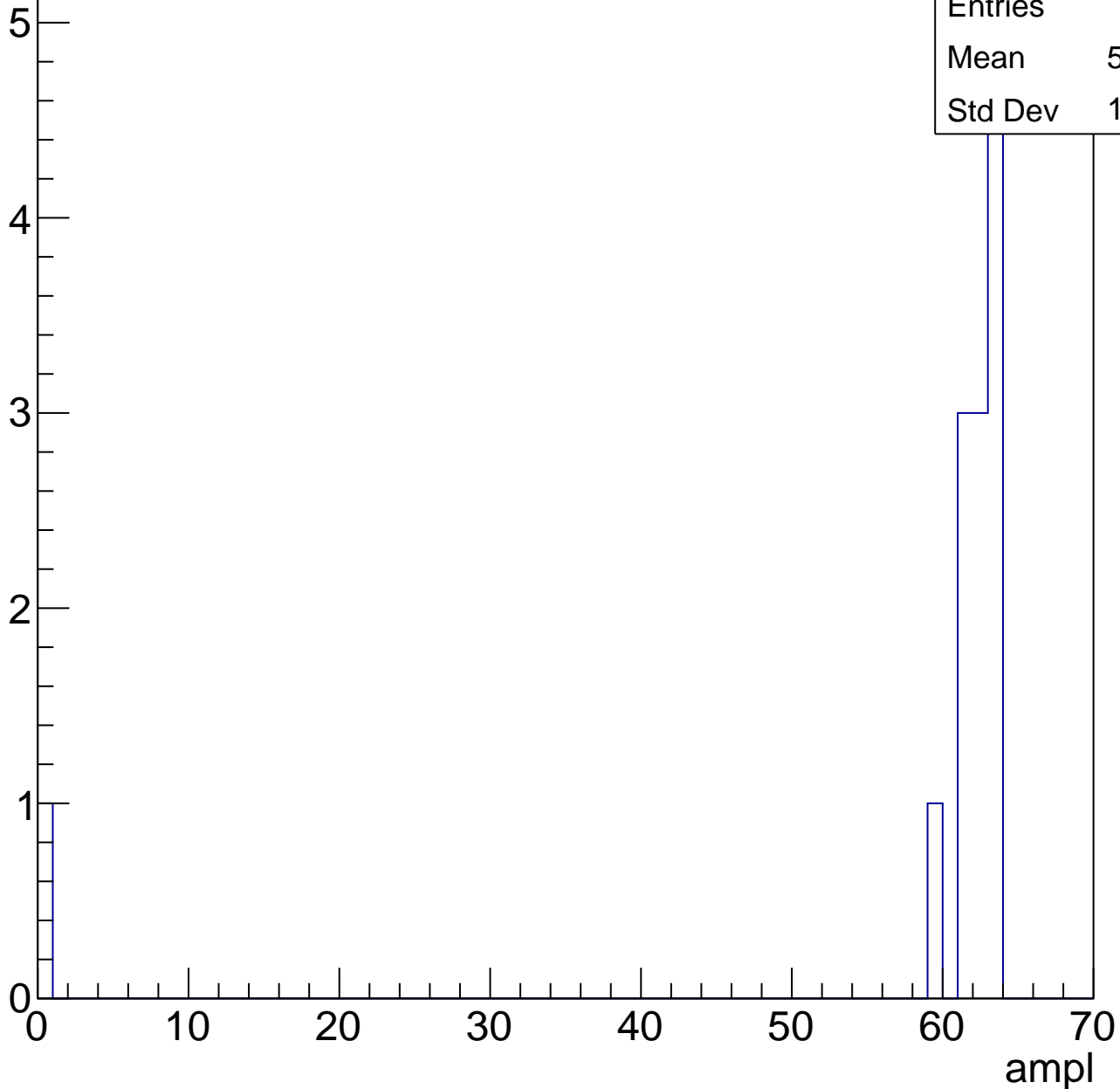


B1L101S, U1-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

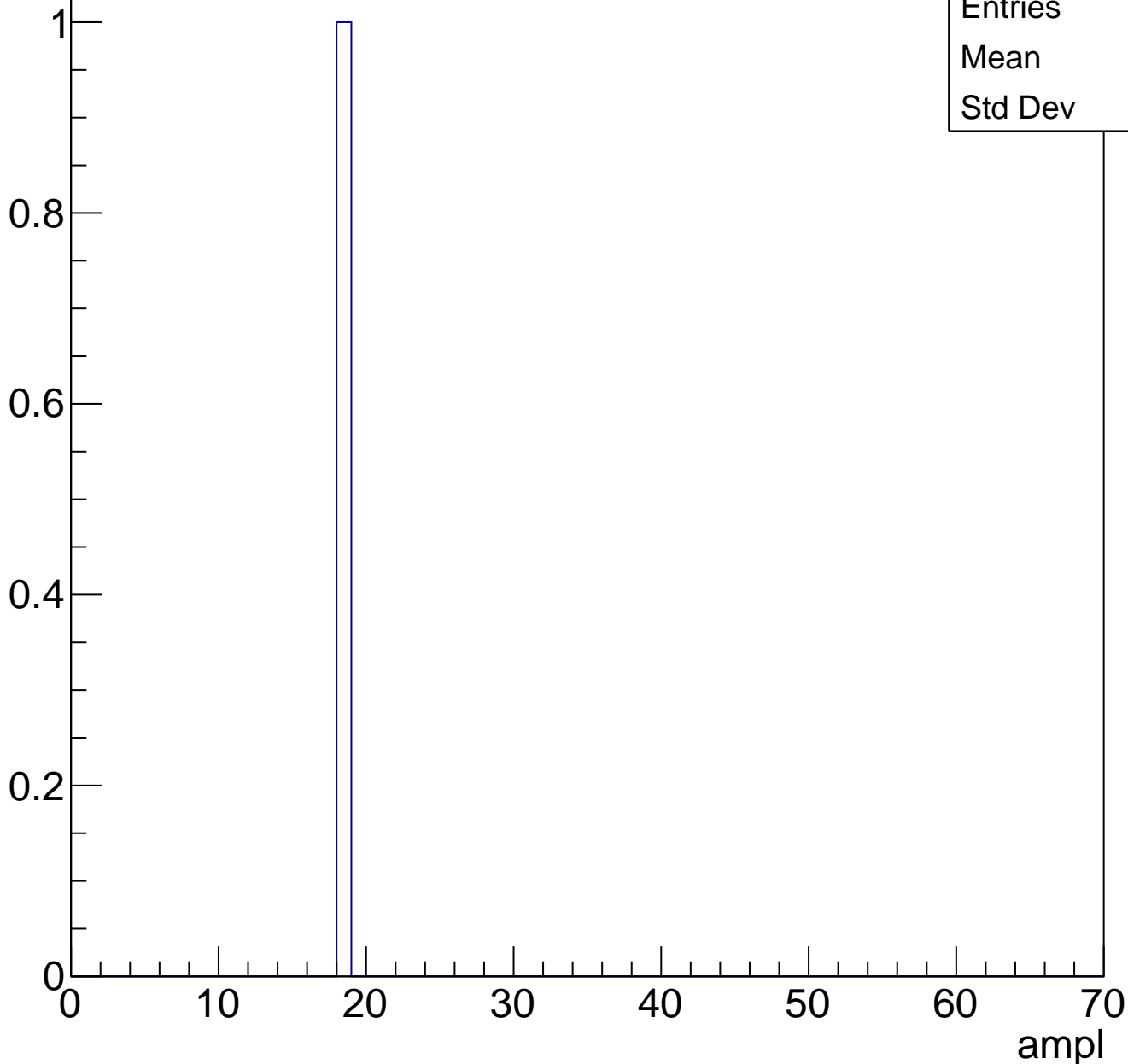
Entries	13
Mean	57.15
Std Dev	16.54



B1L101S, U1-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch32, adc0

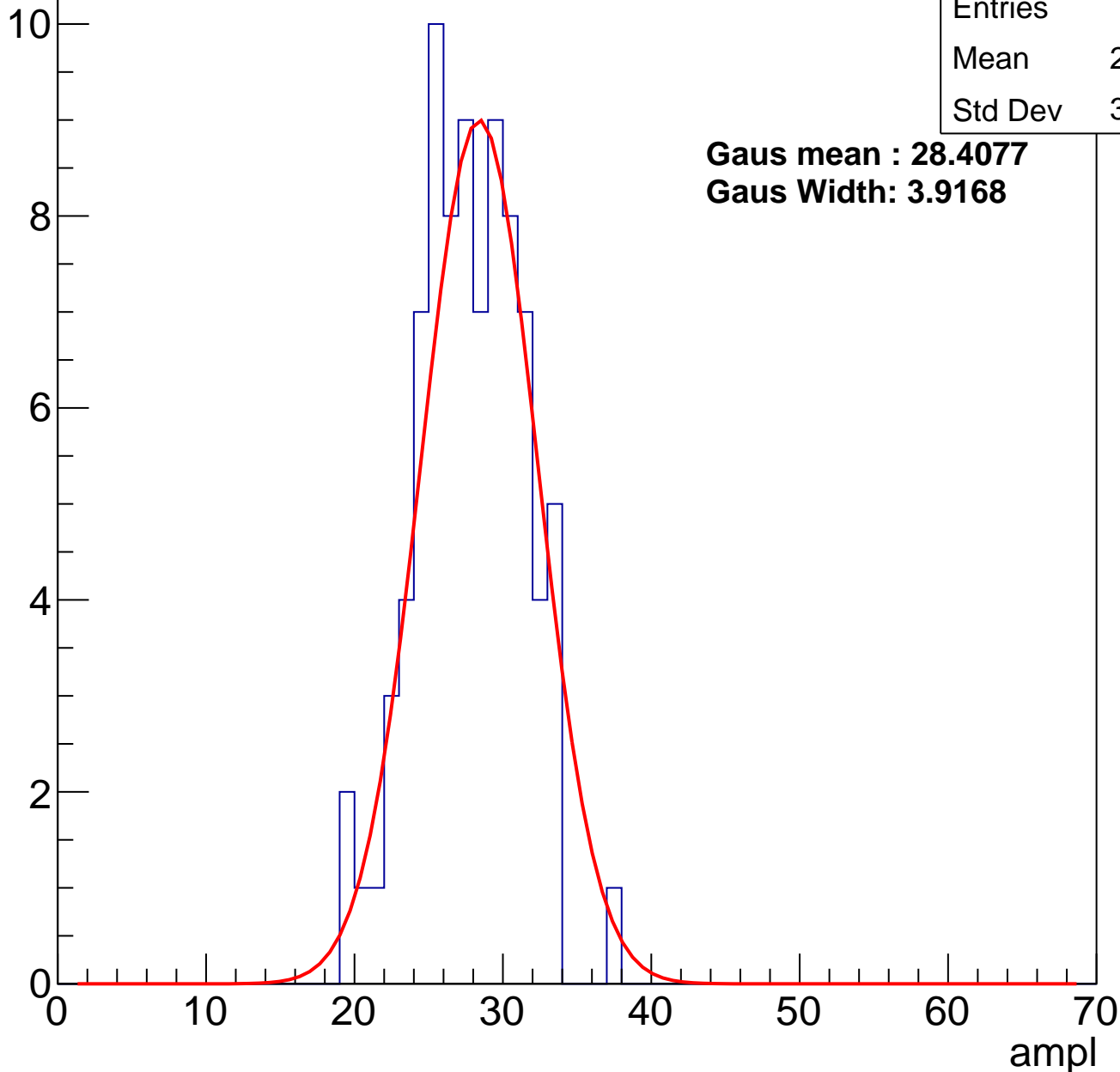
calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	27.33
Std Dev	3.512

Gaus mean : 28.4077

Gaus Width: 3.9168

Entry



B1L101S, U1-ch32, adc1

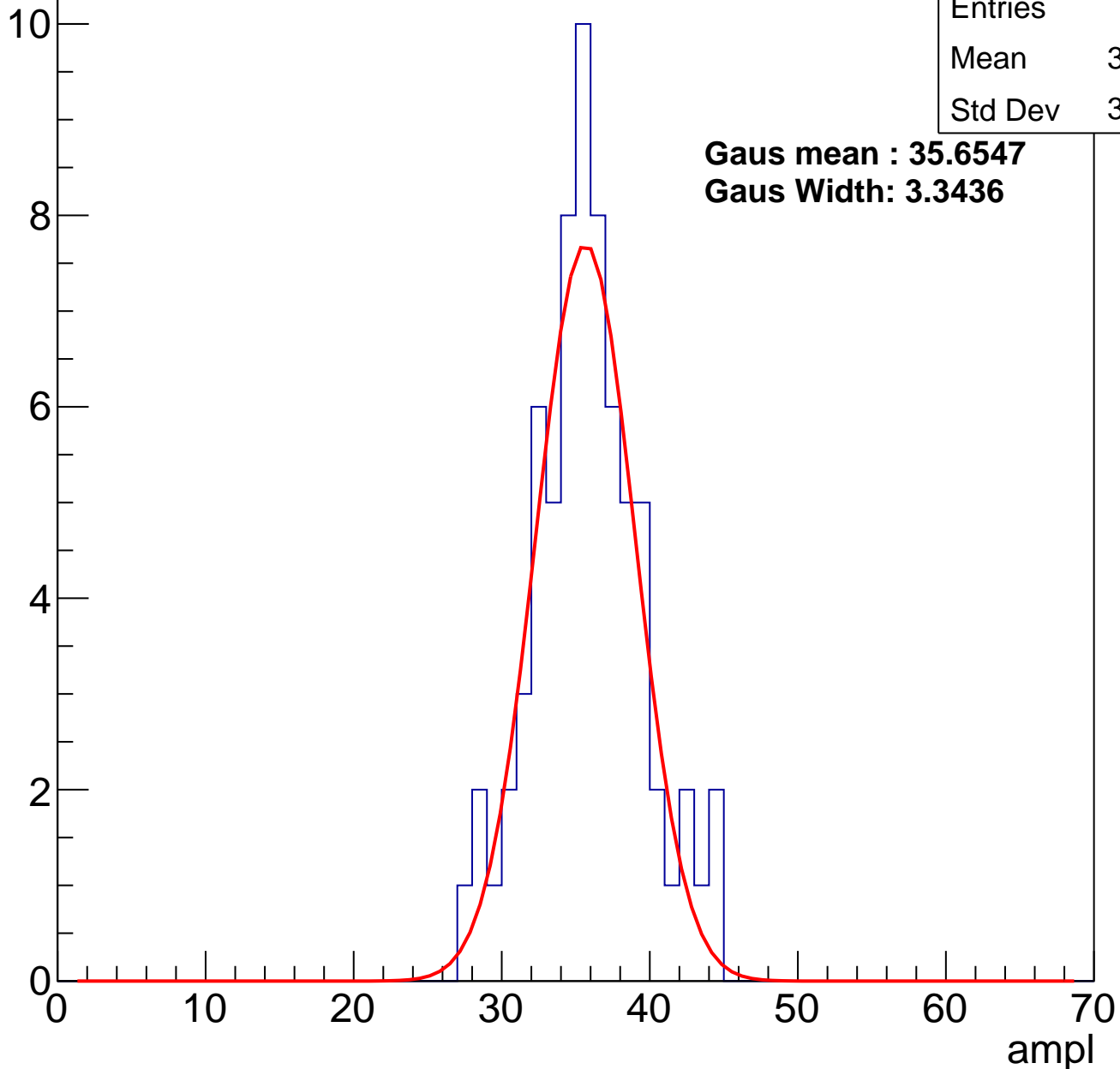
calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	35.36
Std Dev	3.657

Gaus mean : 35.6547

Gaus Width: 3.3436

Entry



B1L101S, U1-ch32, adc2

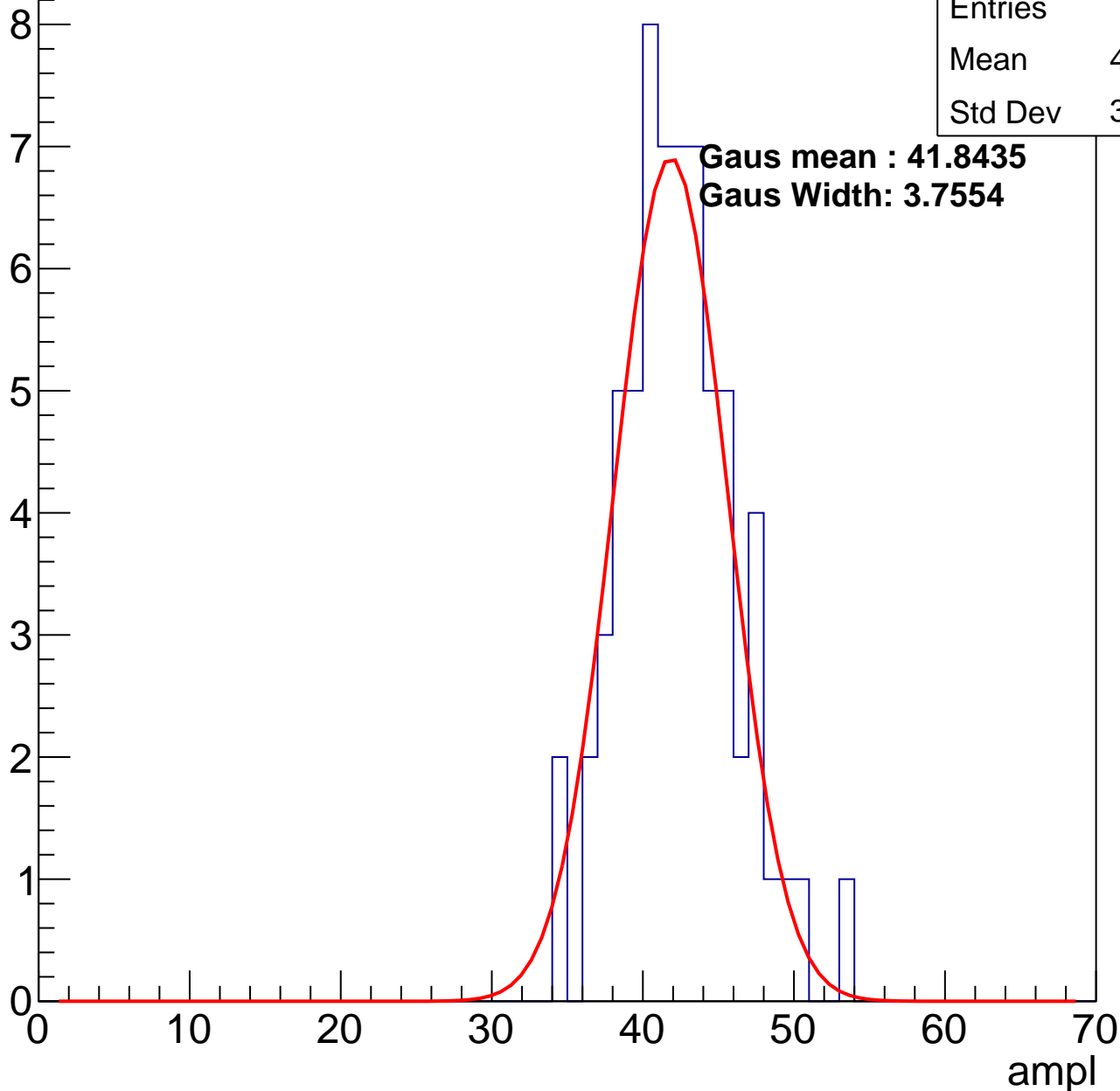
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.86
Std Dev	3.717

Gaus mean : 41.8435

Gaus Width: 3.7554

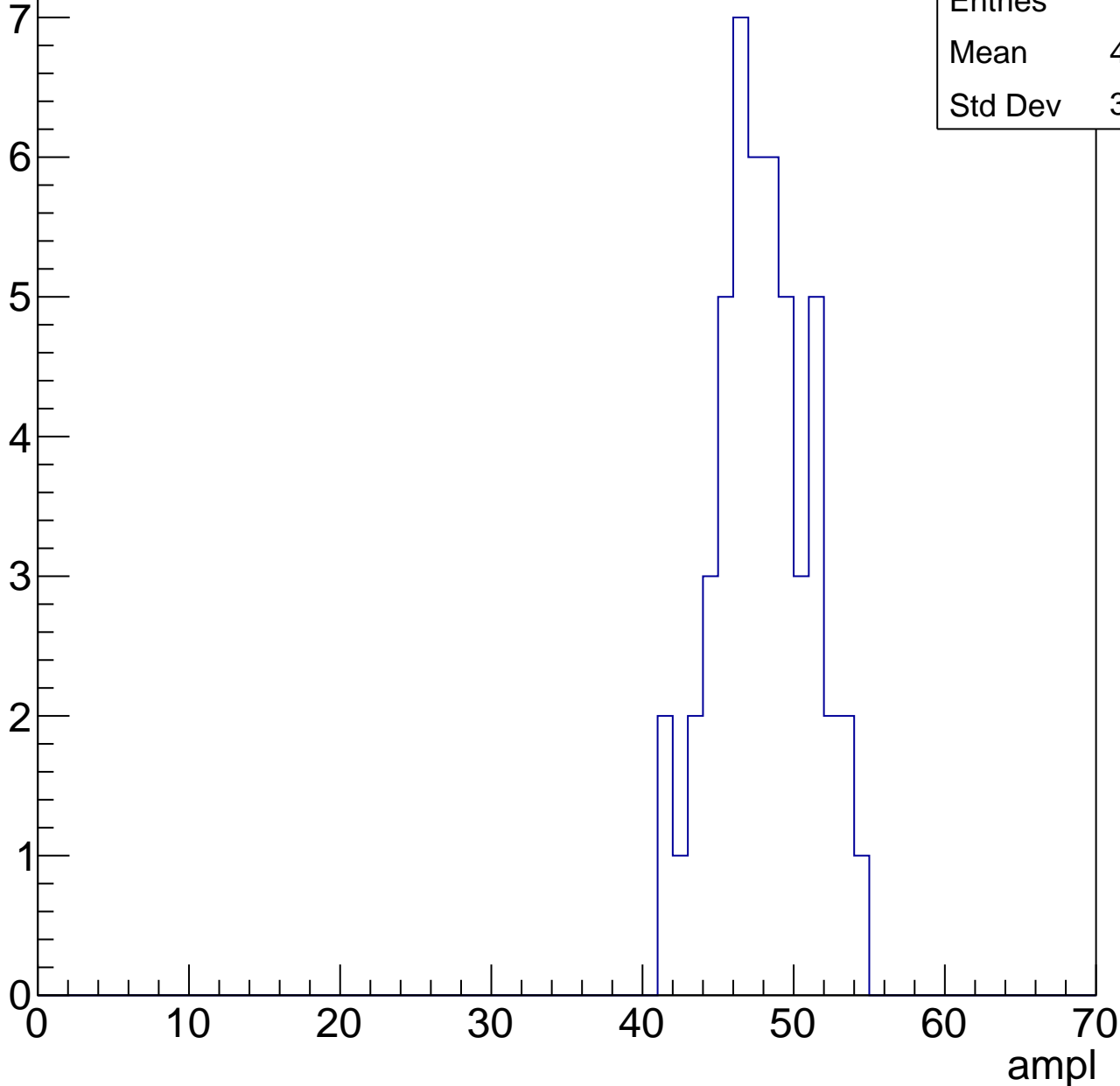


B1L101S, U1-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	47.46
Std Dev	3.087

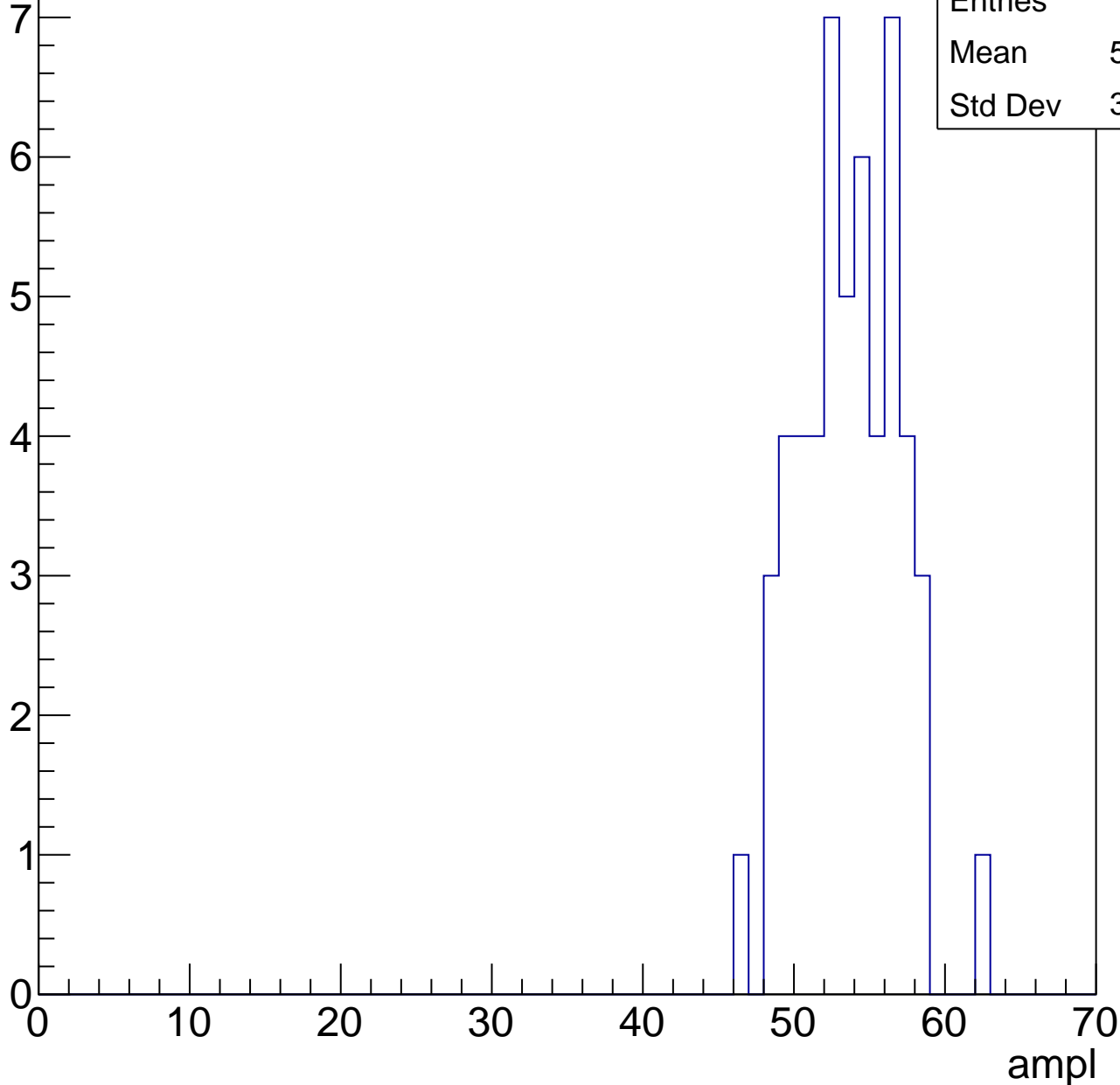


B1L101S, U1-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	53.19
Std Dev	3.222

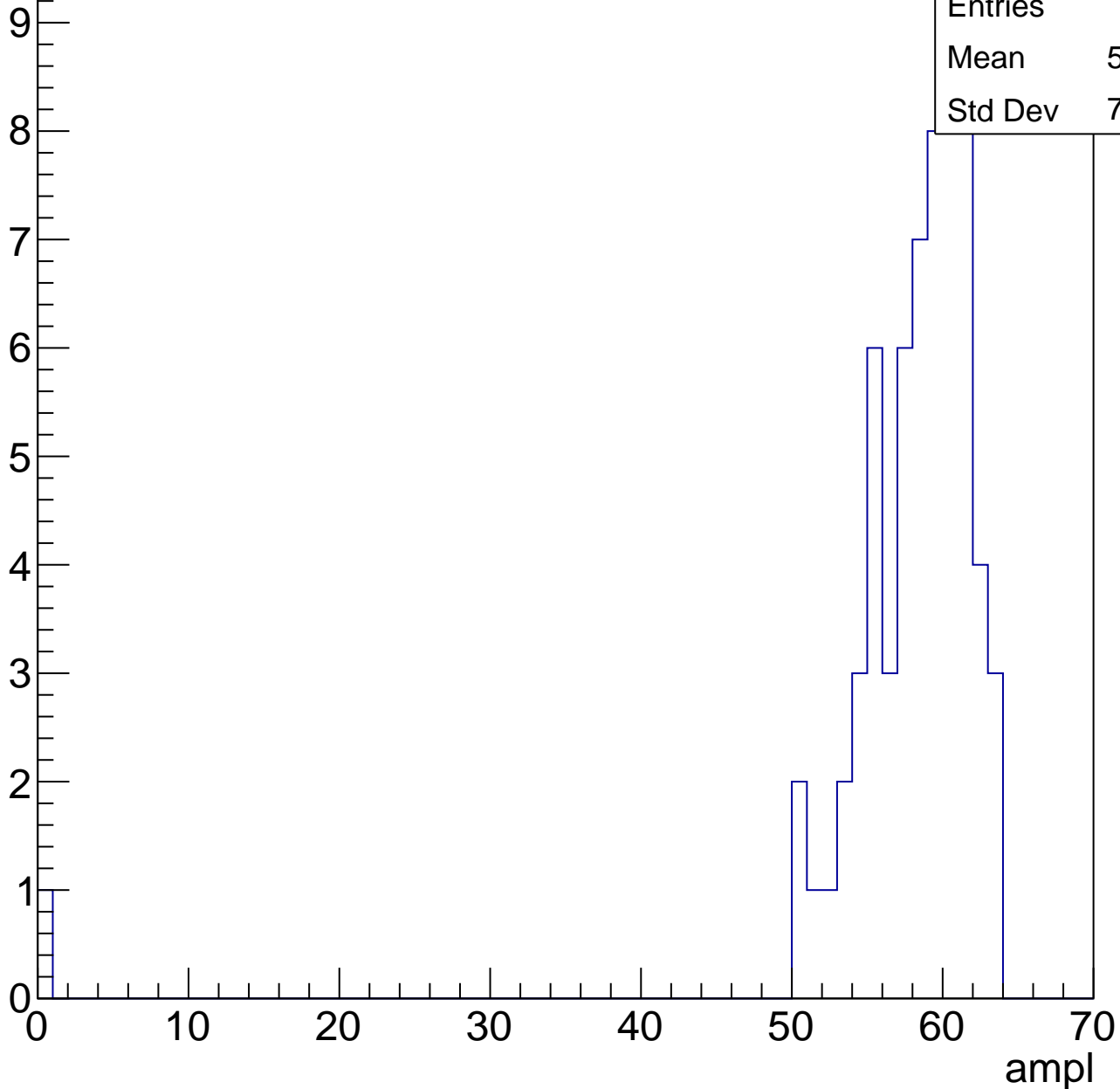


B1L101S, U1-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	57.09
Std Dev	7.856

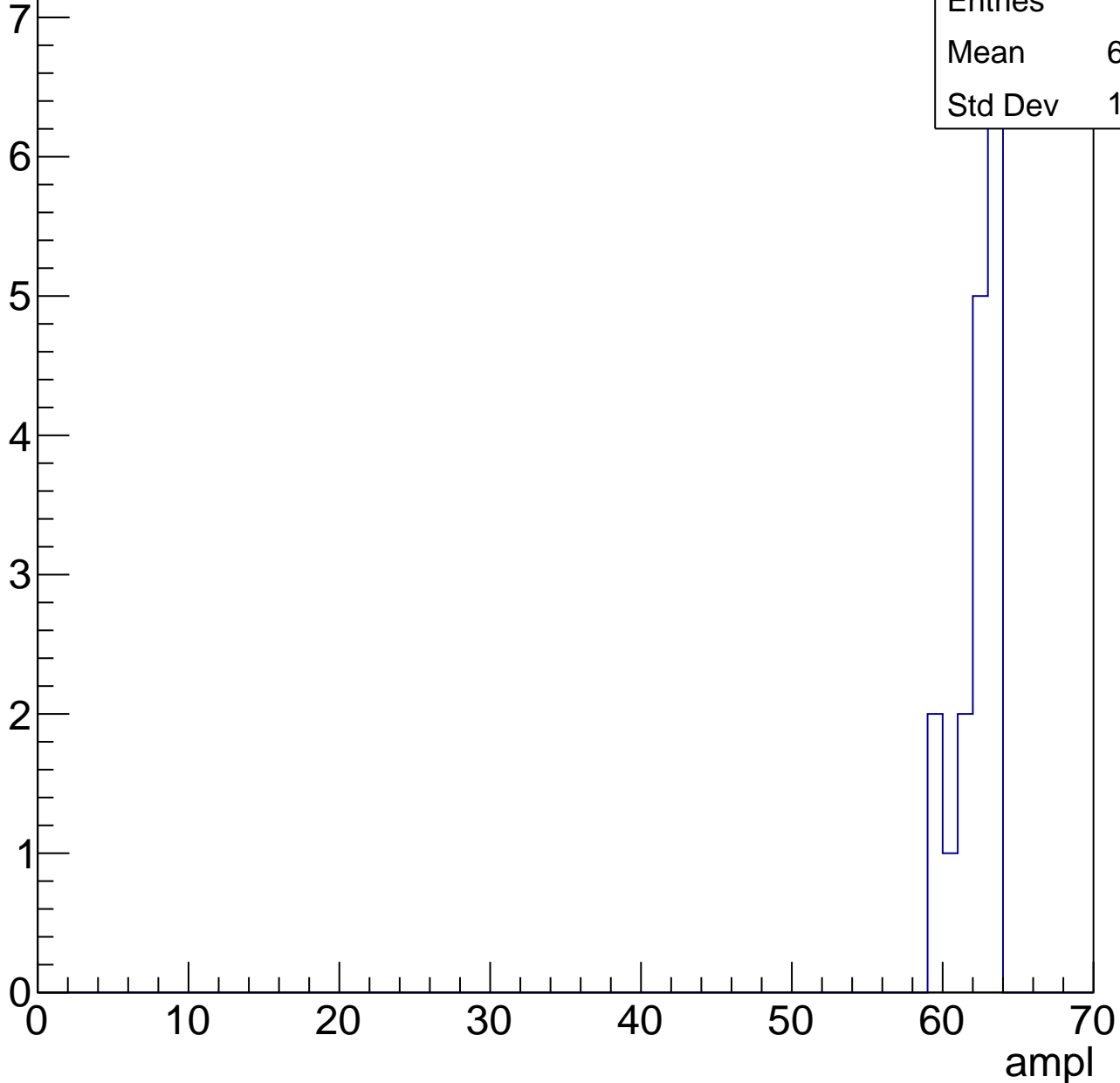


B1L101S, U1-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61.82
Std Dev	1.339



B1L101S, U1-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U1-ch33, adc0

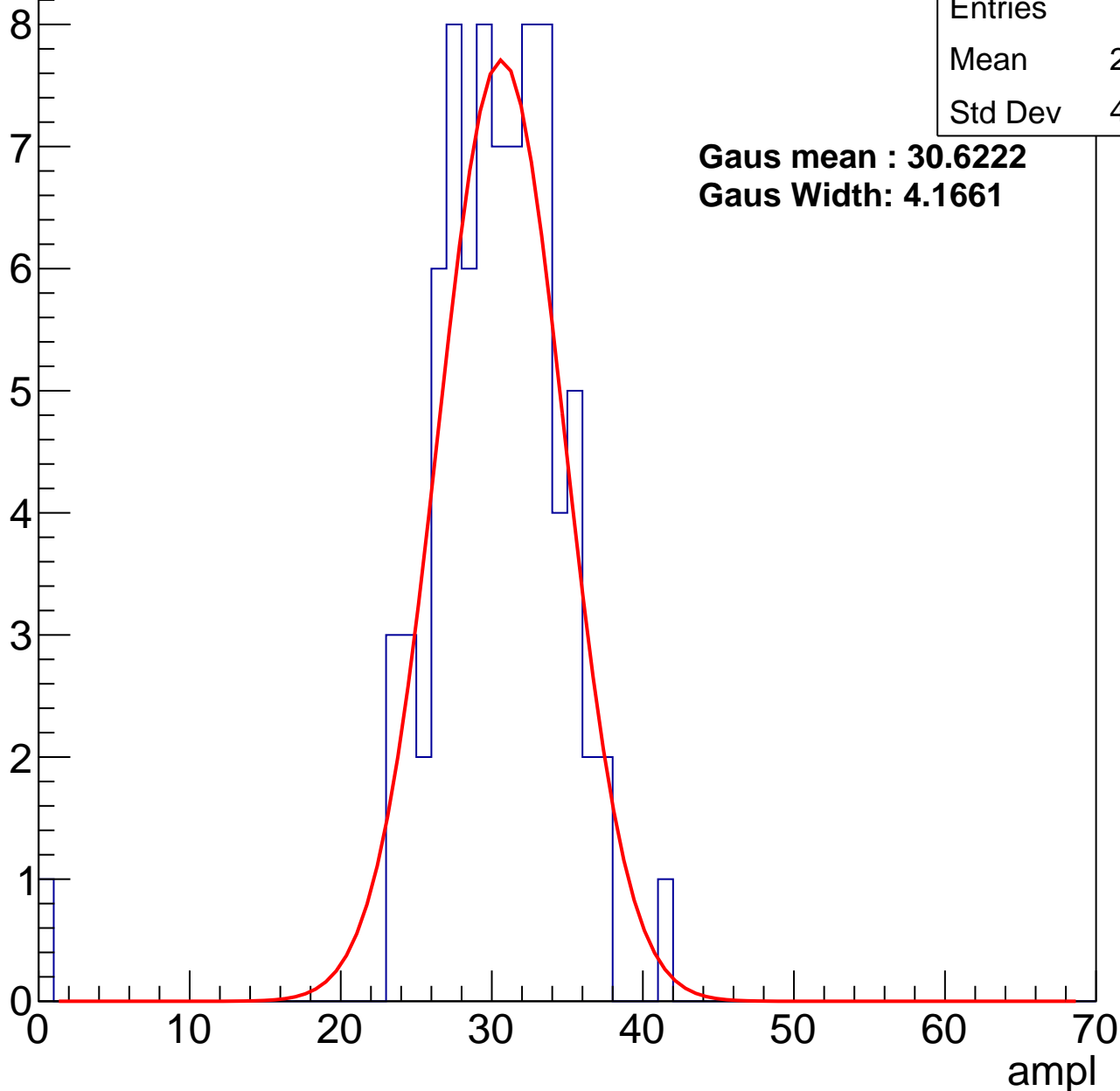
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	29.73
Std Dev	4.957

Gaus mean : 30.6222

Gaus Width: 4.1661



B1L101S, U1-ch33, adc1

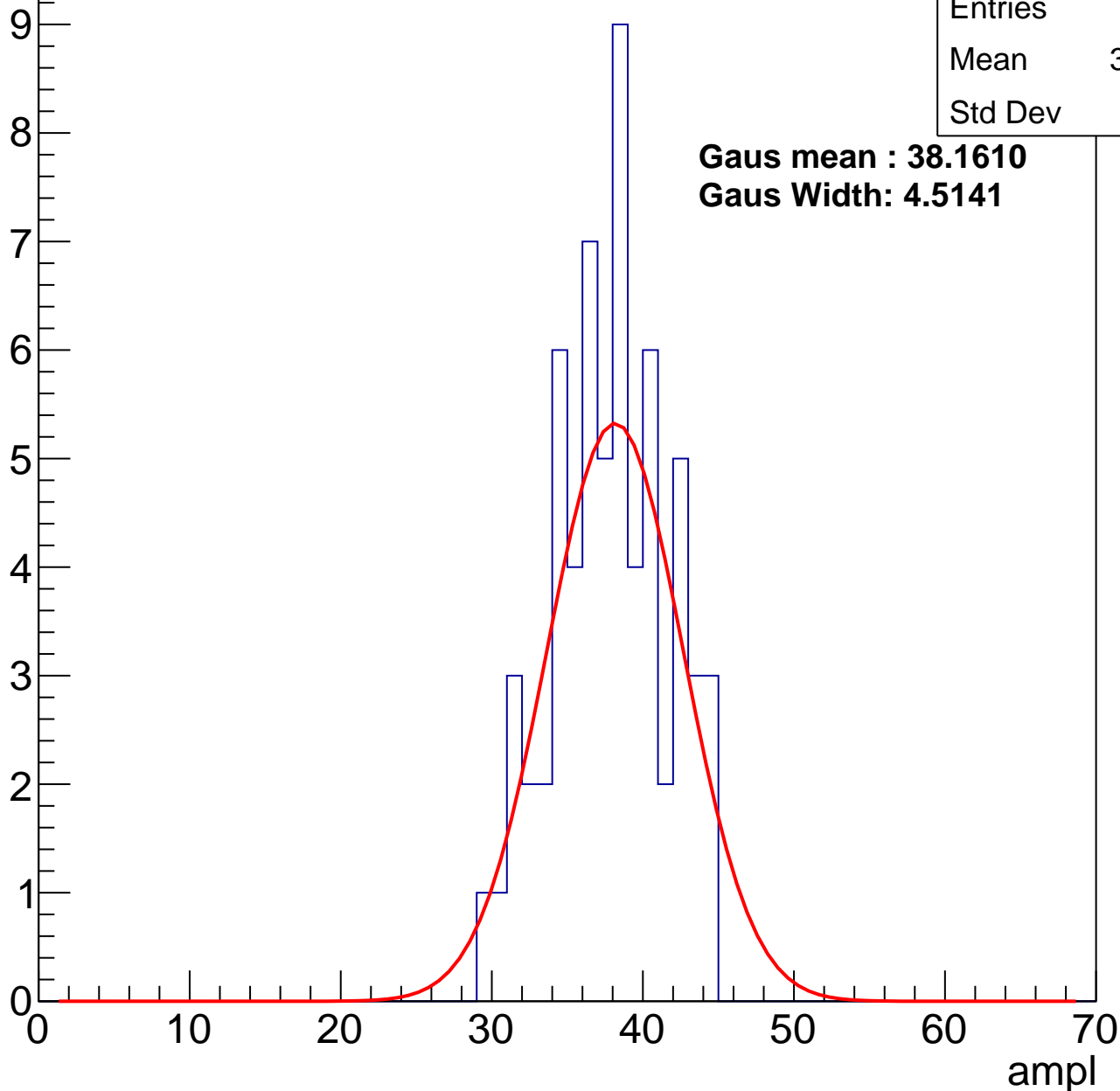
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.37
Std Dev	3.7

Gaus mean : 38.1610

Gaus Width: 4.5141



B1L101S, U1-ch33, adc2

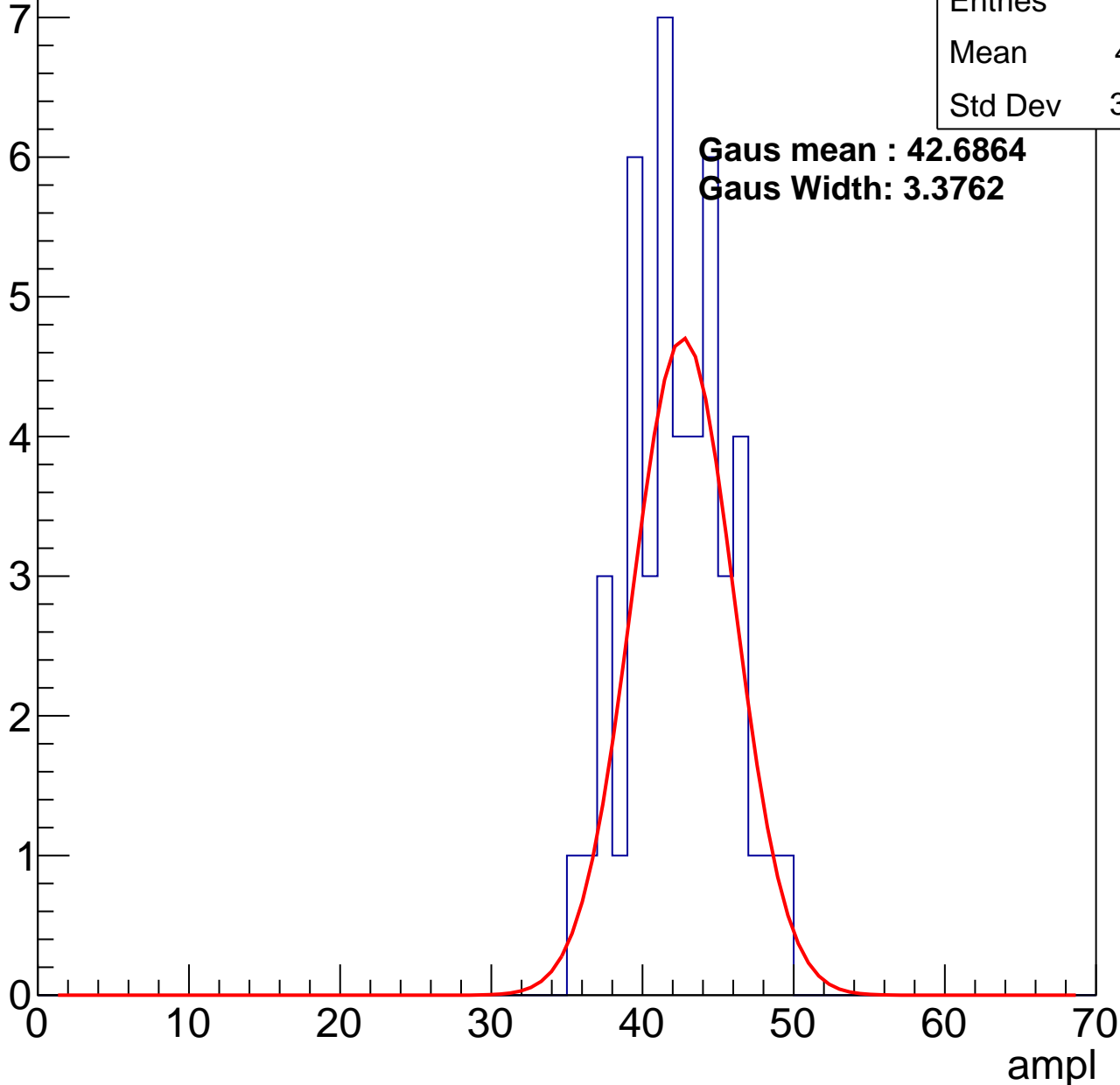
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	41.91
Std Dev	3.222

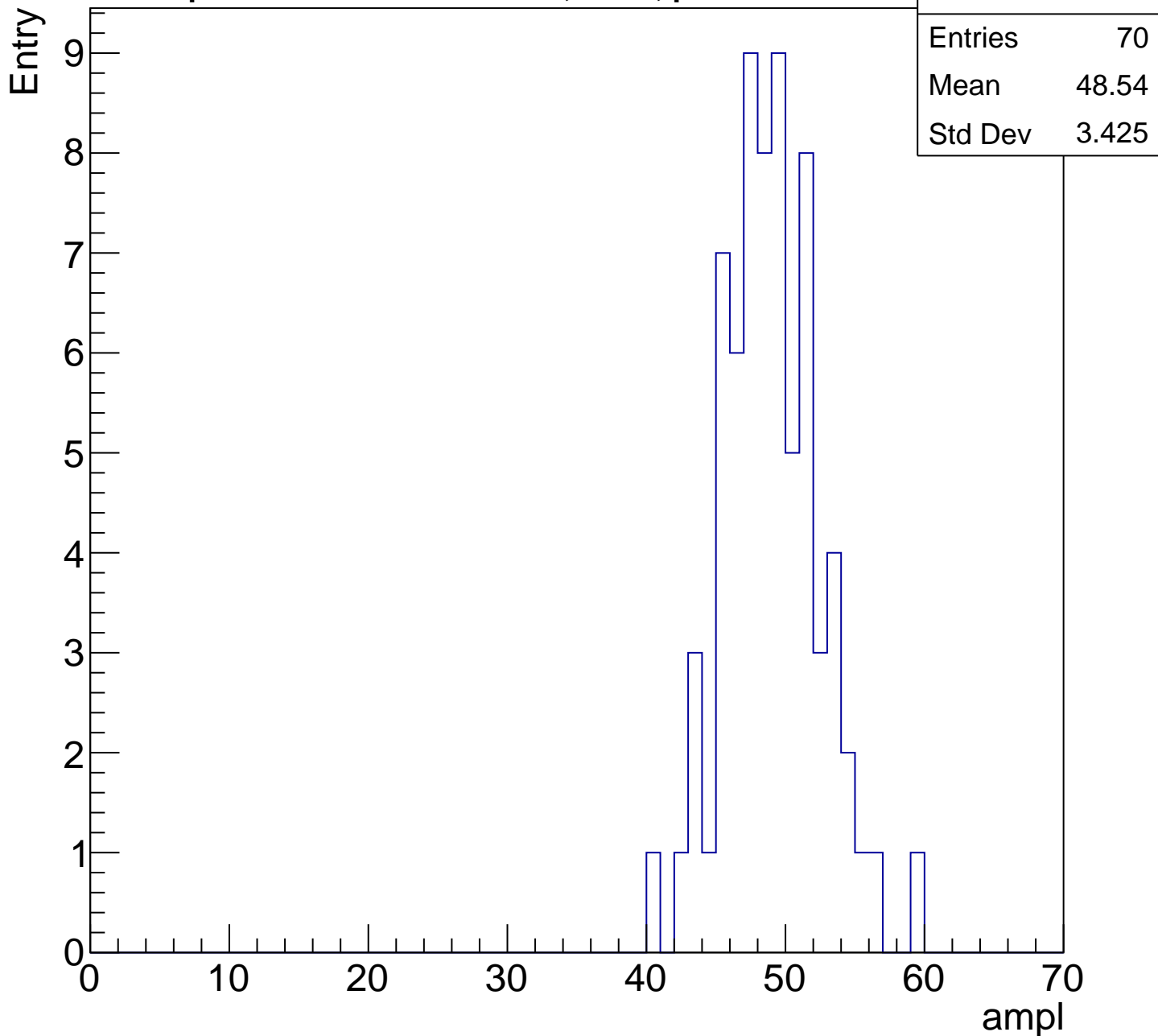
Gaus mean : 42.6864

Gaus Width: 3.3762



B1L101S, U1-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

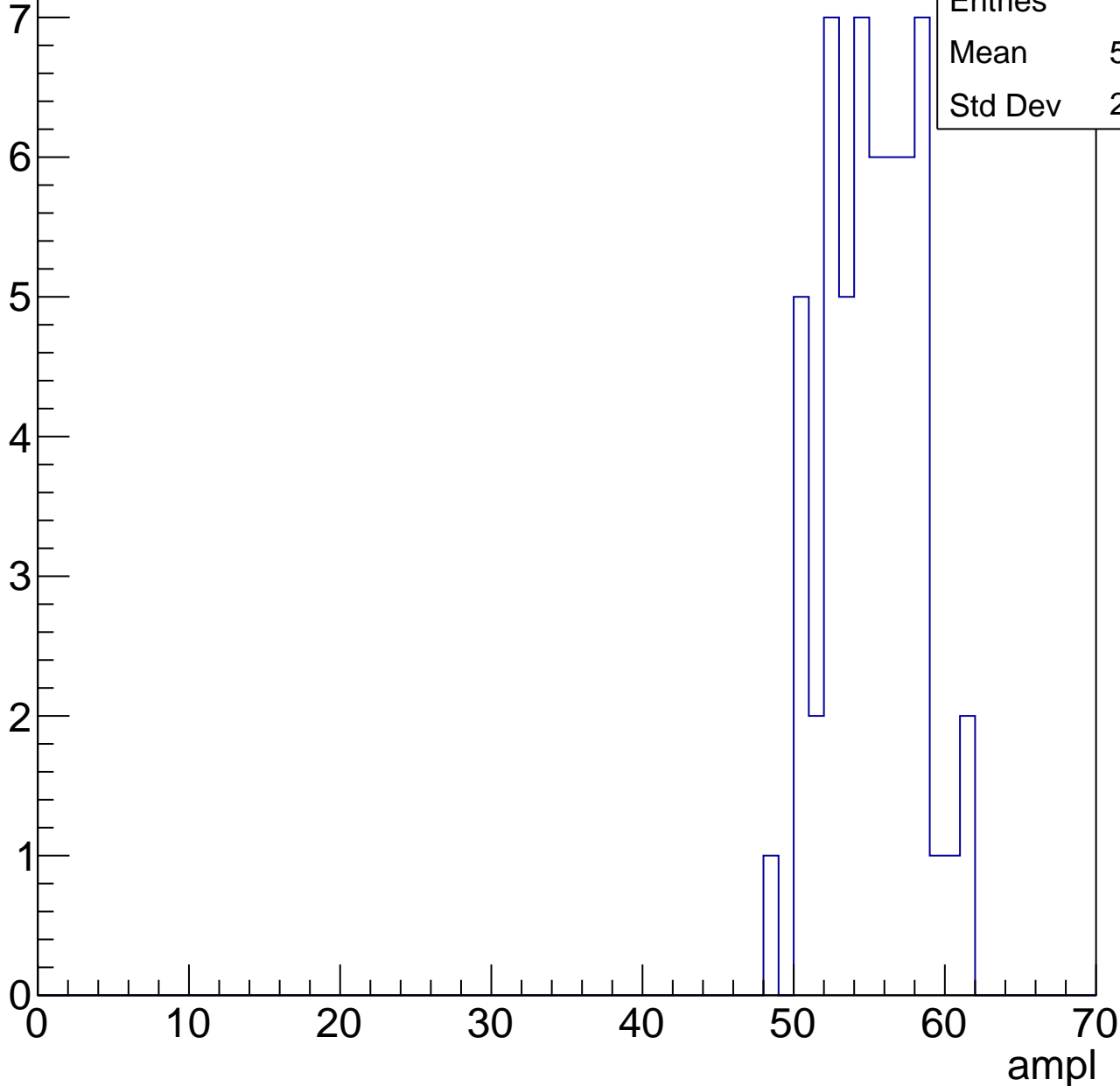


B1L101S, U1-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

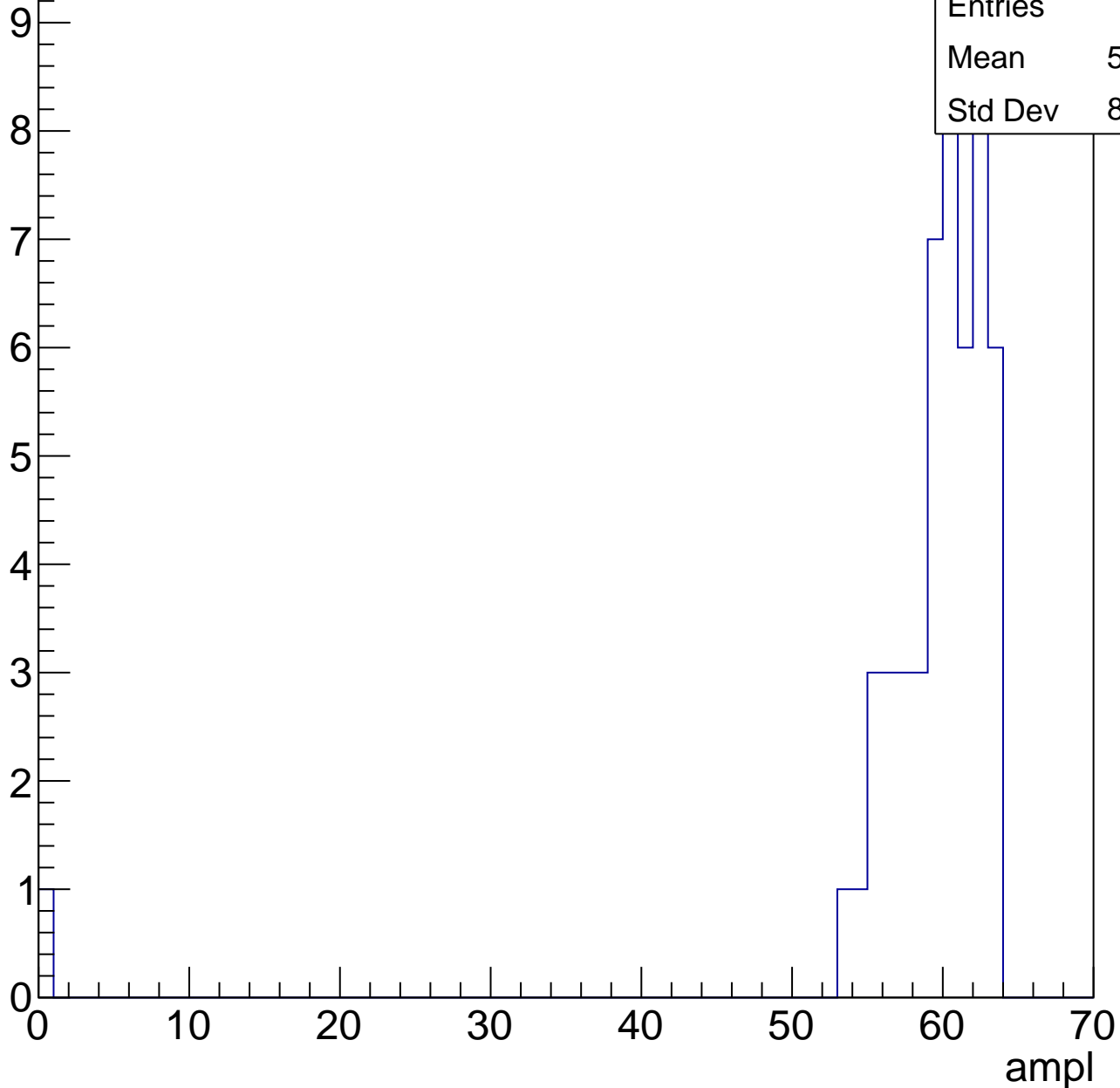
Entries	56
Mean	54.68
Std Dev	2.977



B1L101S, U1-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

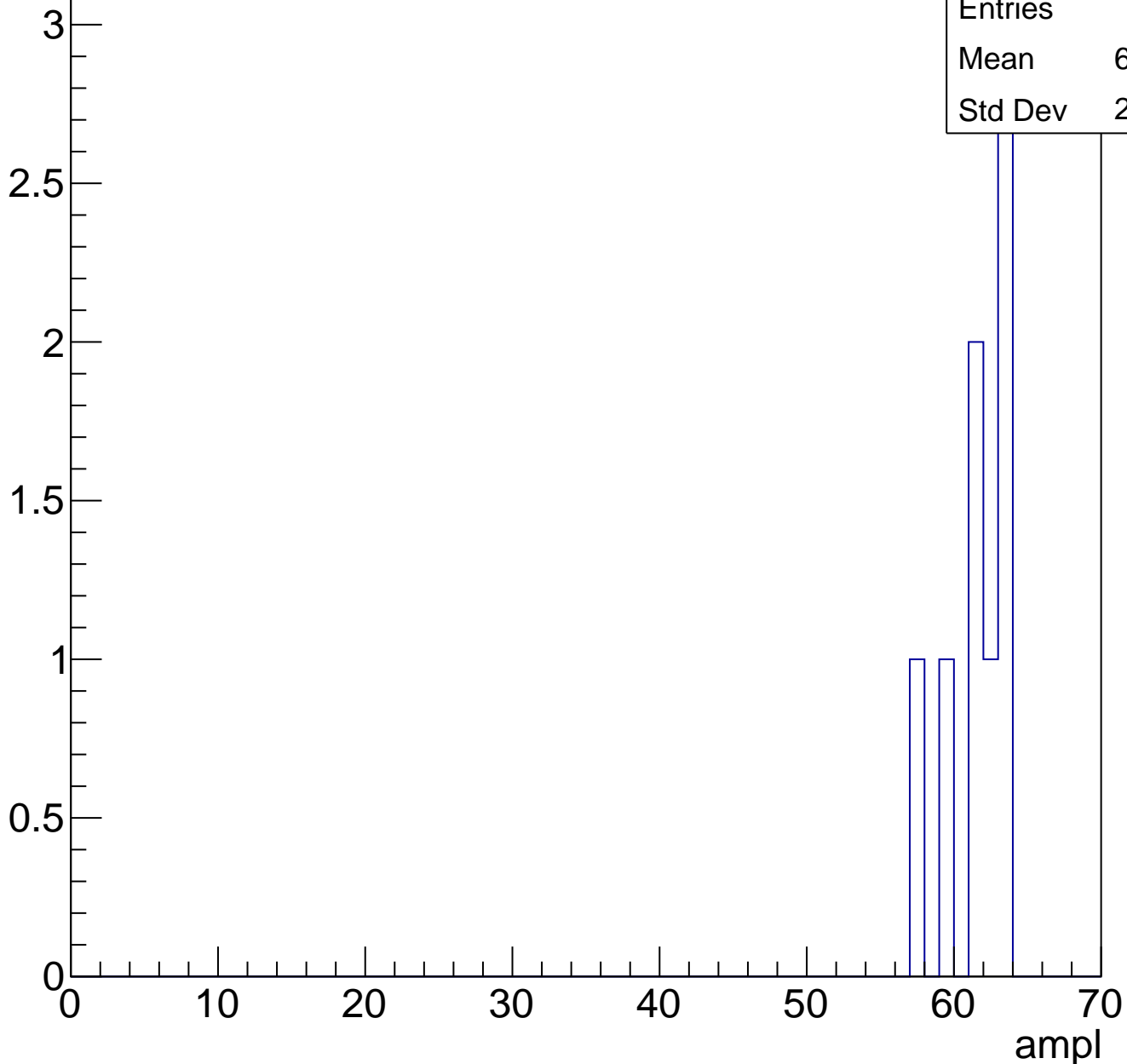
Entry



B1L101S, U1-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch34, adc0

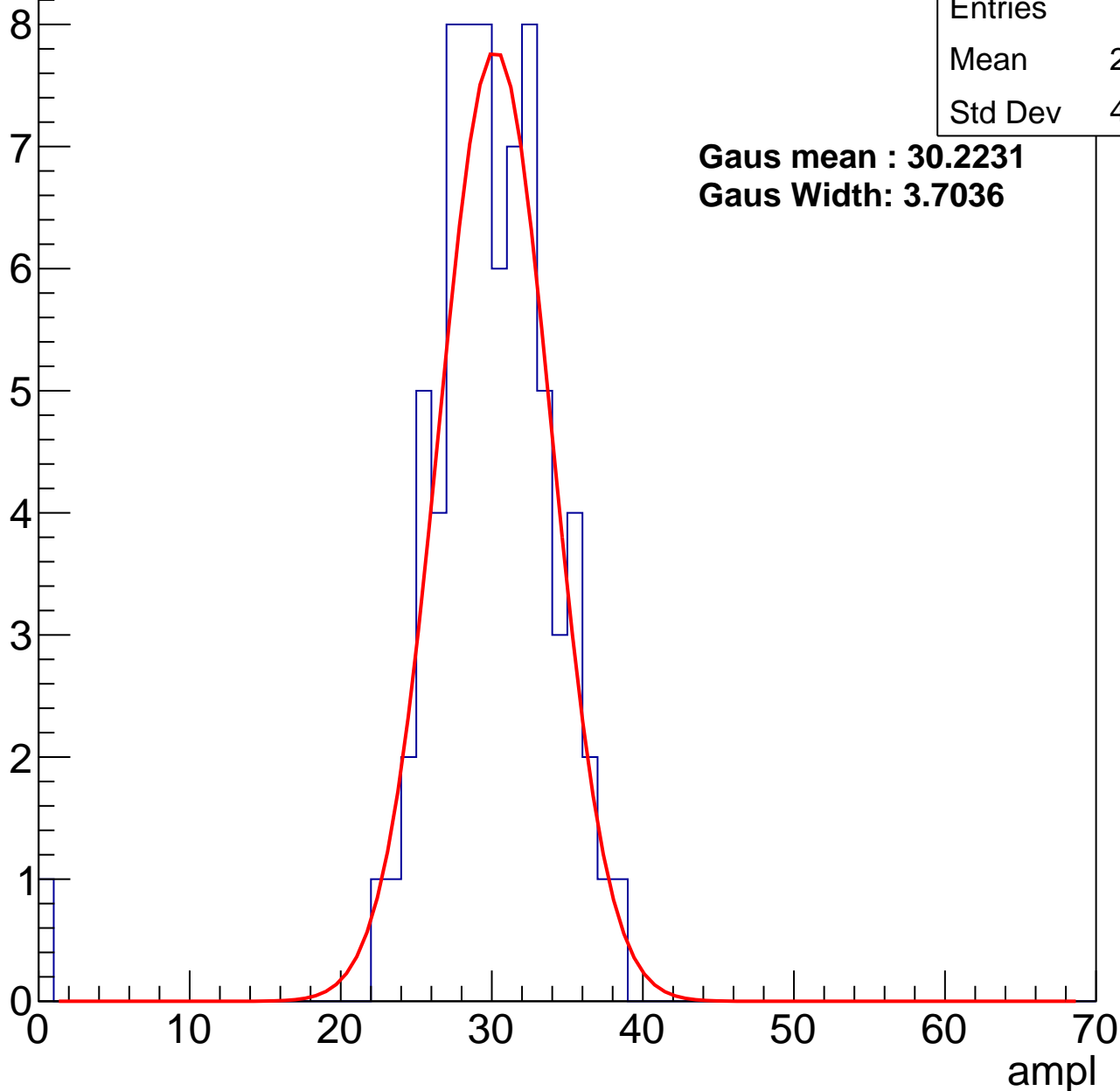
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.35
Std Dev	4.859

Gaus mean : 30.2231

Gaus Width: 3.7036



B1L101S, U1-ch34, adc1

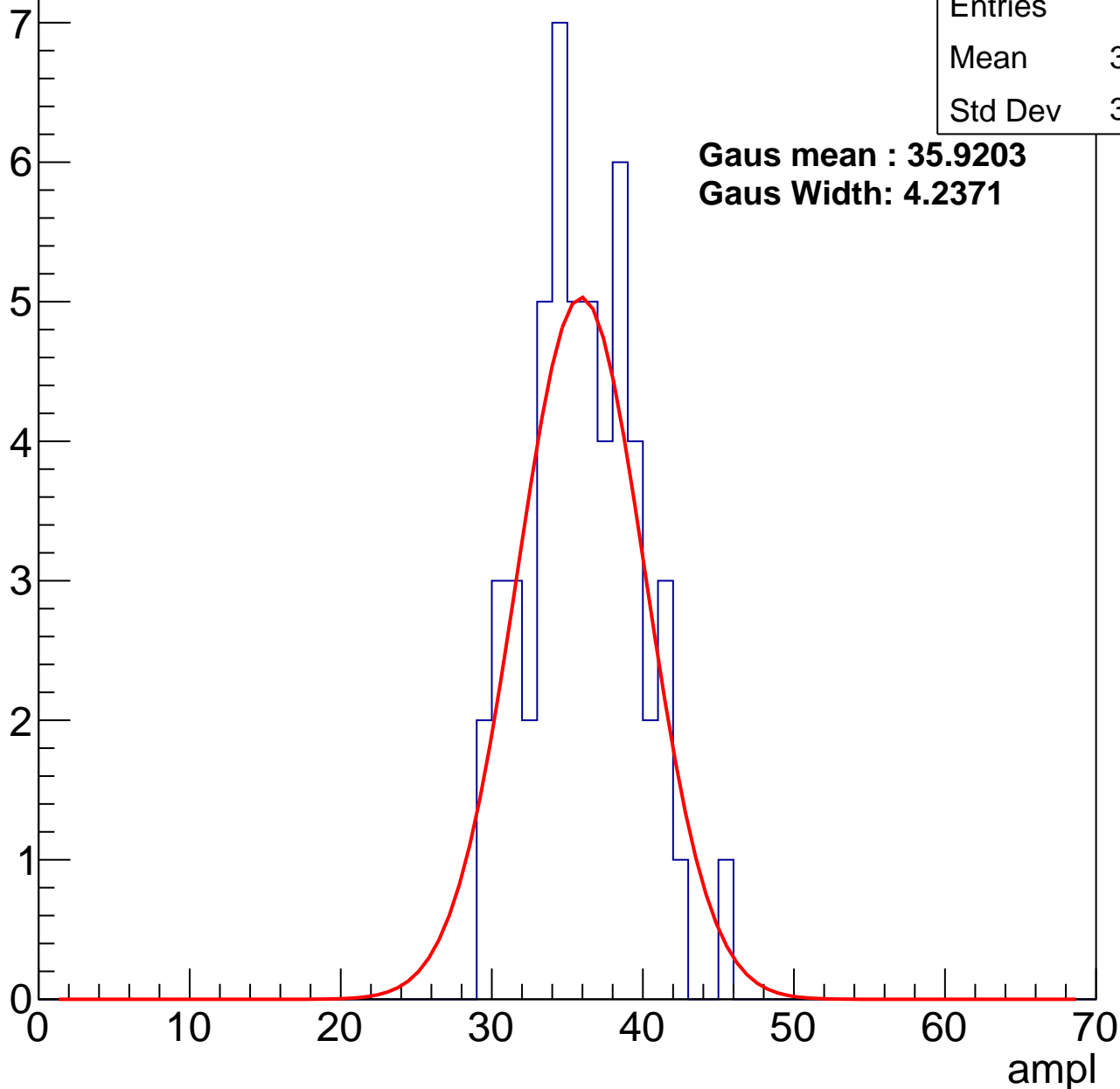
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	35.57
Std Dev	3.553

Gaus mean : 35.9203

Gaus Width: 4.2371



B1L101S, U1-ch34, adc2

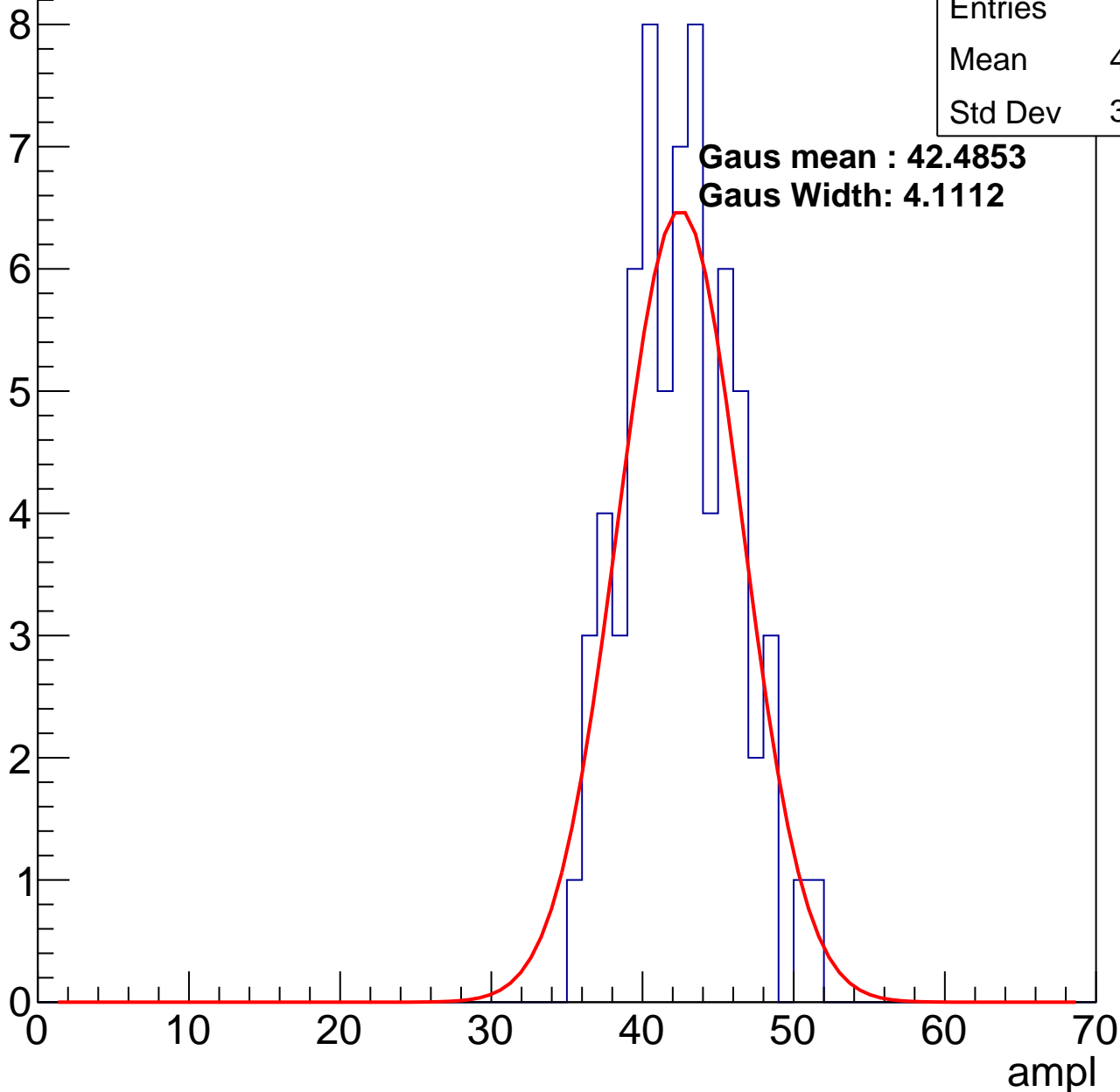
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.04
Std Dev	3.576

Gaus mean : 42.4853

Gaus Width: 4.1112

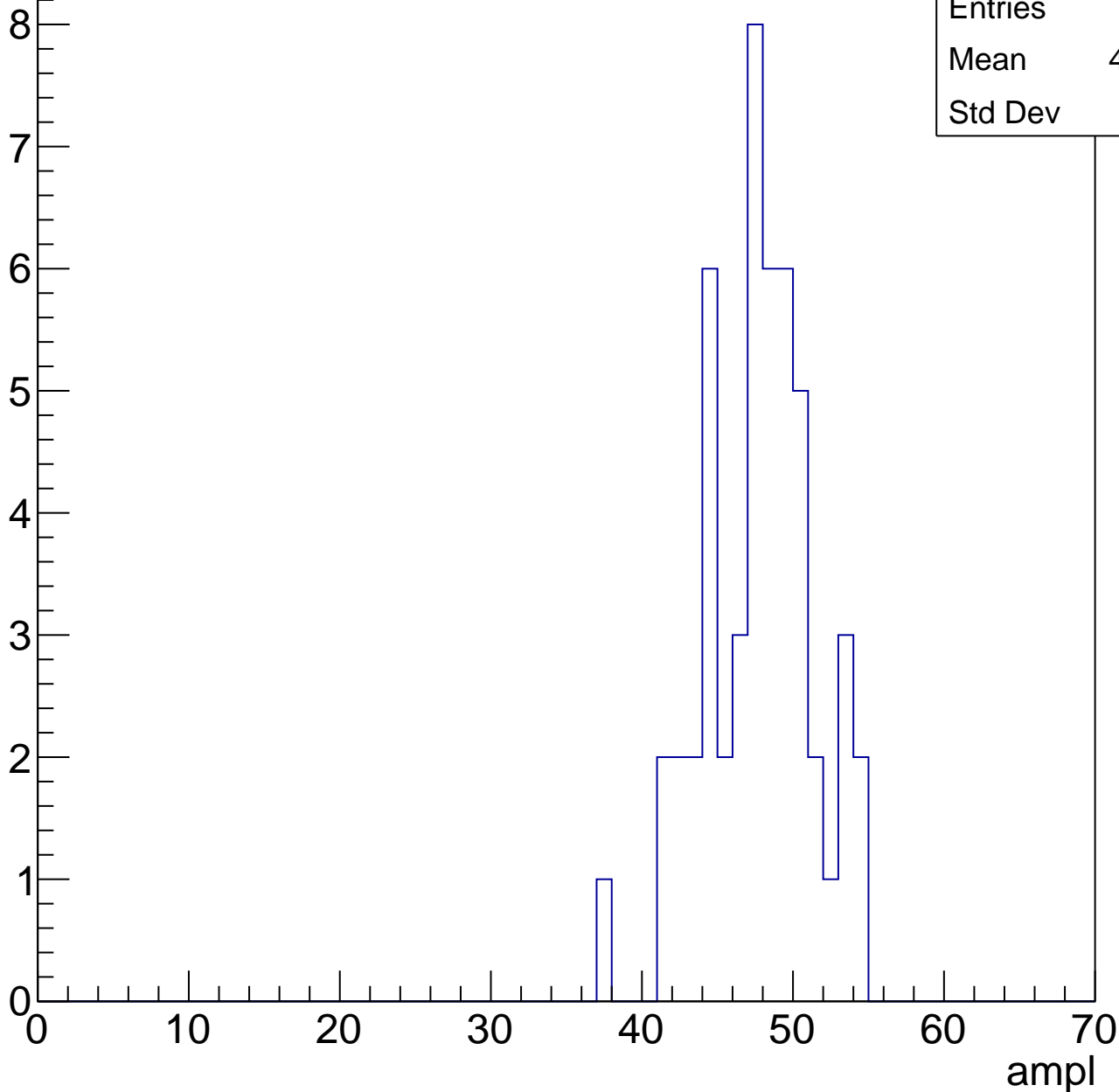


B1L101S, U1-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	47.25
Std Dev	3.58

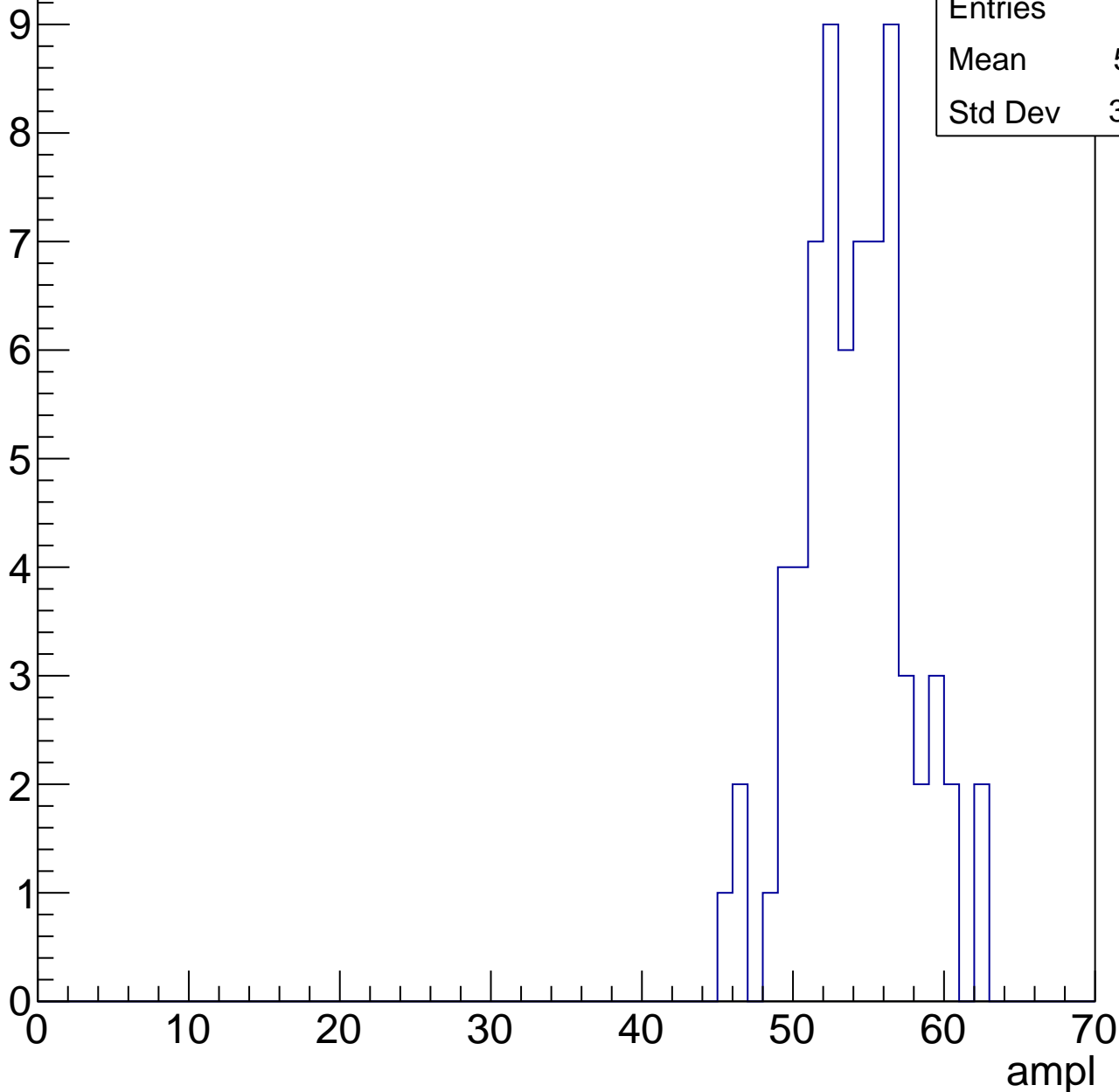


B1L101S, U1-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	53.61
Std Dev	3.572

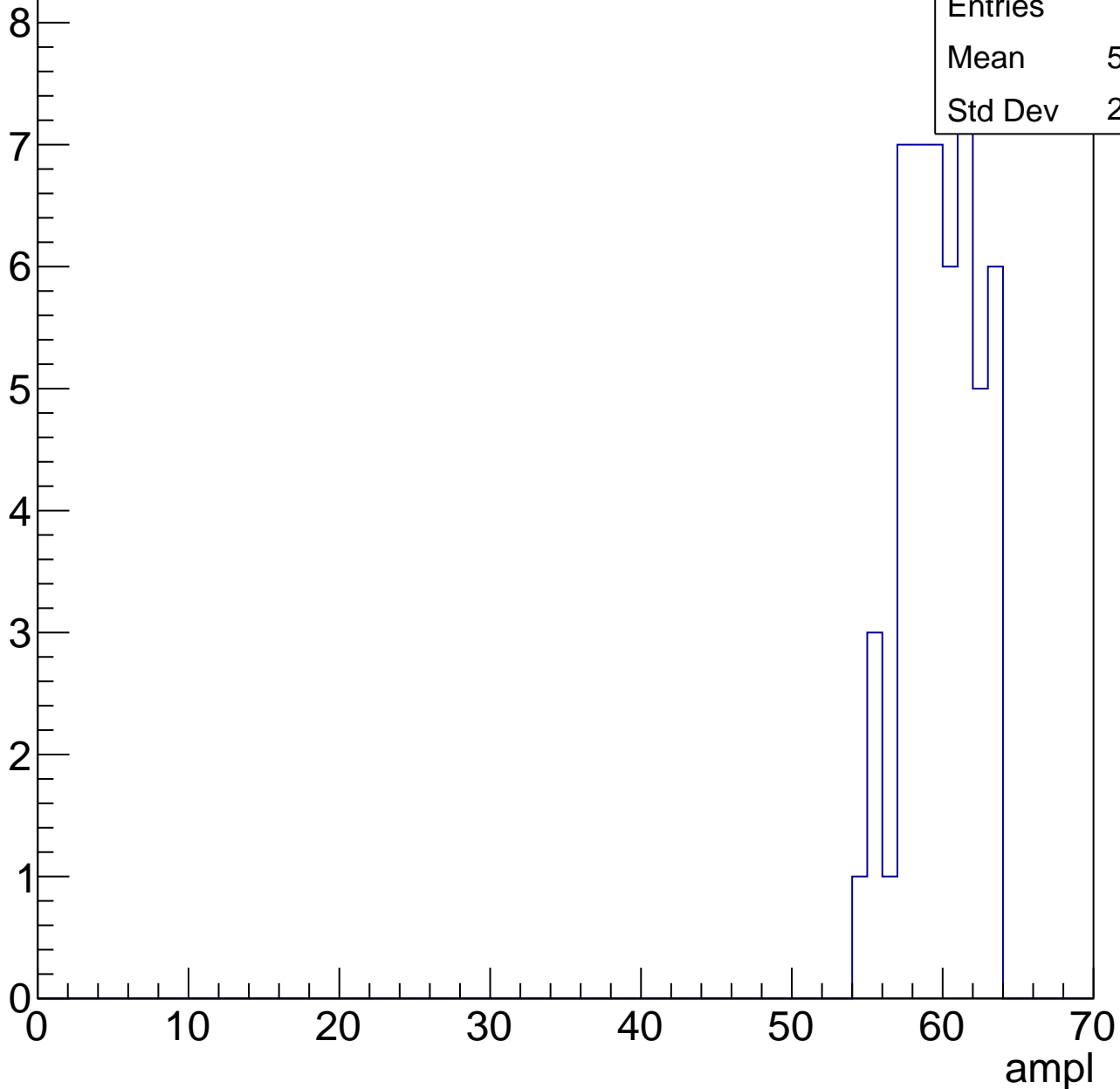


B1L101S, U1-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

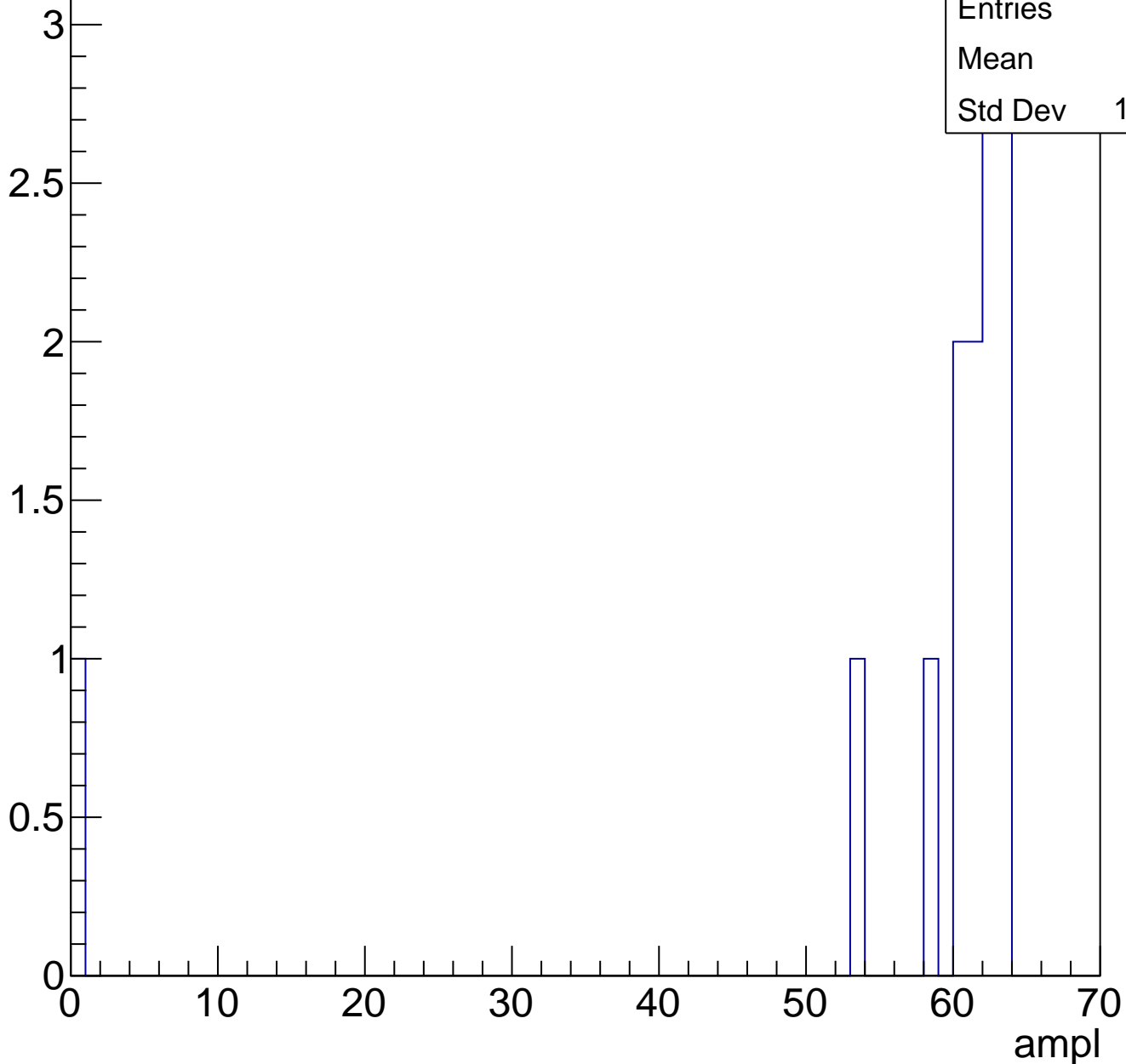
Entries	51
Mean	59.39
Std Dev	2.377



B1L101S, U1-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U1-ch35, adc0

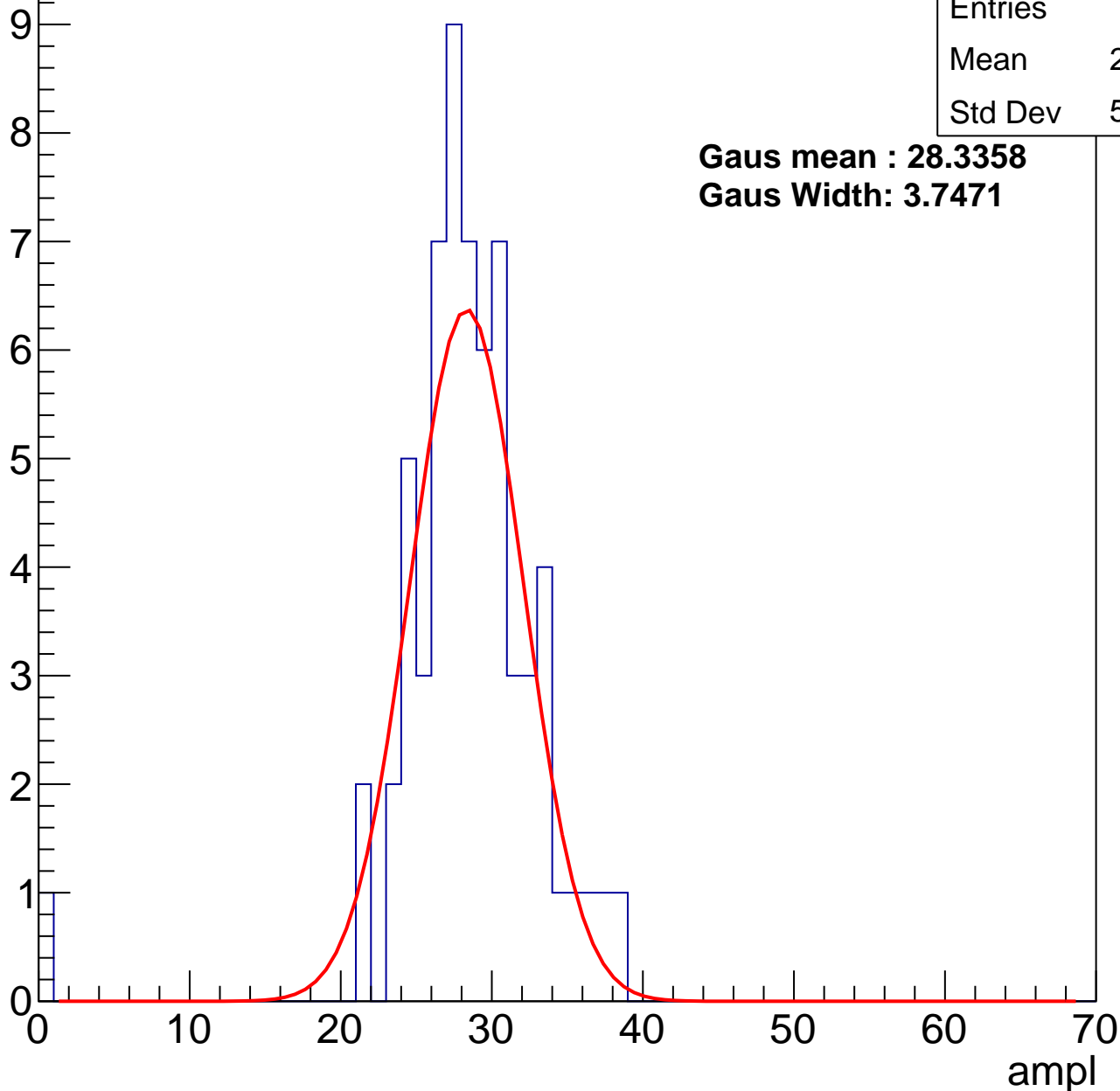
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.95
Std Dev	5.023

Gaus mean : 28.3358

Gaus Width: 3.7471



B1L101S, U1-ch35, adc1

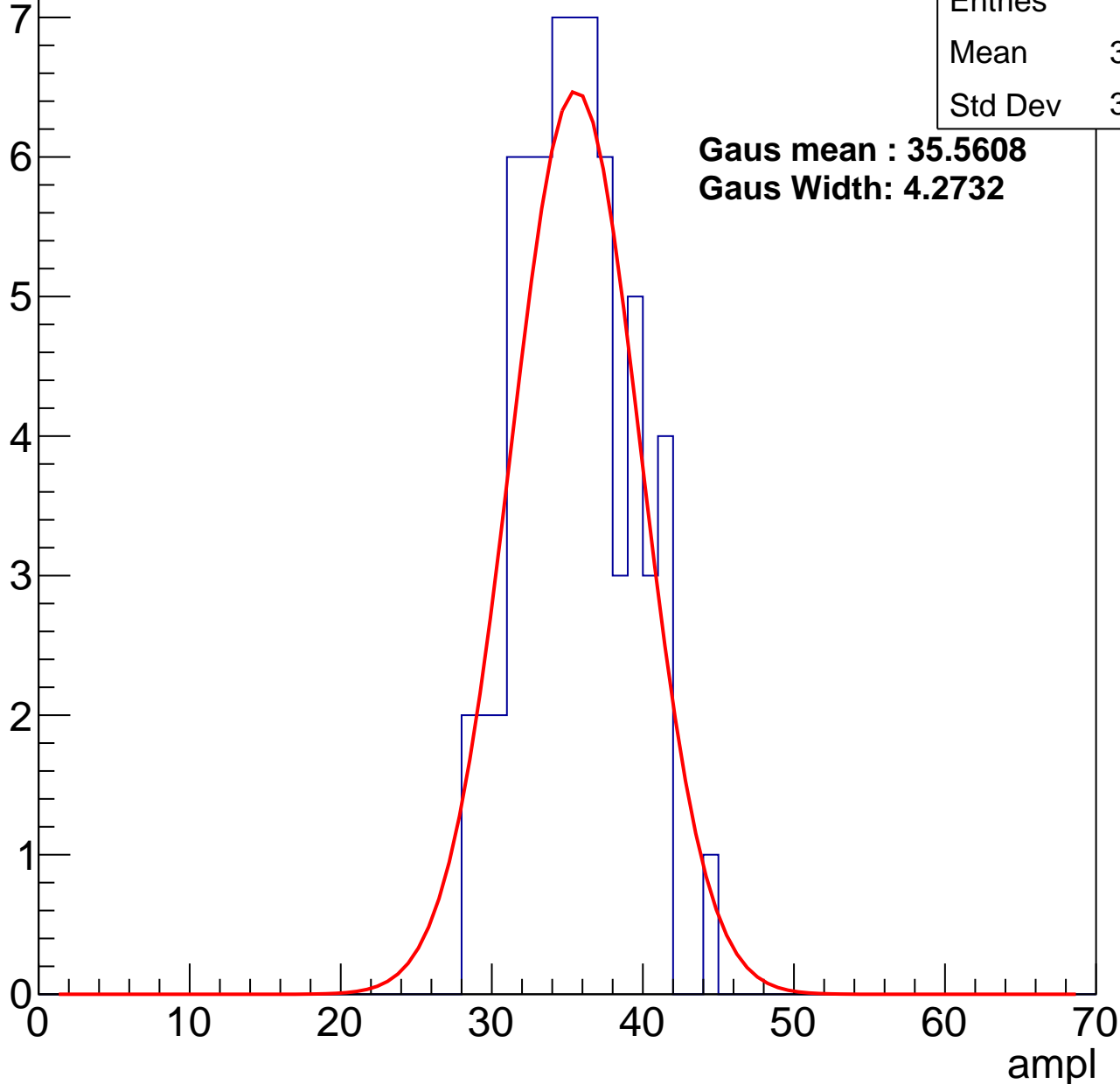
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.99
Std Dev	3.539

Gaus mean : 35.5608

Gaus Width: 4.2732



B1L101S, U1-ch35, adc2

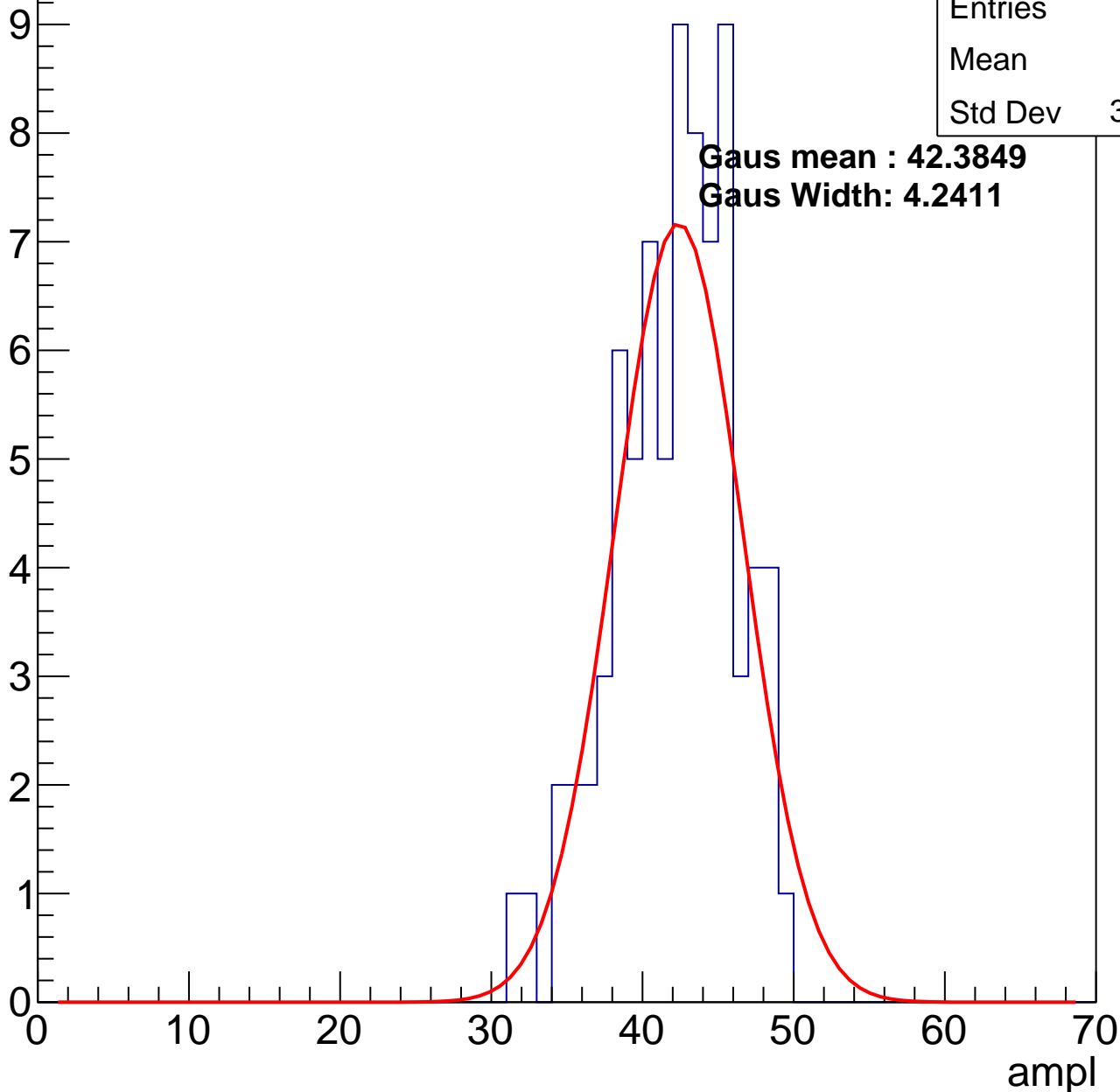
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	41.7
Std Dev	3.928

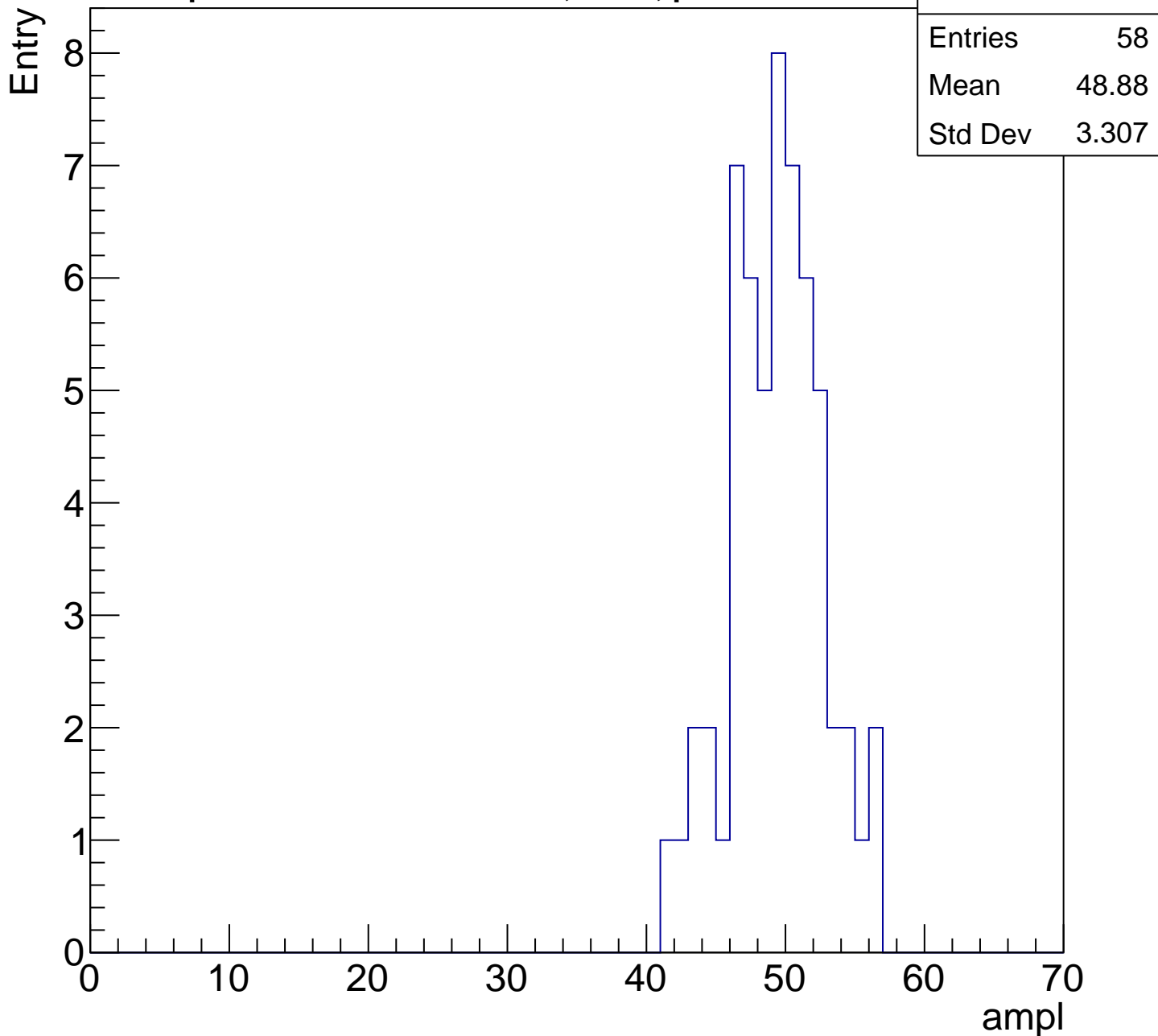
Gaus mean : 42.3849

Gaus Width: 4.2411



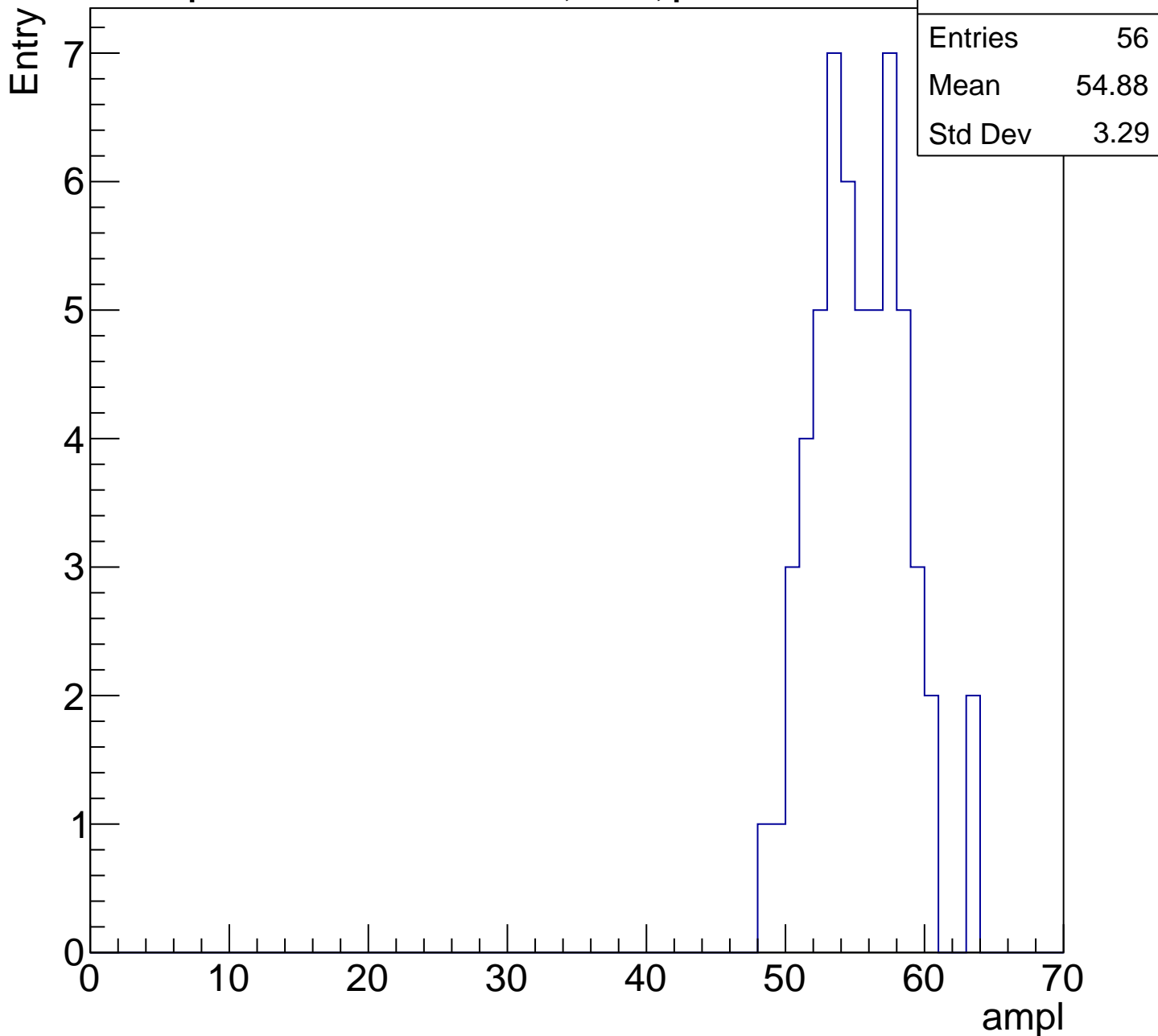
B1L101S, U1-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 59

Mean 59.61

Std Dev 2.719

8

6

4

2

0

0

10

20

30

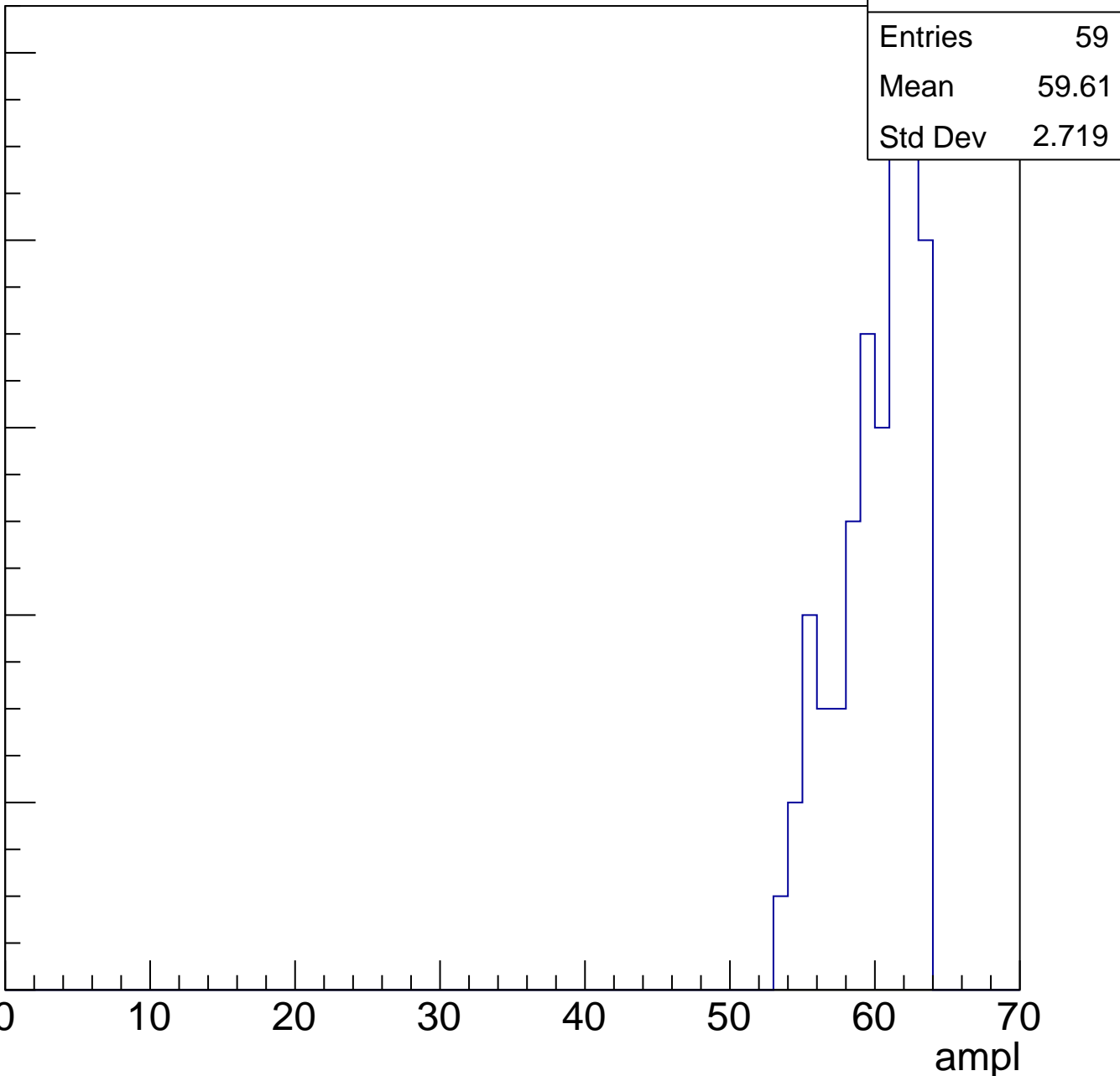
40

50

60

70

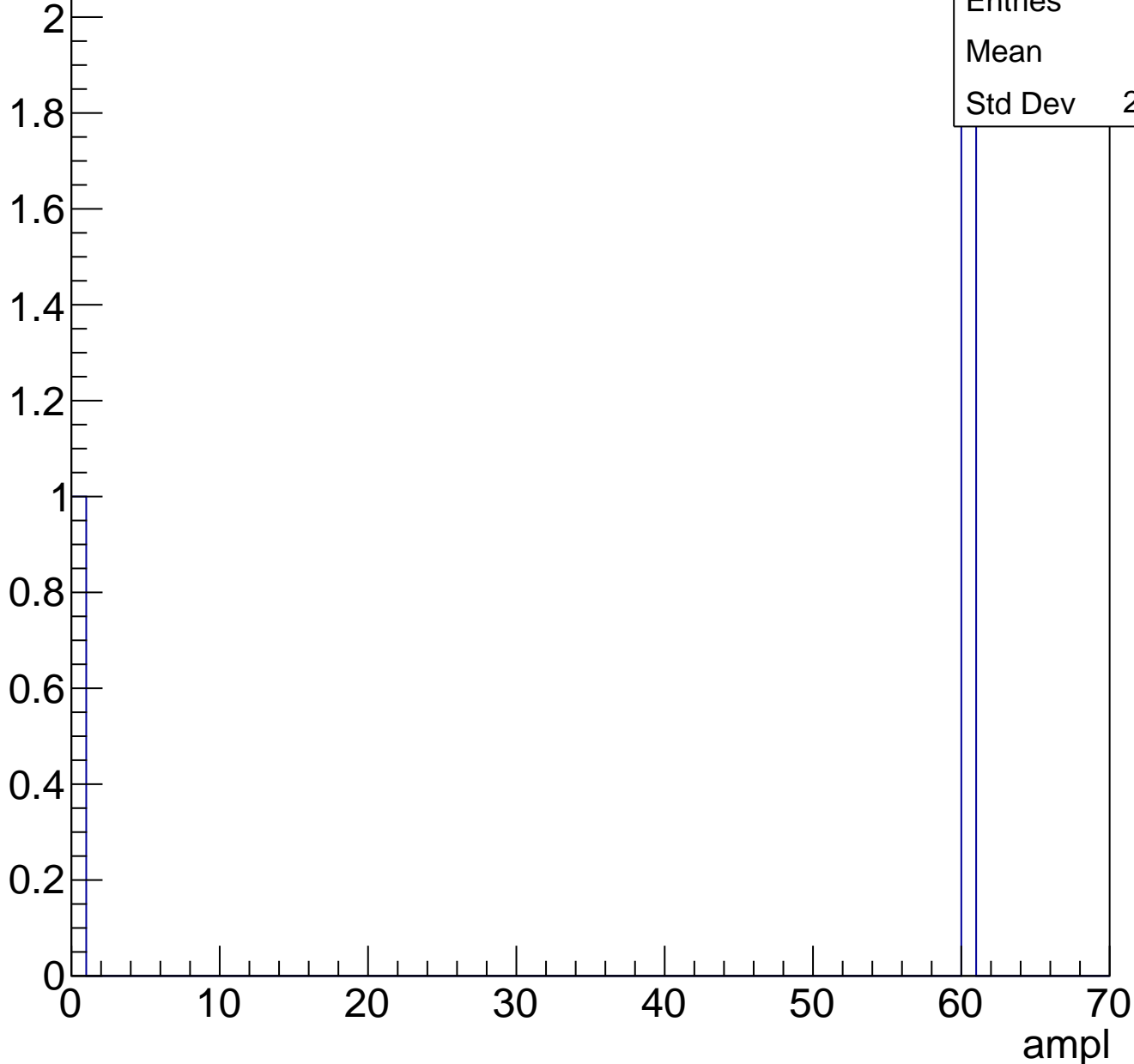
ampl



B1L101S, U1-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L101S, U1-ch36, adc0

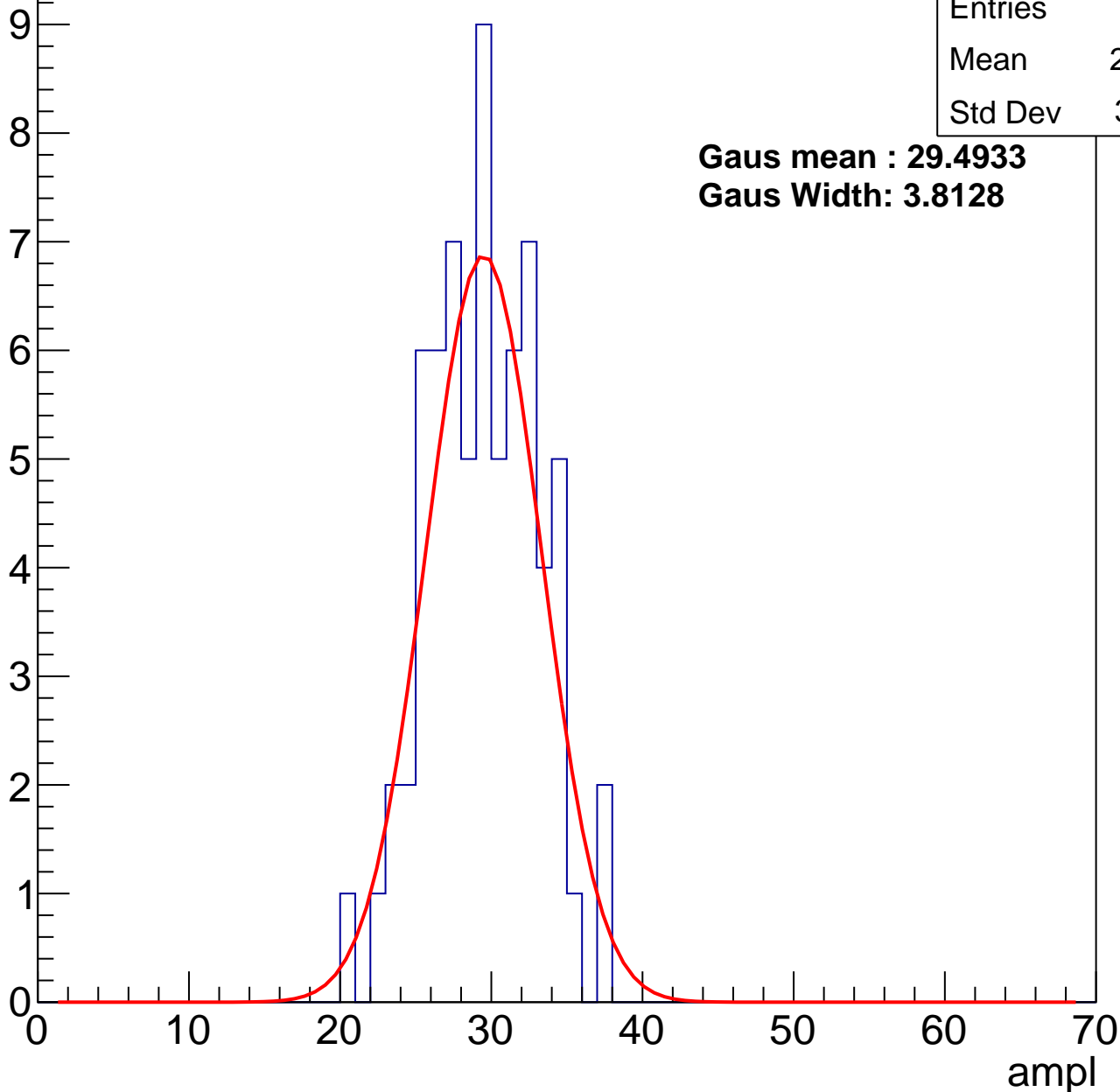
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.03
Std Dev	3.571

Gaus mean : 29.4933

Gaus Width: 3.8128



B1L101S, U1-ch36, adc1

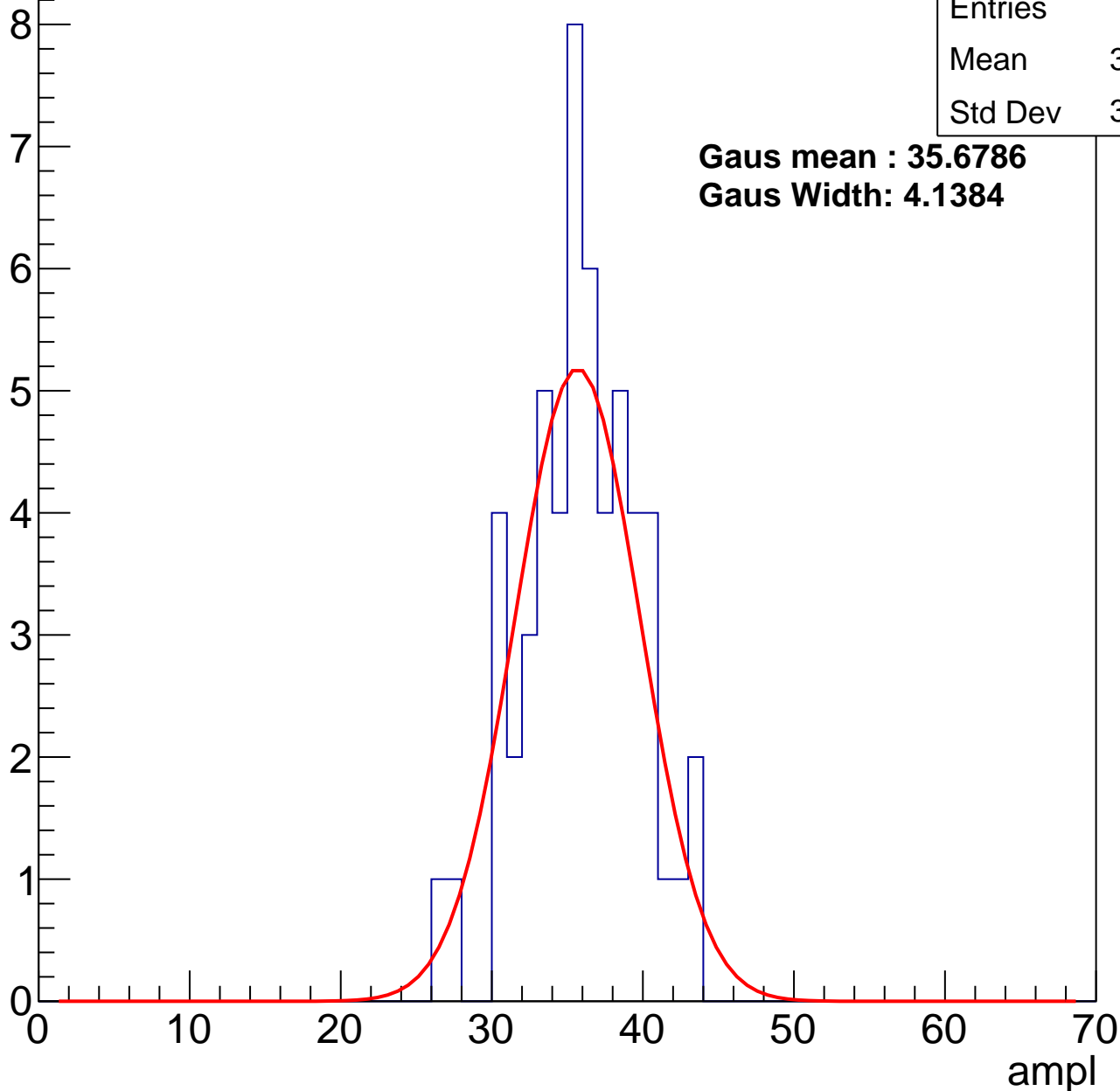
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.47
Std Dev	3.717

Gaus mean : 35.6786

Gaus Width: 4.1384



B1L101S, U1-ch36, adc2

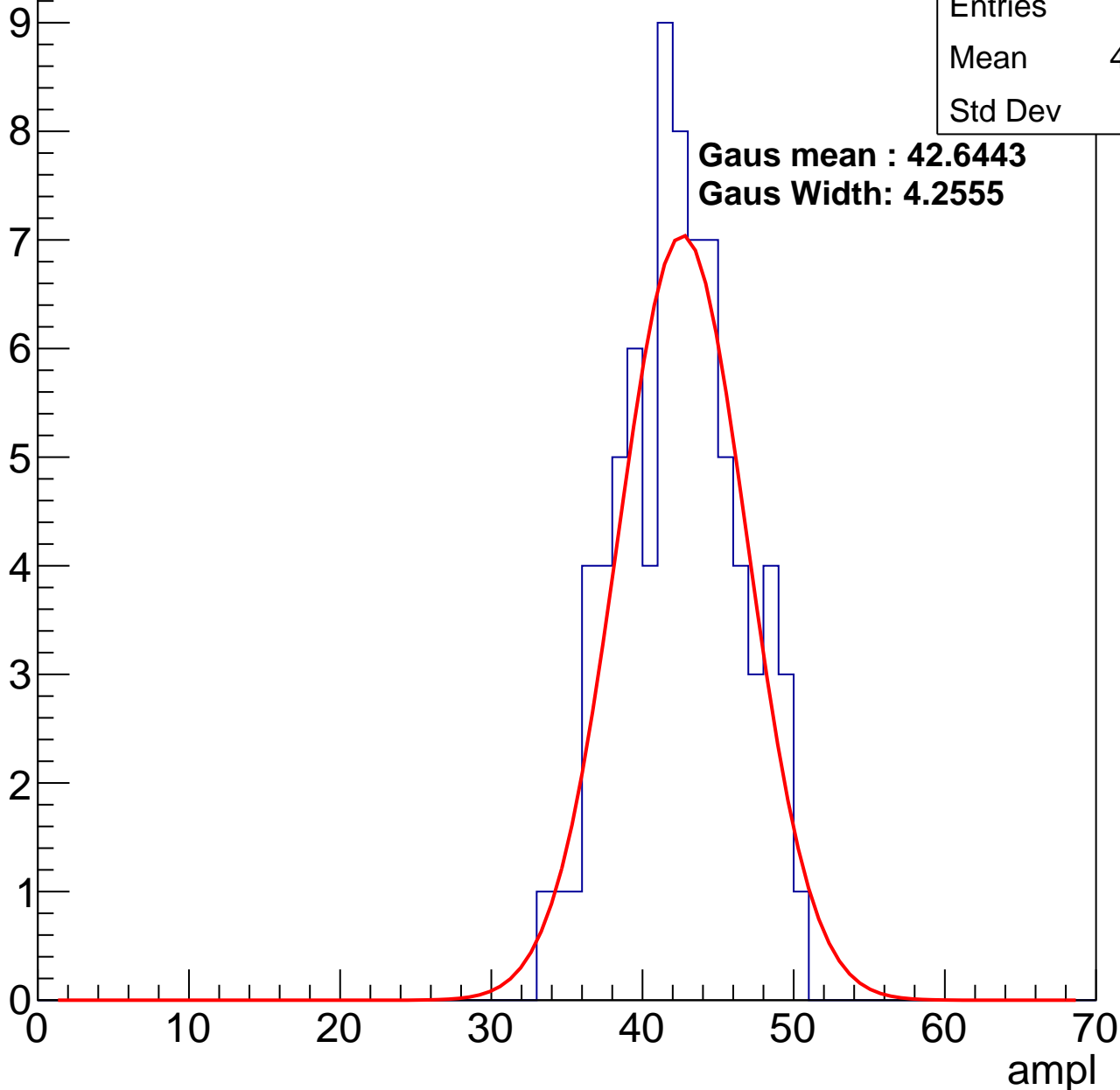
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	41.96
Std Dev	3.9

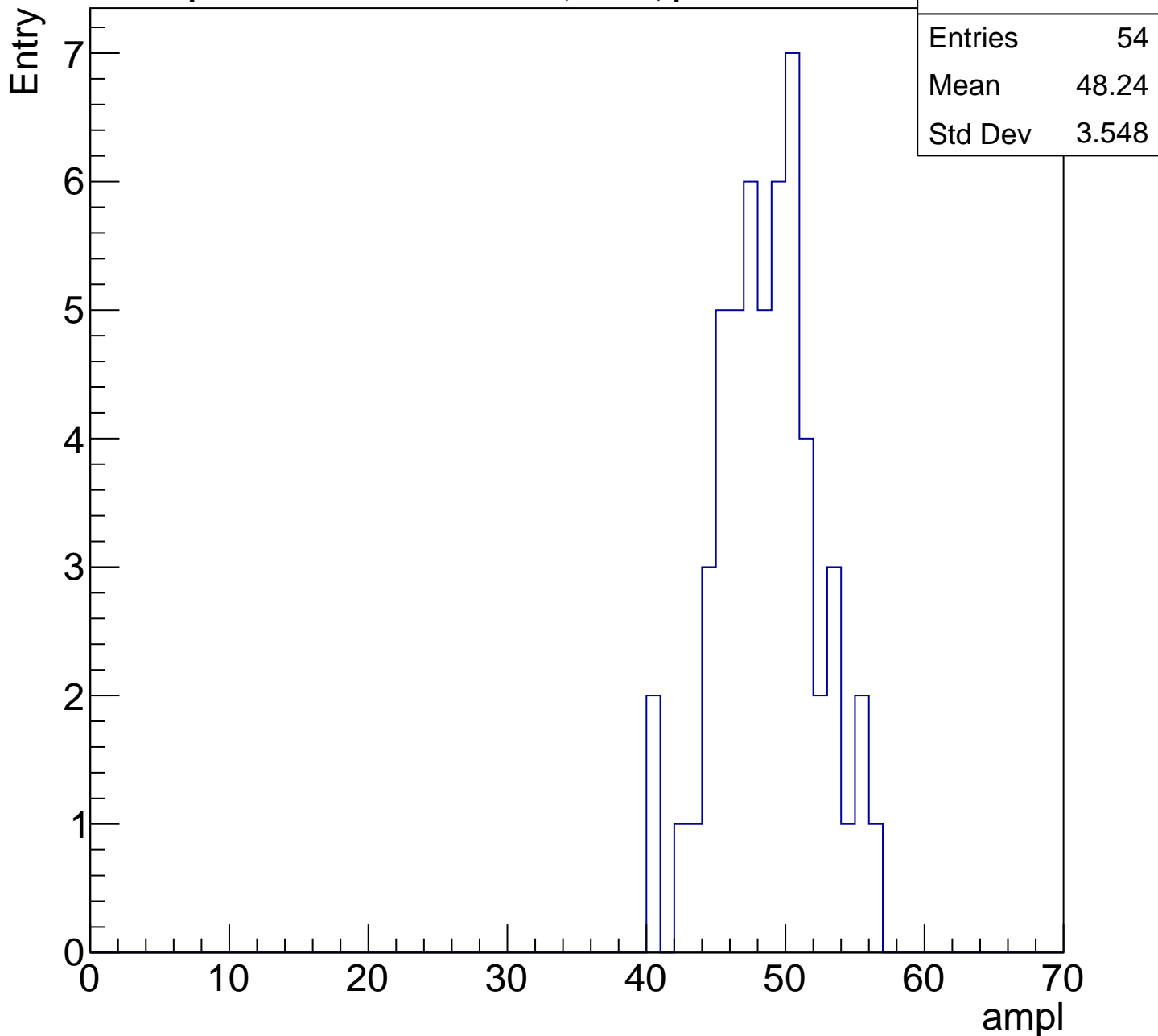
Gaus mean : 42.6443

Gaus Width: 4.2555



B1L101S, U1-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

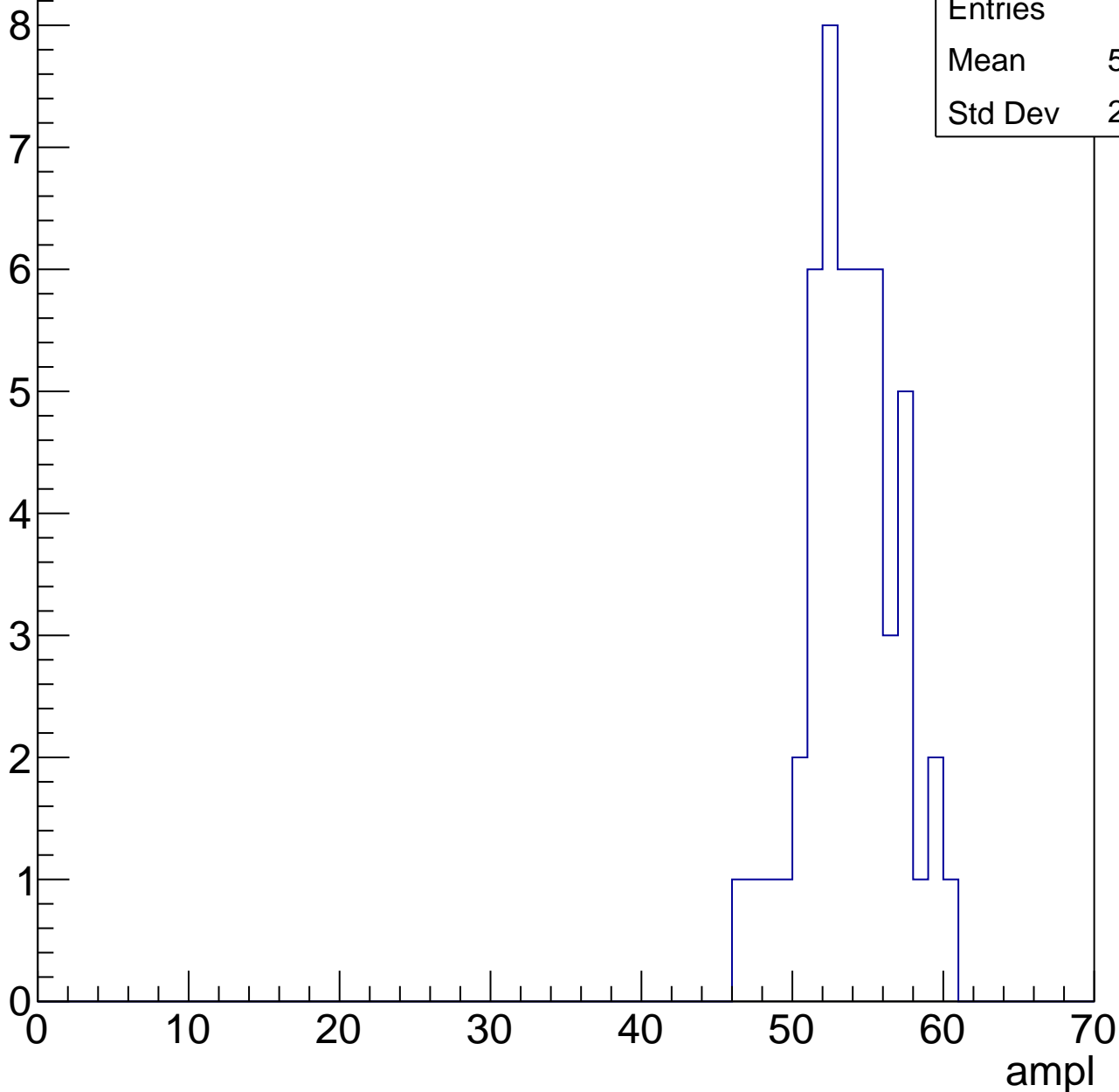


B1L101S, U1-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	53.46
Std Dev	2.995

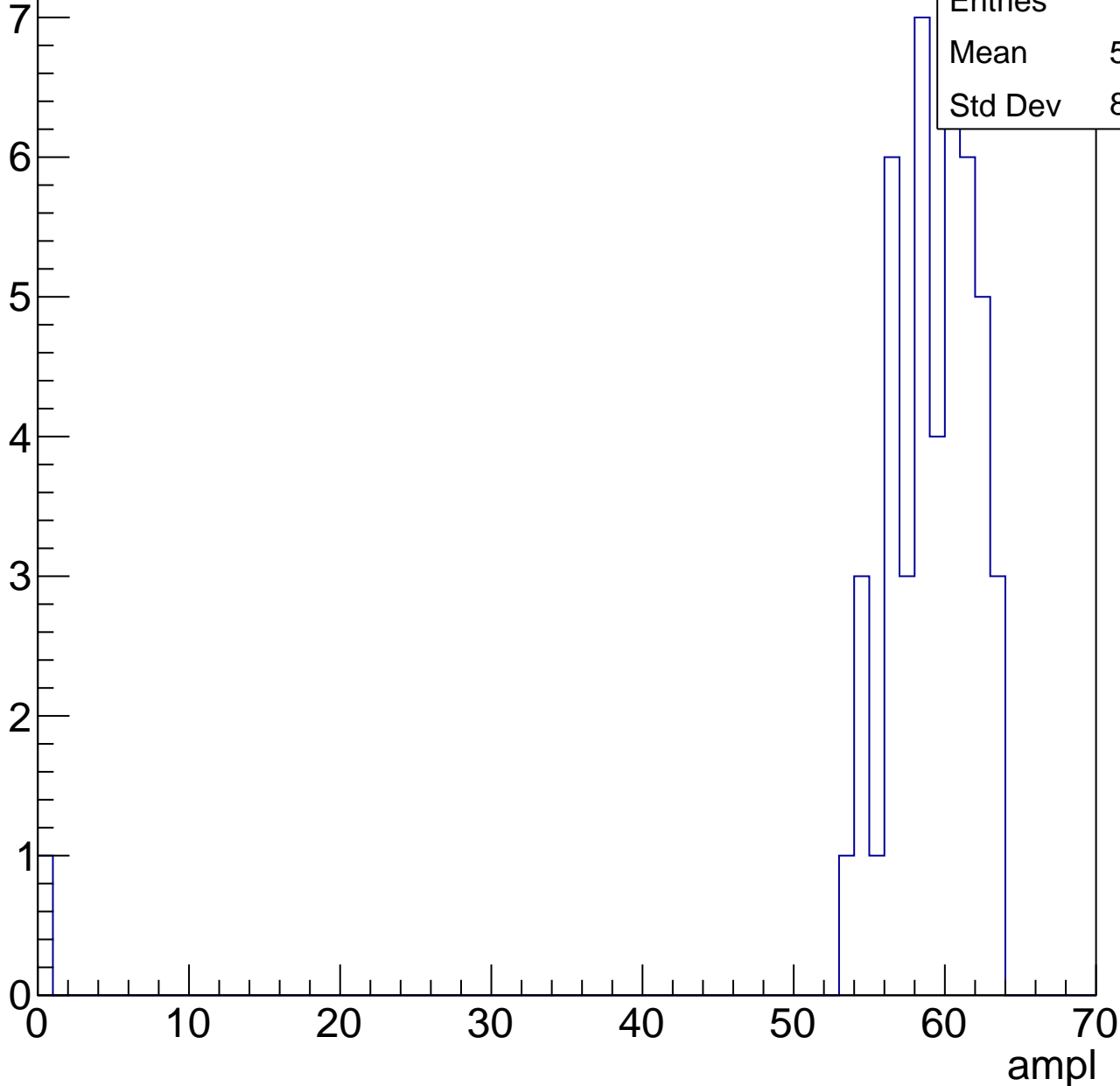


B1L101S, U1-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	57.53
Std Dev	8.877

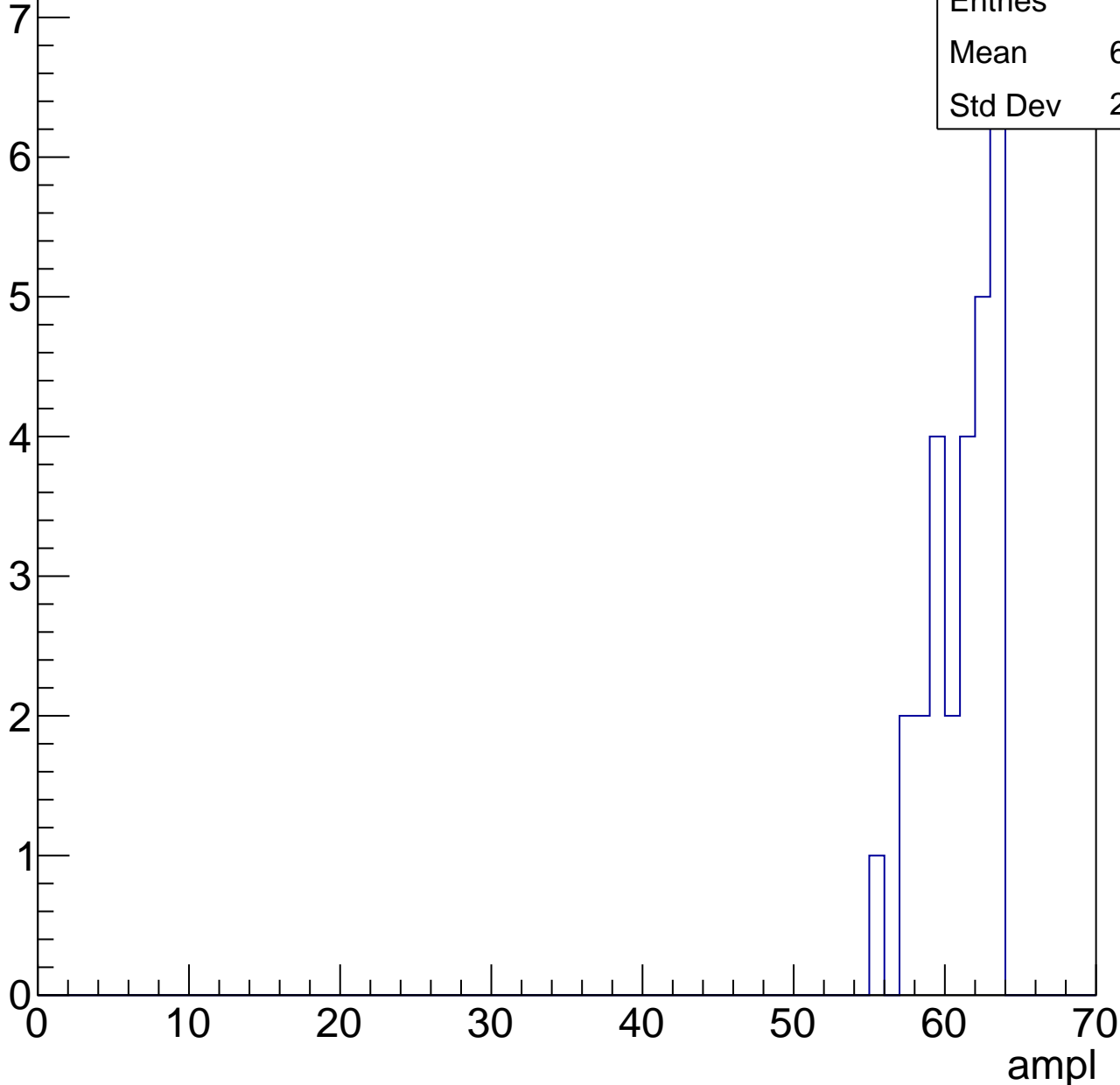


B1L101S, U1-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	60.59
Std Dev	2.215



B1L101S, U1-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch37, adc0

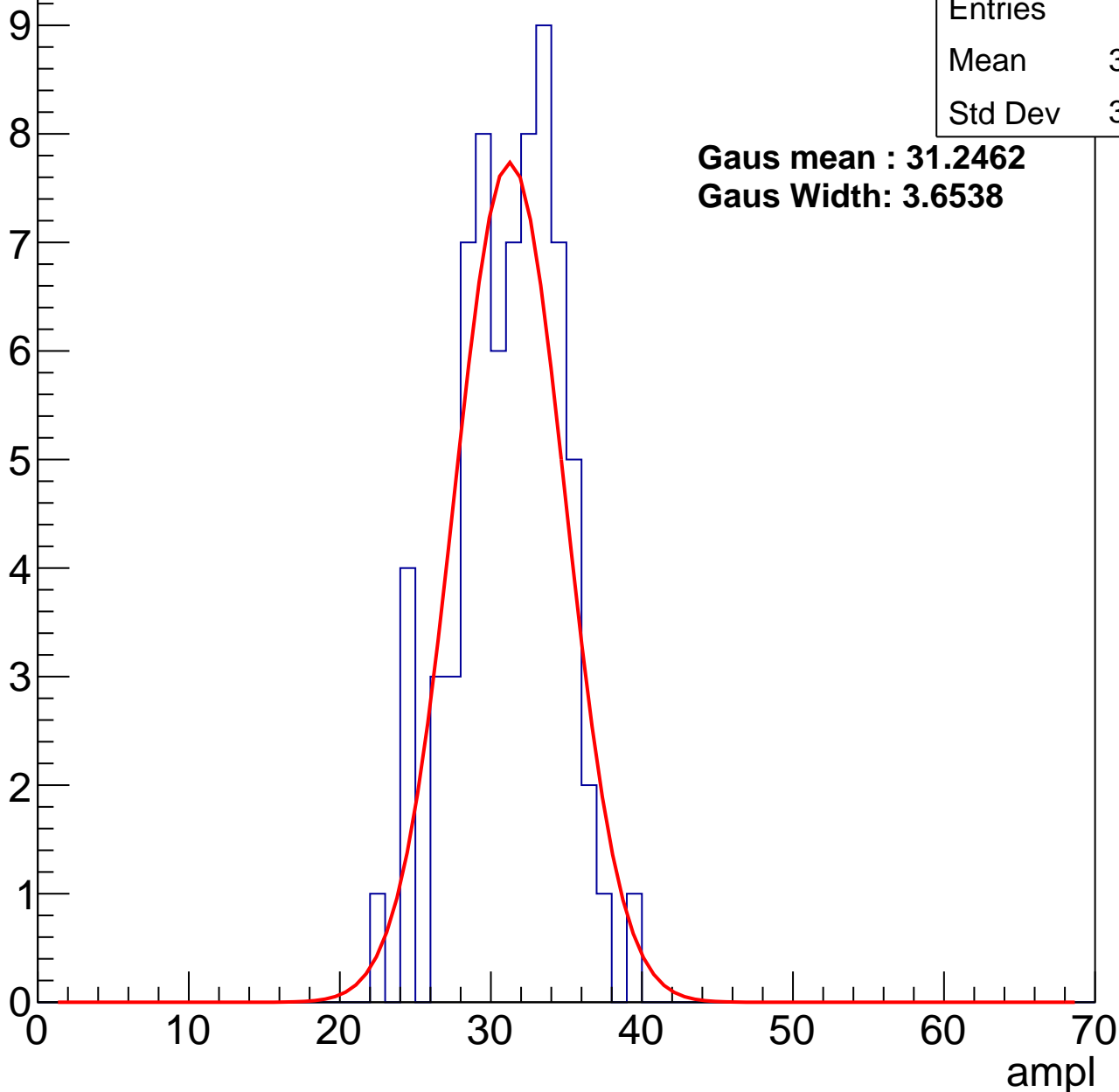
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.78
Std Dev	3.404

Gaus mean : 31.2462

Gaus Width: 3.6538



B1L101S, U1-ch37, adc1

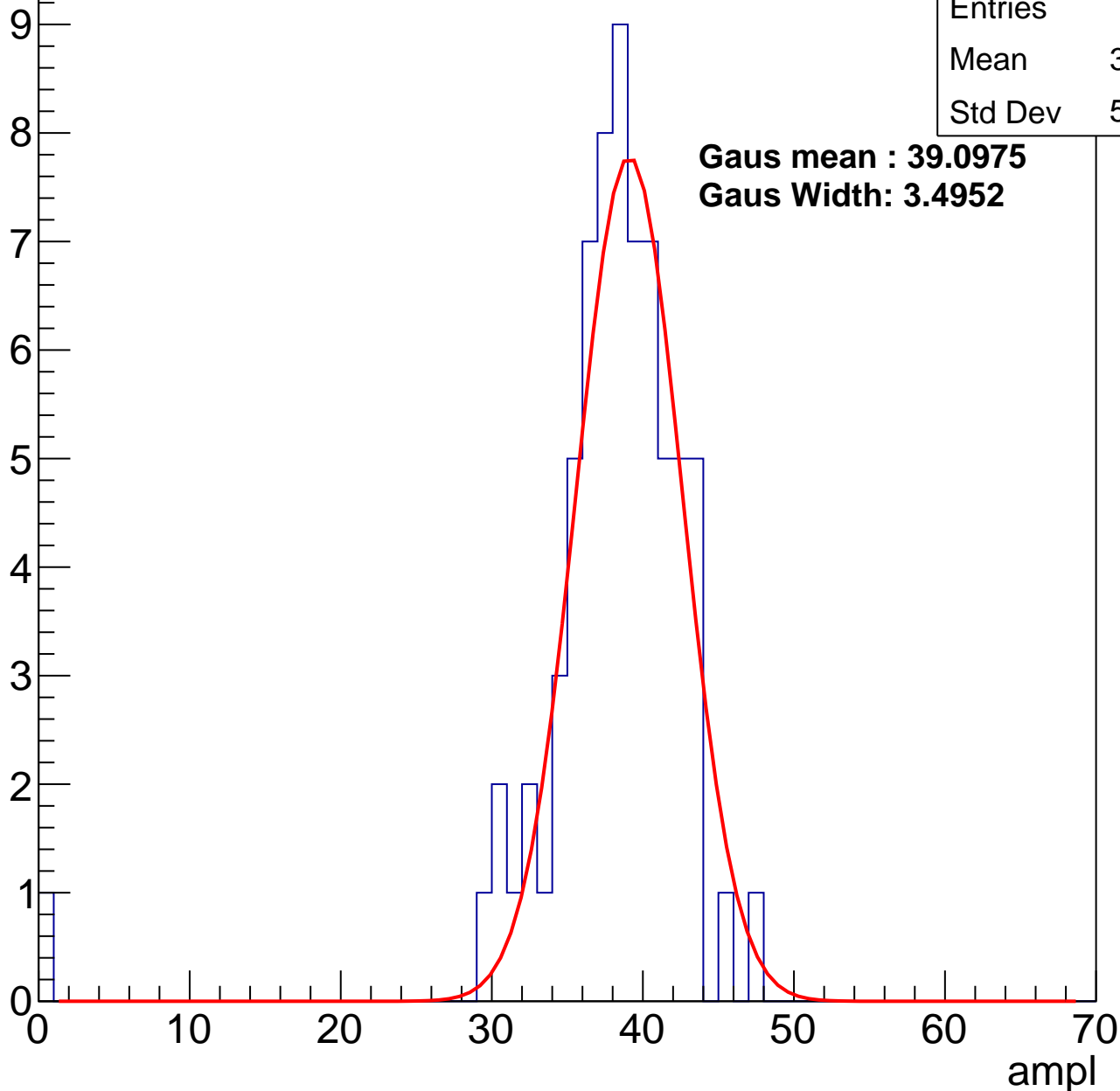
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.45
Std Dev	5.718

Gaus mean : 39.0975

Gaus Width: 3.4952



B1L101S, U1-ch37, adc2

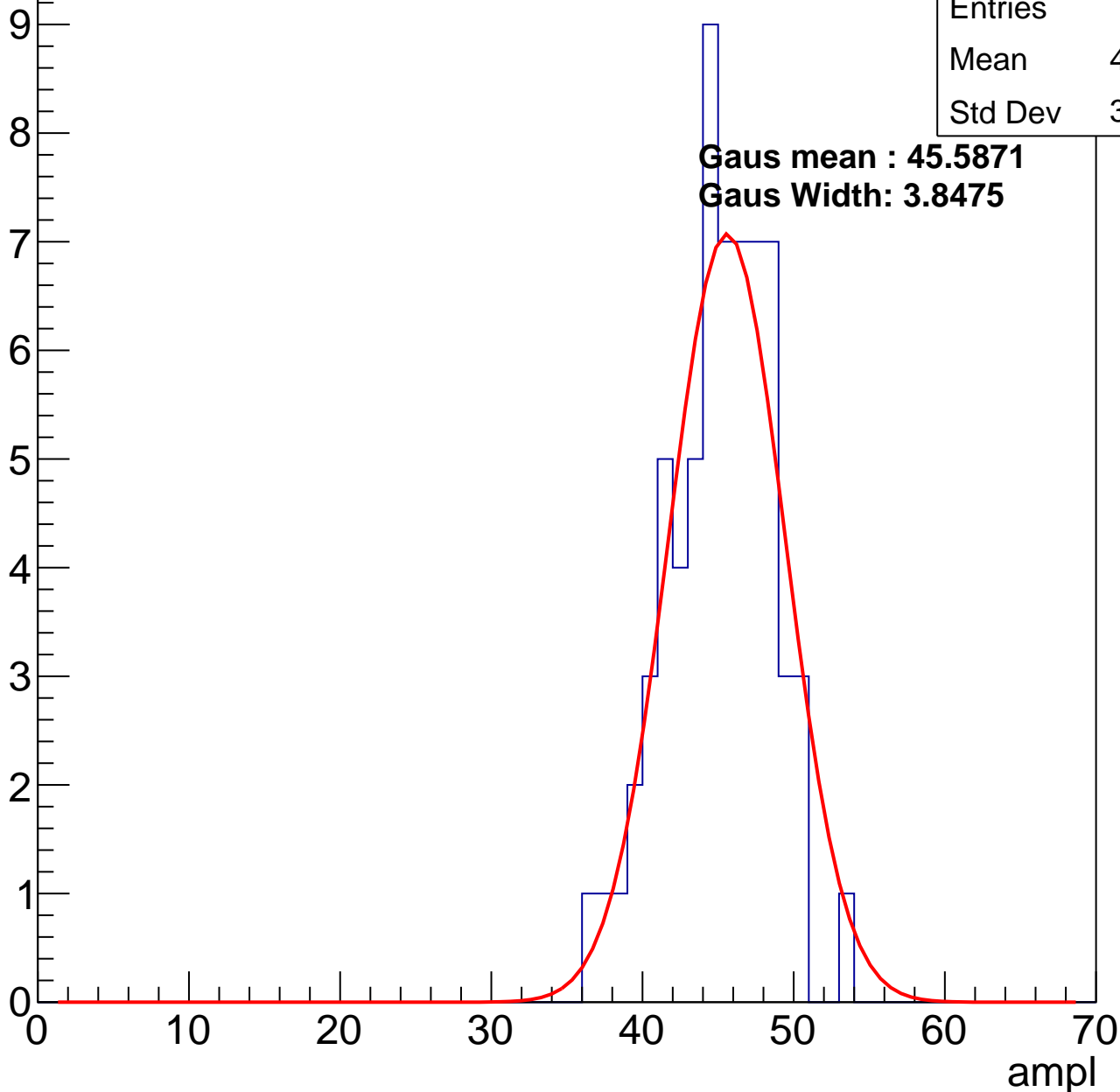
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.62
Std Dev	3.397

Gaus mean : 45.5871

Gaus Width: 3.8475

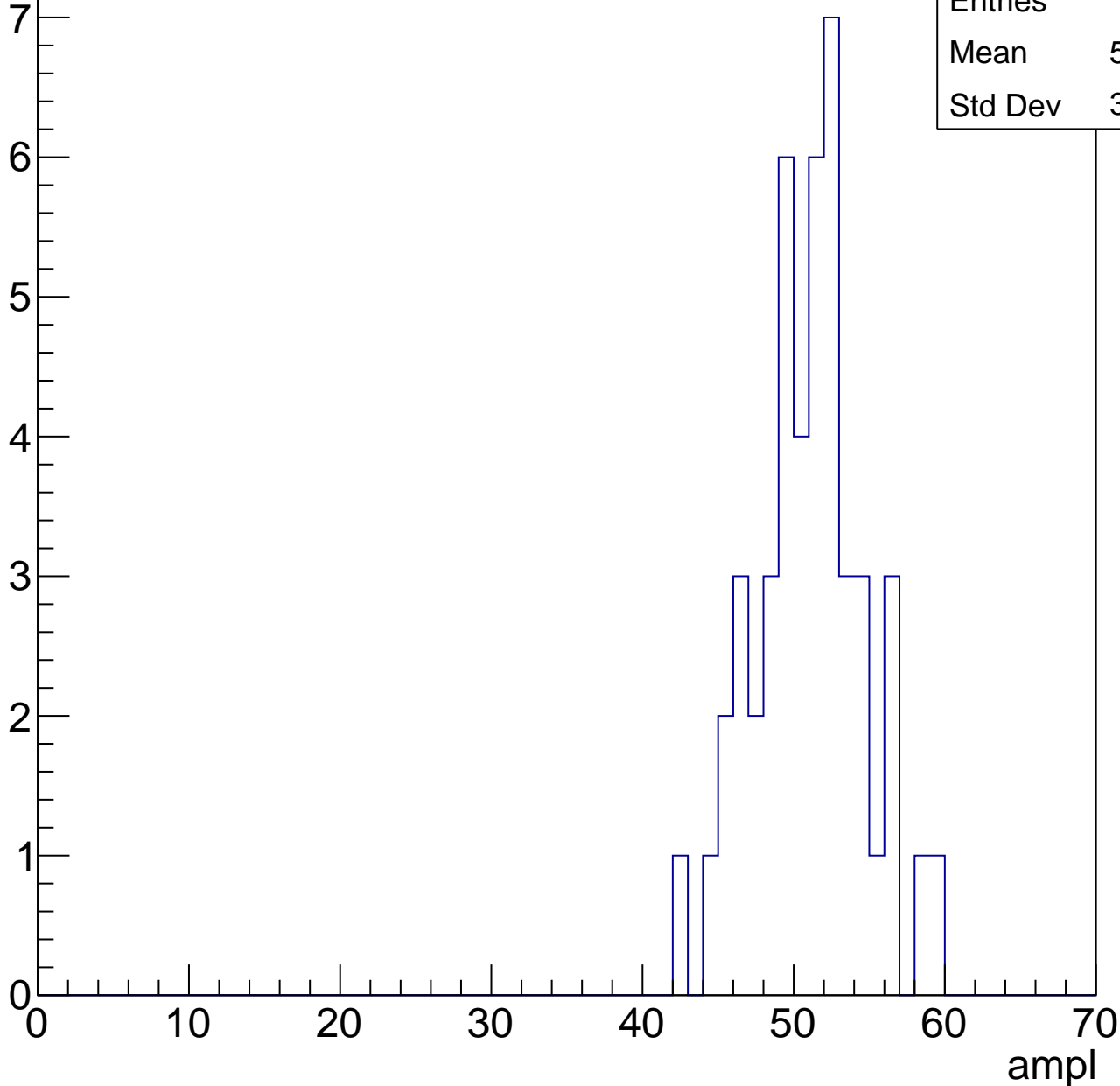


B1L101S, U1-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	50.57
Std Dev	3.589



B1L101S, U1-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 62

Mean 55.63

Std Dev 3.209

0

0

10

20

30

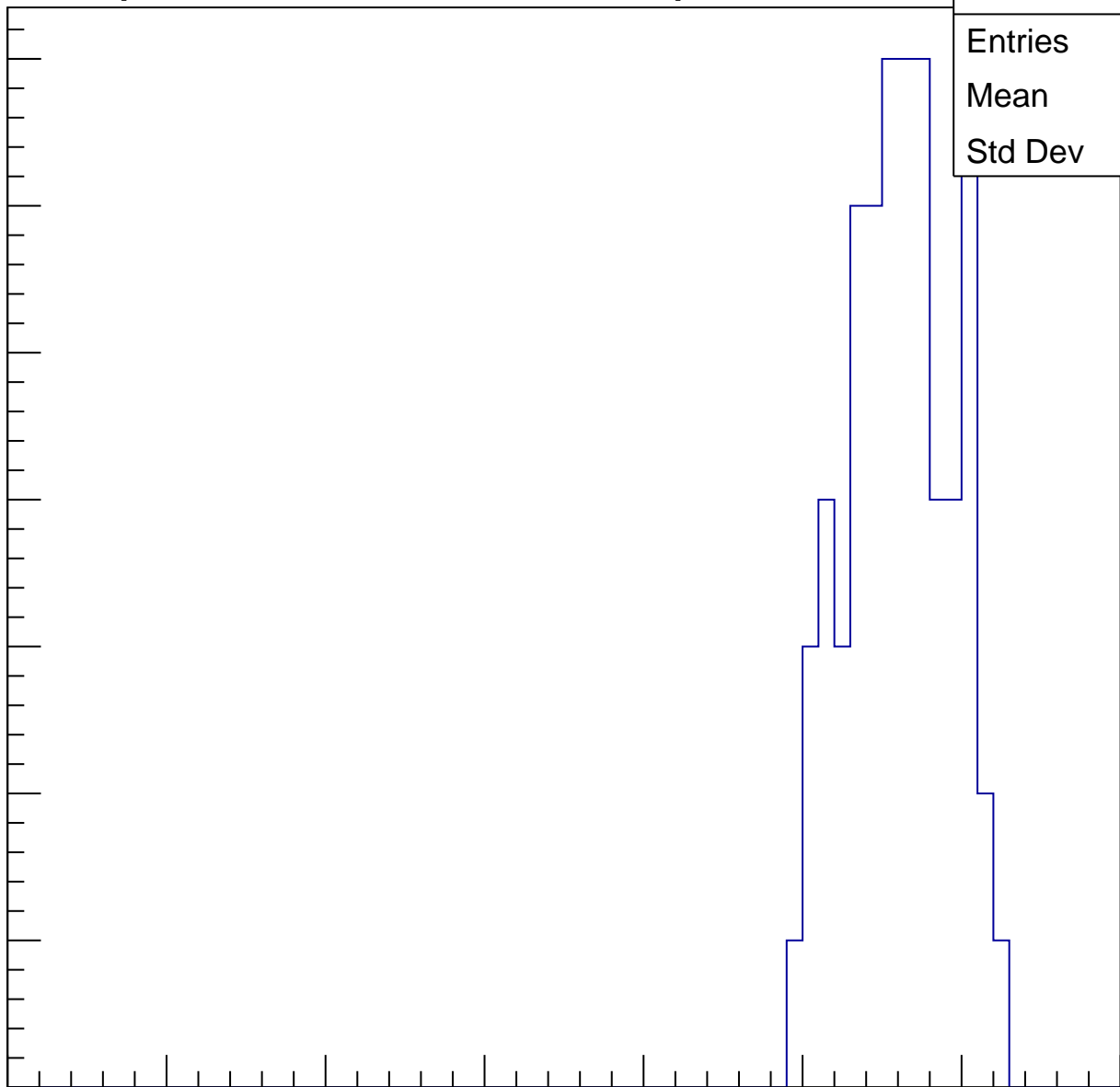
40

50

60

70

ampl

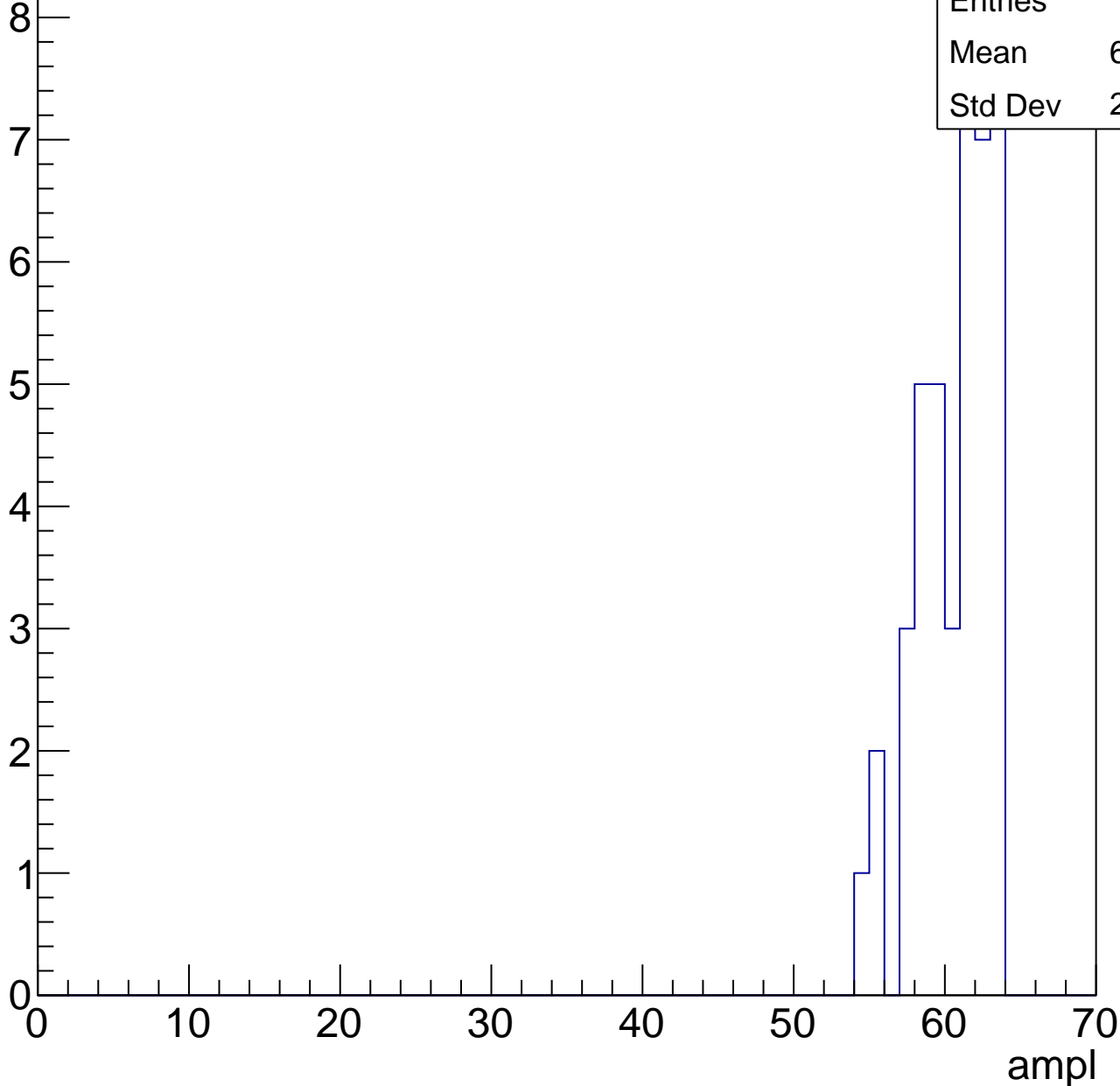


B1L101S, U1-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	60.14
Std Dev	2.416



B1L101S, U1-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch38, adc0

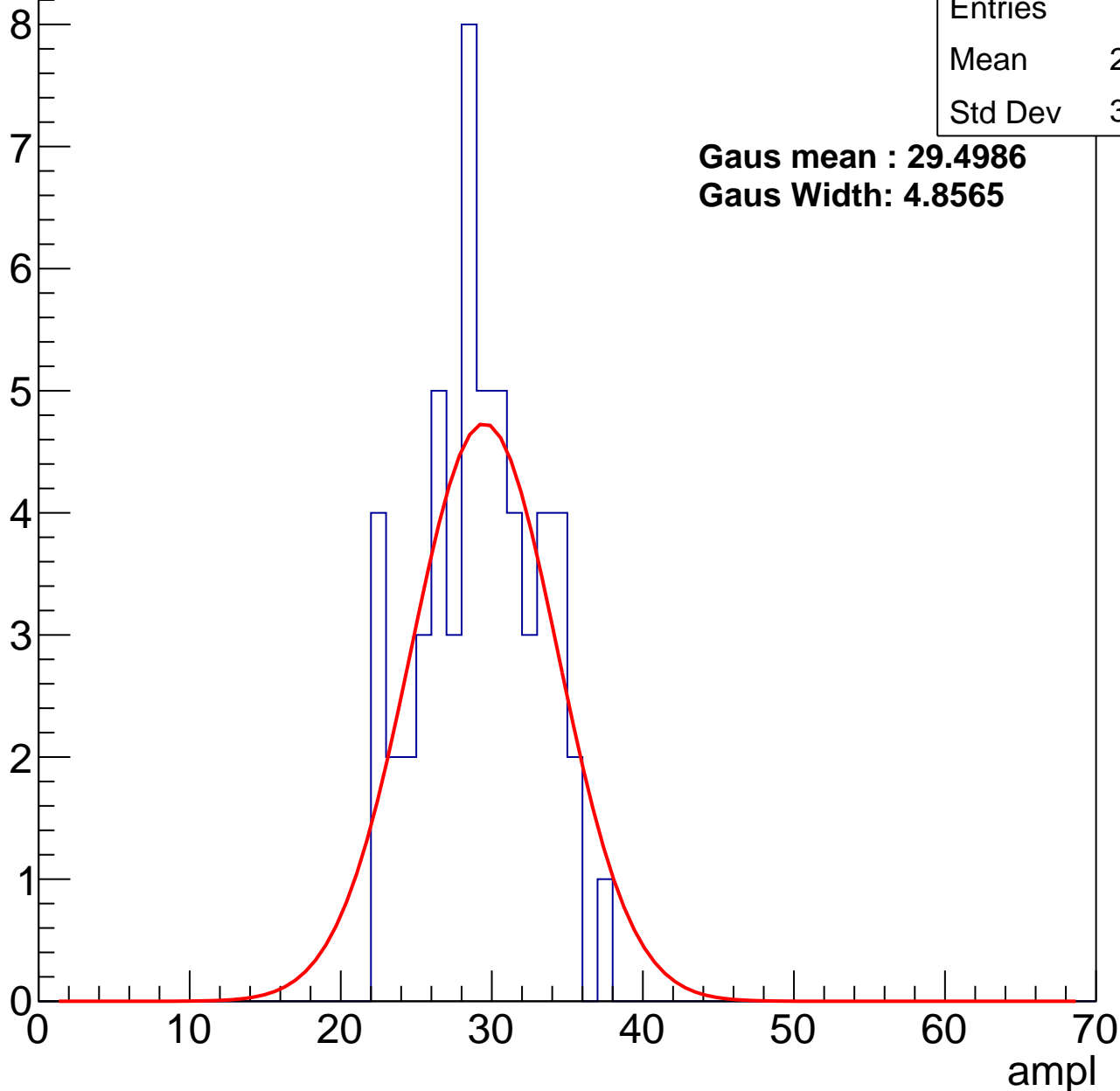
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.76
Std Dev	3.756

Gaus mean : 29.4986

Gaus Width: 4.8565



B1L101S, U1-ch38, adc1

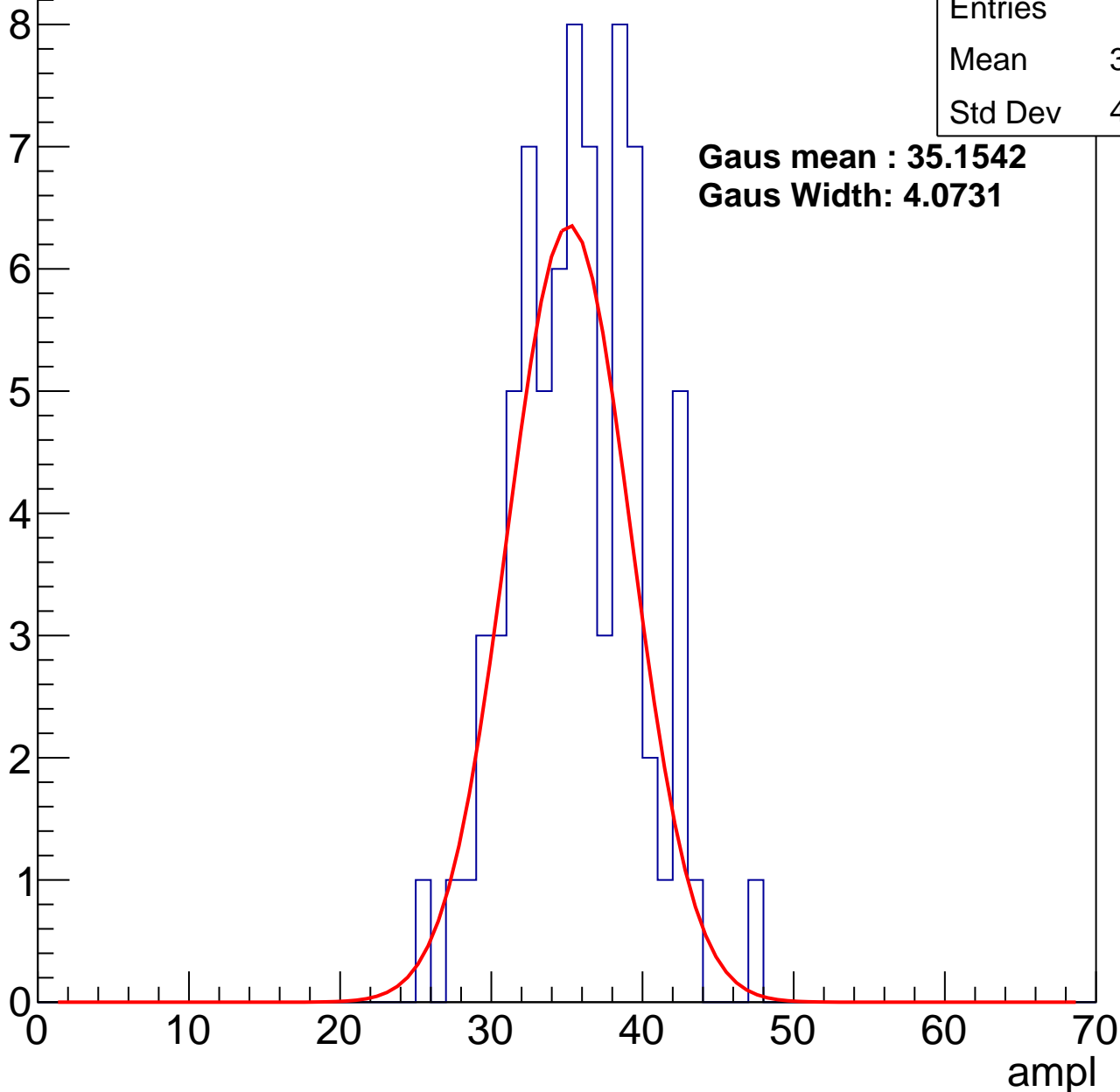
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.28
Std Dev	4.165

Gaus mean : 35.1542

Gaus Width: 4.0731



B1L101S, U1-ch38, adc2

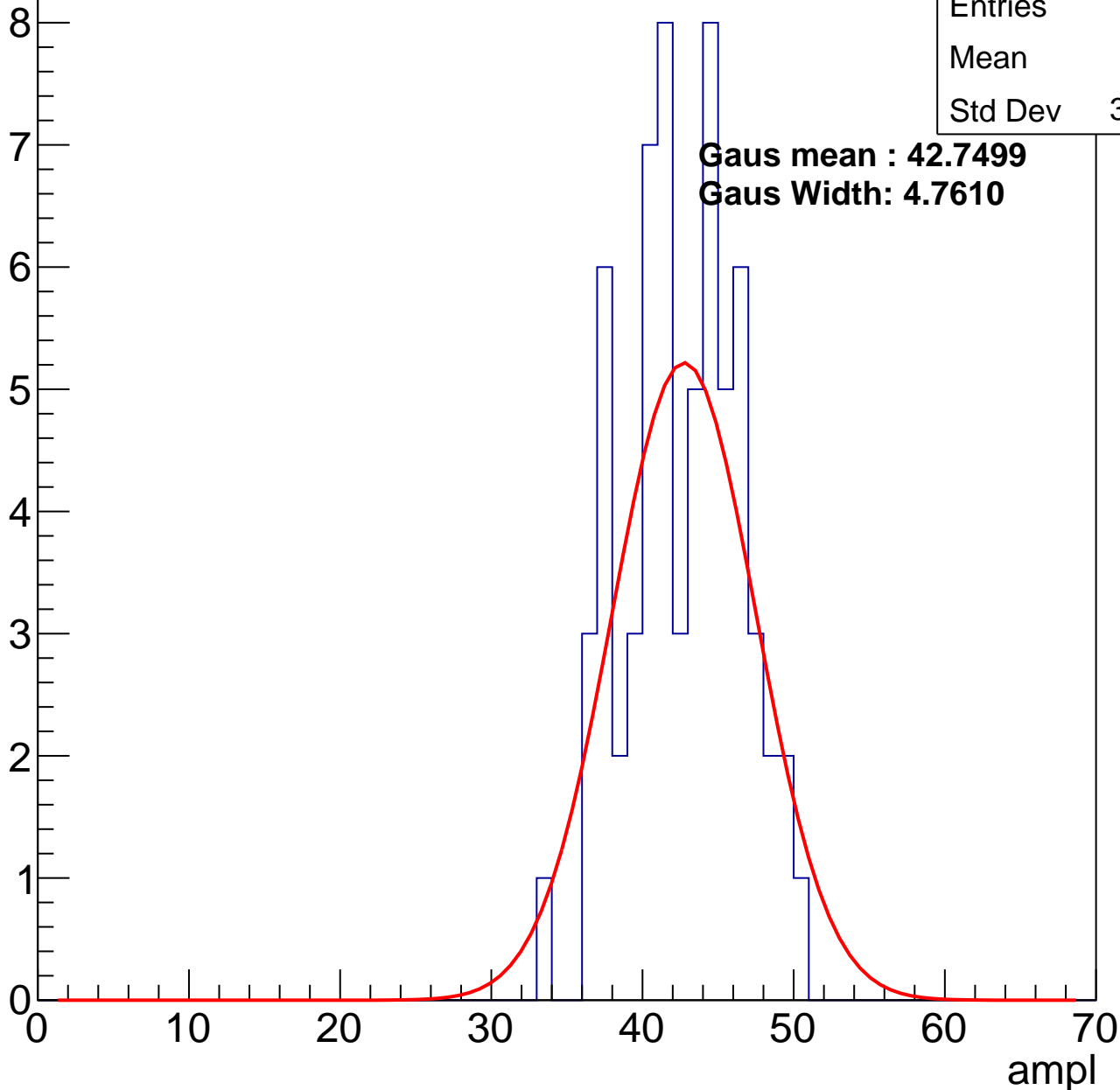
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.2
Std Dev	3.759

Gaus mean : 42.7499

Gaus Width: 4.7610

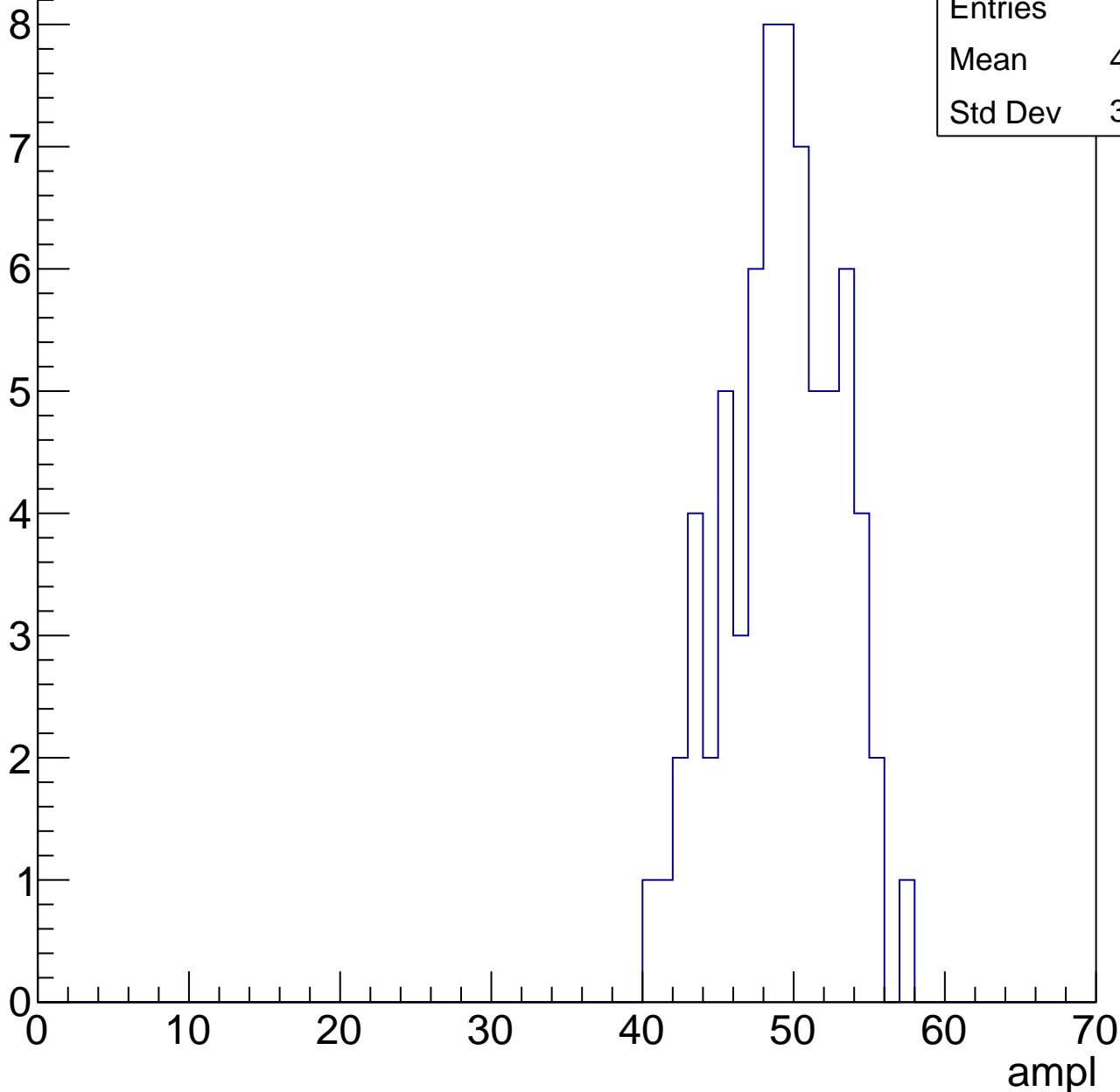


B1L101S, U1-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

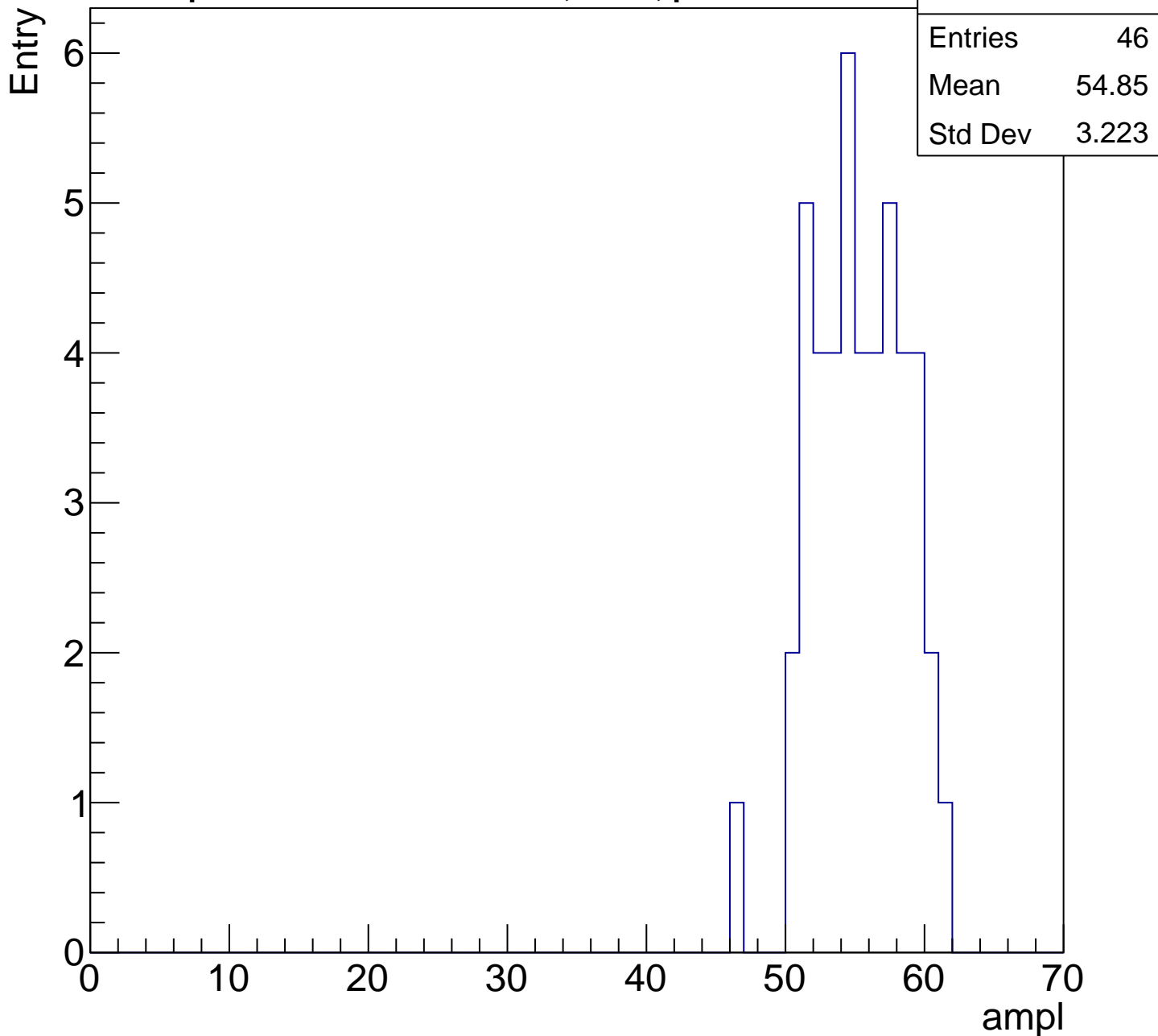
Entry

Entries	70
Mean	48.74
Std Dev	3.733



B1L101S, U1-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

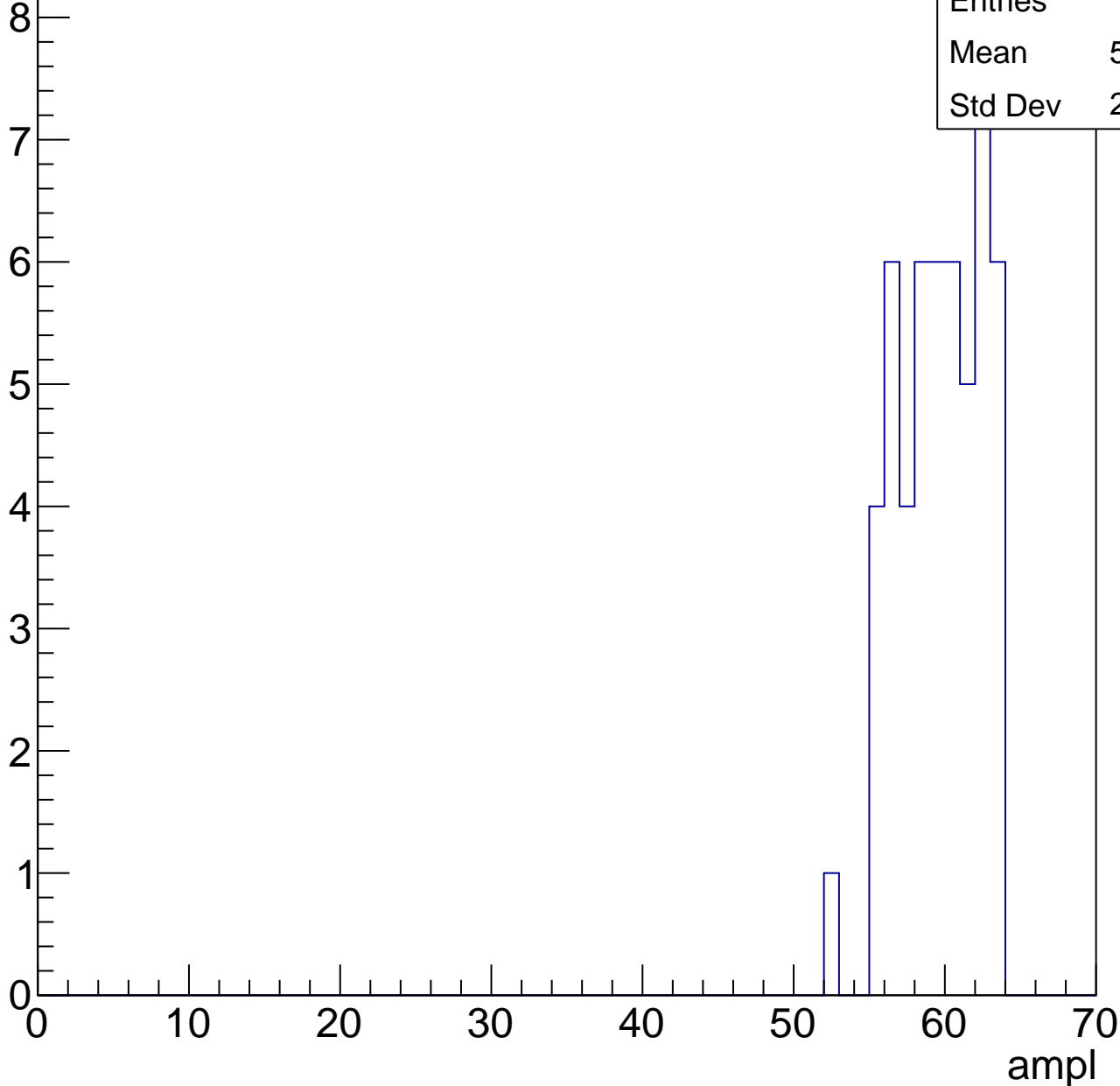


B1L101S, U1-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

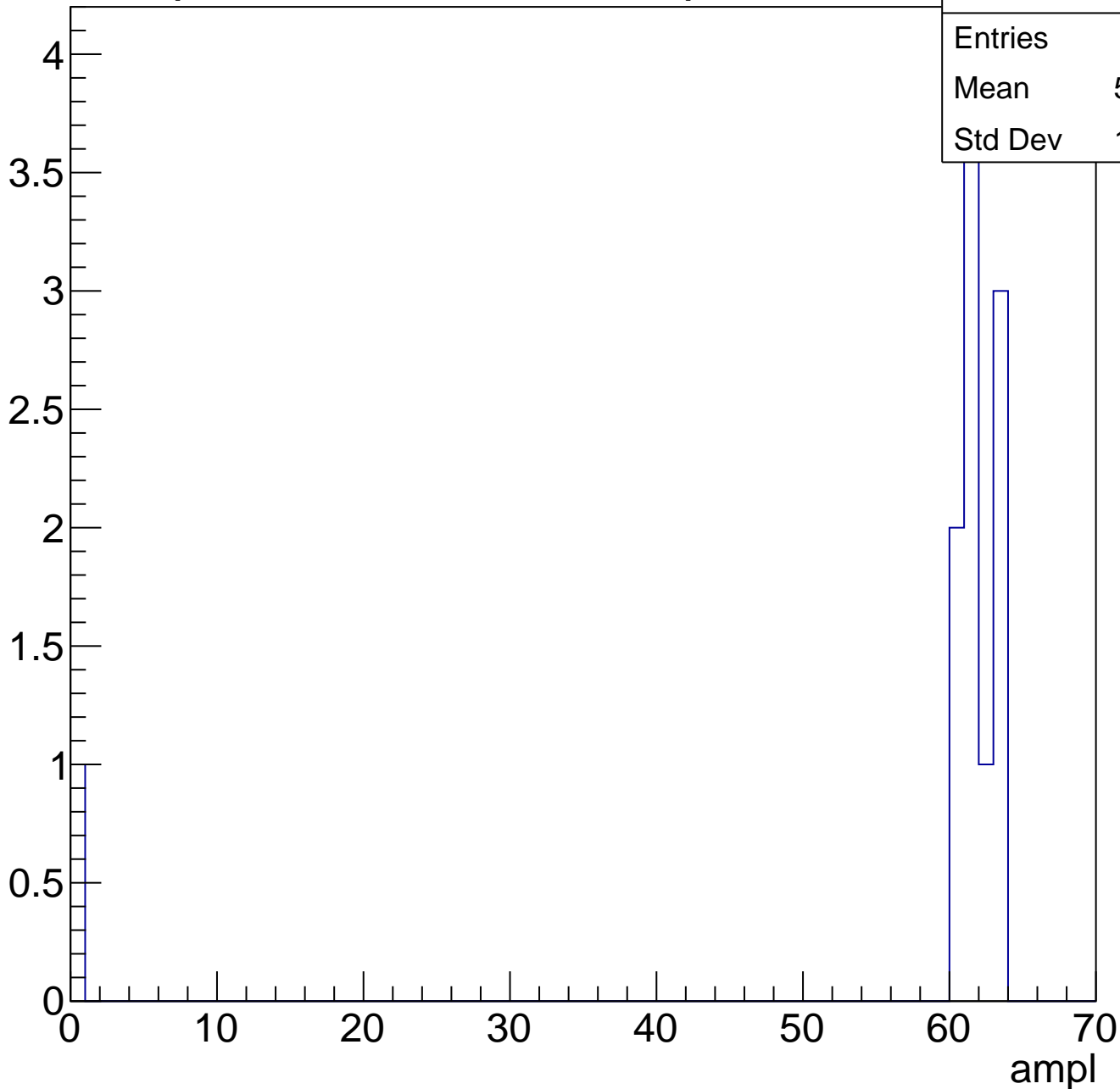
Entries	52
Mean	59.17
Std Dev	2.708



B1L101S, U1-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

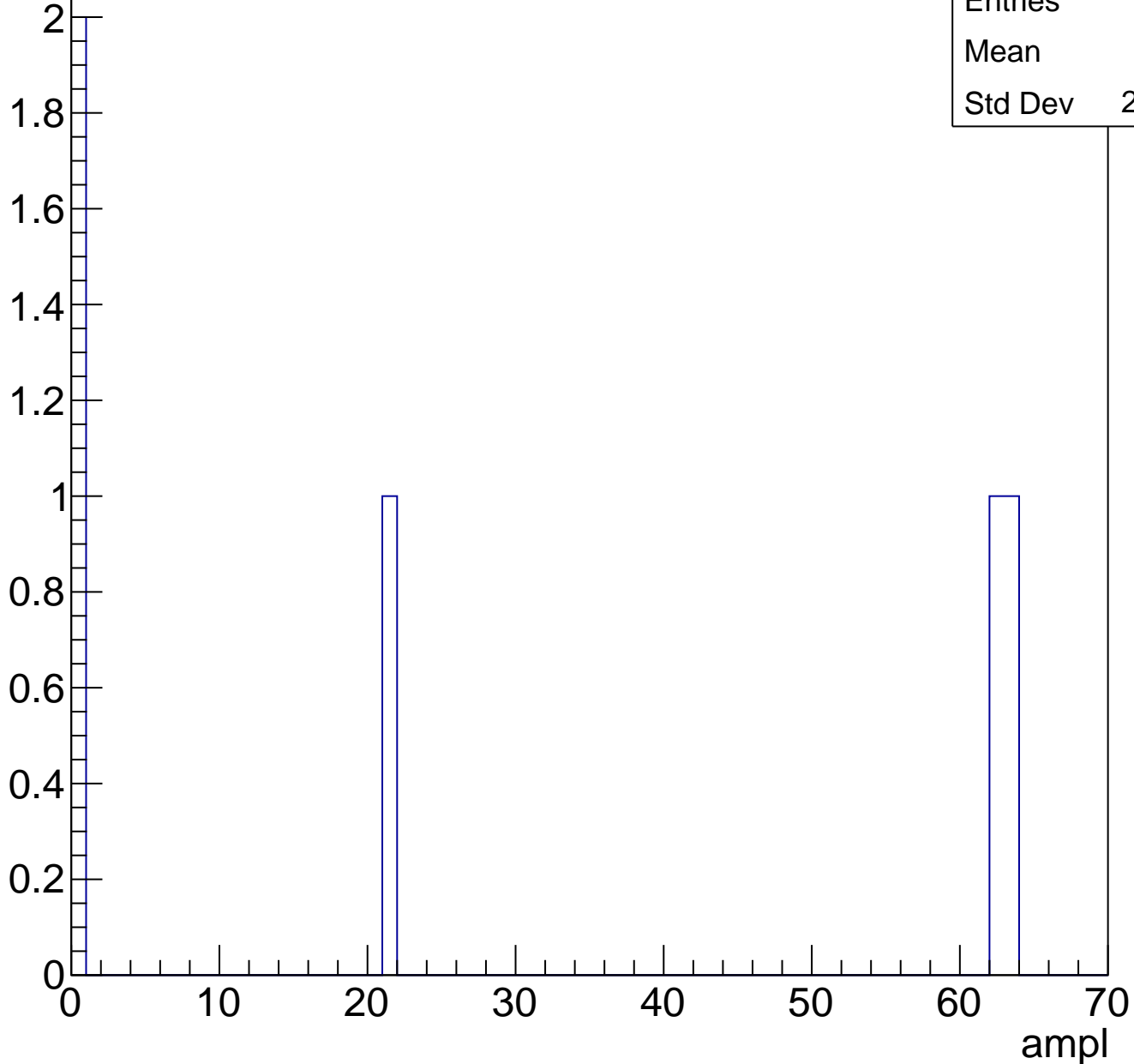
Entry



B1L101S, U1-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	29.2
Std Dev	28.25

B1L101S, U1-ch39, adc0

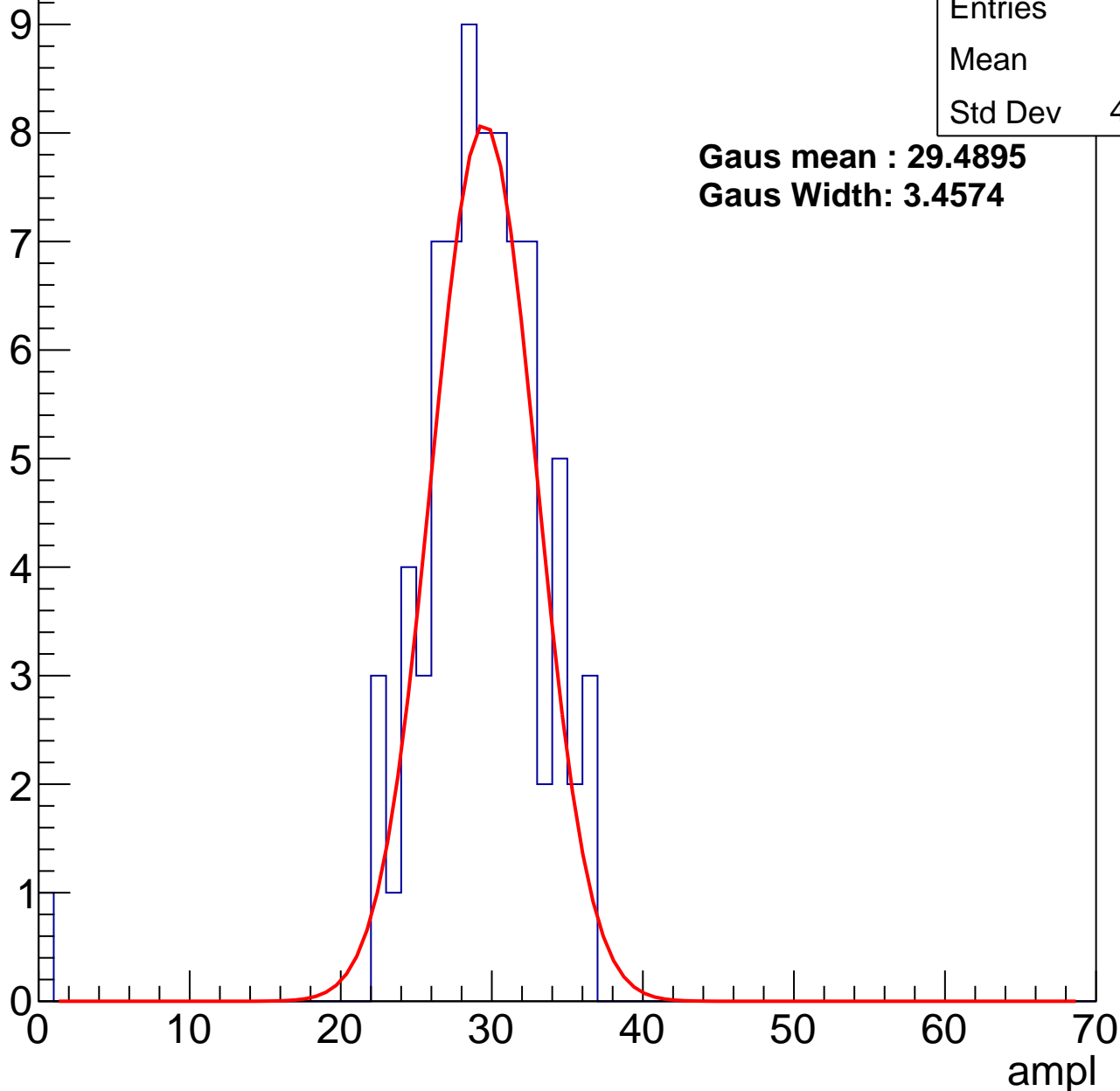
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.7
Std Dev	4.754

Gaus mean : 29.4895

Gaus Width: 3.4574



B1L101S, U1-ch39, adc1

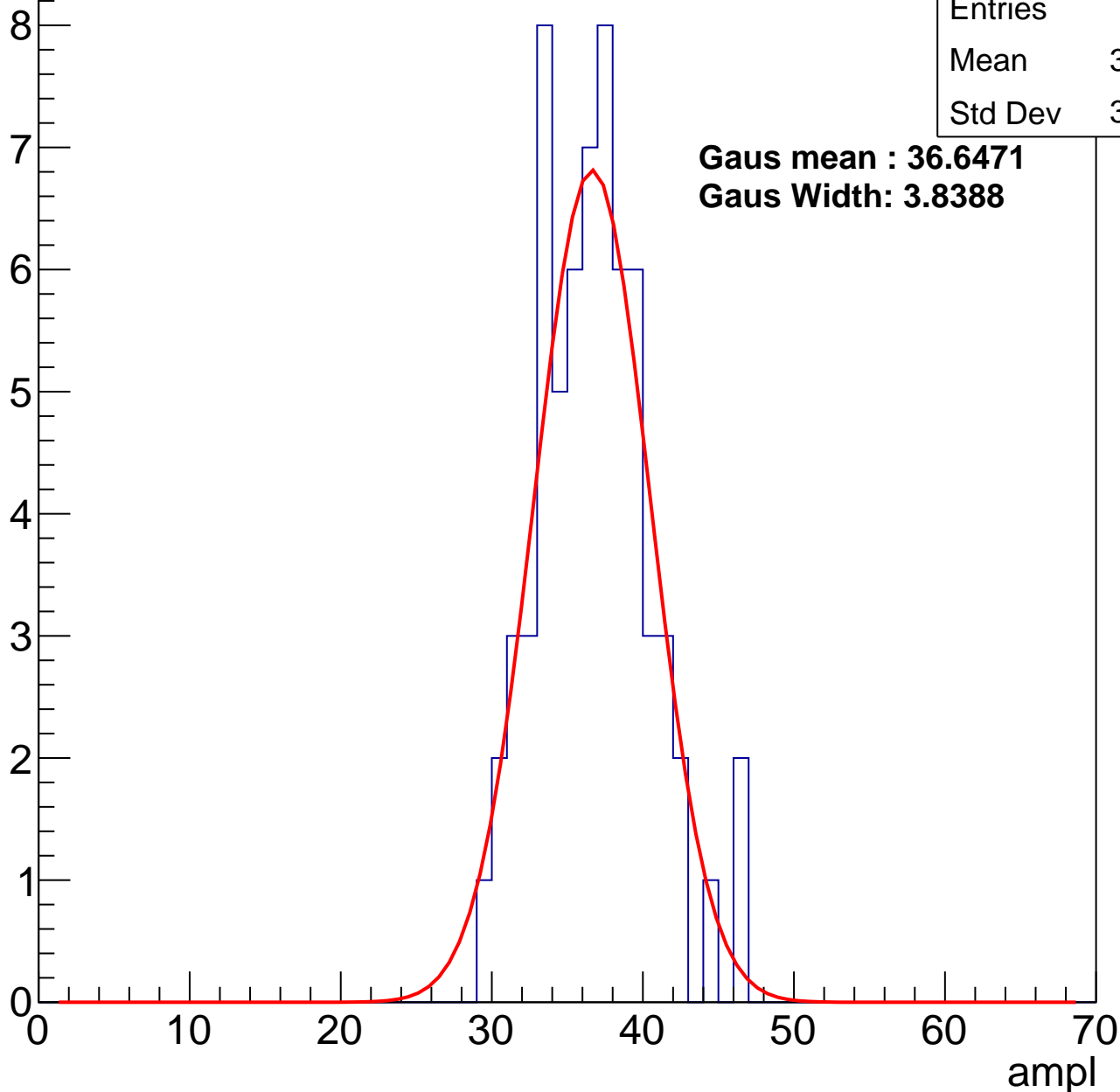
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.29
Std Dev	3.655

Gaus mean : 36.6471

Gaus Width: 3.8388

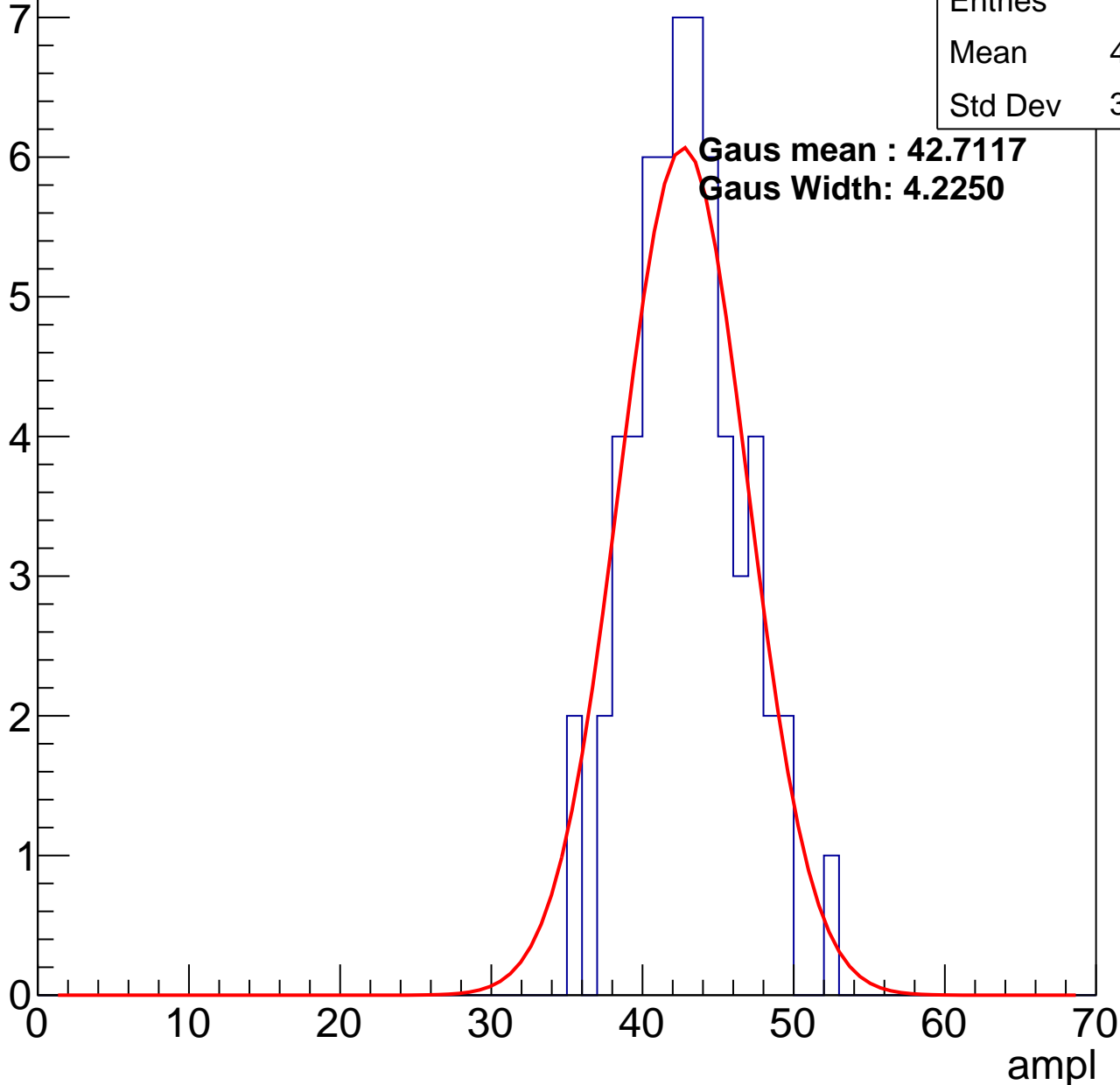


B1L101S, U1-ch39, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

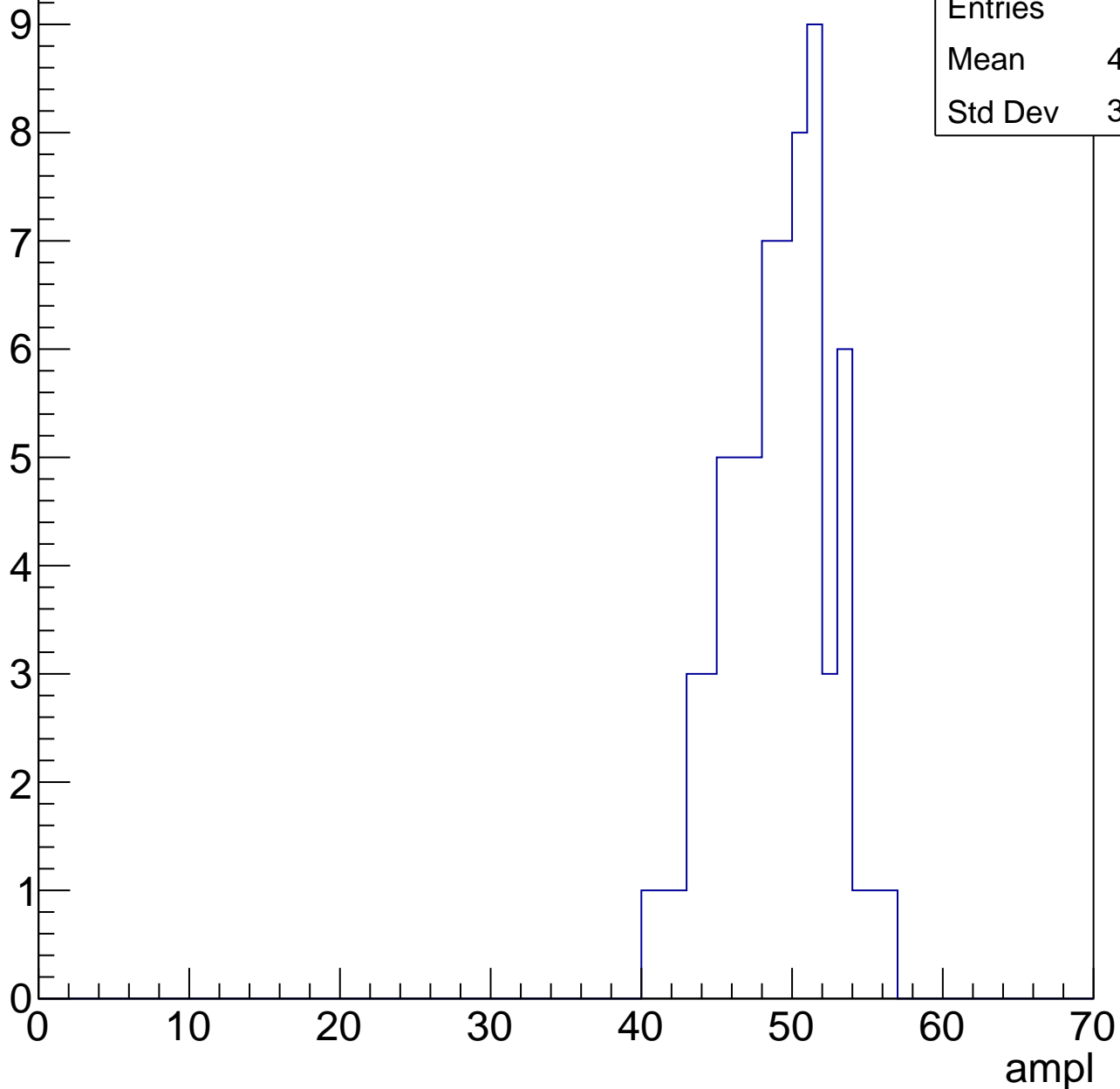
Entries	60
Mean	42.48
Std Dev	3.533



B1L101S, U1-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



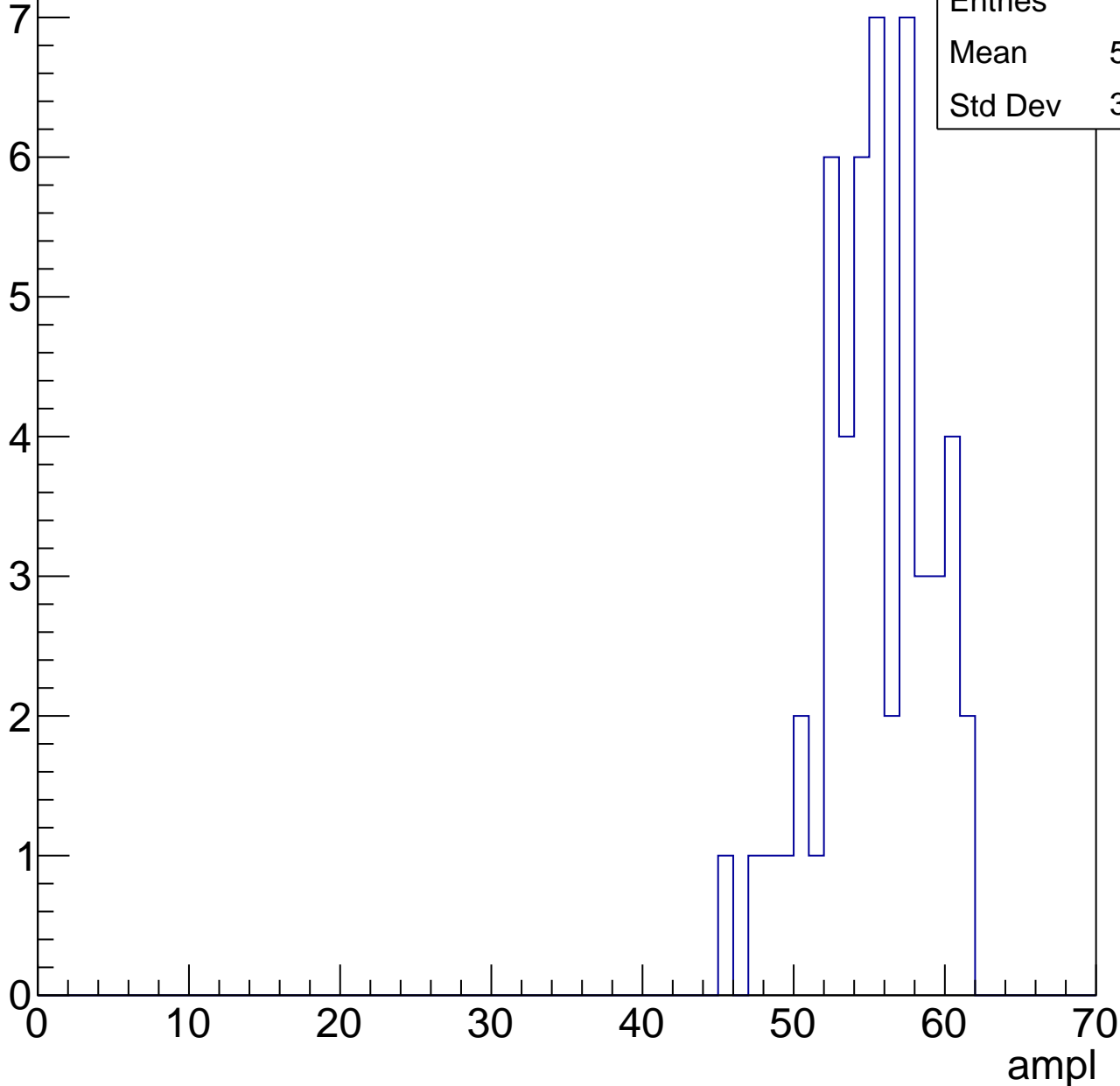
Entries	67
Mean	48.52
Std Dev	3.435

B1L101S, U1-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	54.84
Std Dev	3.616

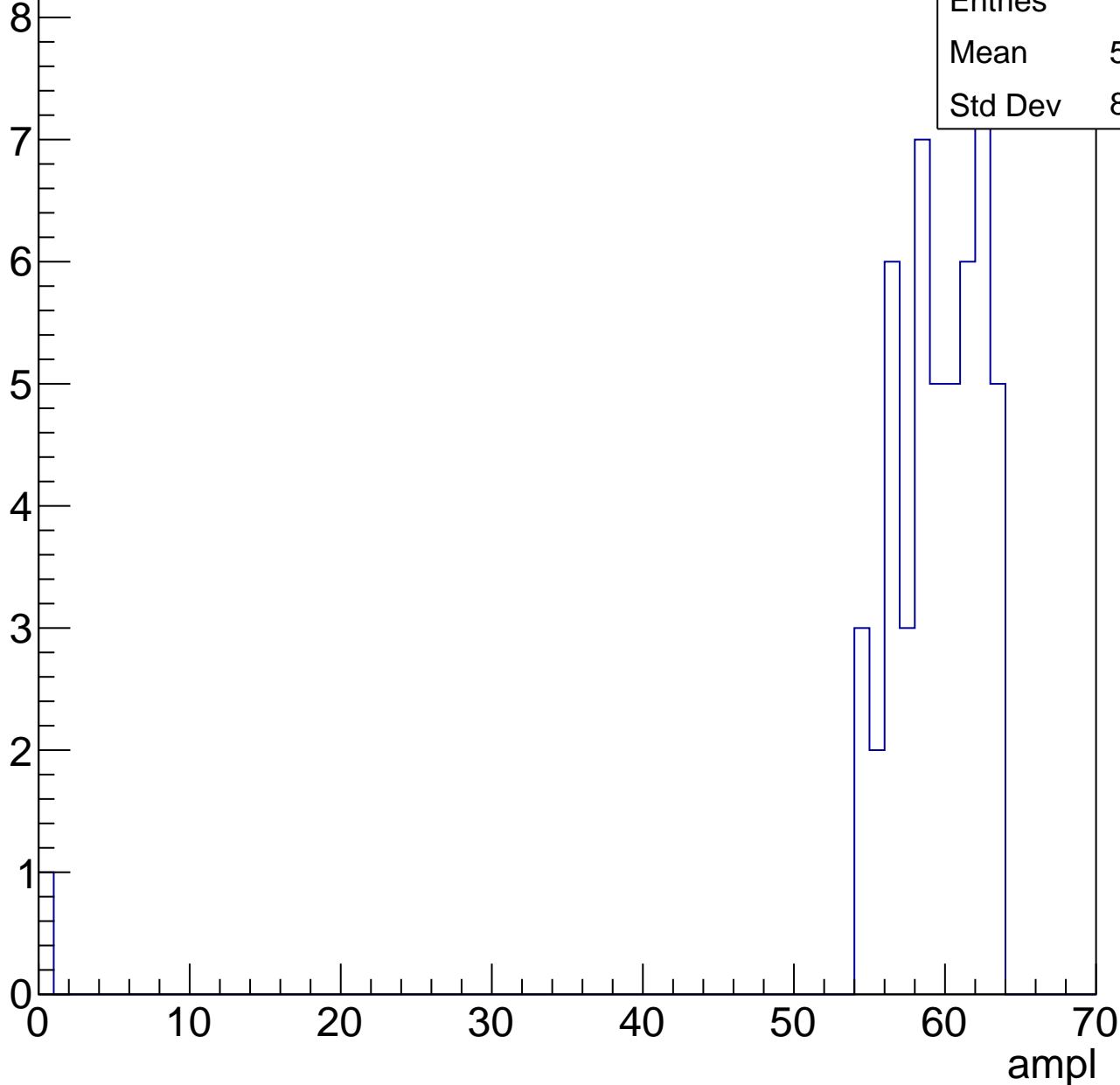


B1L101S, U1-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.98
Std Dev	8.619

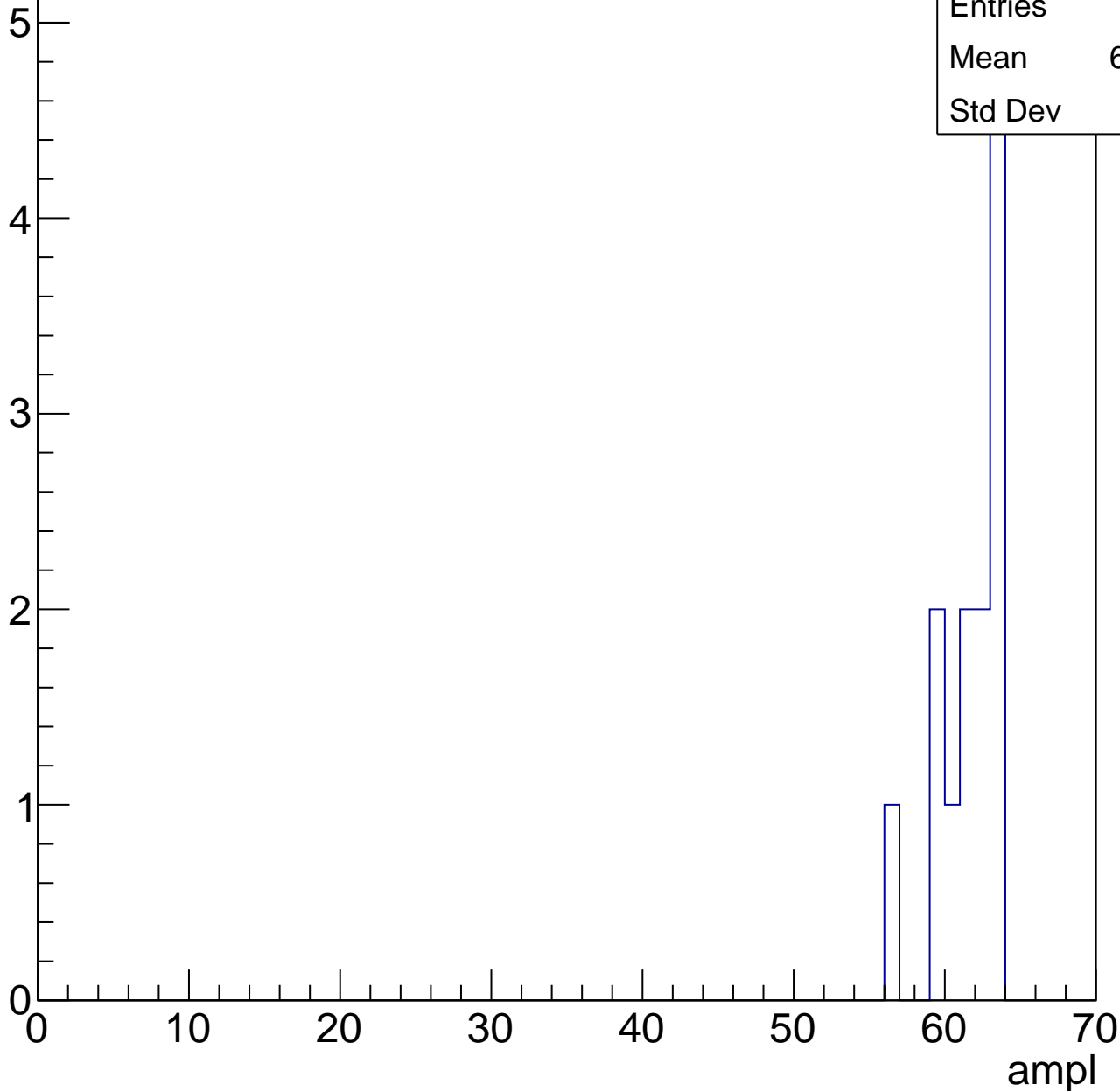


B1L101S, U1-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.15
Std Dev	2.07



B1L101S, U1-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch40, adc0

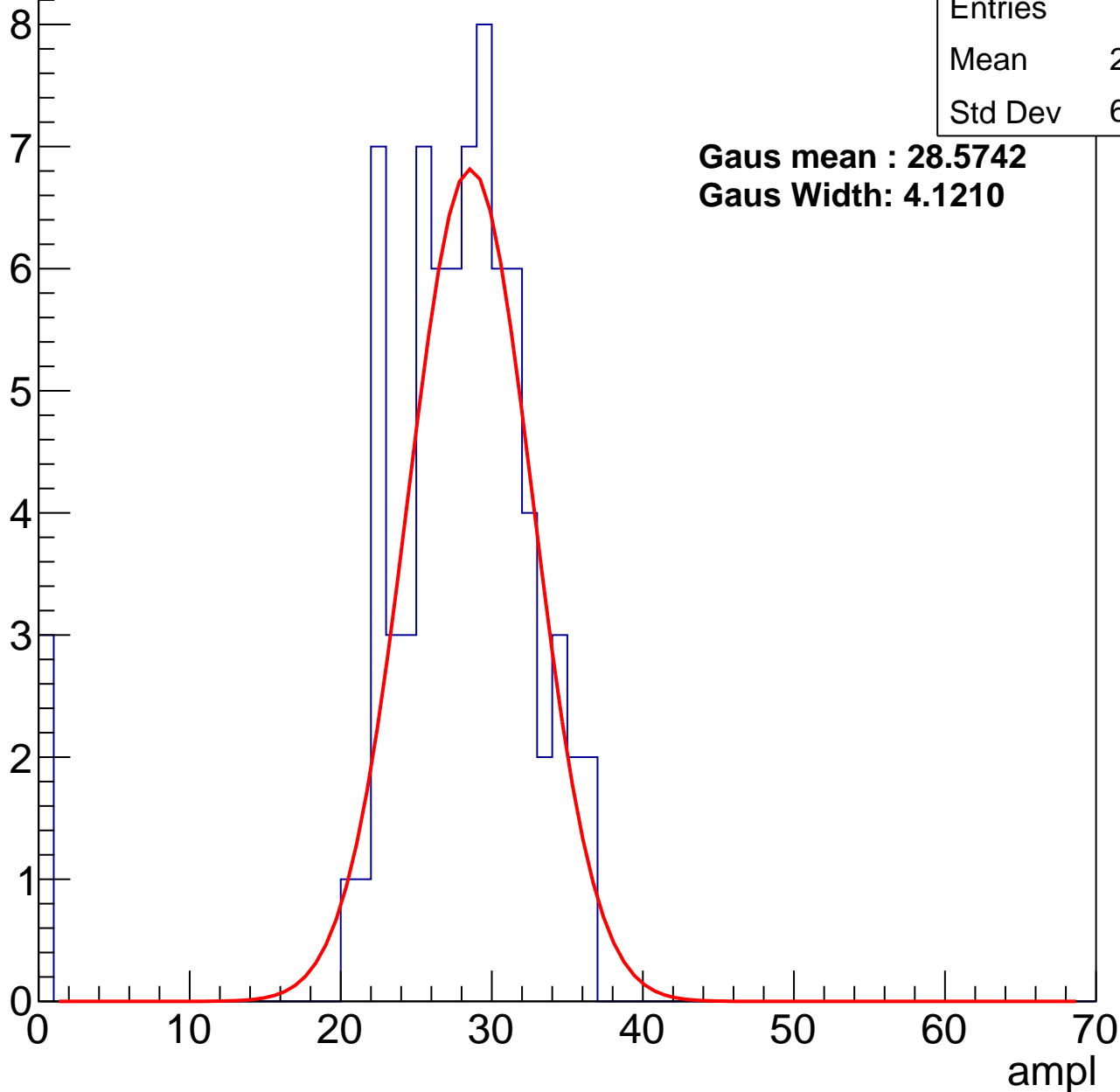
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	26.77
Std Dev	6.596

Gaus mean : 28.5742

Gaus Width: 4.1210



B1L101S, U1-ch40, adc1

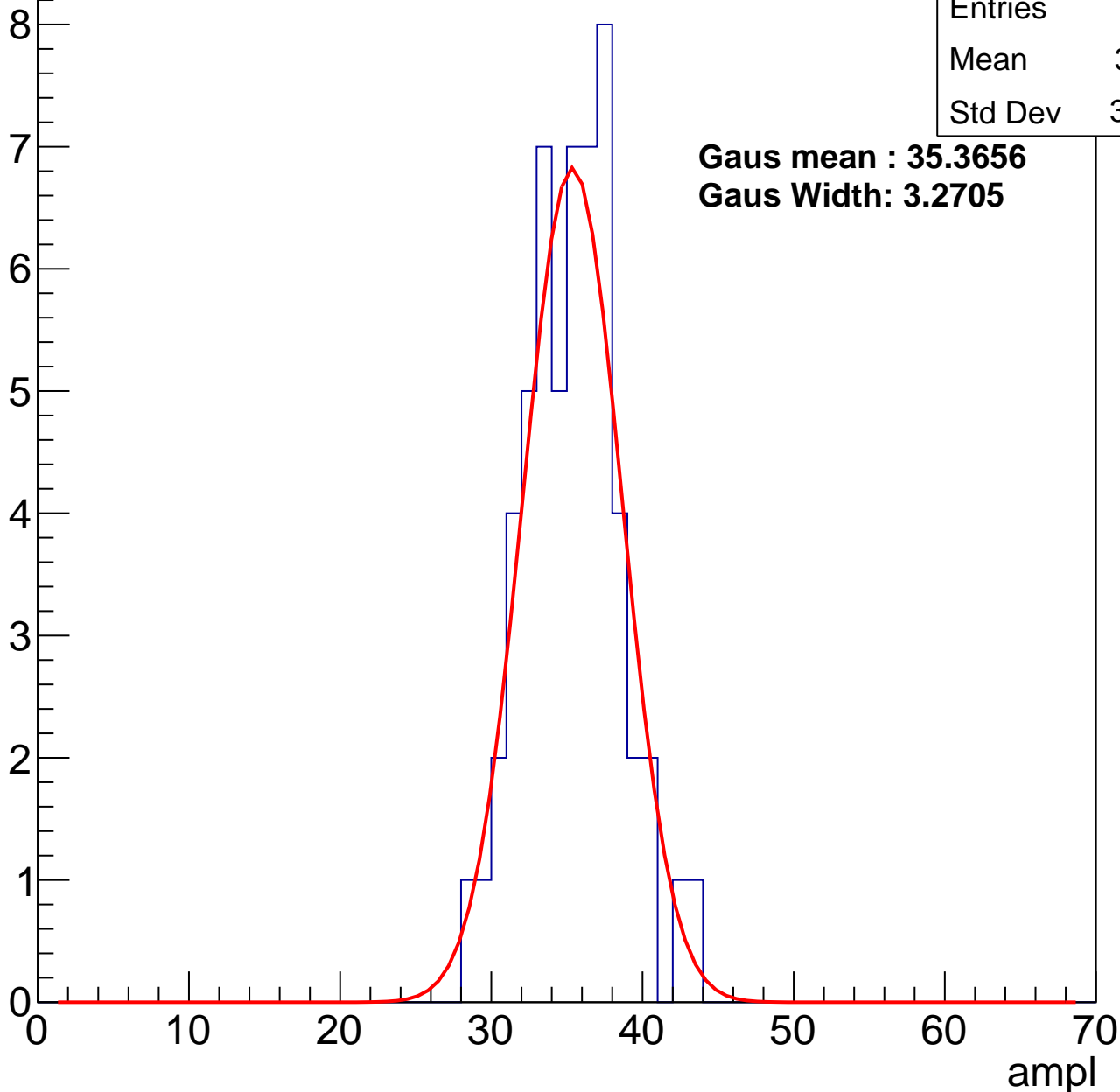
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	34.91
Std Dev	3.097

Gaus mean : 35.3656

Gaus Width: 3.2705



B1L101S, U1-ch40, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	41.72
Std Dev	4.17

Gaus mean : 42.4915

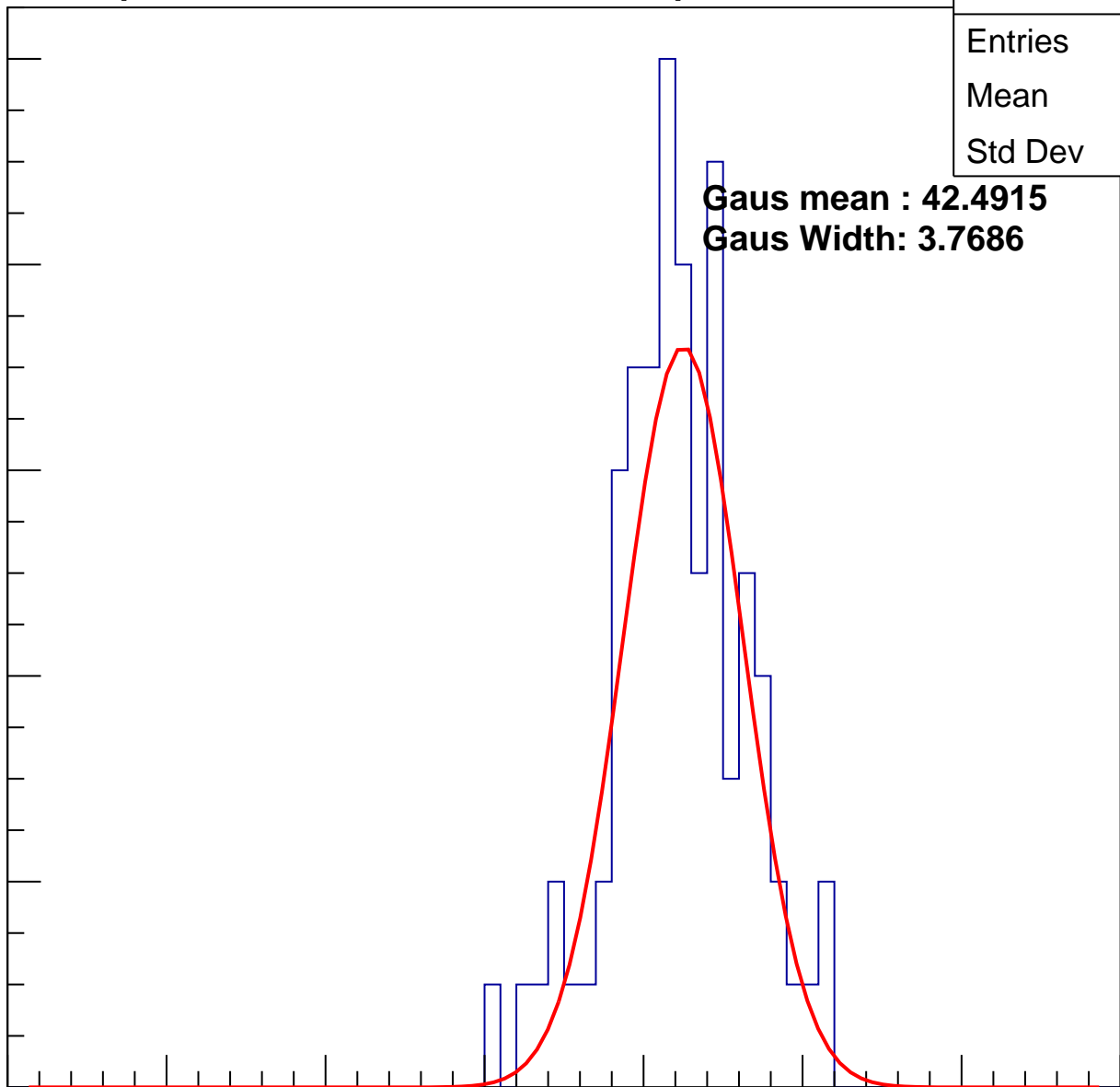
Gaus Width: 3.7686

Entry

10
8
6
4
2
0

ampl

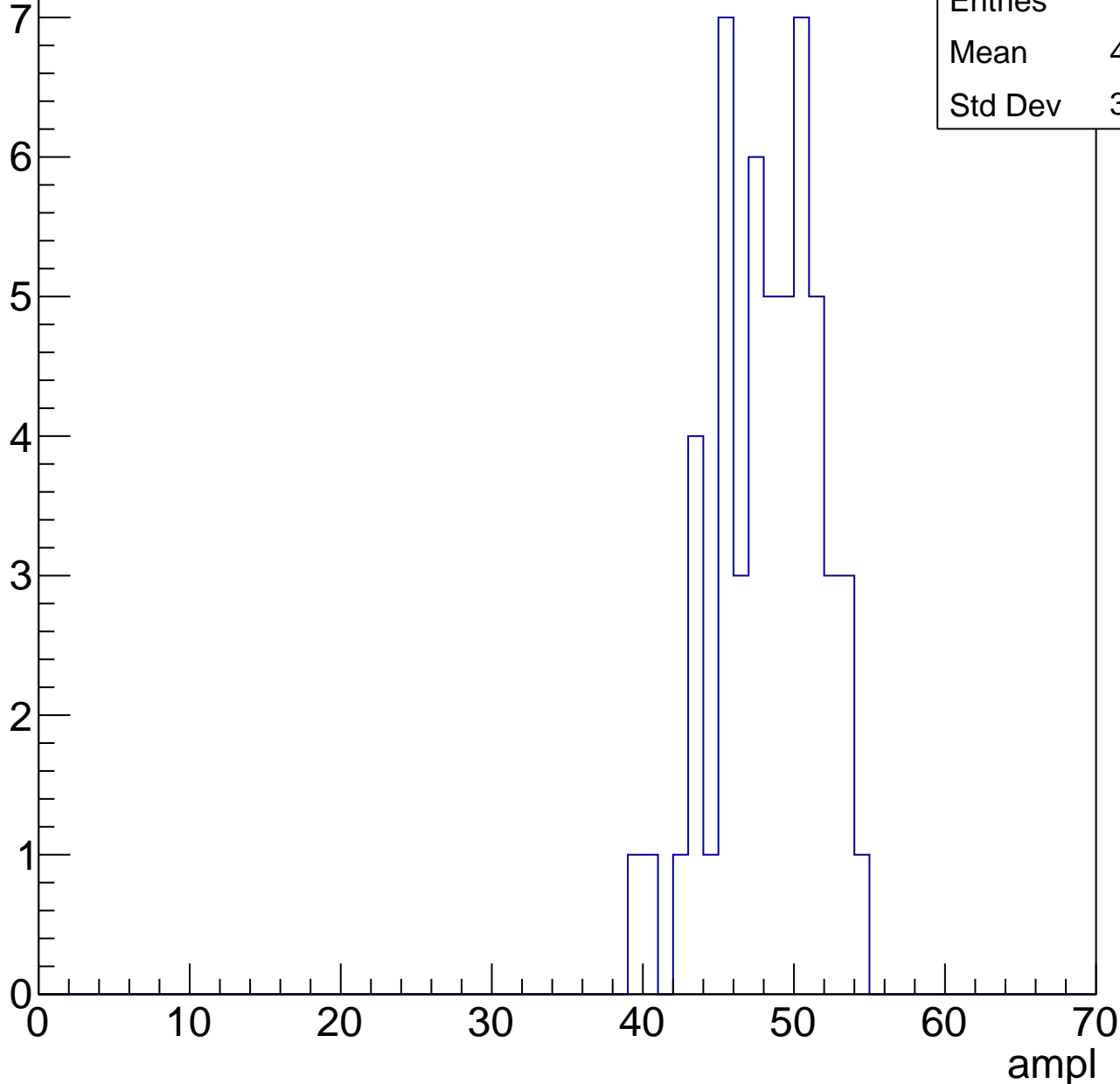
0 10 20 30 40 50 60 70



B1L101S, U1-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

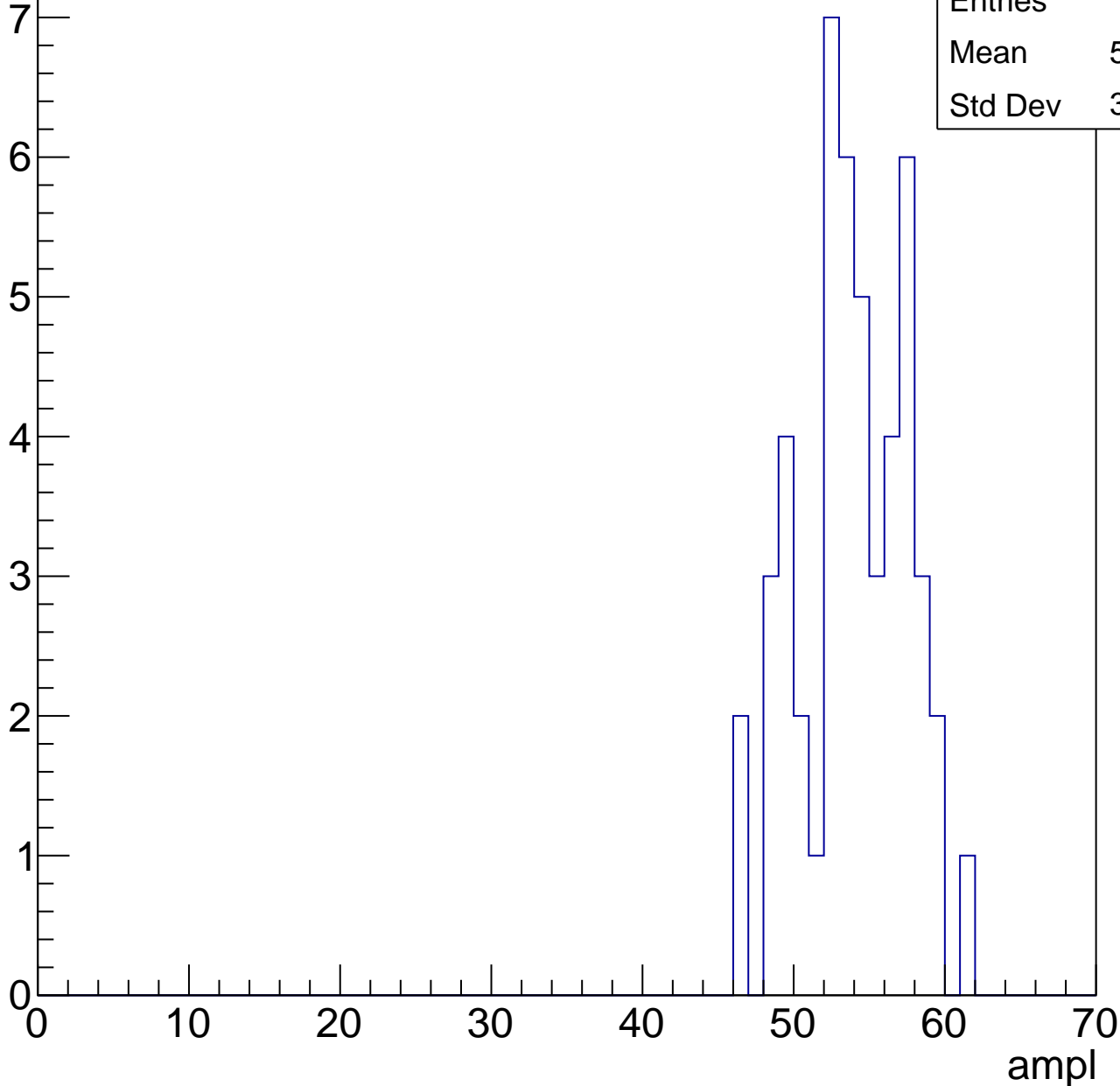


B1L101S, U1-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	53.45
Std Dev	3.546

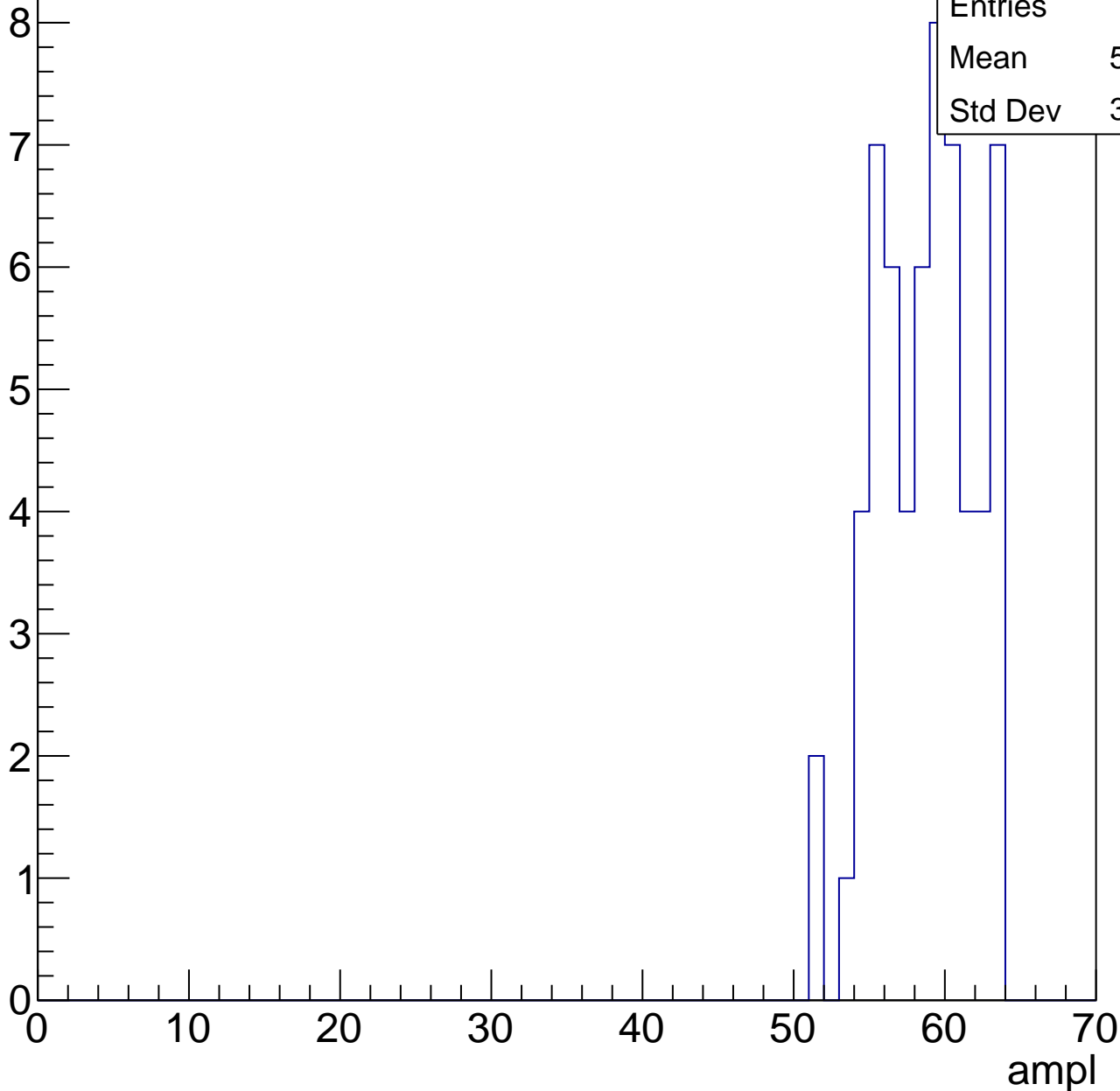


B1L101S, U1-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	58.22
Std Dev	3.126

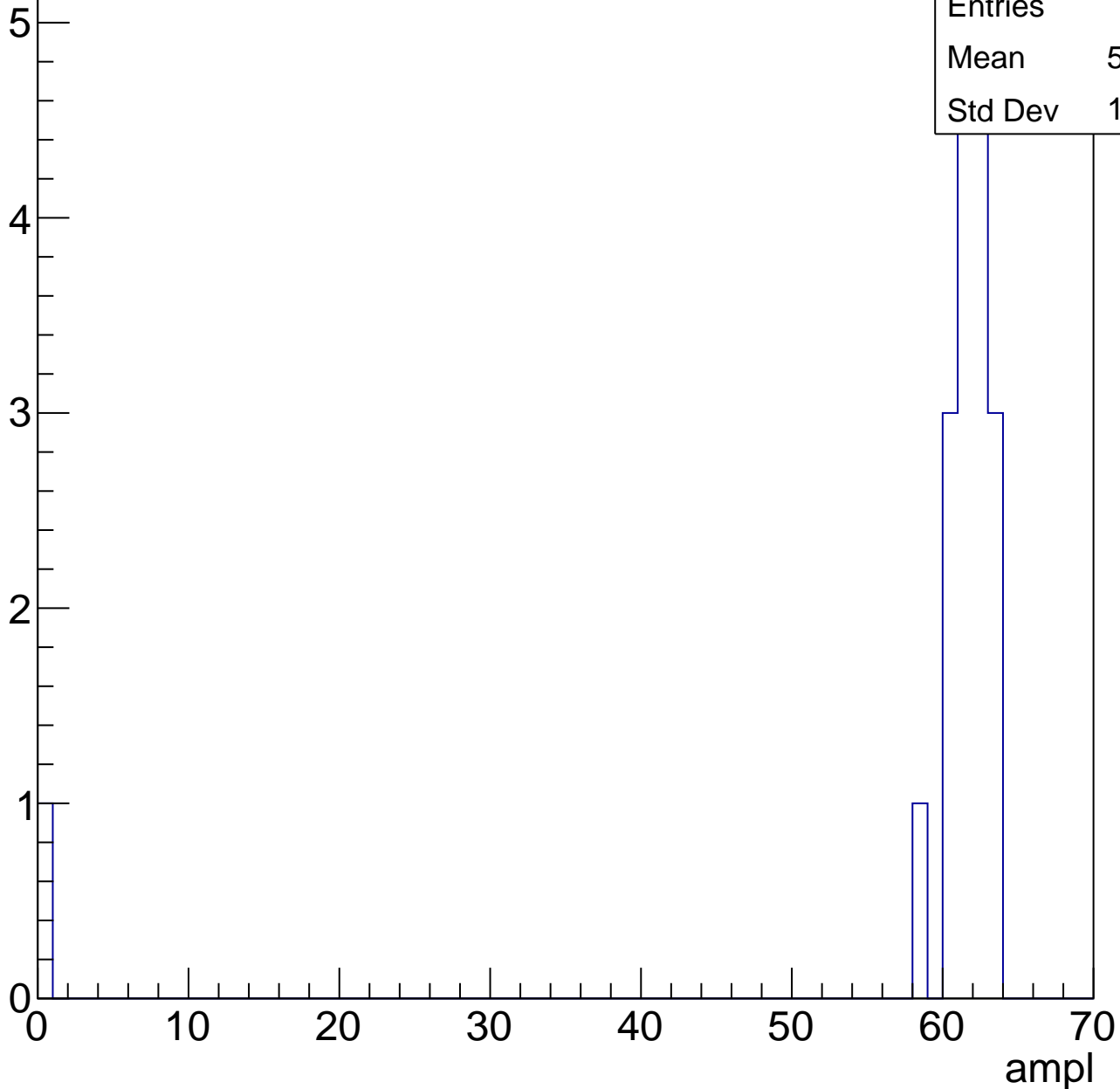


B1L101S, U1-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	57.89
Std Dev	14.09



B1L101S, U1-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch41, adc0

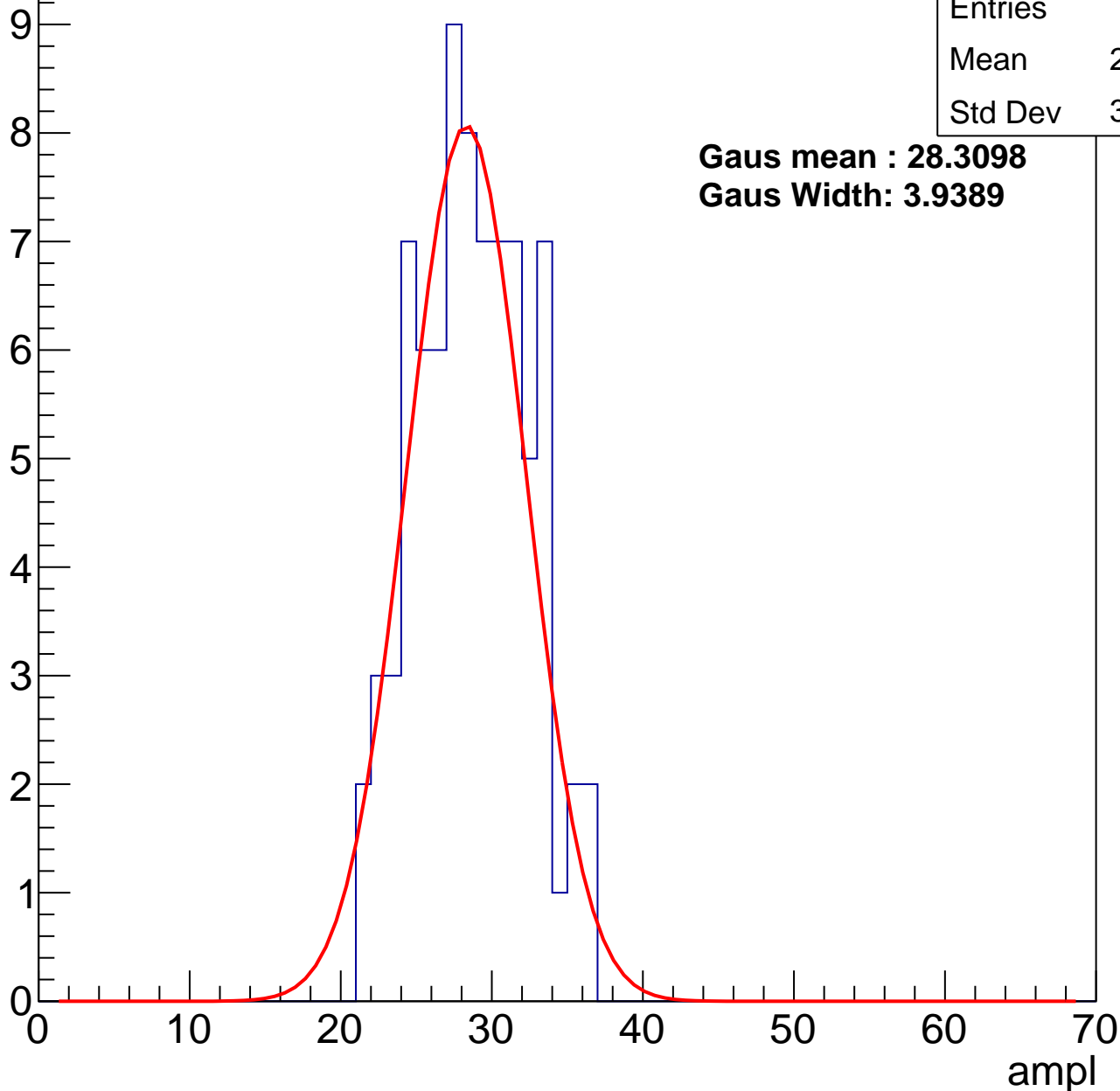
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	28.23
Std Dev	3.647

Gaus mean : 28.3098

Gaus Width: 3.9389



B1L101S, U1-ch41, adc1

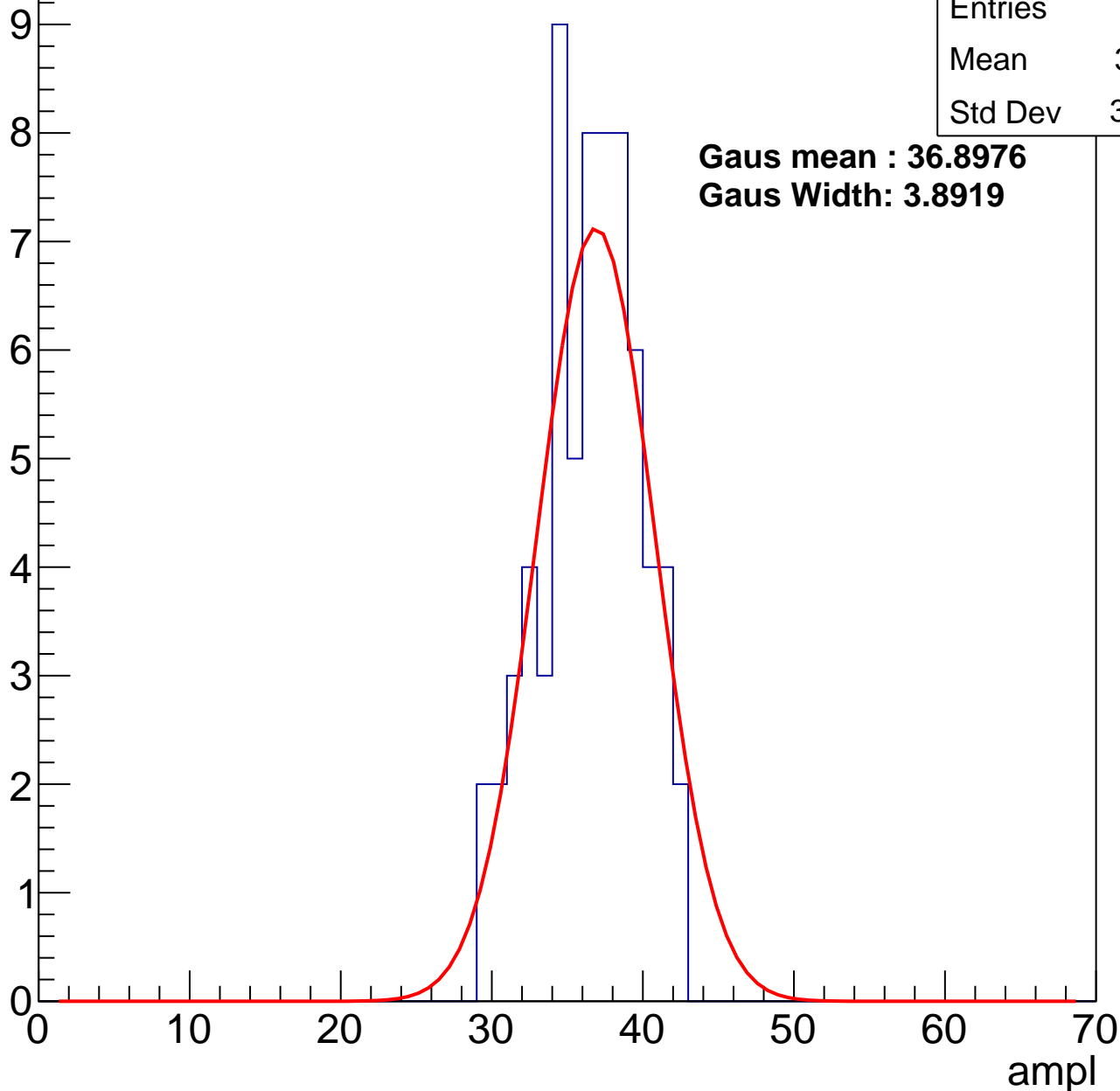
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.01
Std Dev	3.224

Gaus mean : 36.8976

Gaus Width: 3.8919



B1L101S, U1-ch41, adc2

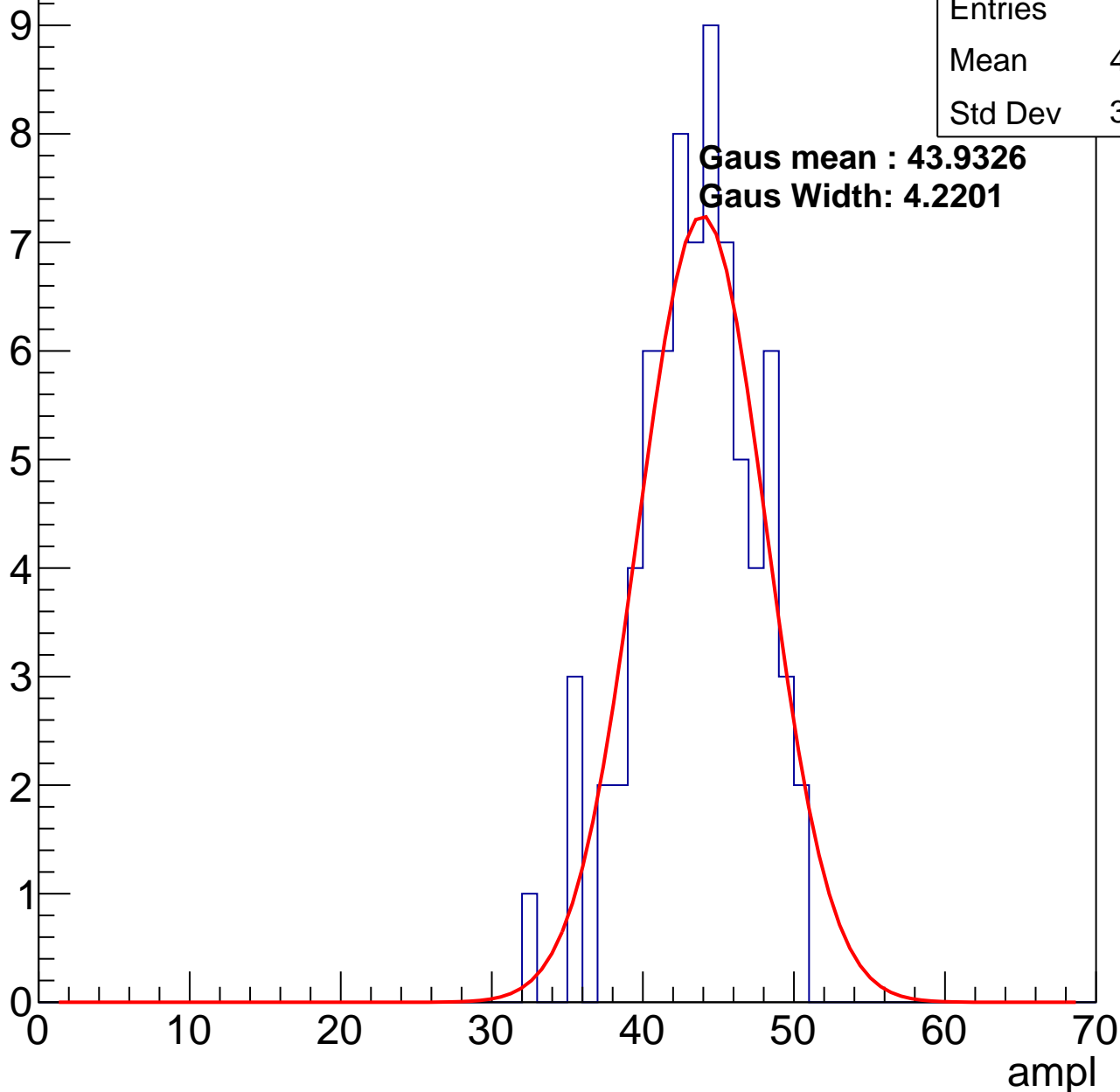
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.07
Std Dev	3.806

Gaus mean : 43.9326

Gaus Width: 4.2201

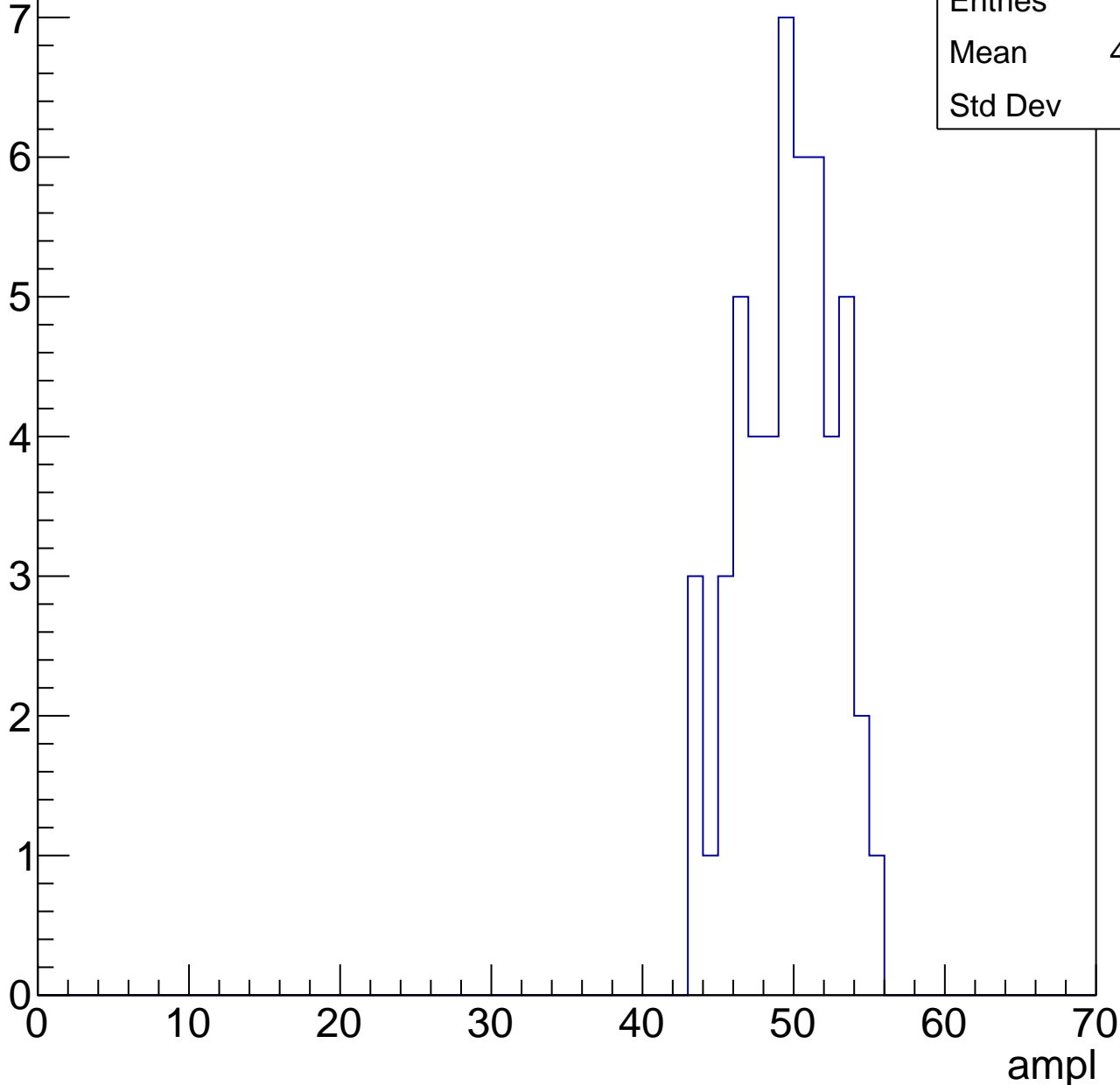


B1L101S, U1-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	49.08
Std Dev	3.06

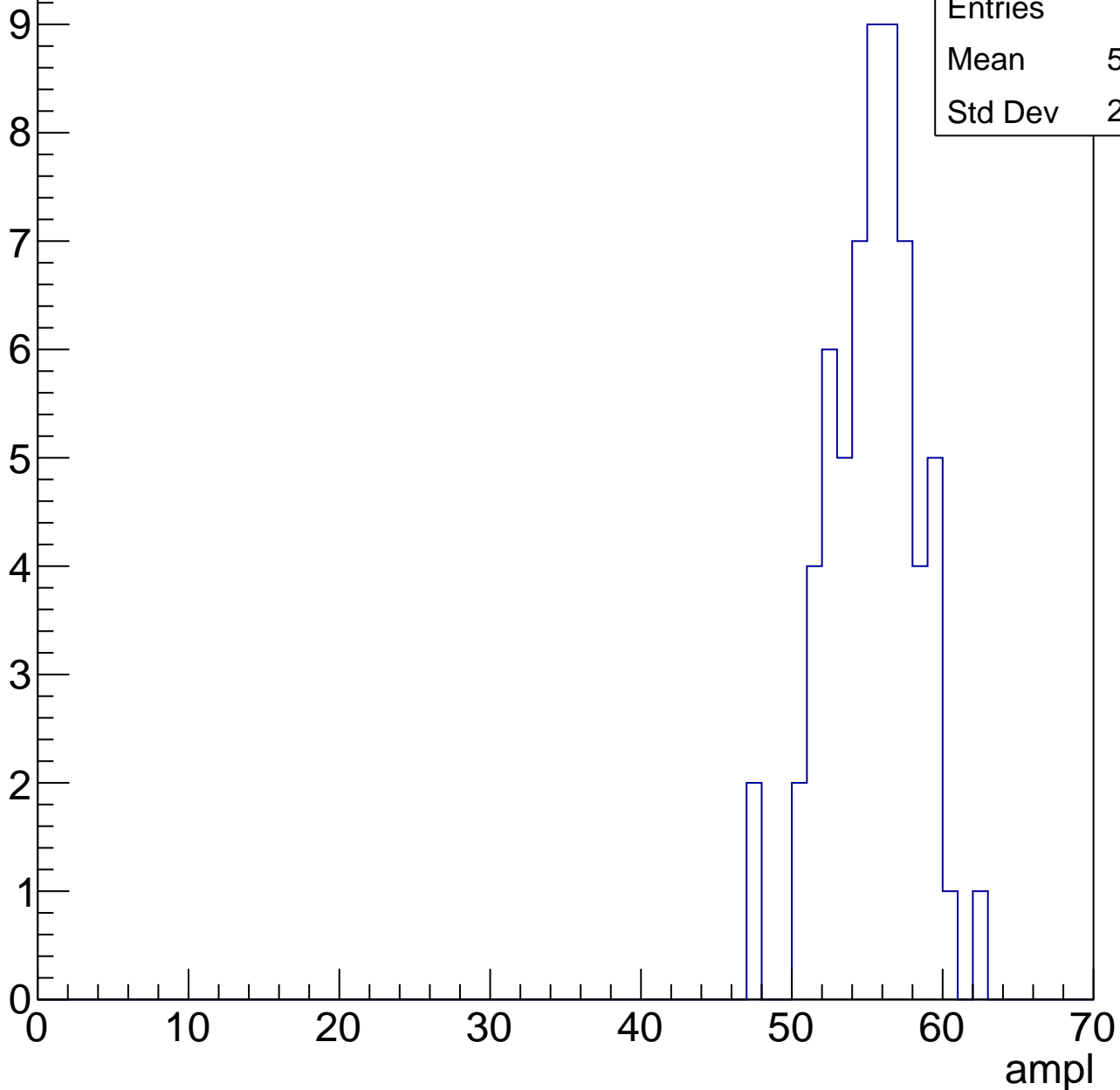


B1L101S, U1-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.84
Std Dev	2.974

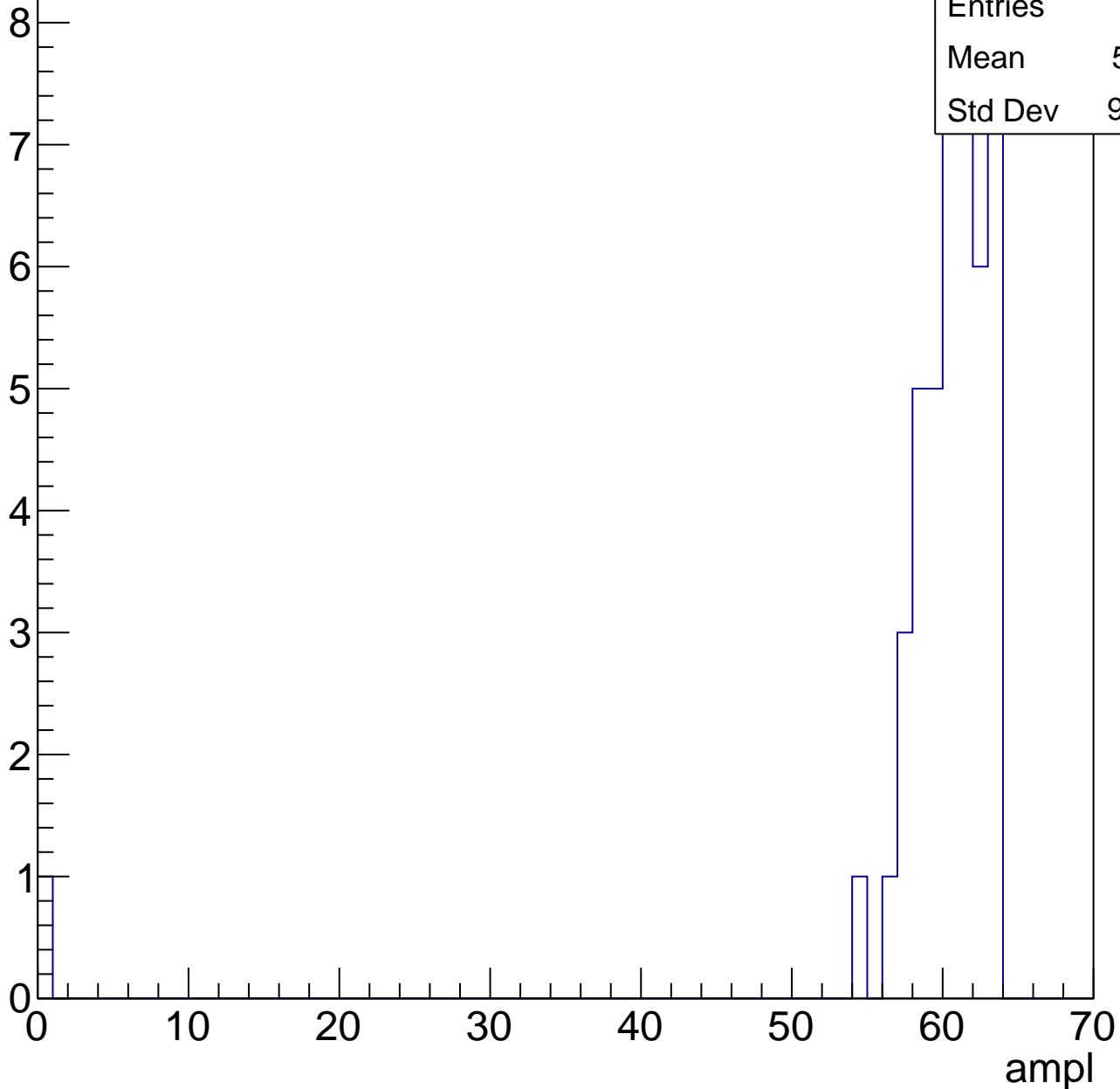


B1L101S, U1-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

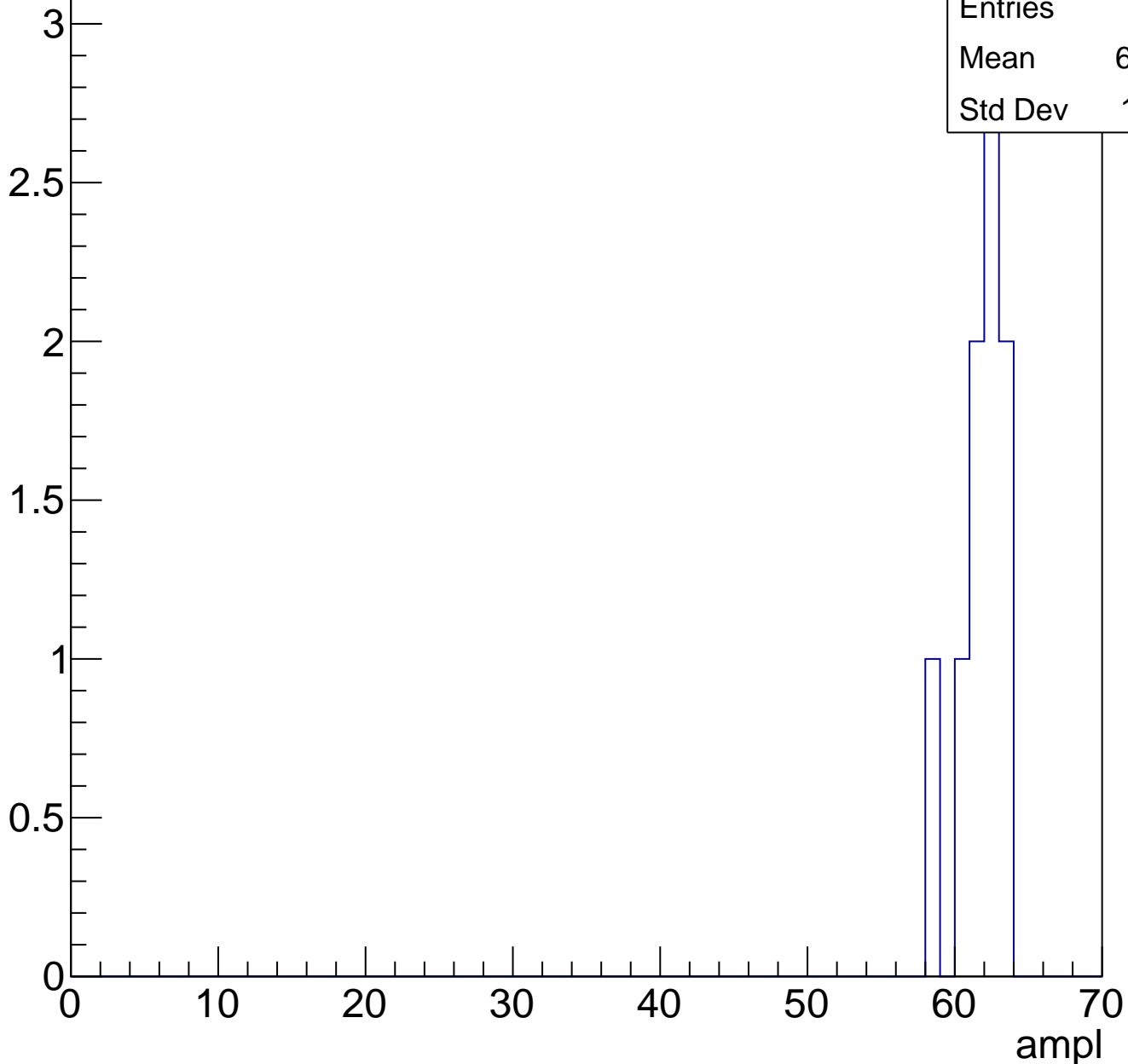
Entries	46
Mean	58.91
Std Dev	9.033



B1L101S, U1-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

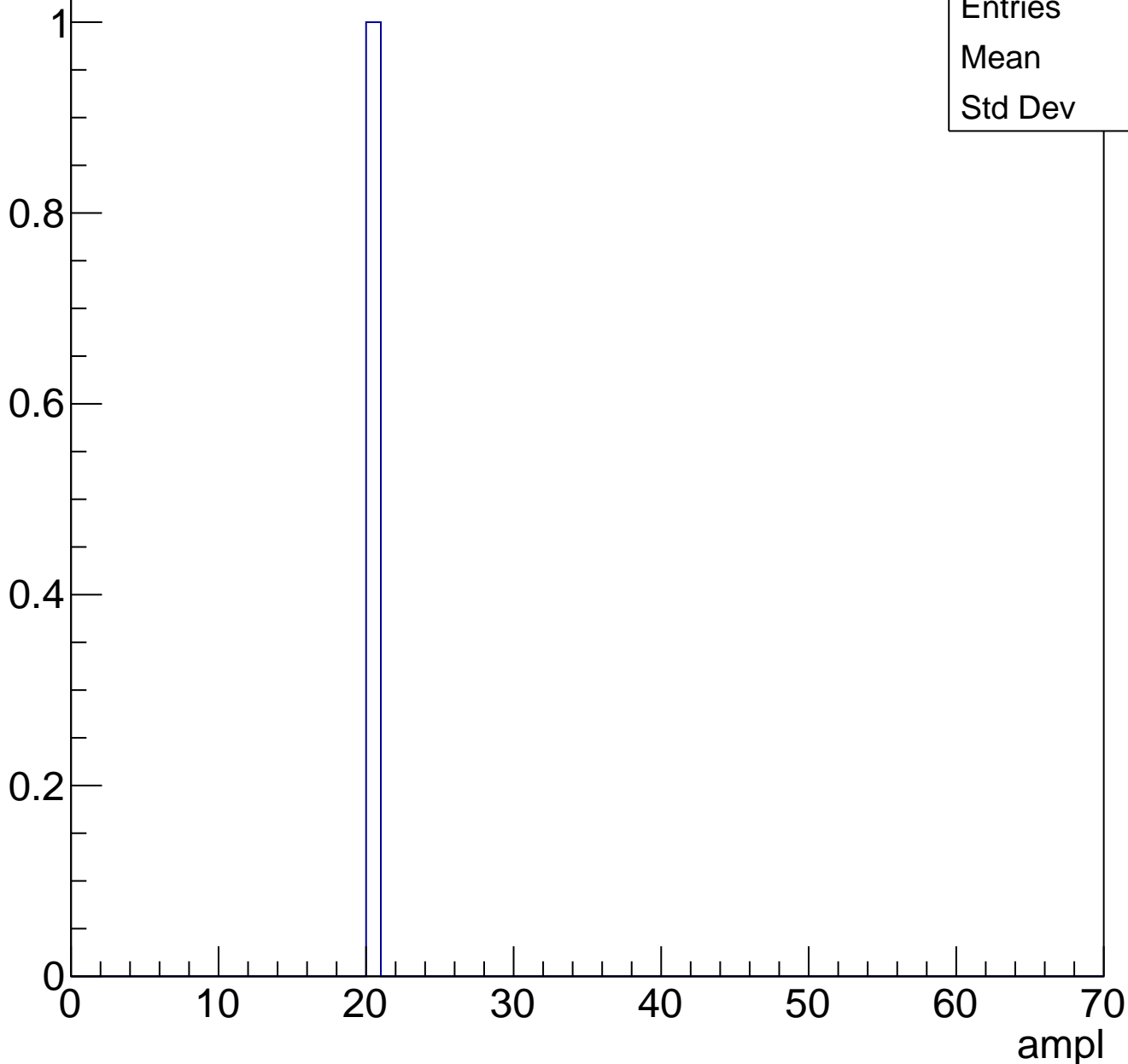


Entries	9
Mean	61.33
Std Dev	1.491

B1L101S, U1-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L101S, U1-ch42, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	27.96
Std Dev	4.995

Gaus mean : 29.4735

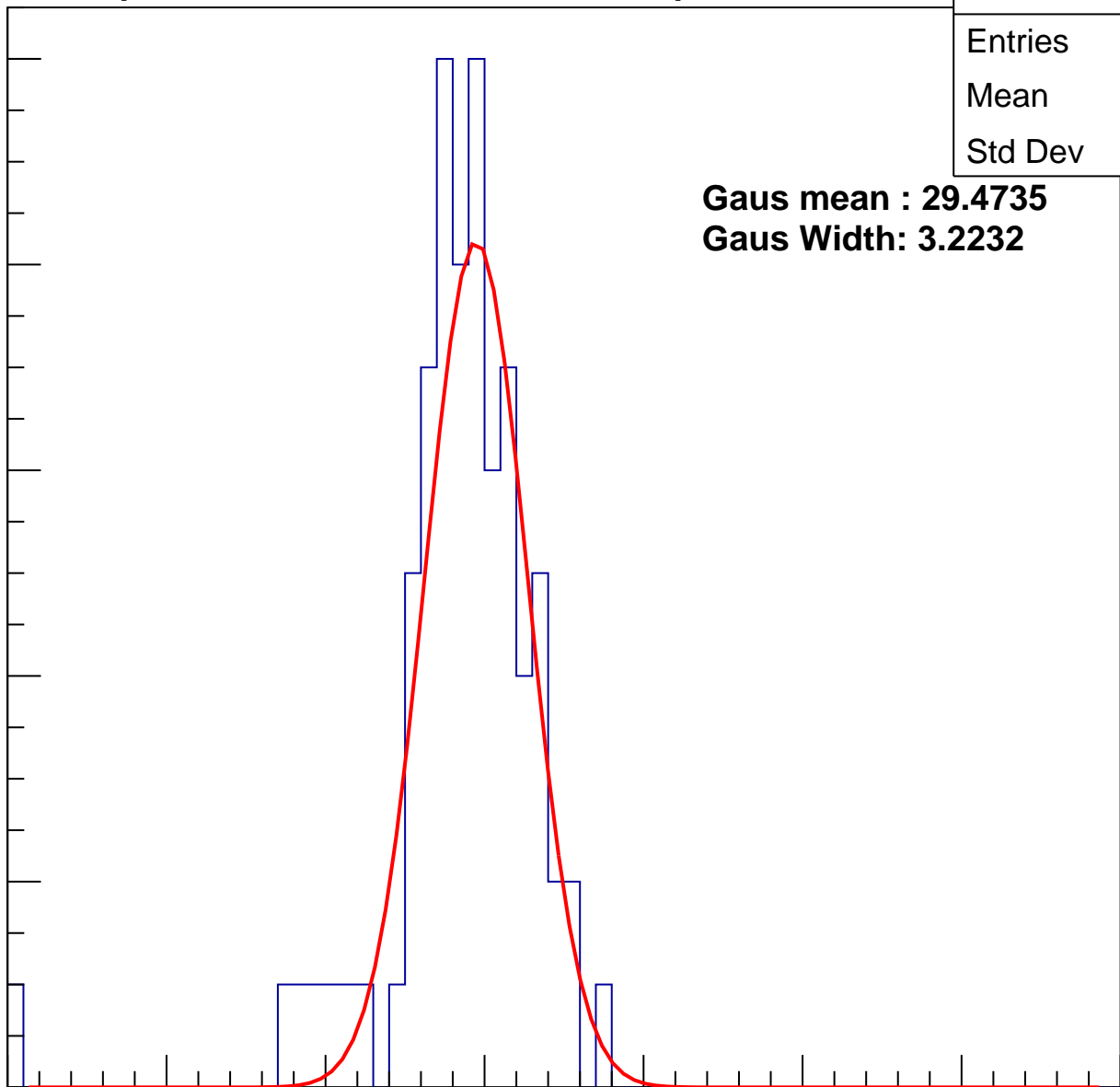
Gaus Width: 3.2232

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch42, adc1

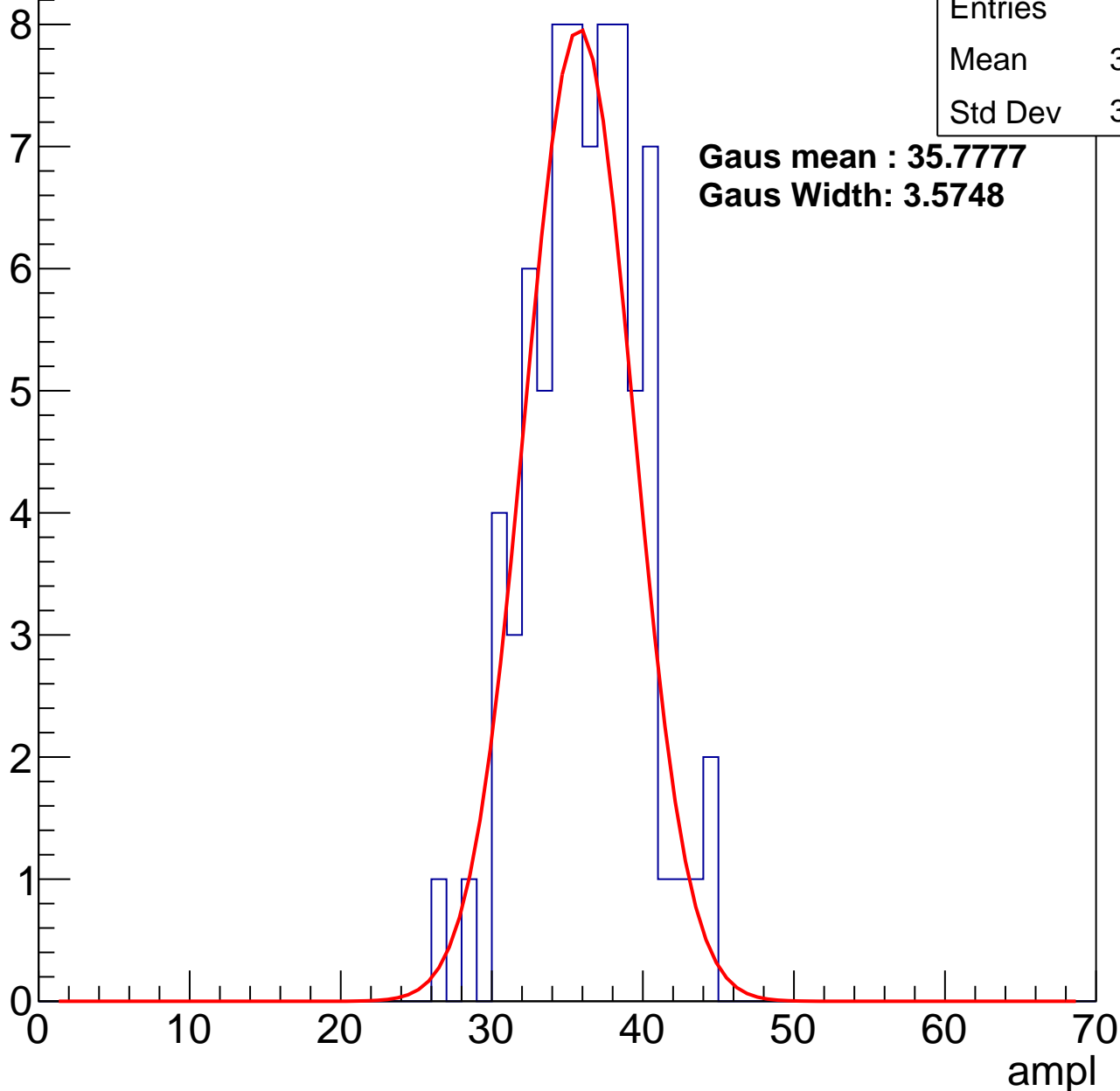
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.75
Std Dev	3.617

Gaus mean : 35.7777

Gaus Width: 3.5748



B1L101S, U1-ch42, adc2

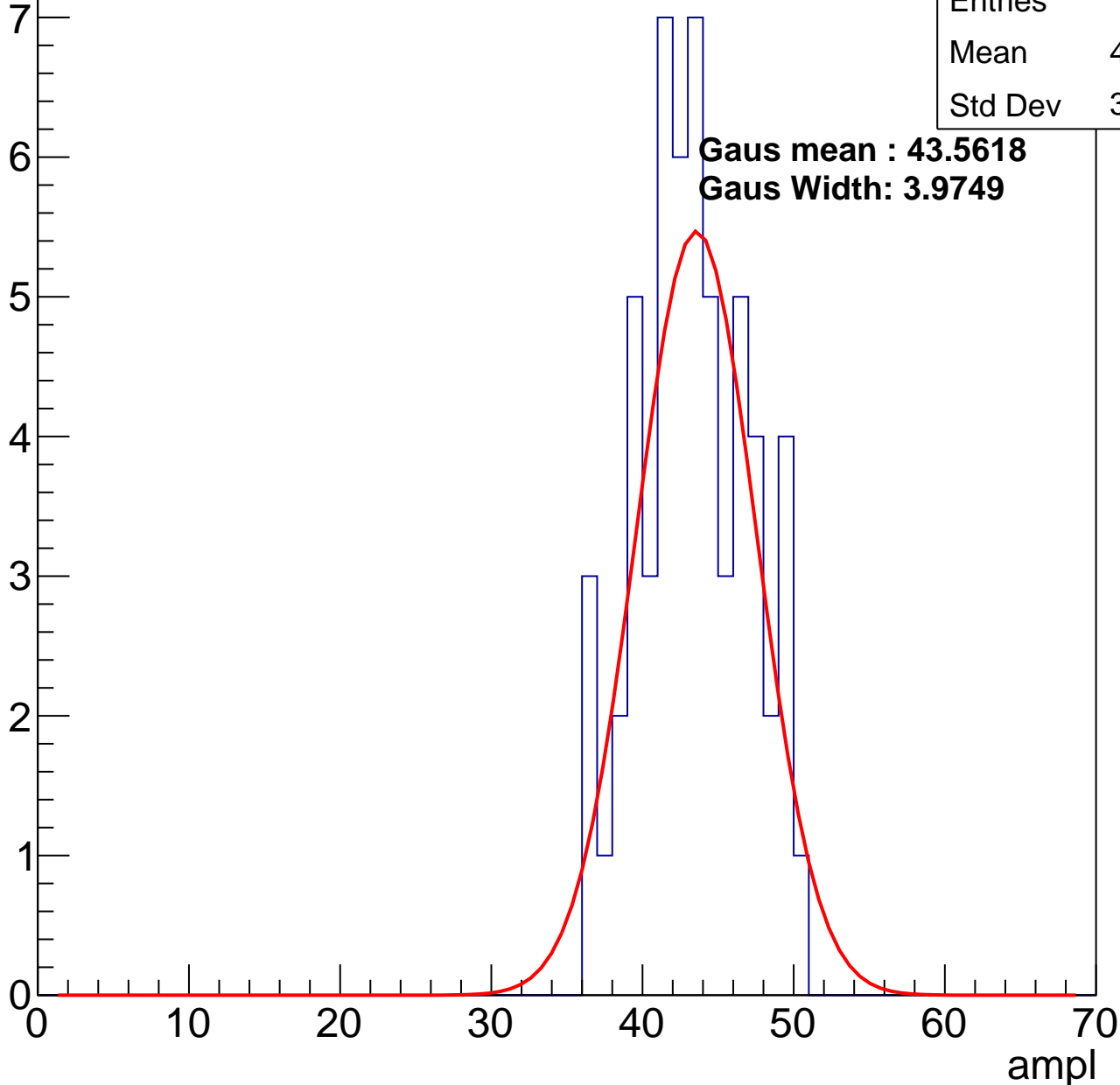
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.95
Std Dev	3.579

Gaus mean : 43.5618

Gaus Width: 3.9749

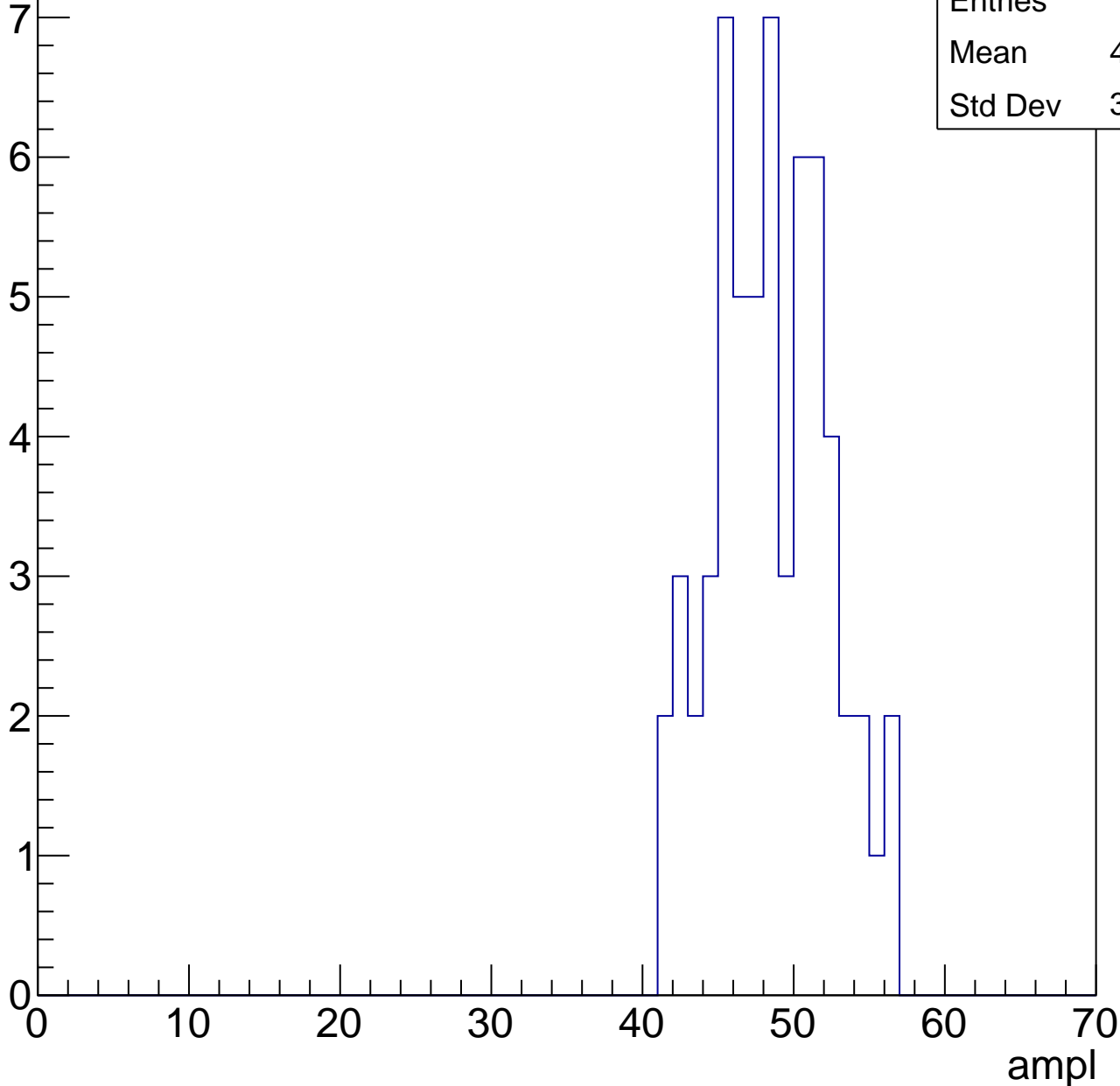


B1L101S, U1-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.07
Std Dev	3.732

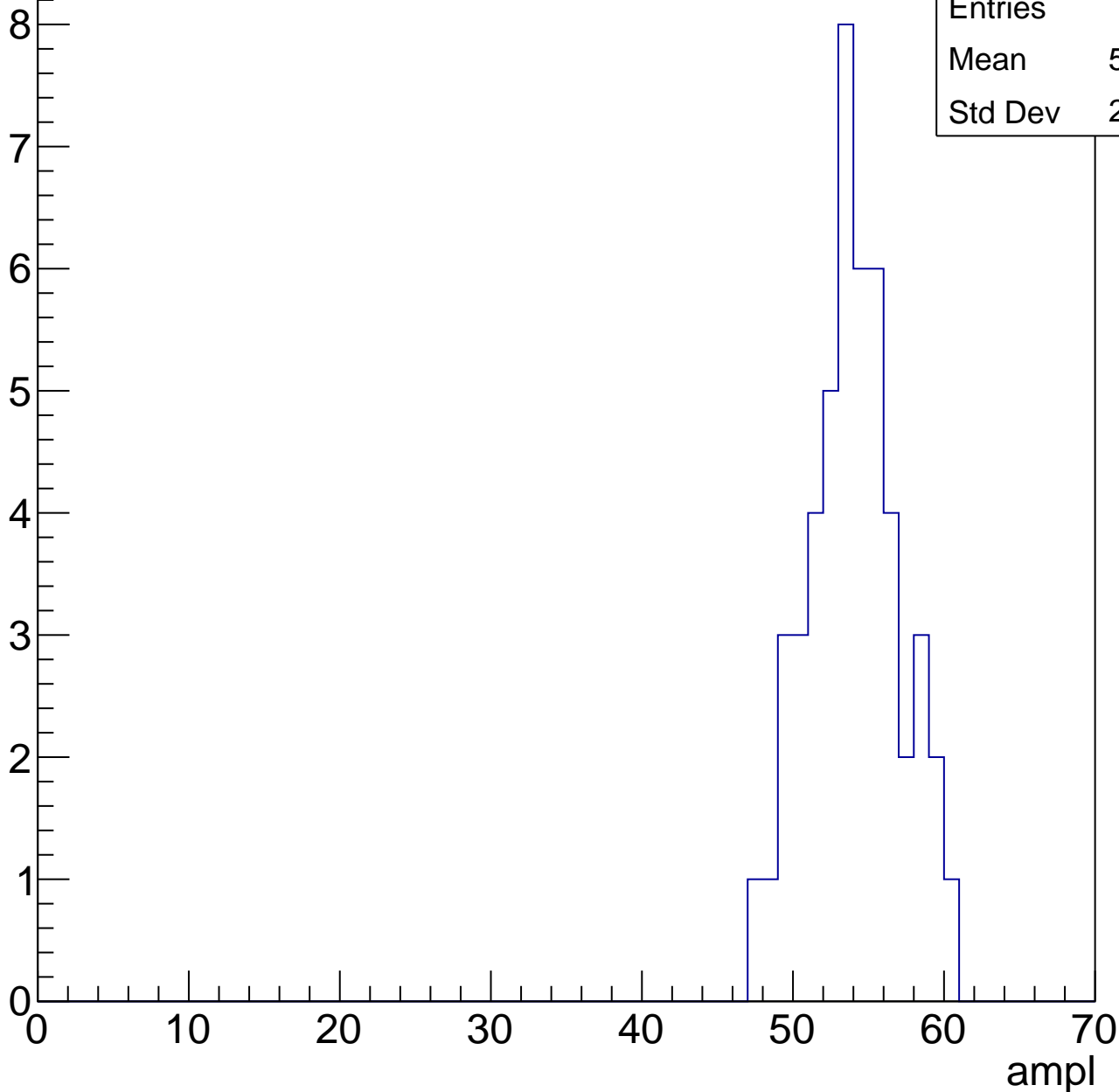


B1L101S, U1-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	53.55
Std Dev	2.983

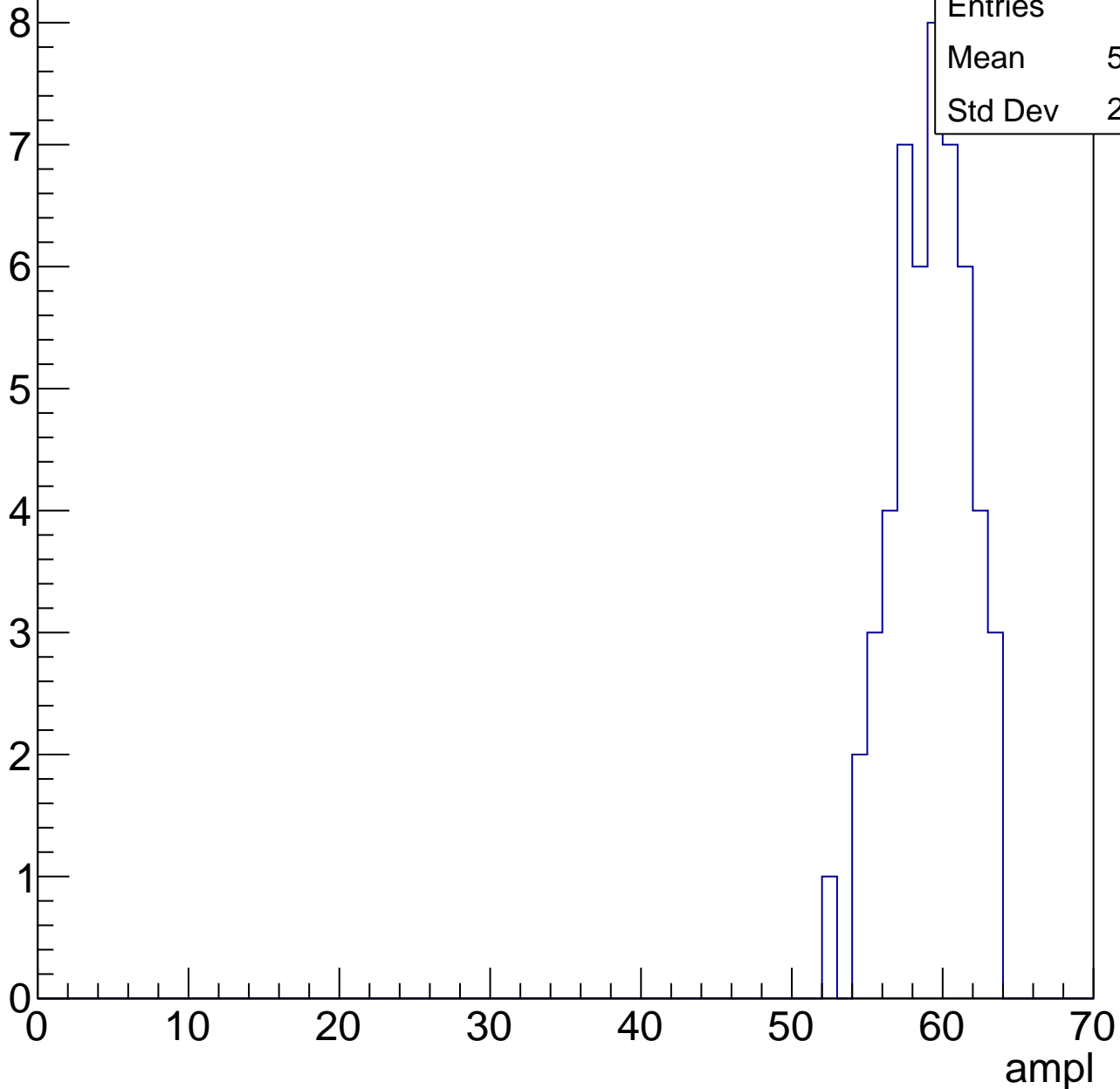


B1L101S, U1-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.65
Std Dev	2.527

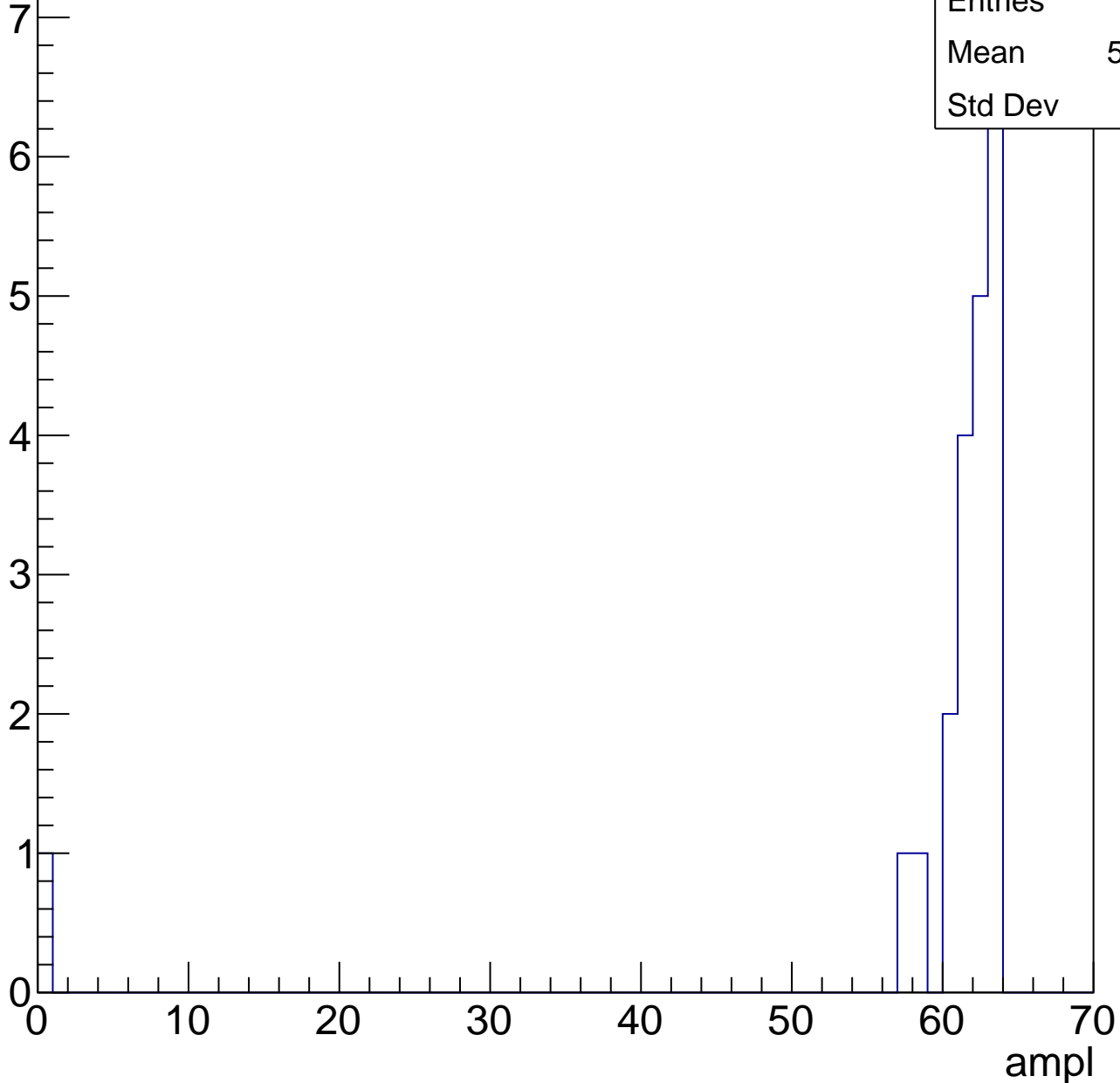


B1L101S, U1-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

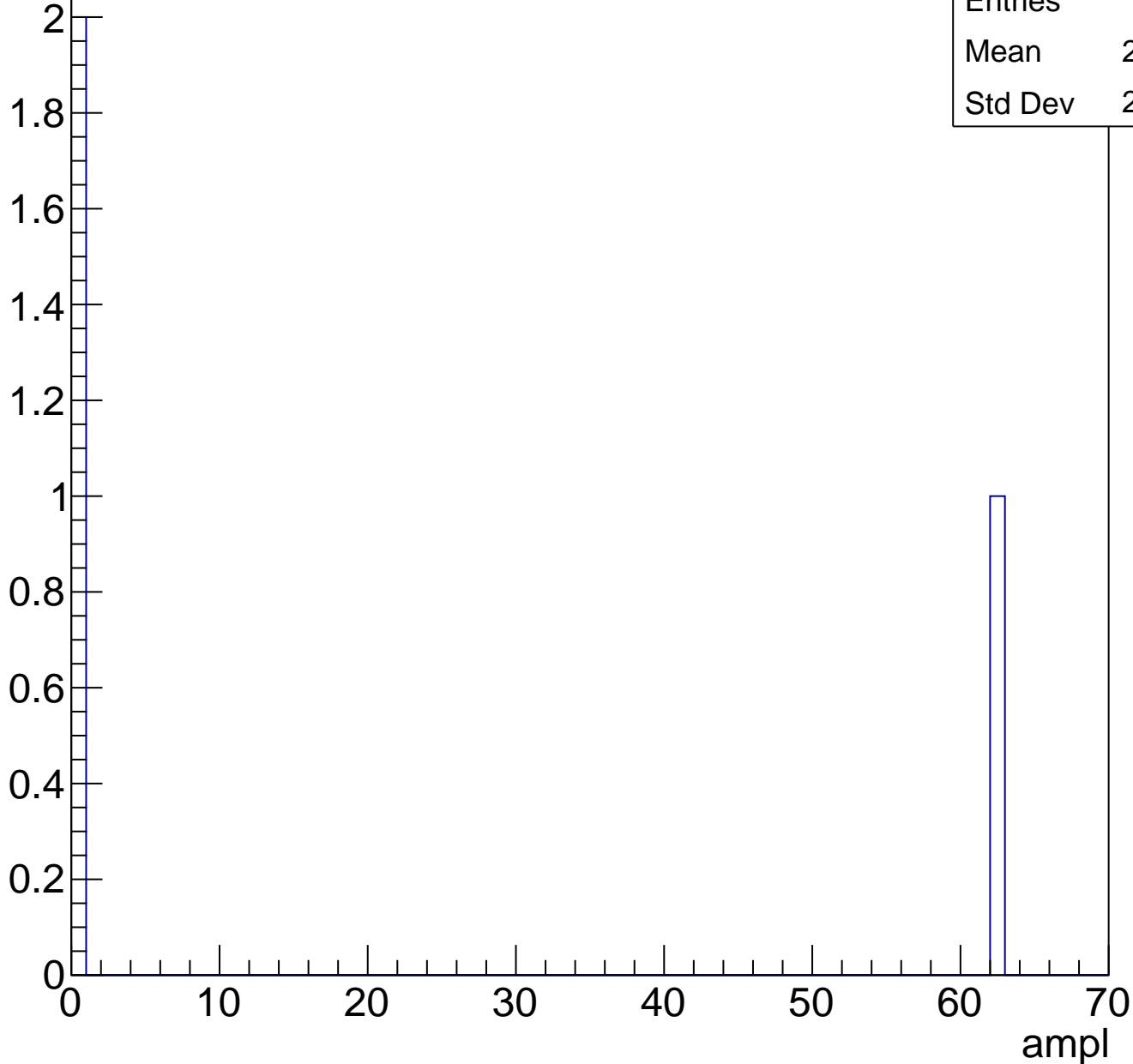
Entries	21
Mean	58.57
Std Dev	13.2



B1L101S, U1-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch43, adc0

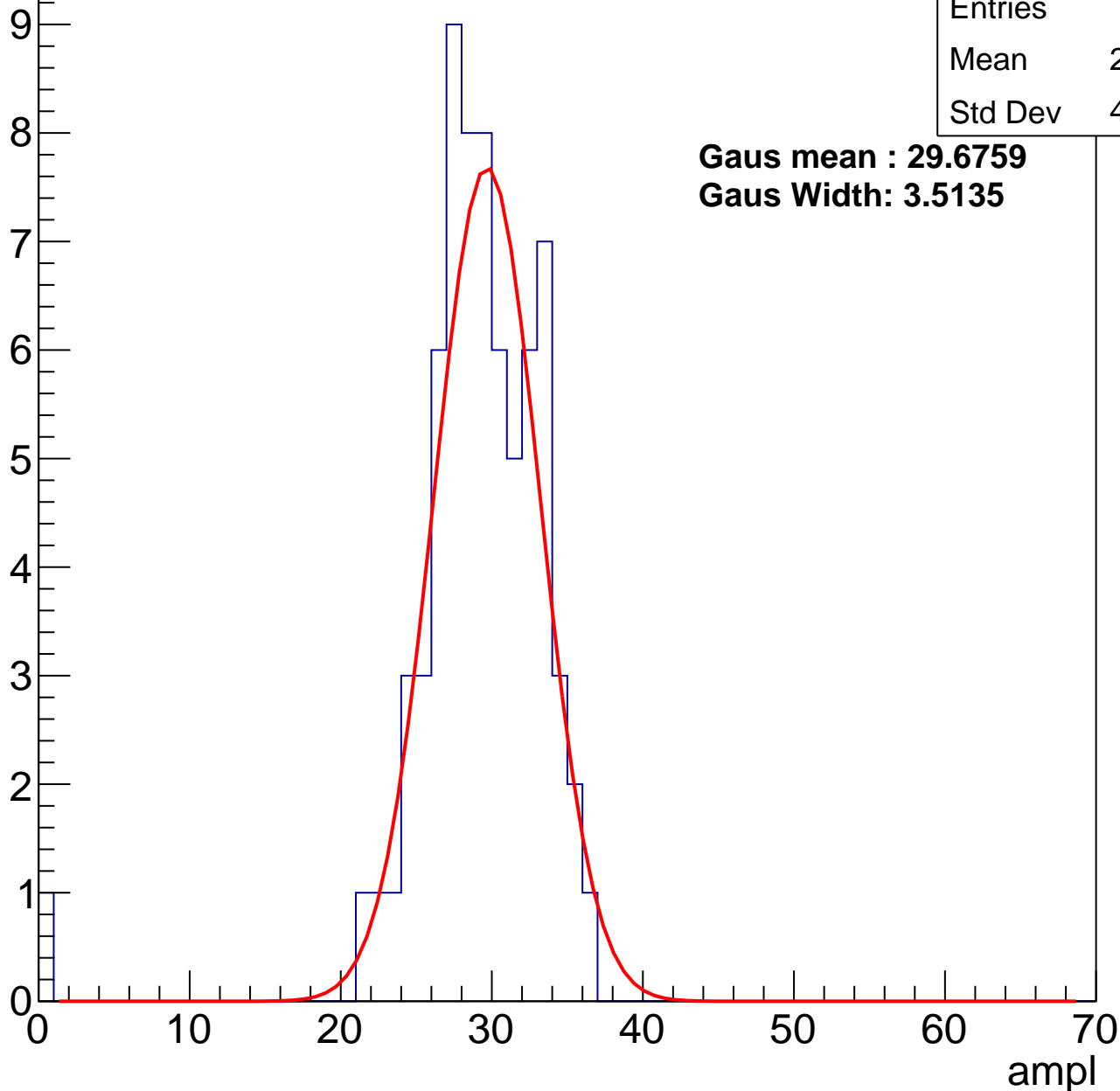
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.65
Std Dev	4.733

Gaus mean : 29.6759

Gaus Width: 3.5135



B1L101S, U1-ch43, adc1

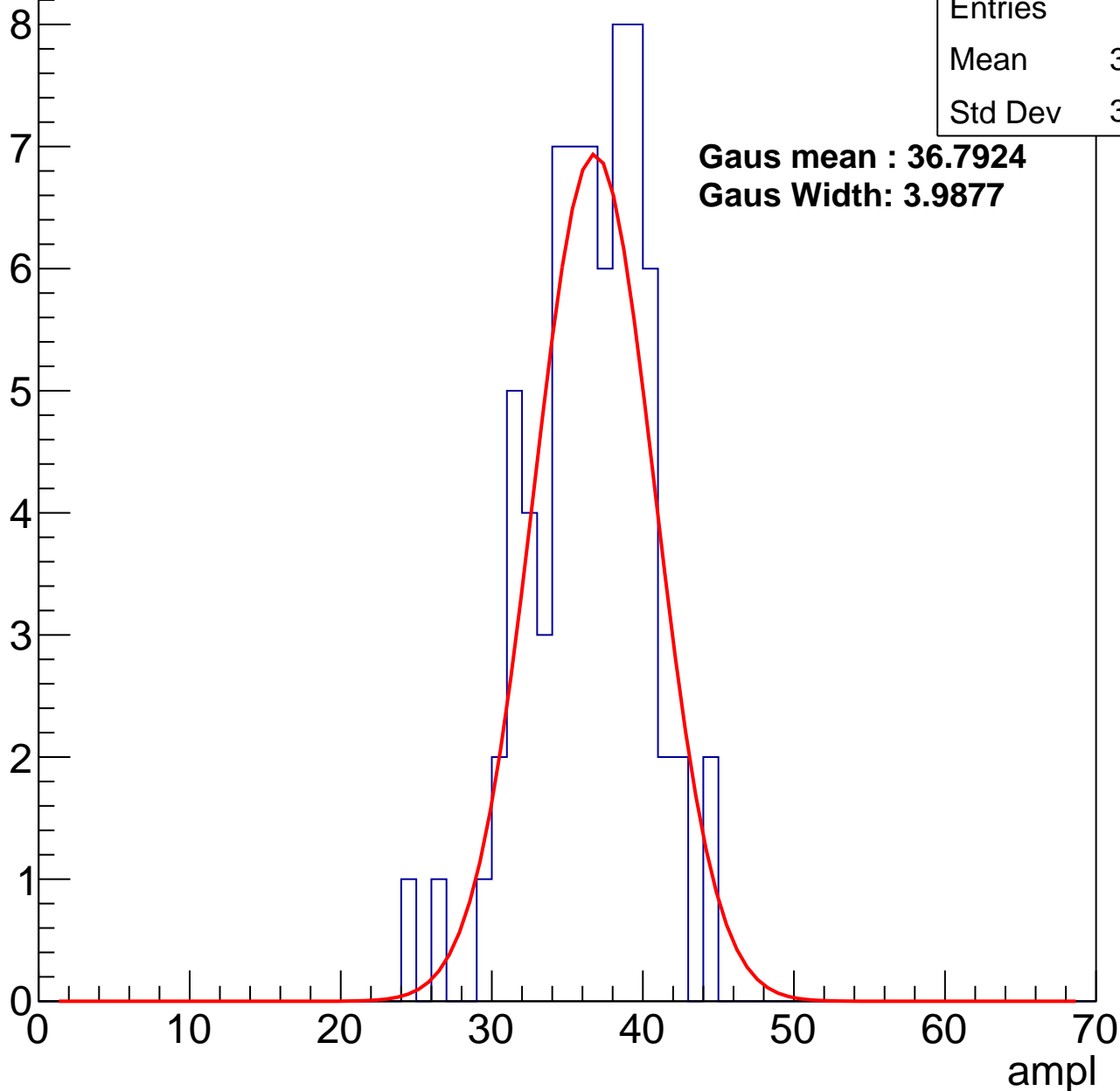
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	35.94
Std Dev	3.858

Gaus mean : 36.7924

Gaus Width: 3.9877



B1L101S, U1-ch43, adc2

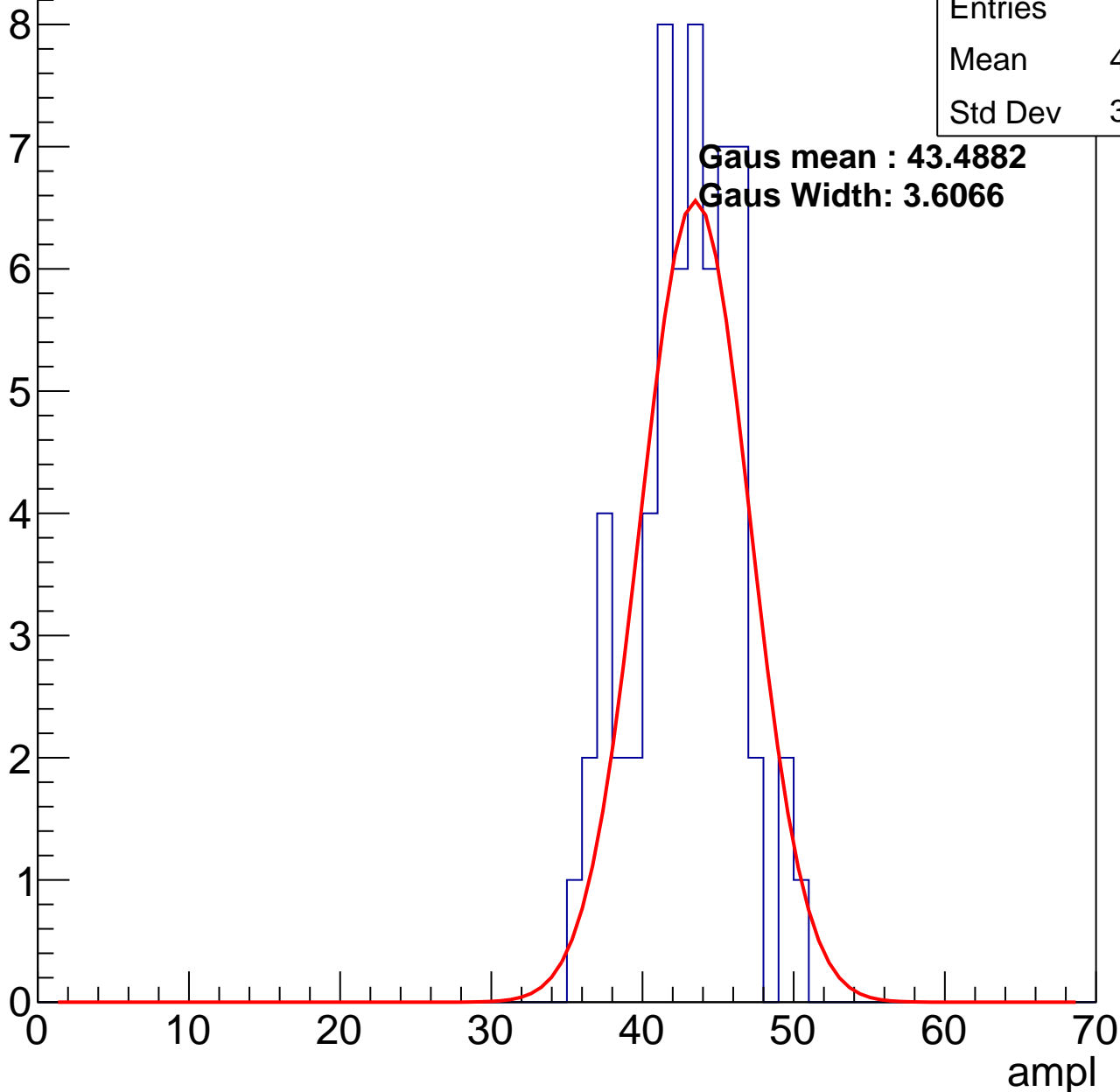
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.52
Std Dev	3.354

Gaus mean : 43.4882

Gaus Width: 3.6066

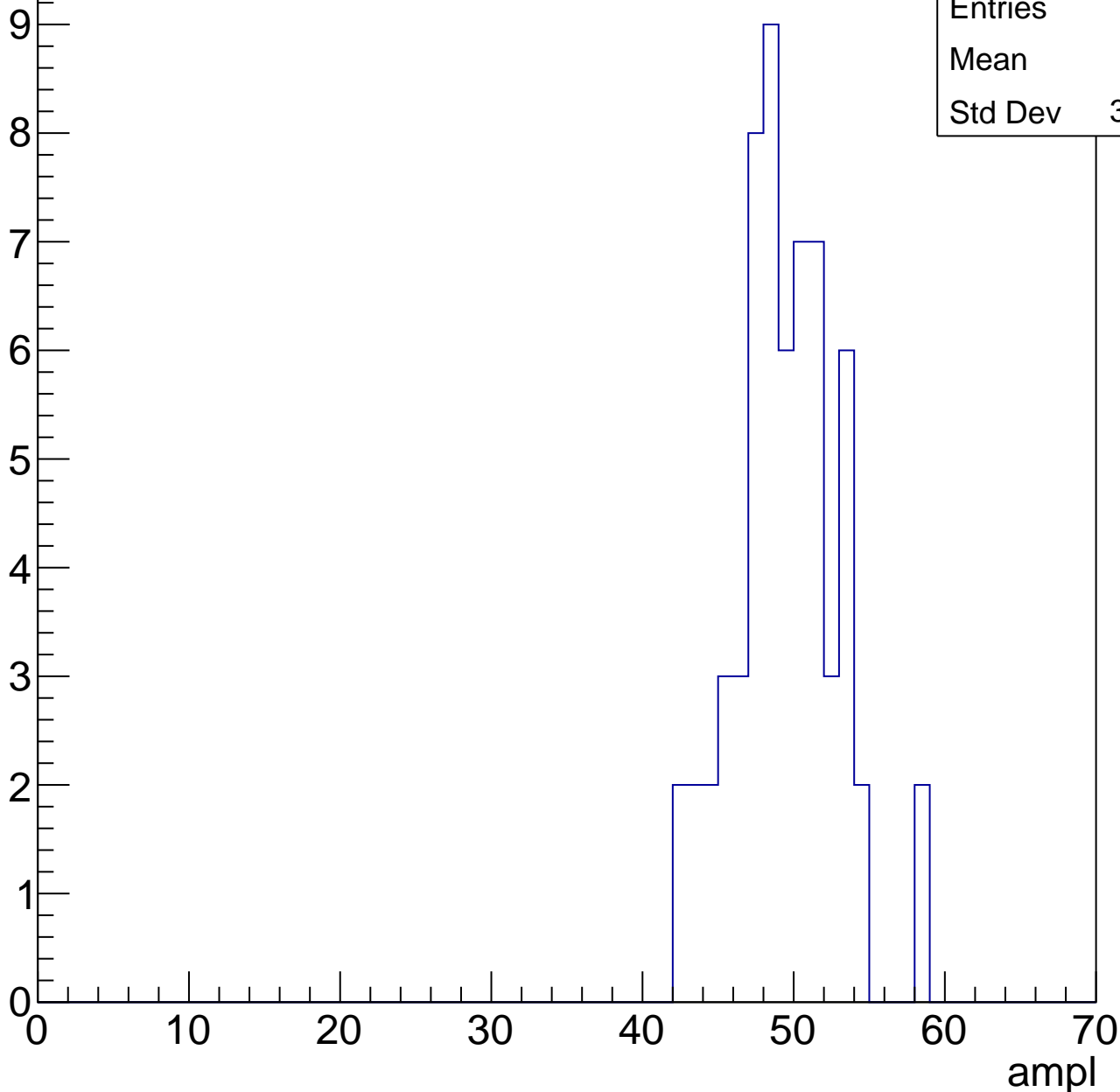


B1L101S, U1-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

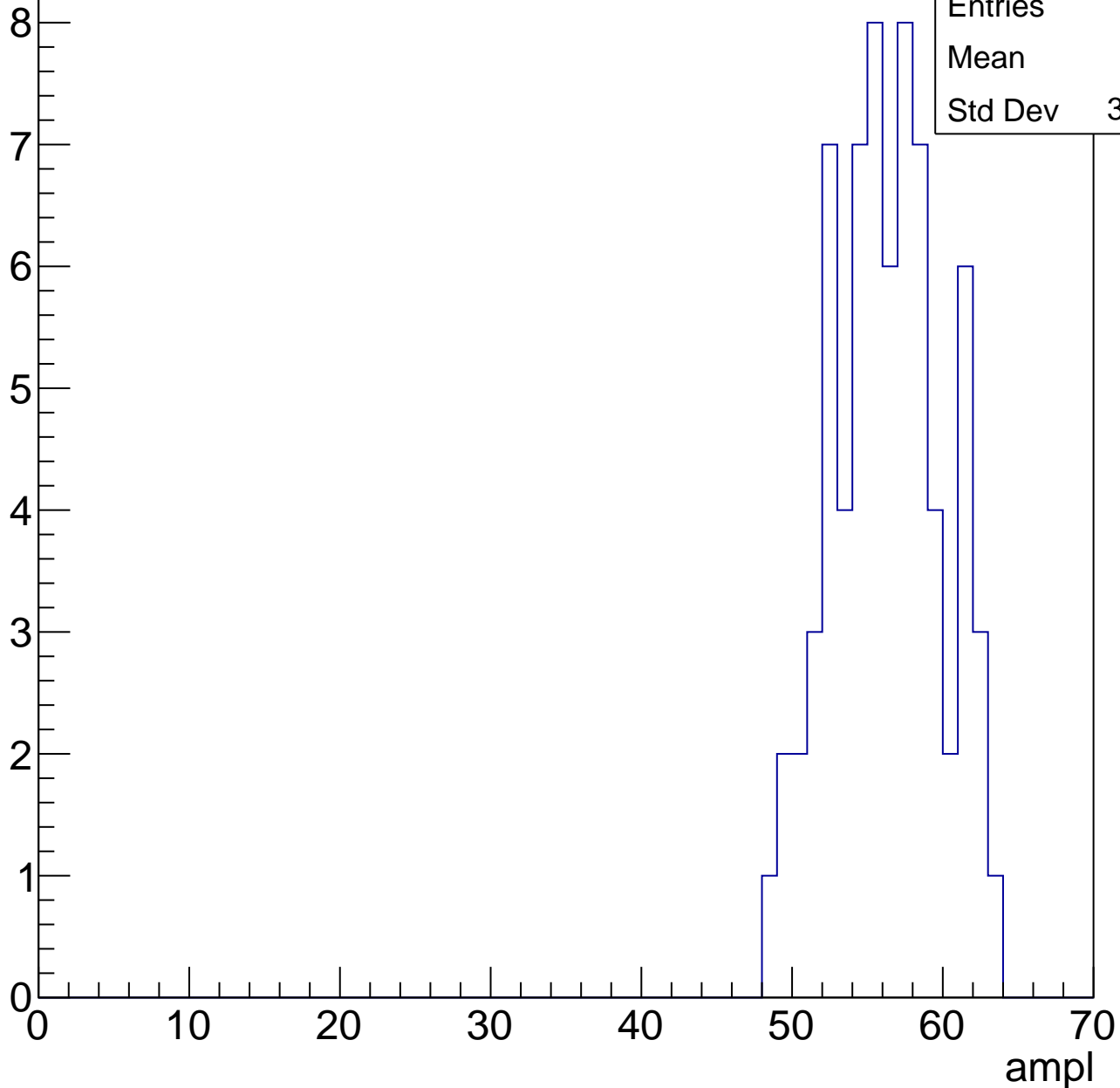
Entries	62
Mean	49
Std Dev	3.374



B1L101S, U1-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

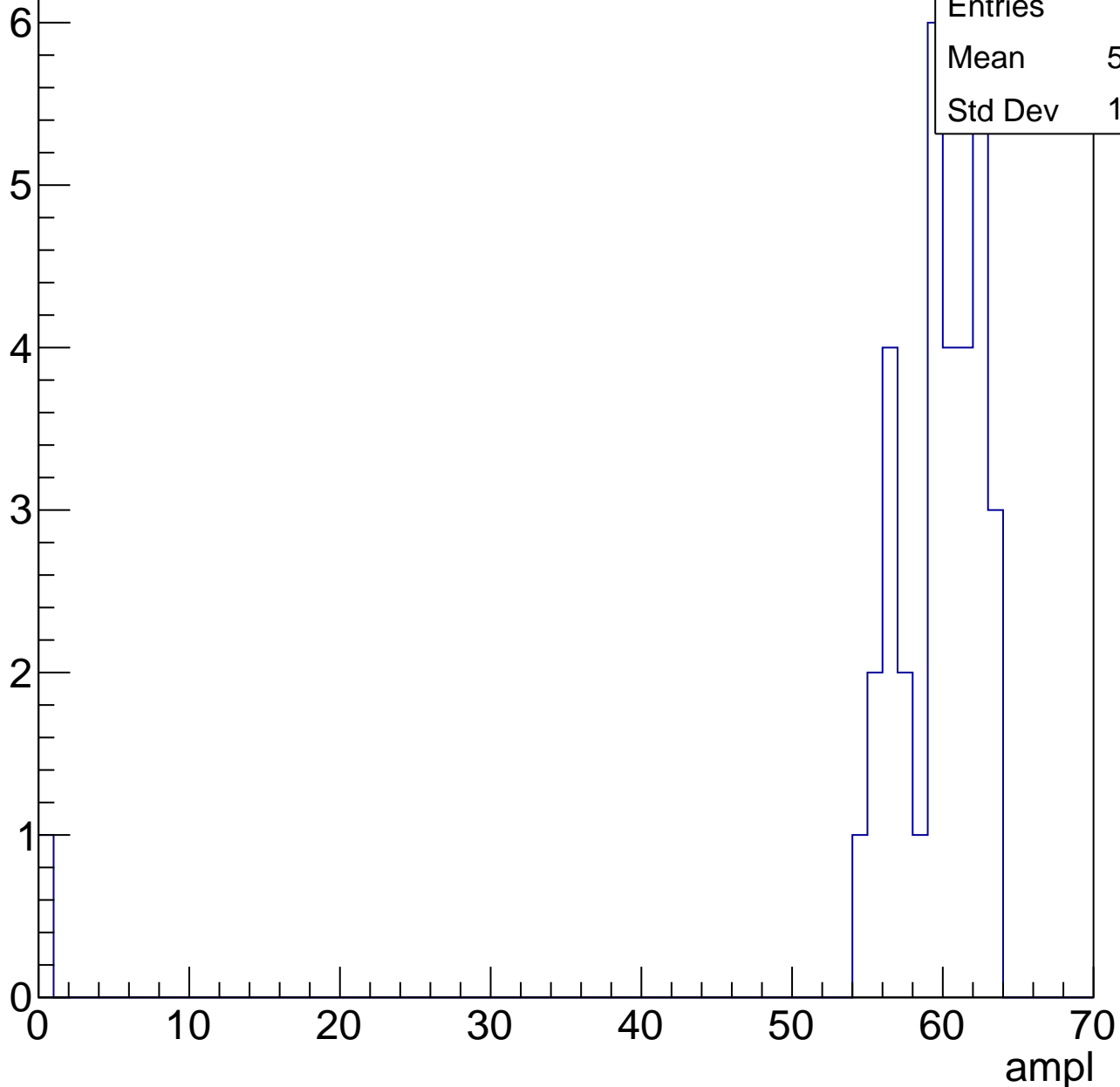


Entries	71
Mean	55.8
Std Dev	3.563

B1L101S, U1-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

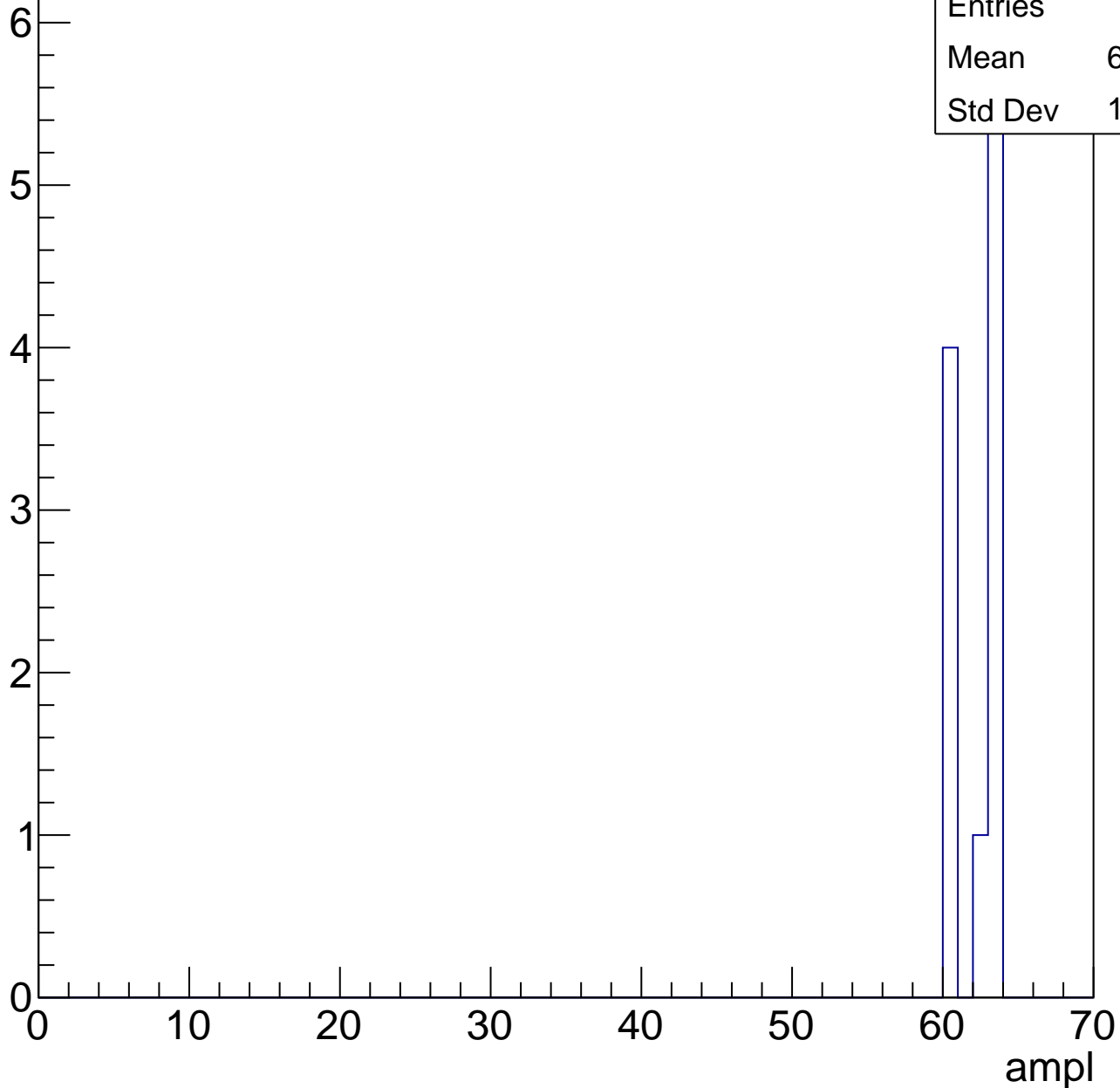


B1L101S, U1-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	61.82
Std Dev	1.402



B1L101S, U1-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch44, adc0

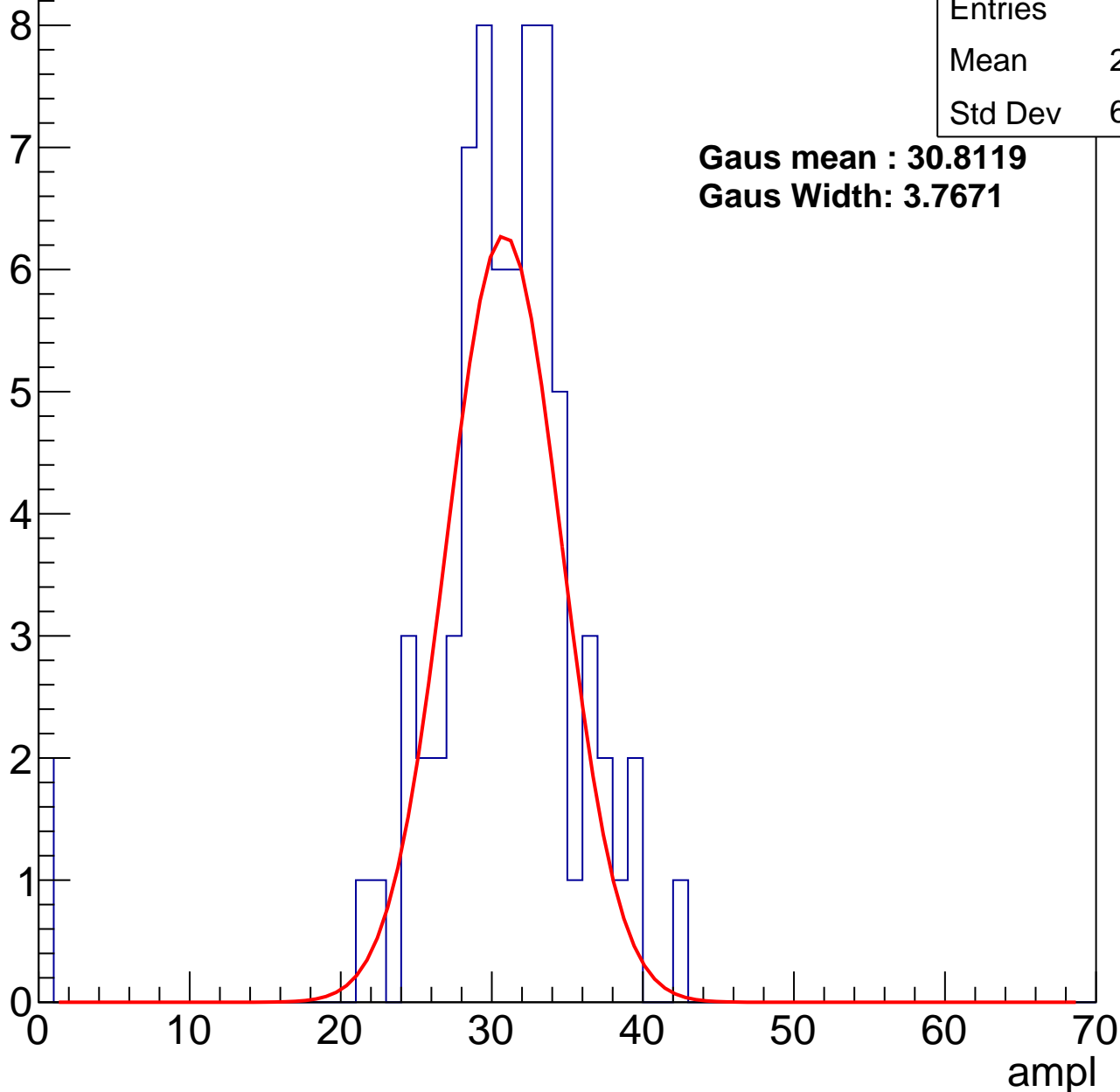
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.96
Std Dev	6.443

Gaus mean : 30.8119

Gaus Width: 3.7671



B1L101S, U1-ch44, adc1

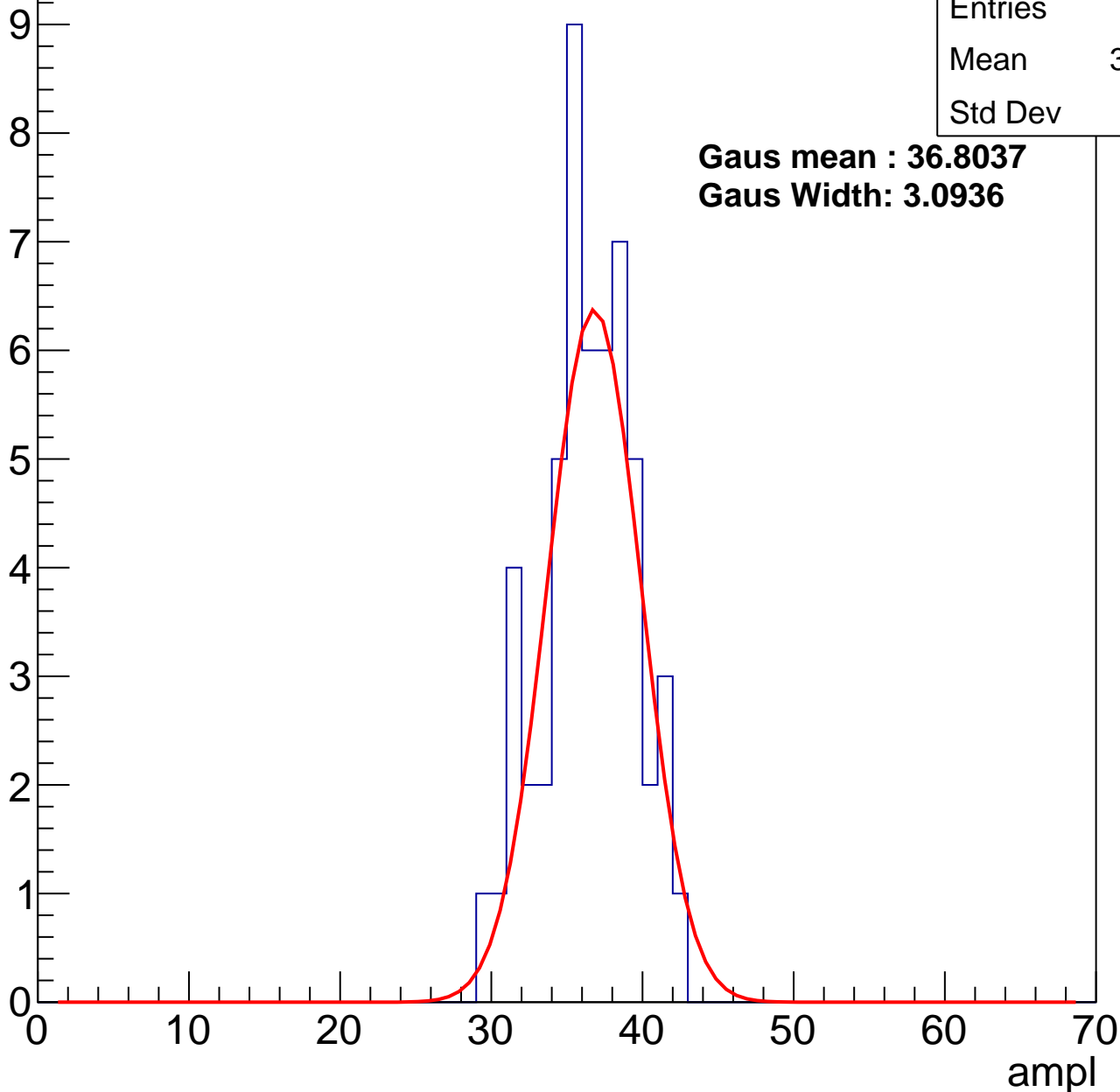
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	35.96
Std Dev	3

Gaus mean : 36.8037

Gaus Width: 3.0936

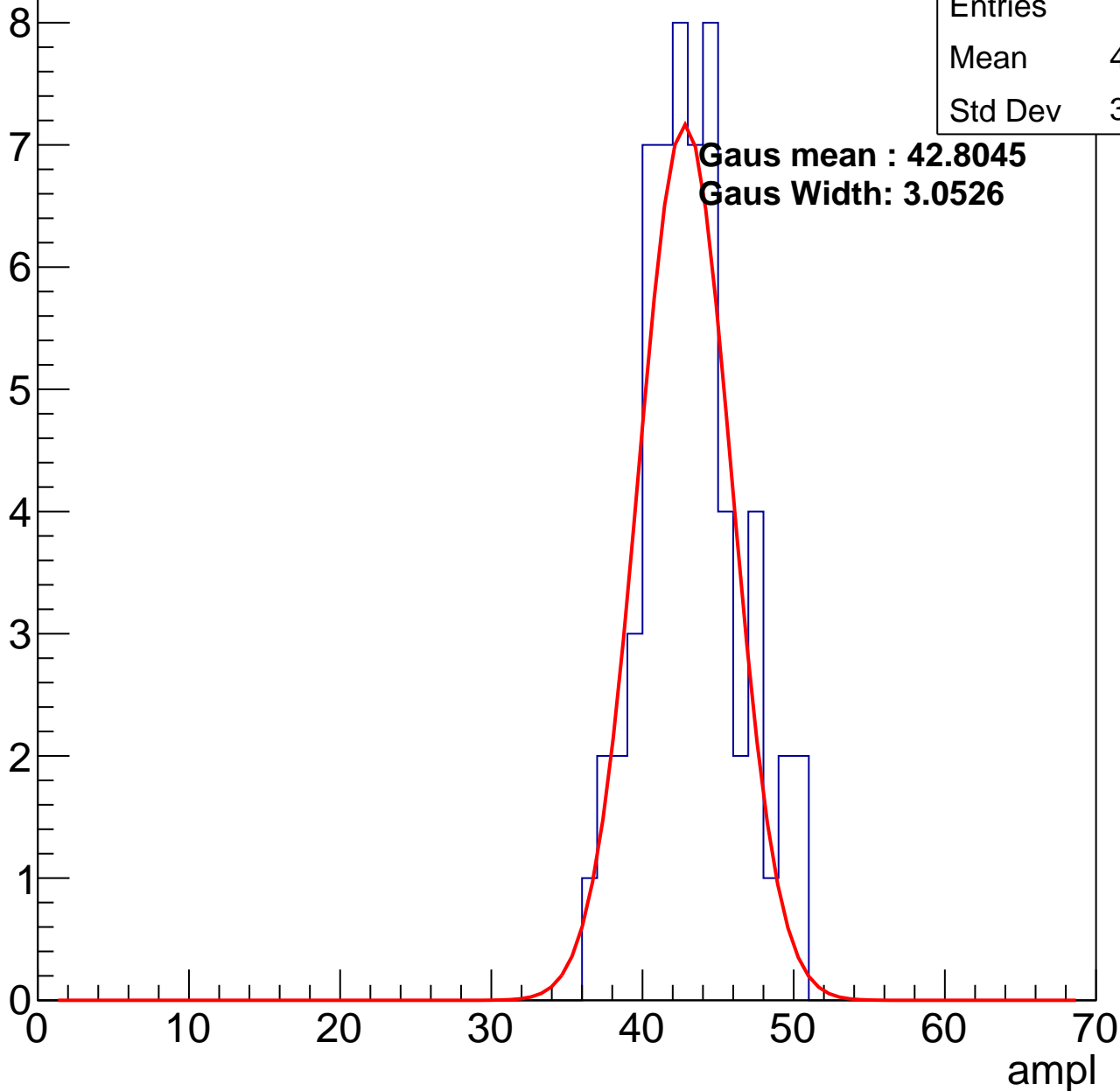


B1L101S, U1-ch44, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.75
Std Dev	3.202

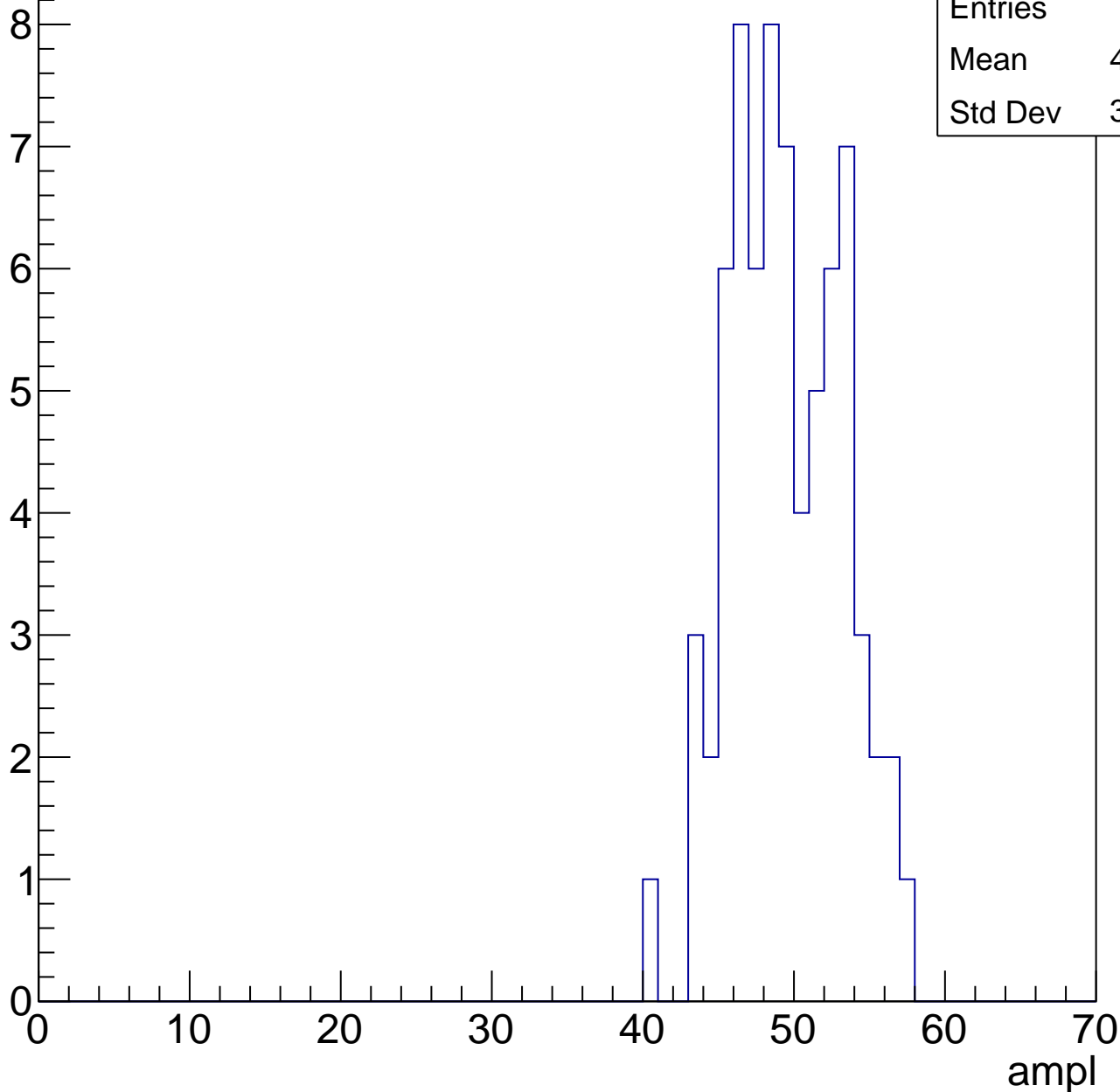


B1L101S, U1-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.06
Std Dev	3.634



B1L101S, U1-ch44, adc4

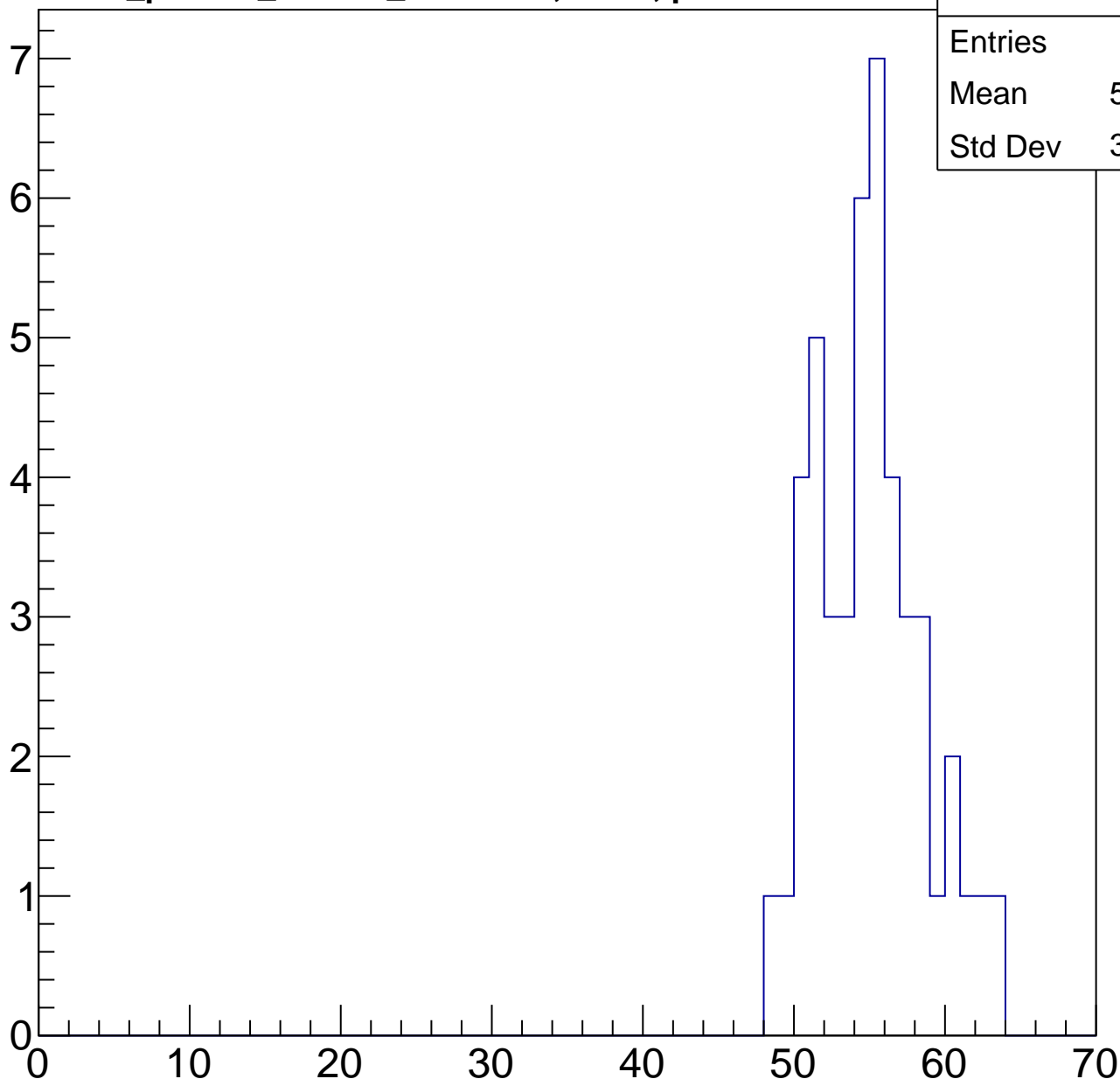
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

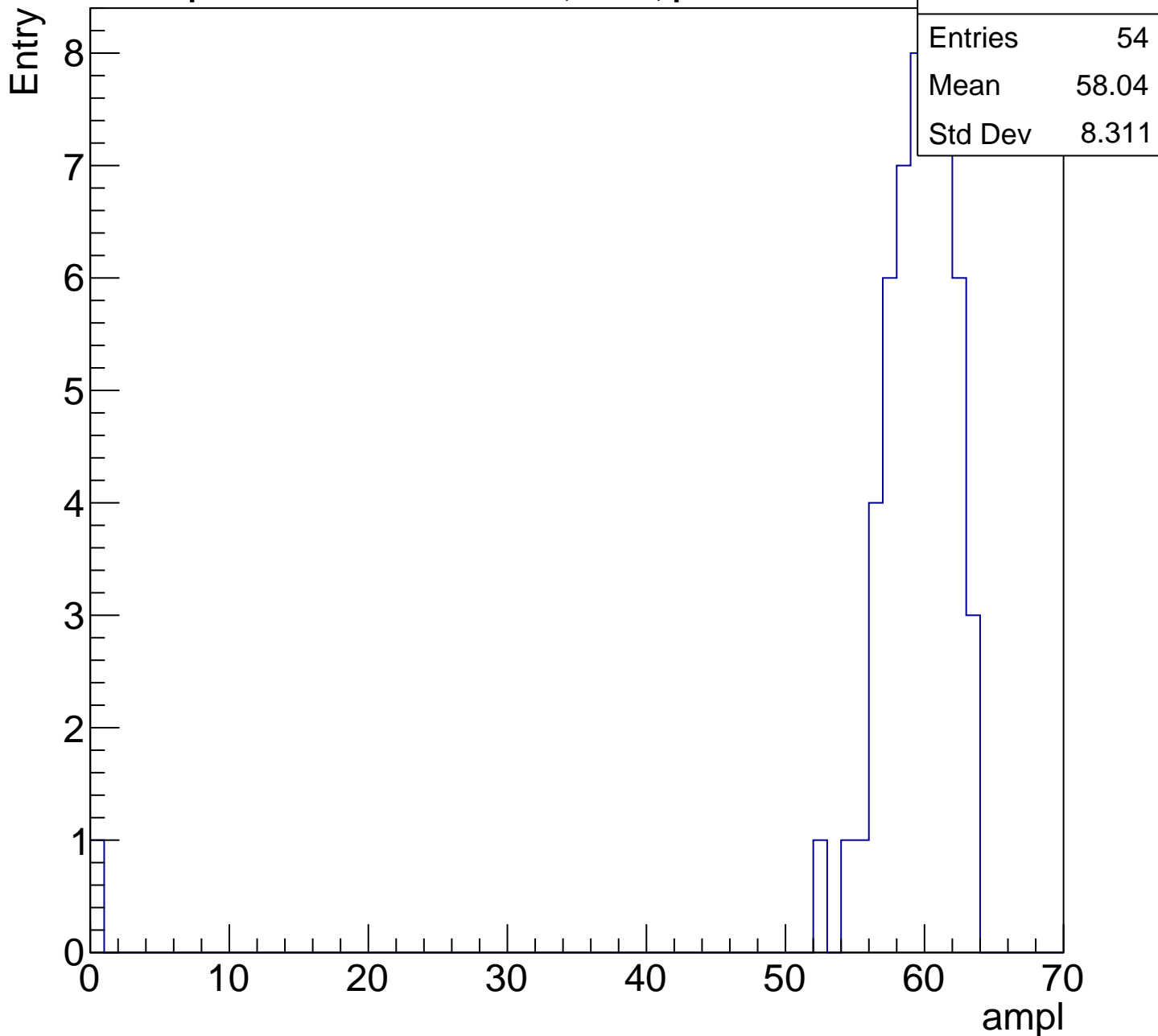
Entries	46
Mean	54.57
Std Dev	3.487

ampl



B1L101S, U1-ch44, adc5

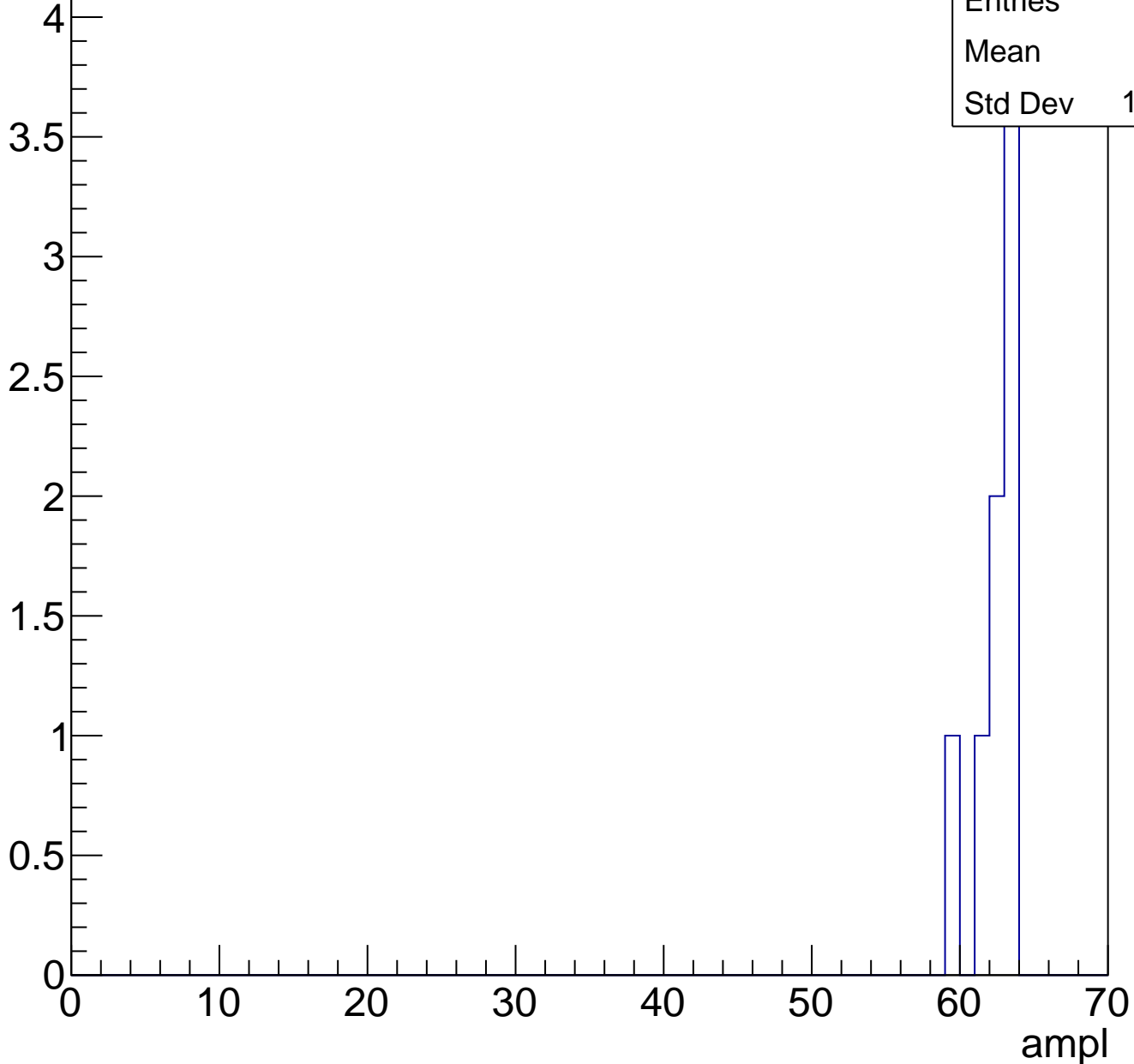
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

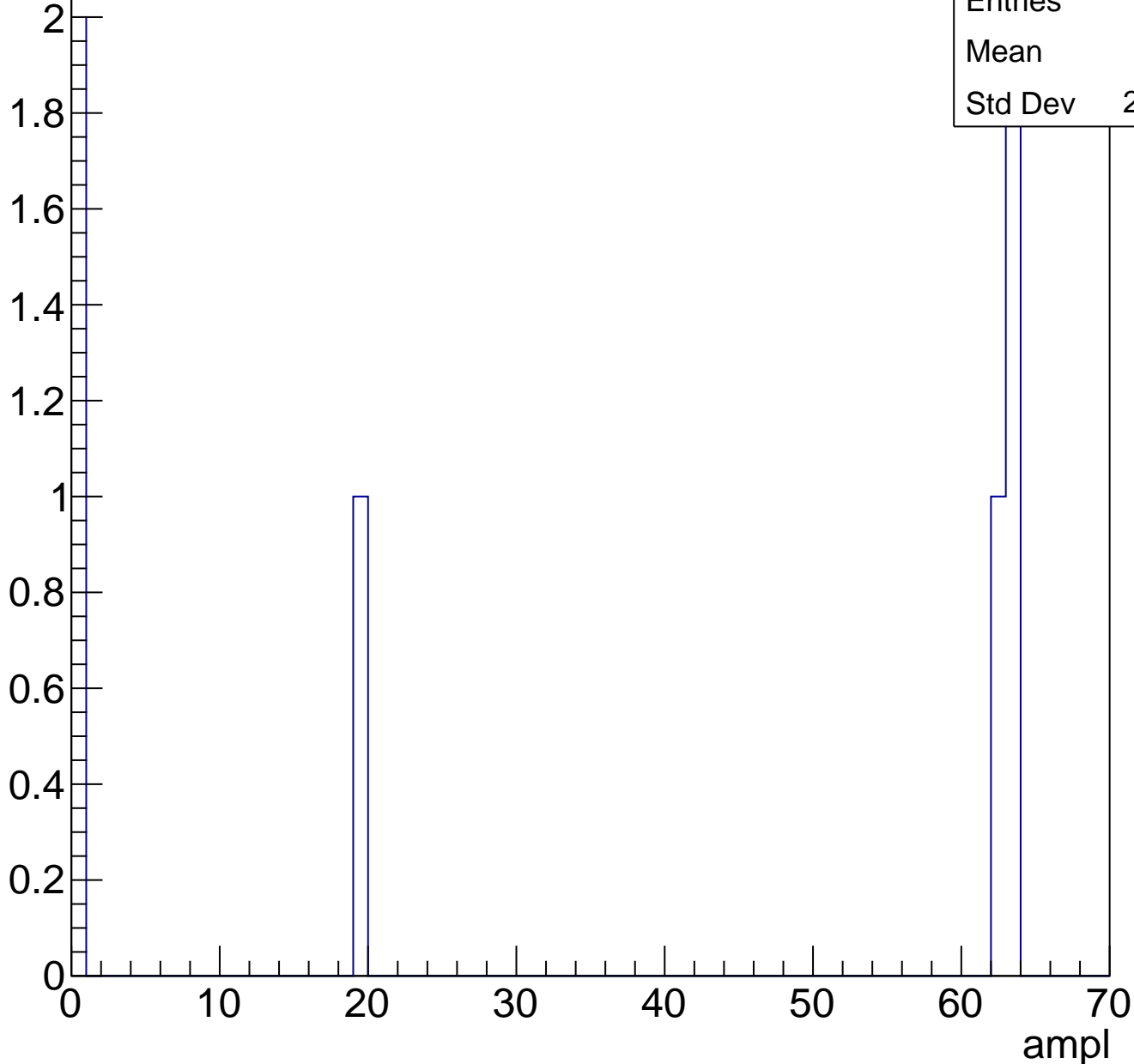
Entry



B1L101S, U1-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	34.5
Std Dev	28.87

B1L101S, U1-ch45, adc0

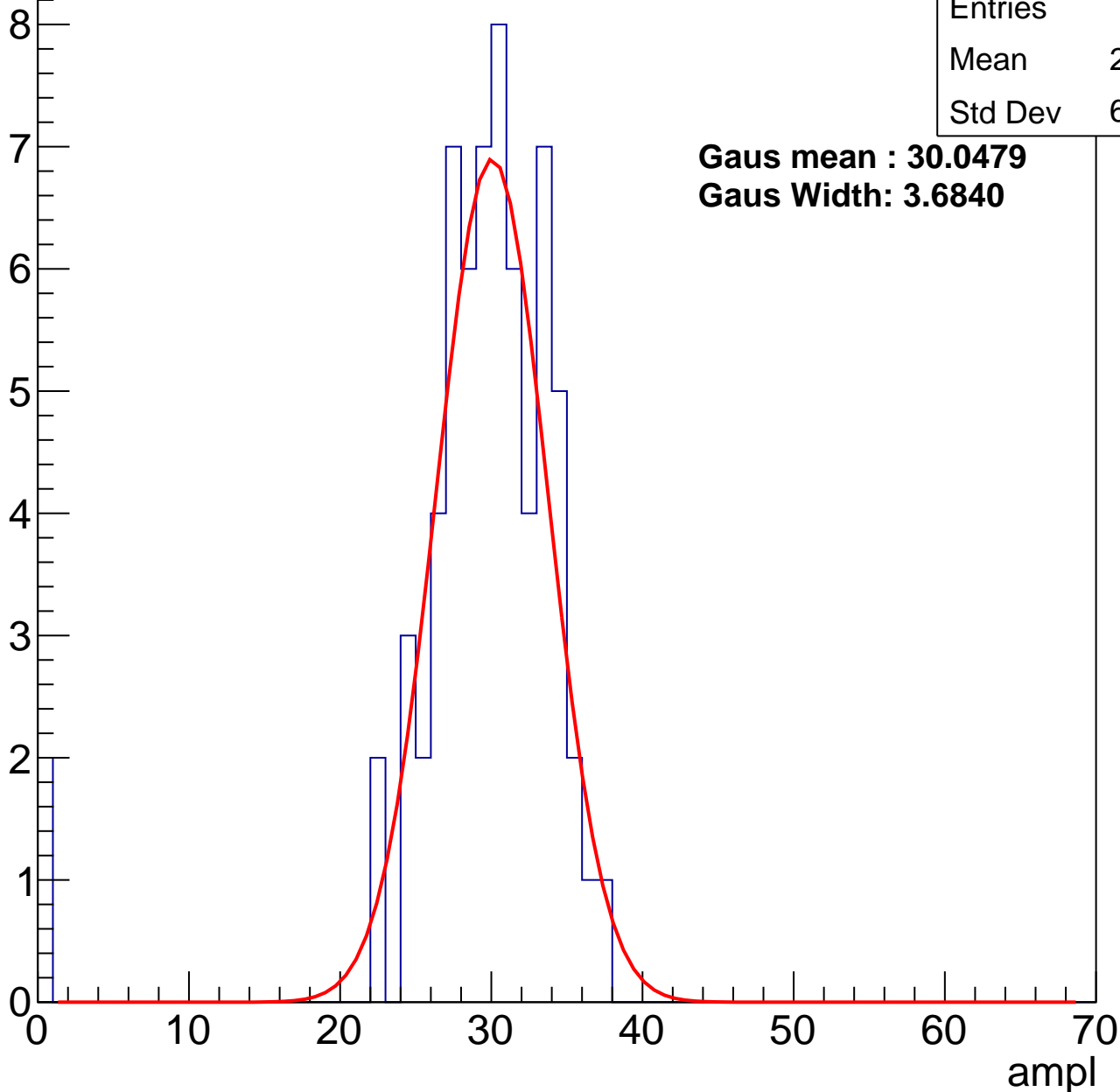
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.78
Std Dev	6.039

Gaus mean : 30.0479

Gaus Width: 3.6840



B1L101S, U1-ch45, adc1

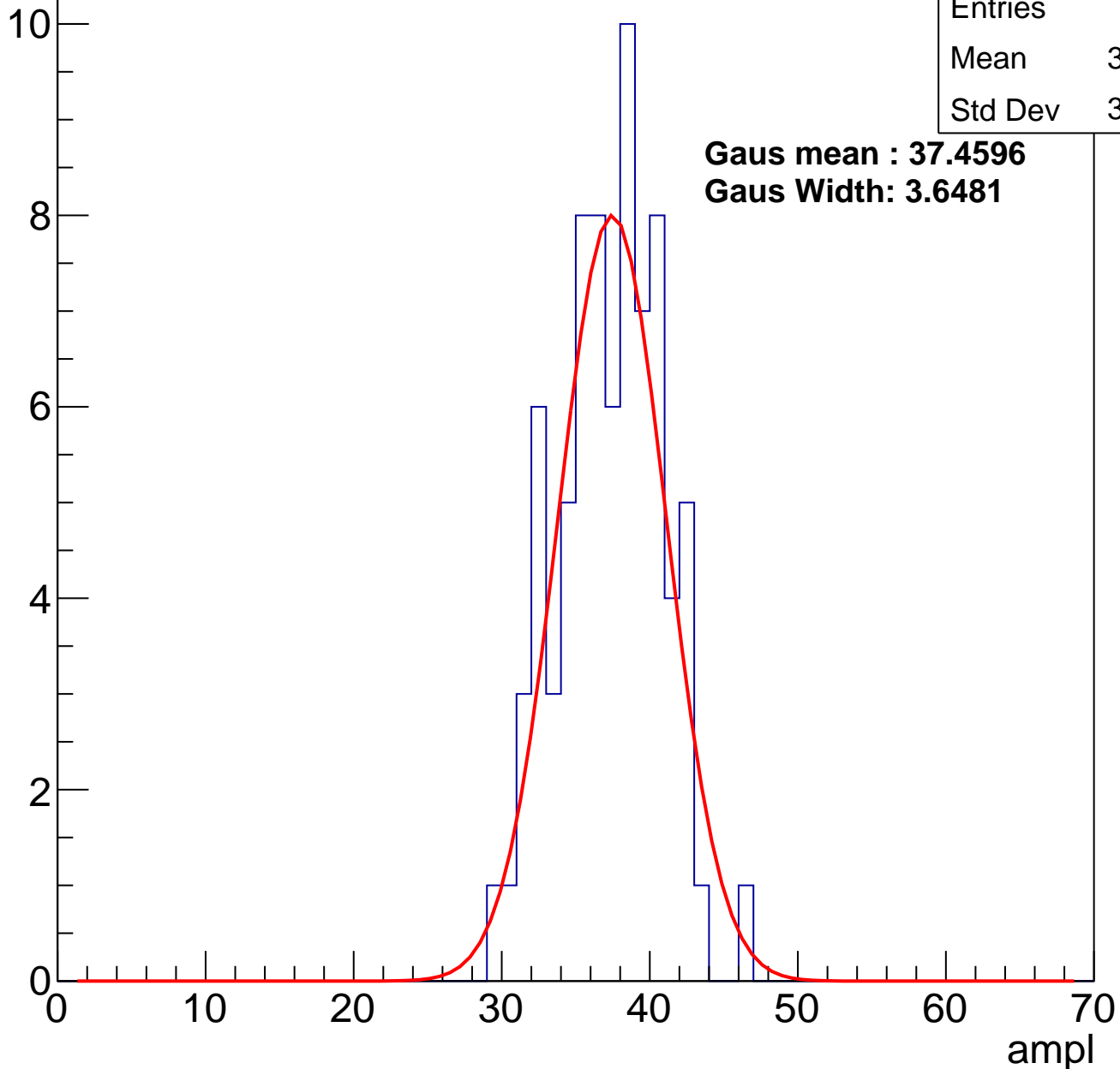
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	36.87
Std Dev	3.454

Gaus mean : 37.4596

Gaus Width: 3.6481

Entry



B1L101S, U1-ch45, adc2

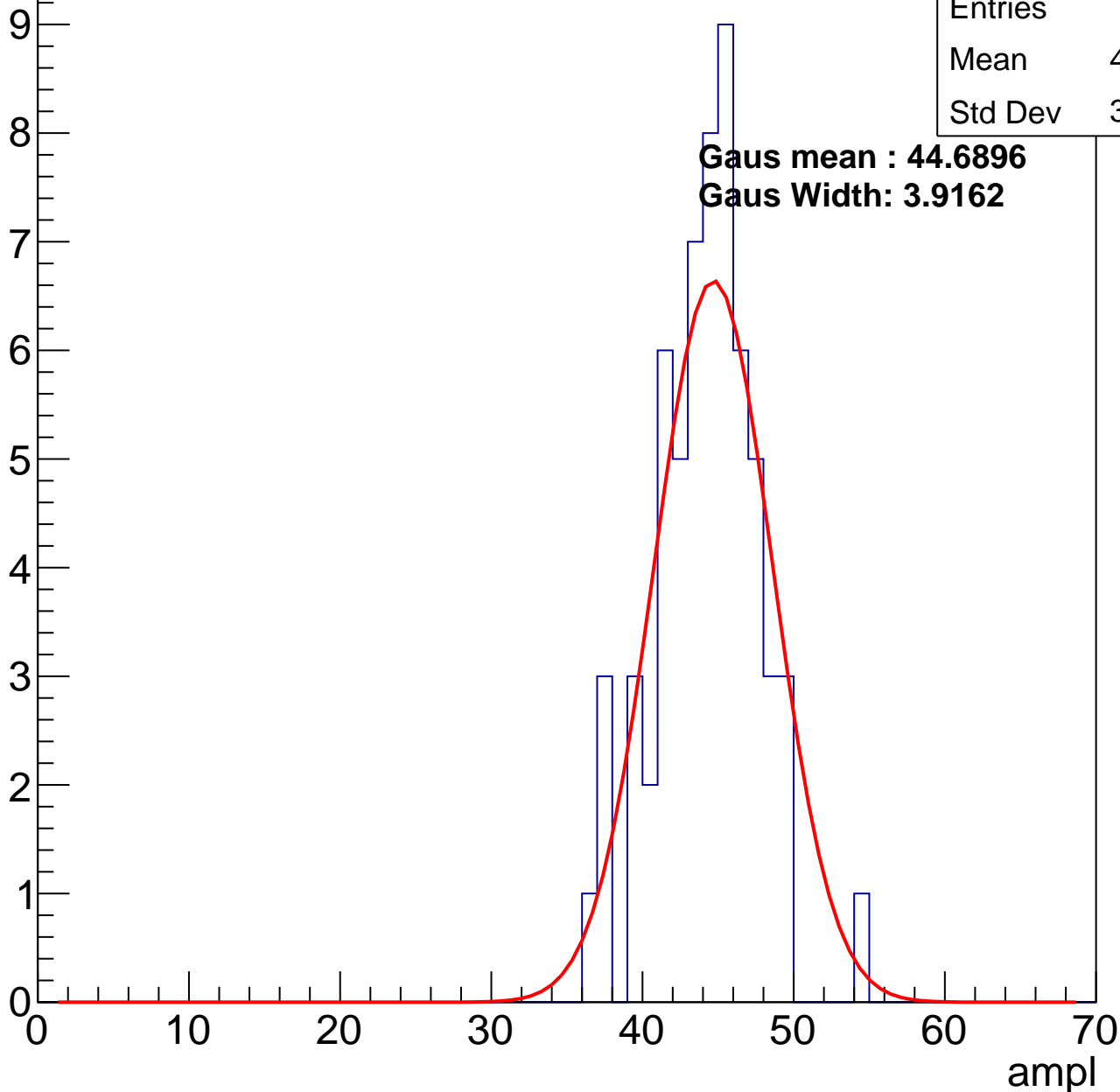
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.77
Std Dev	3.353

Gaus mean : 44.6896

Gaus Width: 3.9162

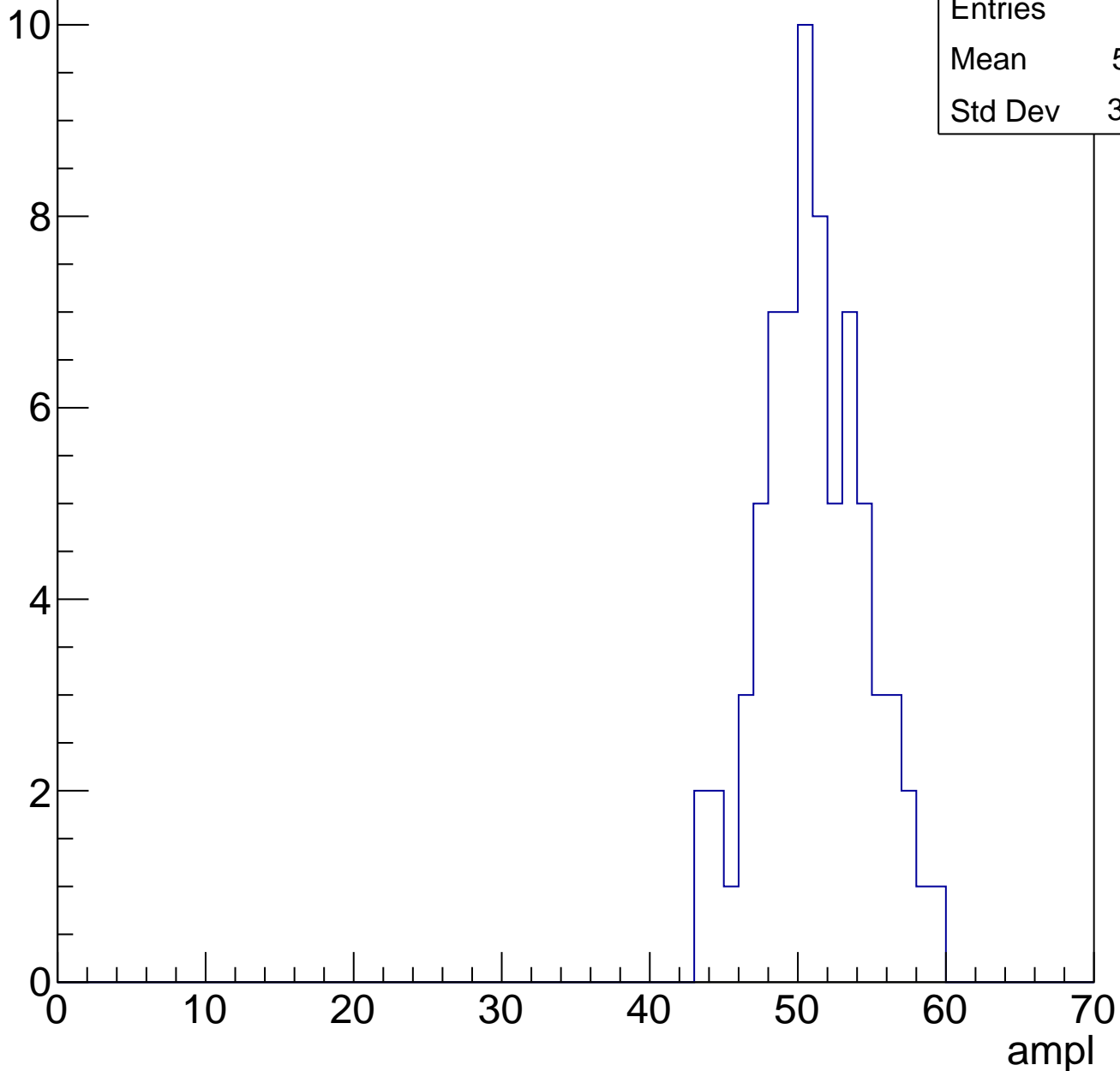


B1L101S, U1-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	50.61
Std Dev	3.522

Entry

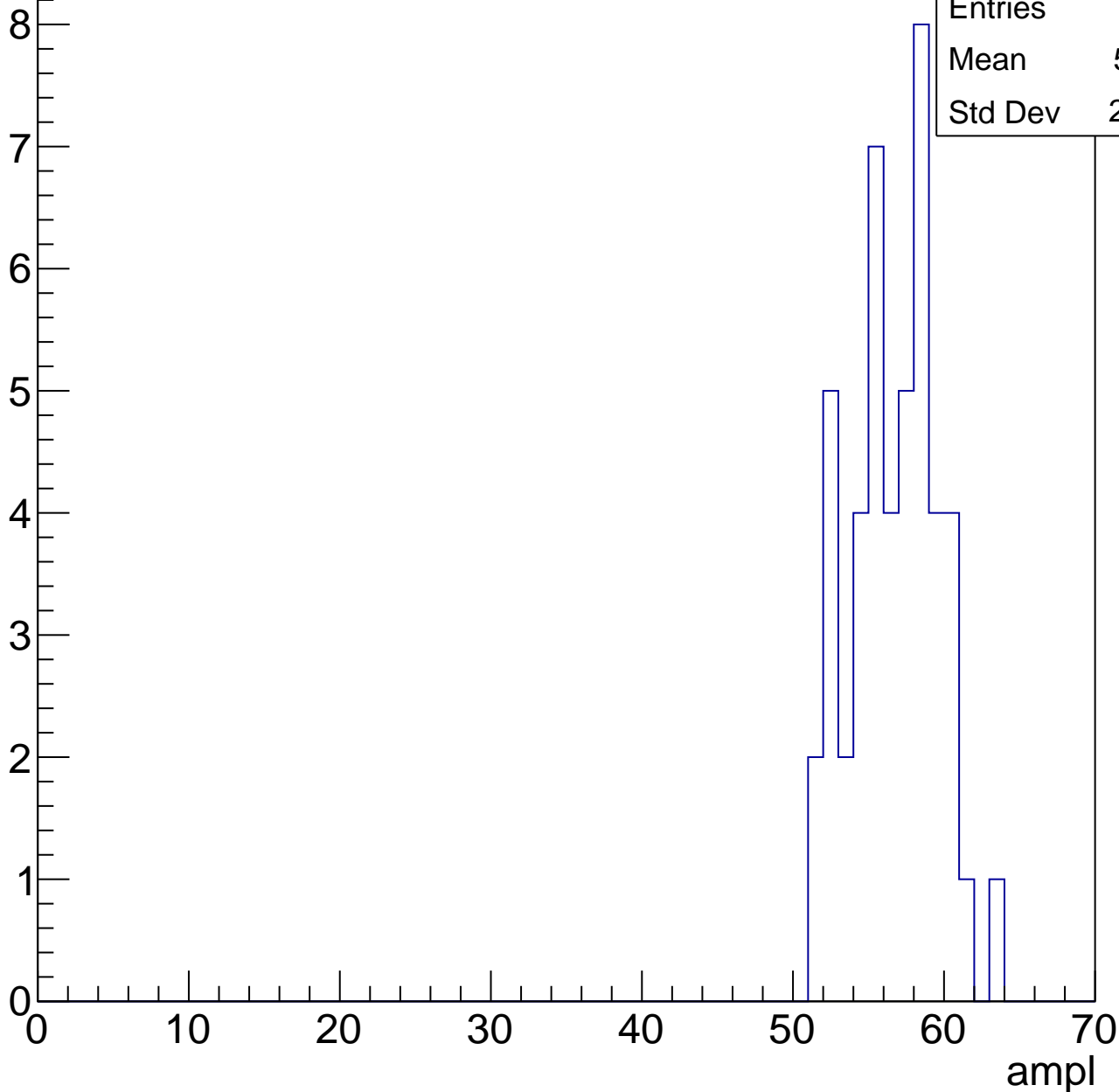


B1L101S, U1-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56.21
Std Dev	2.843

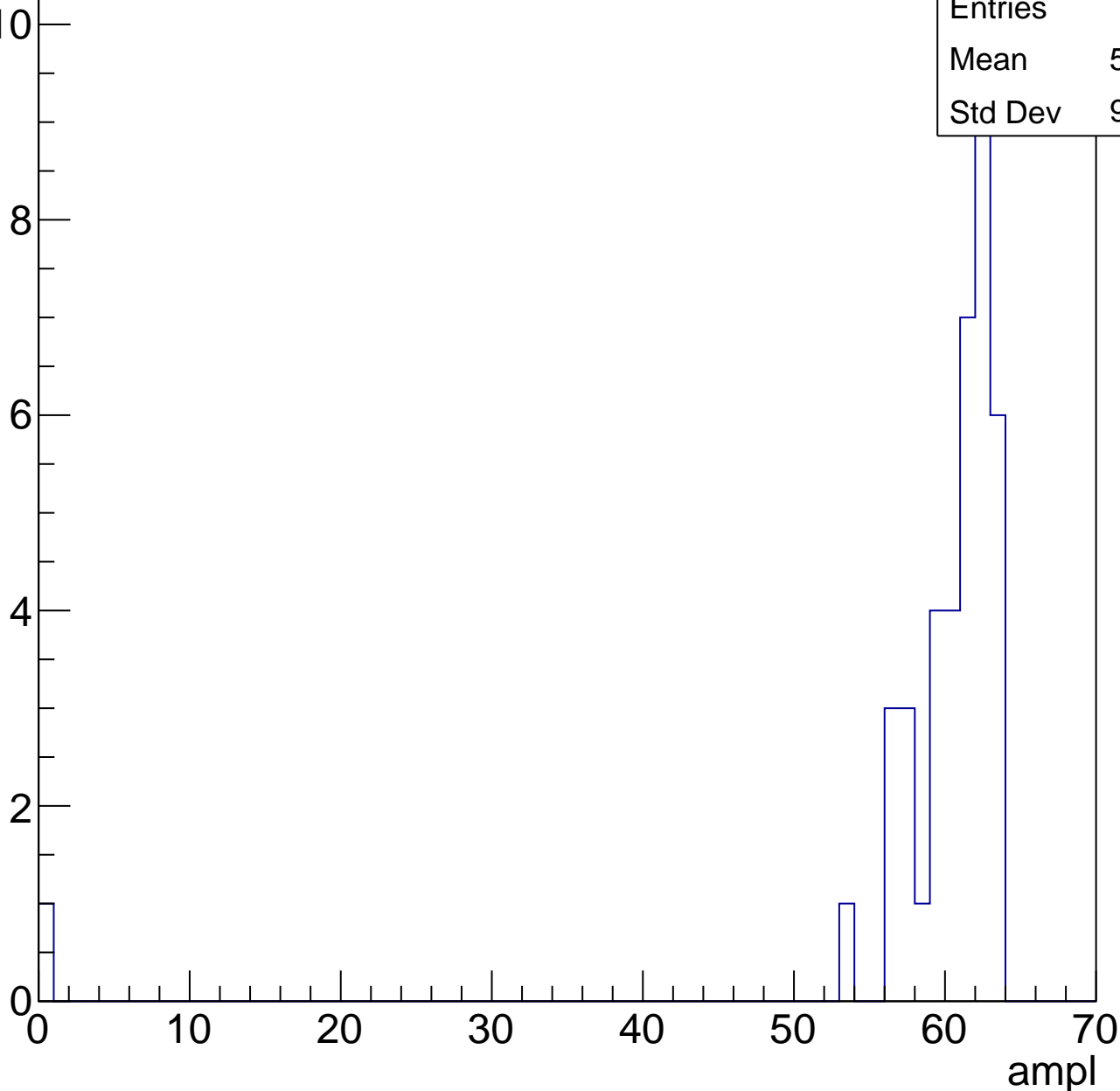


B1L101S, U1-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

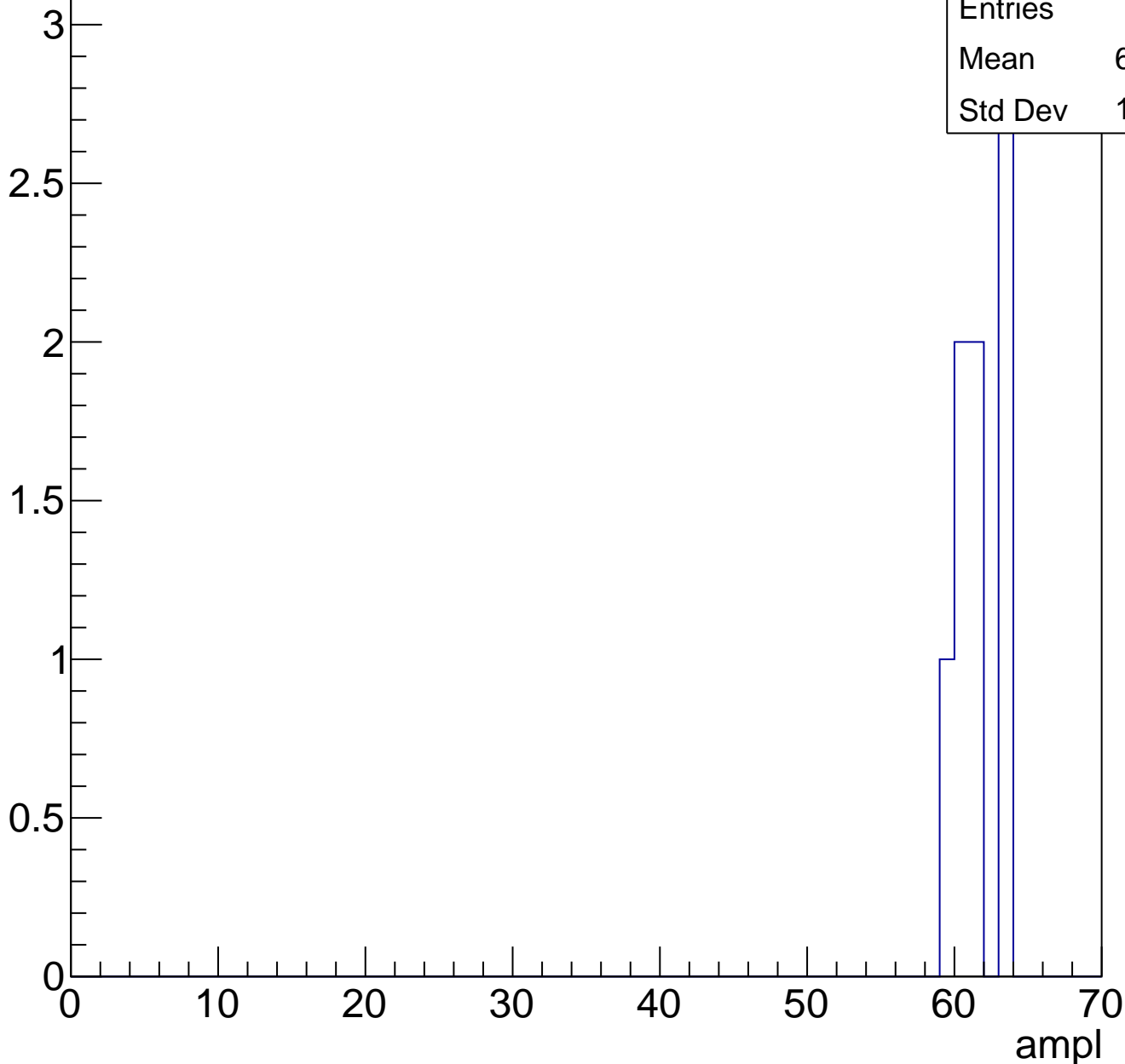
Entries	40
Mean	58.77
Std Dev	9.712



B1L101S, U1-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch46, adc0

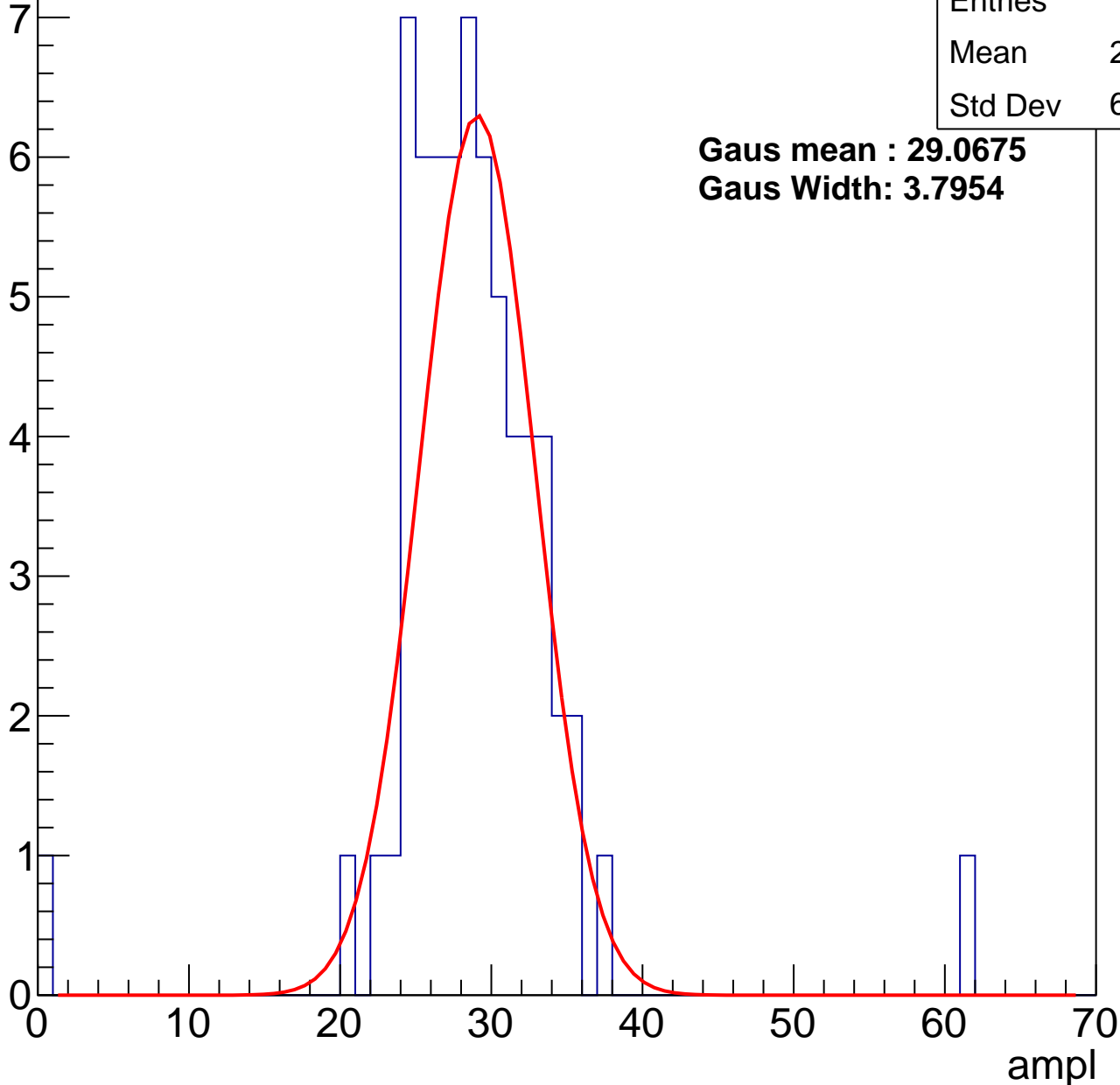
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.32
Std Dev	6.402

Gaus mean : 29.0675

Gaus Width: 3.7954



B1L101S, U1-ch46, adc1

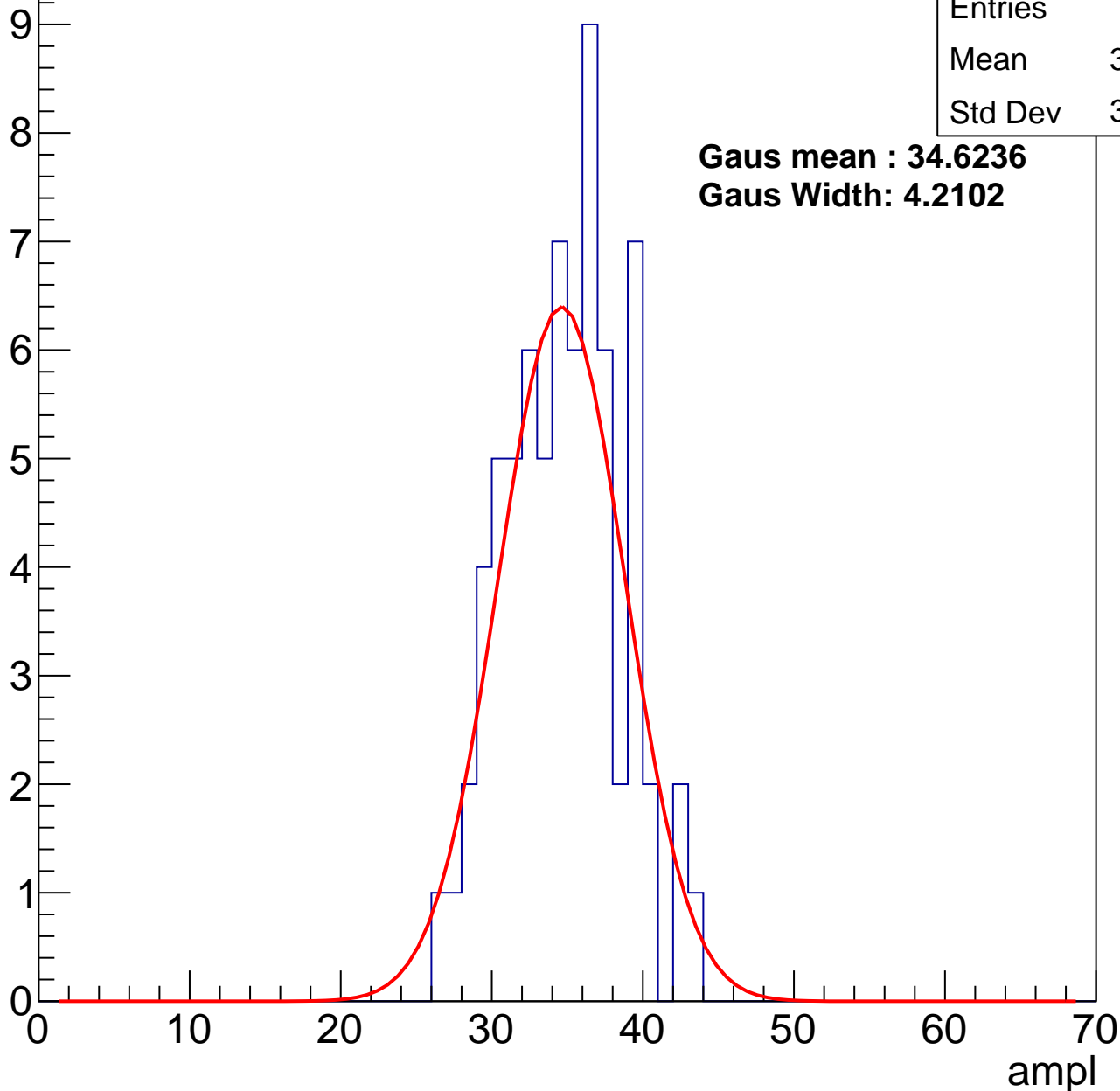
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	34.32
Std Dev	3.782

Gaus mean : 34.6236

Gaus Width: 4.2102



B1L101S, U1-ch46, adc2

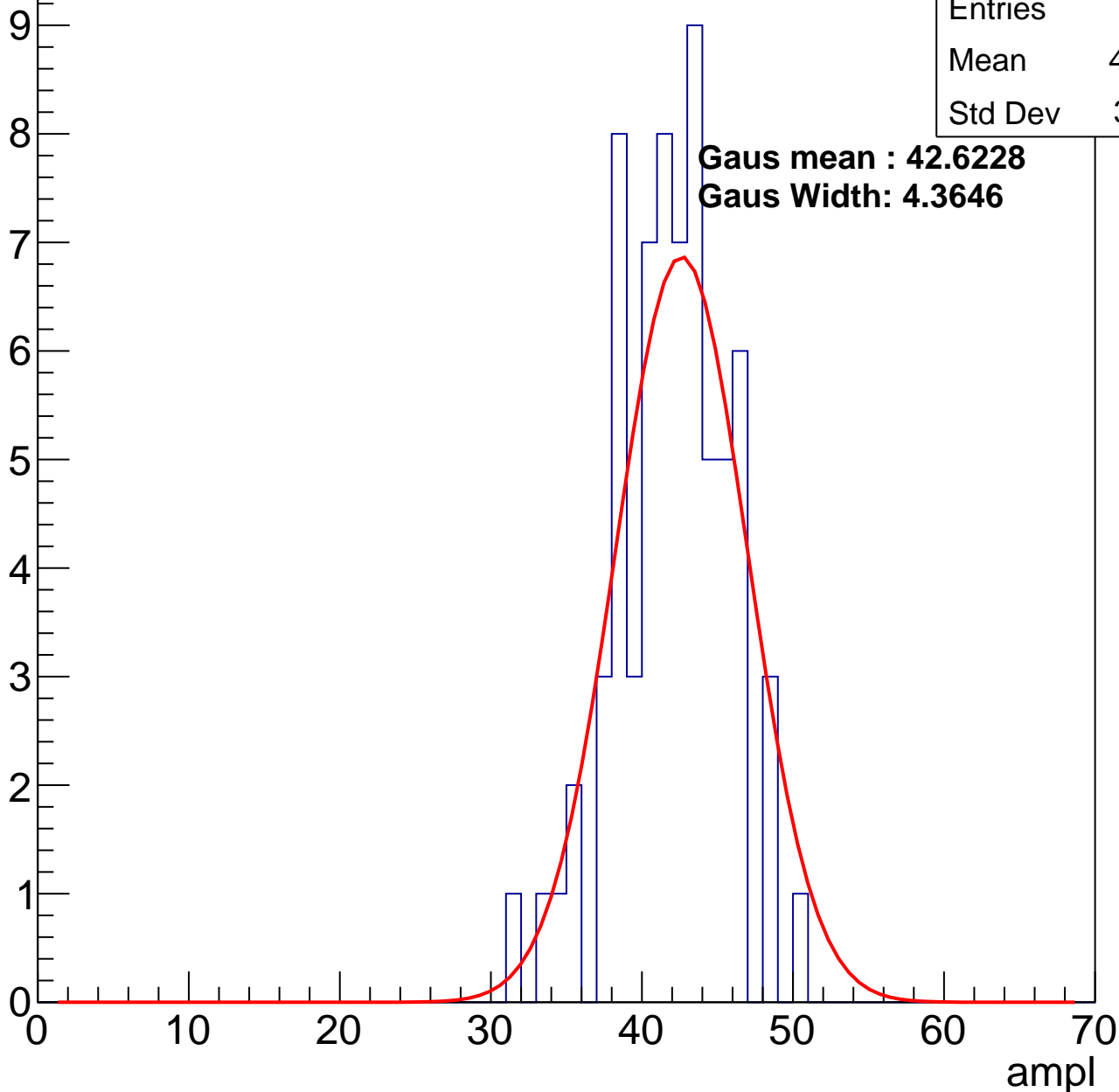
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	41.49
Std Dev	3.691

Gaus mean : 42.6228

Gaus Width: 4.3646

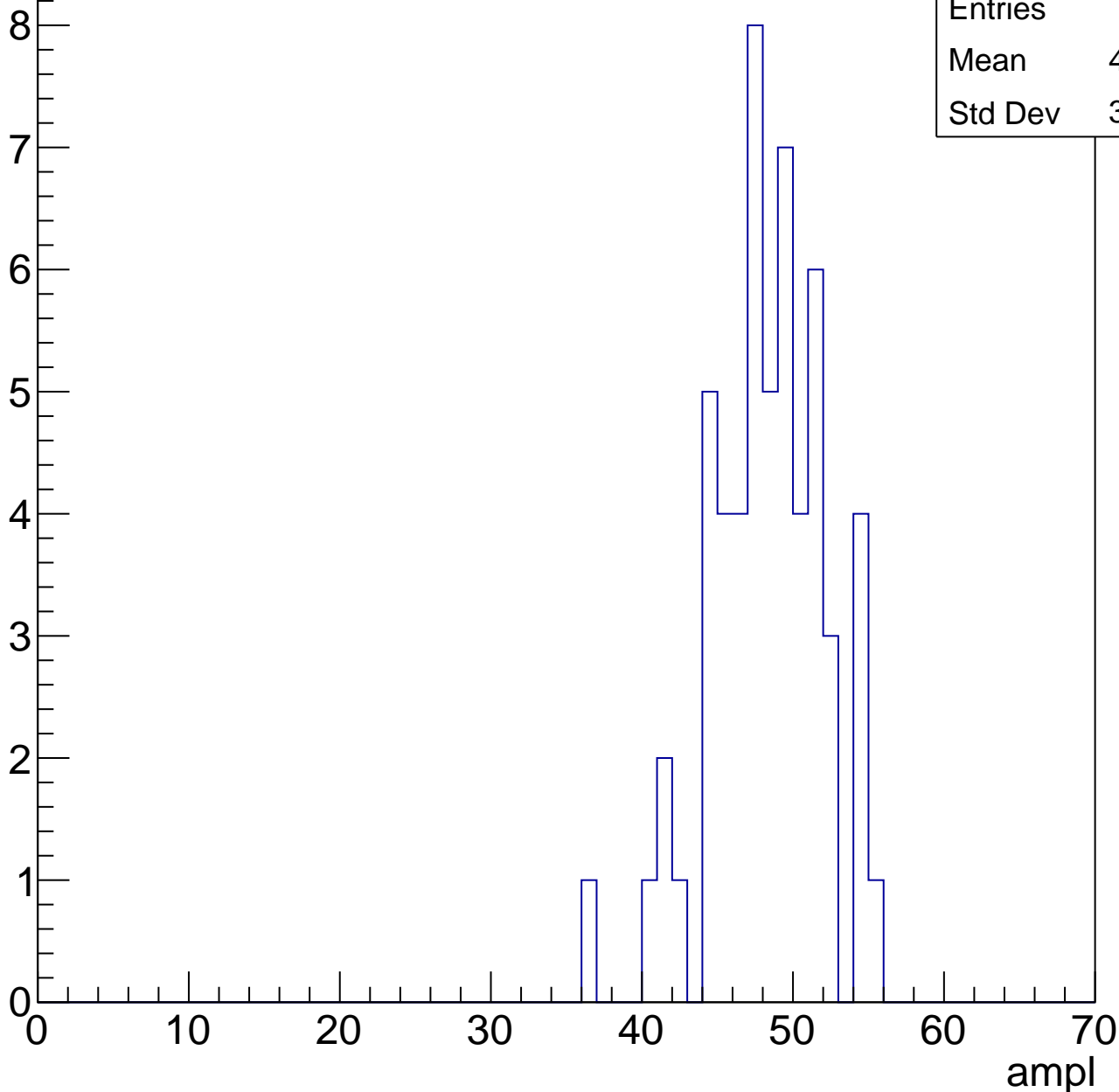


B1L101S, U1-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	47.79
Std Dev	3.774

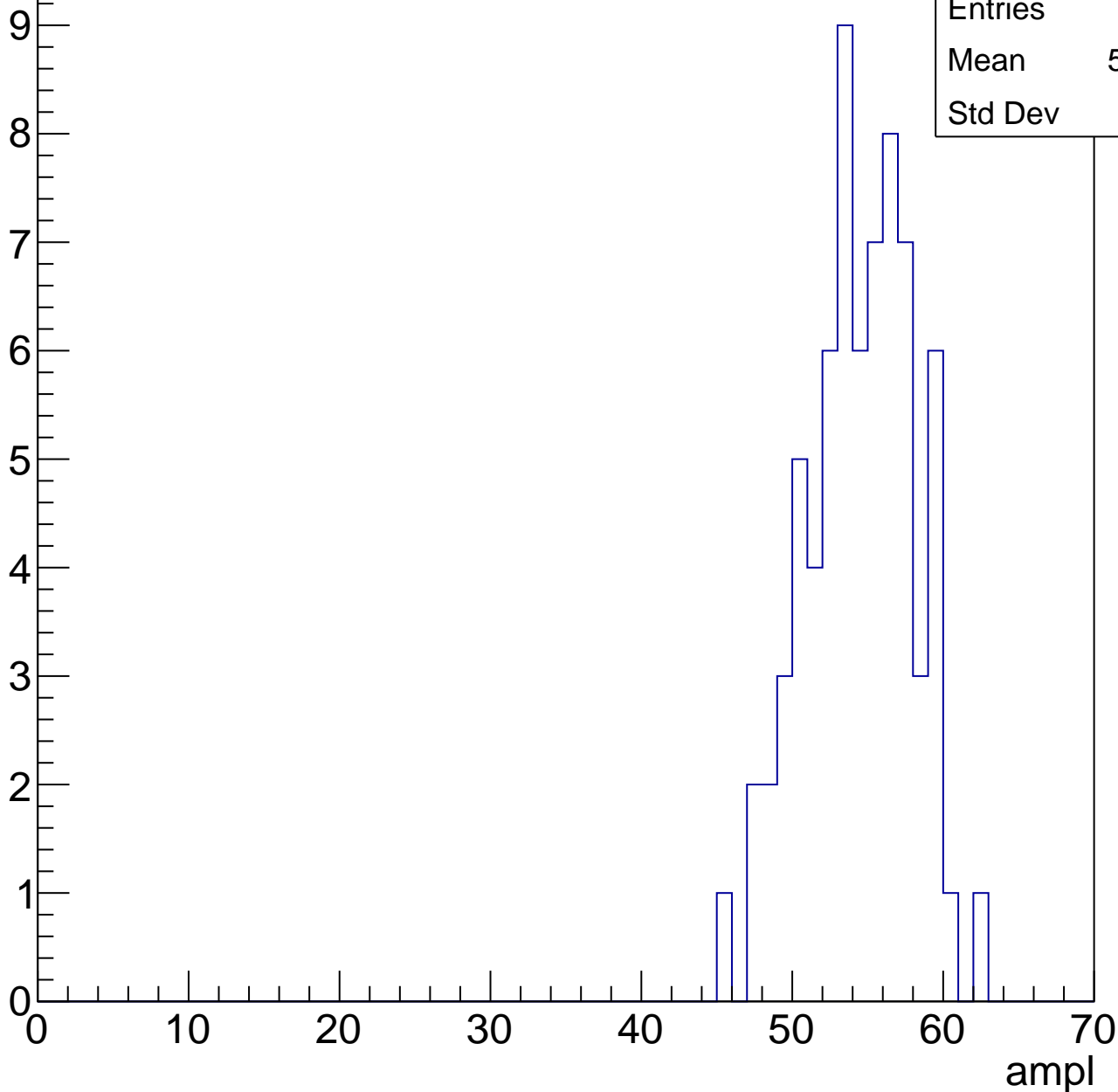


B1L101S, U1-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	53.96
Std Dev	3.51

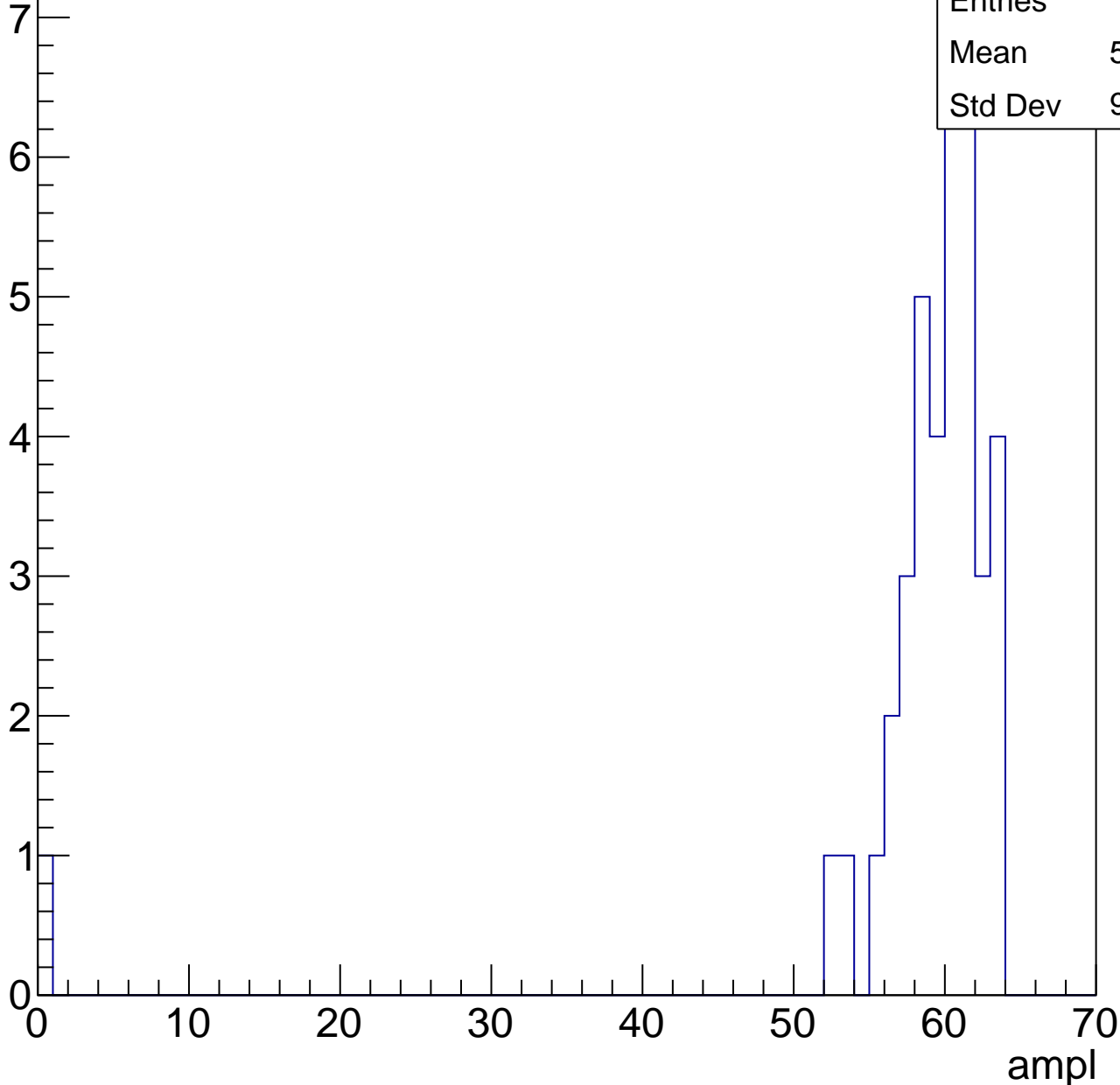


B1L101S, U1-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	57.79
Std Dev	9.722

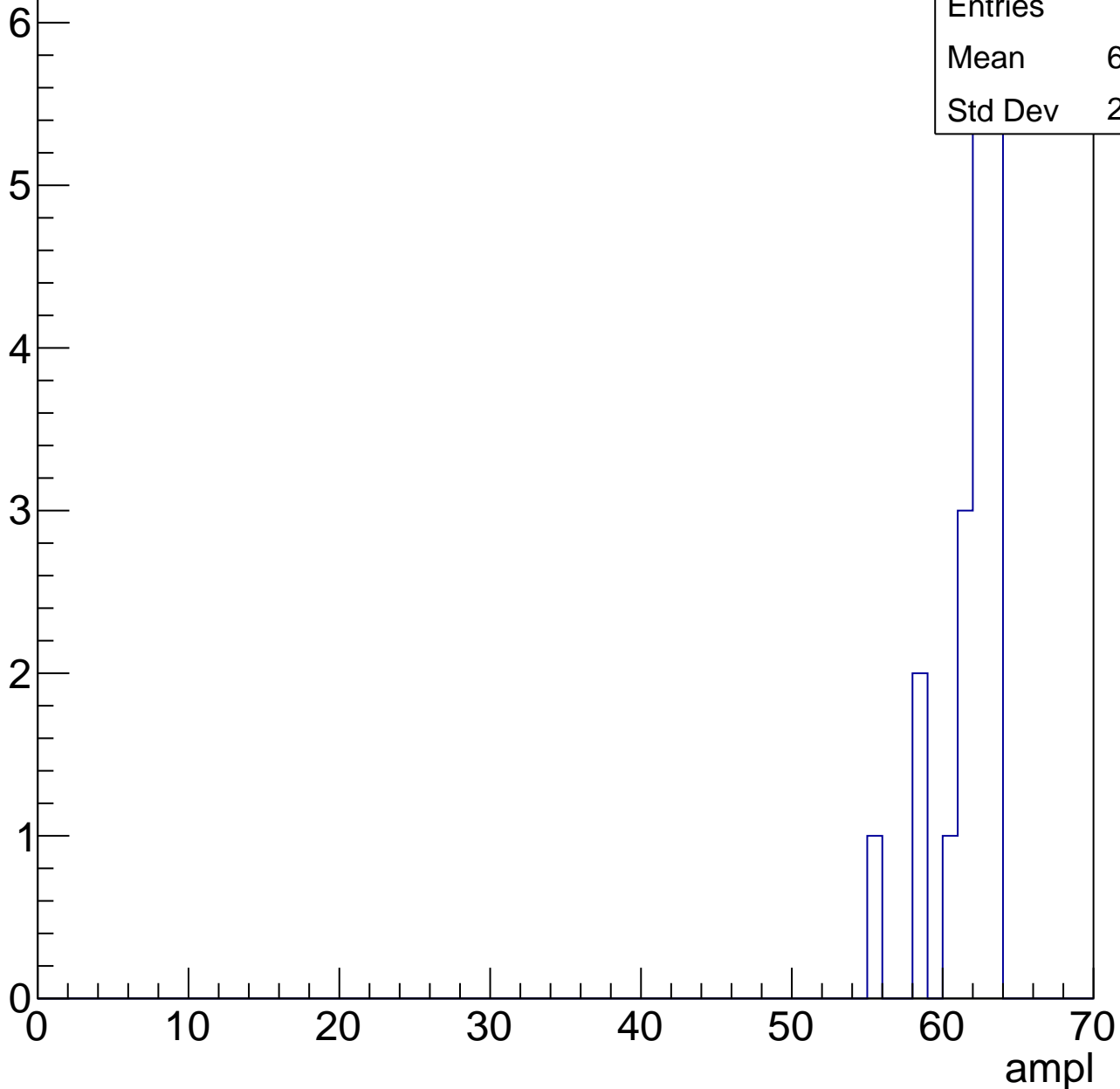


B1L101S, U1-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

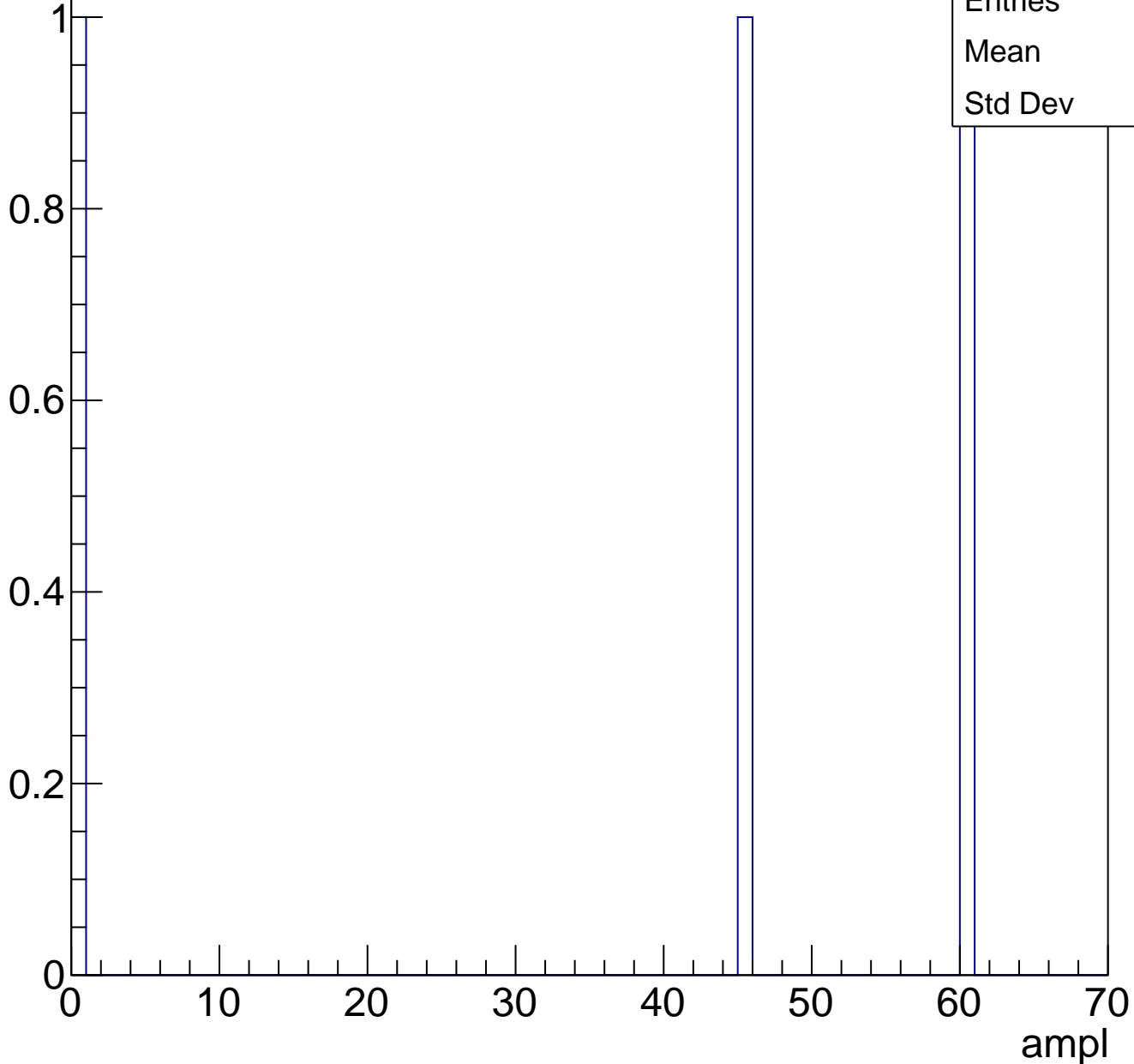
Entries	19
Mean	61.26
Std Dev	2.099



B1L101S, U1-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch47, adc0

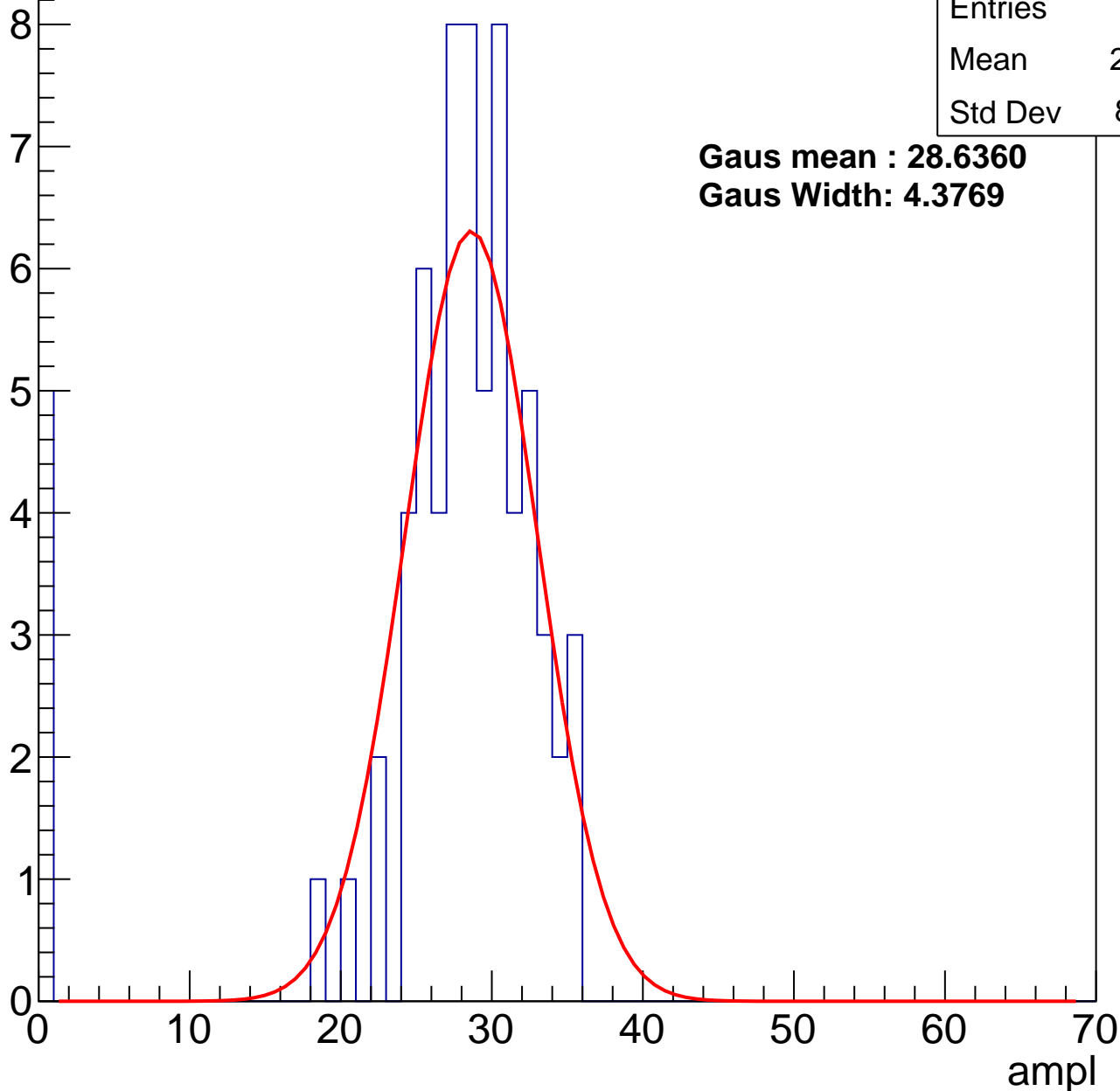
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	26.28
Std Dev	8.111

Gaus mean : 28.6360

Gaus Width: 4.3769



B1L101S, U1-ch47, adc1

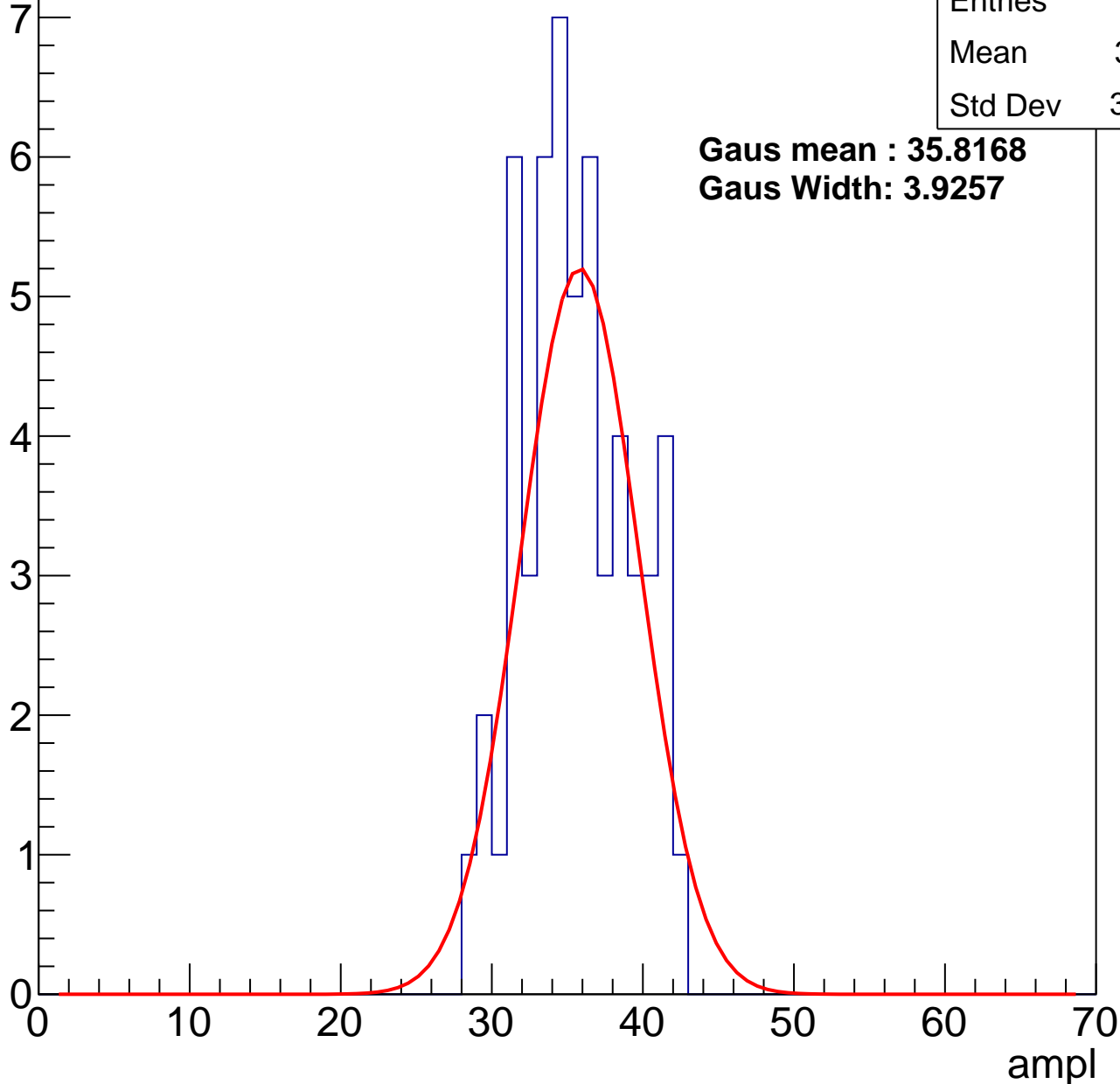
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.11
Std Dev	3.489

Gaus mean : 35.8168

Gaus Width: 3.9257

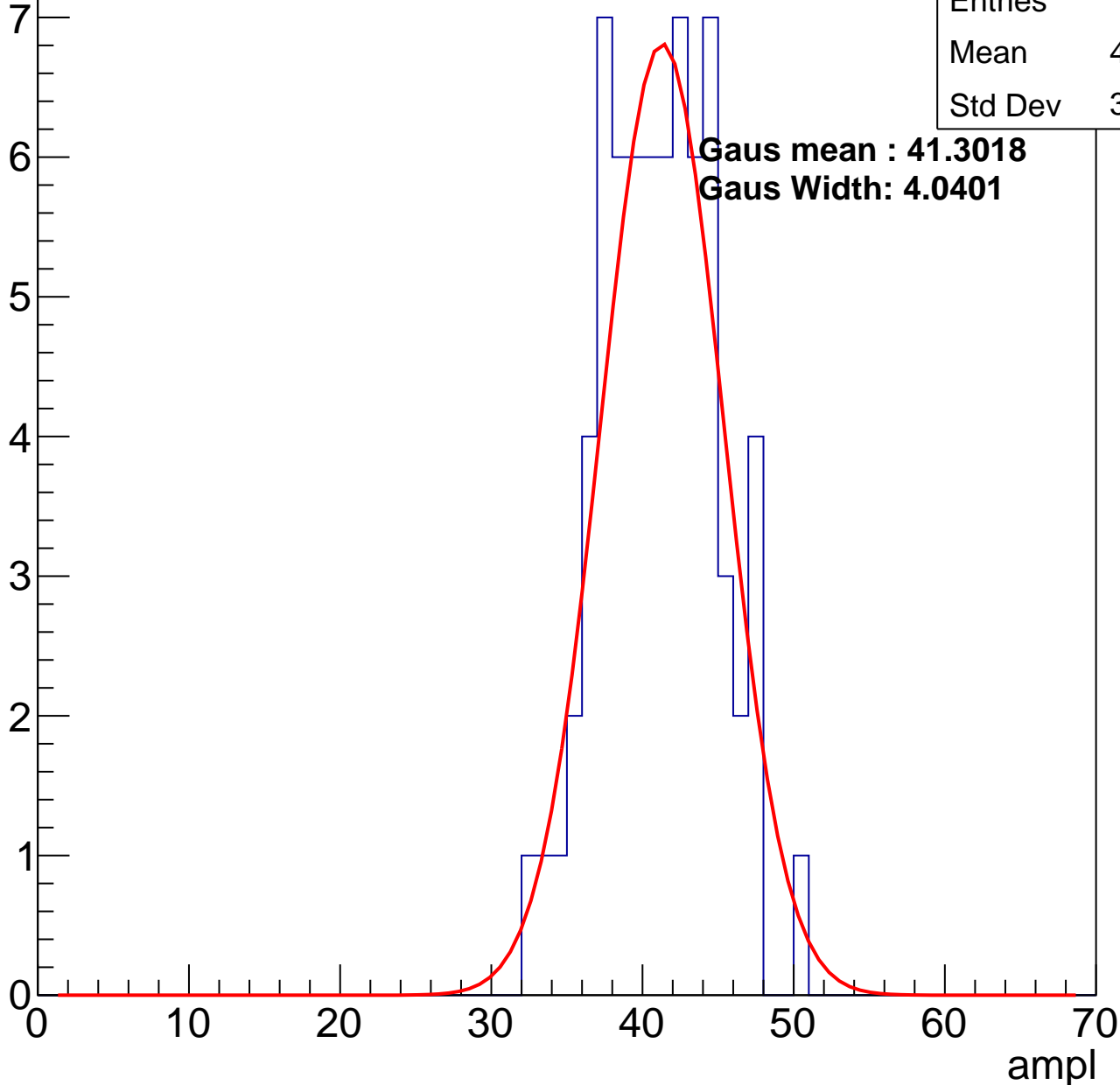


B1L101S, U1-ch47, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	40.64
Std Dev	3.723

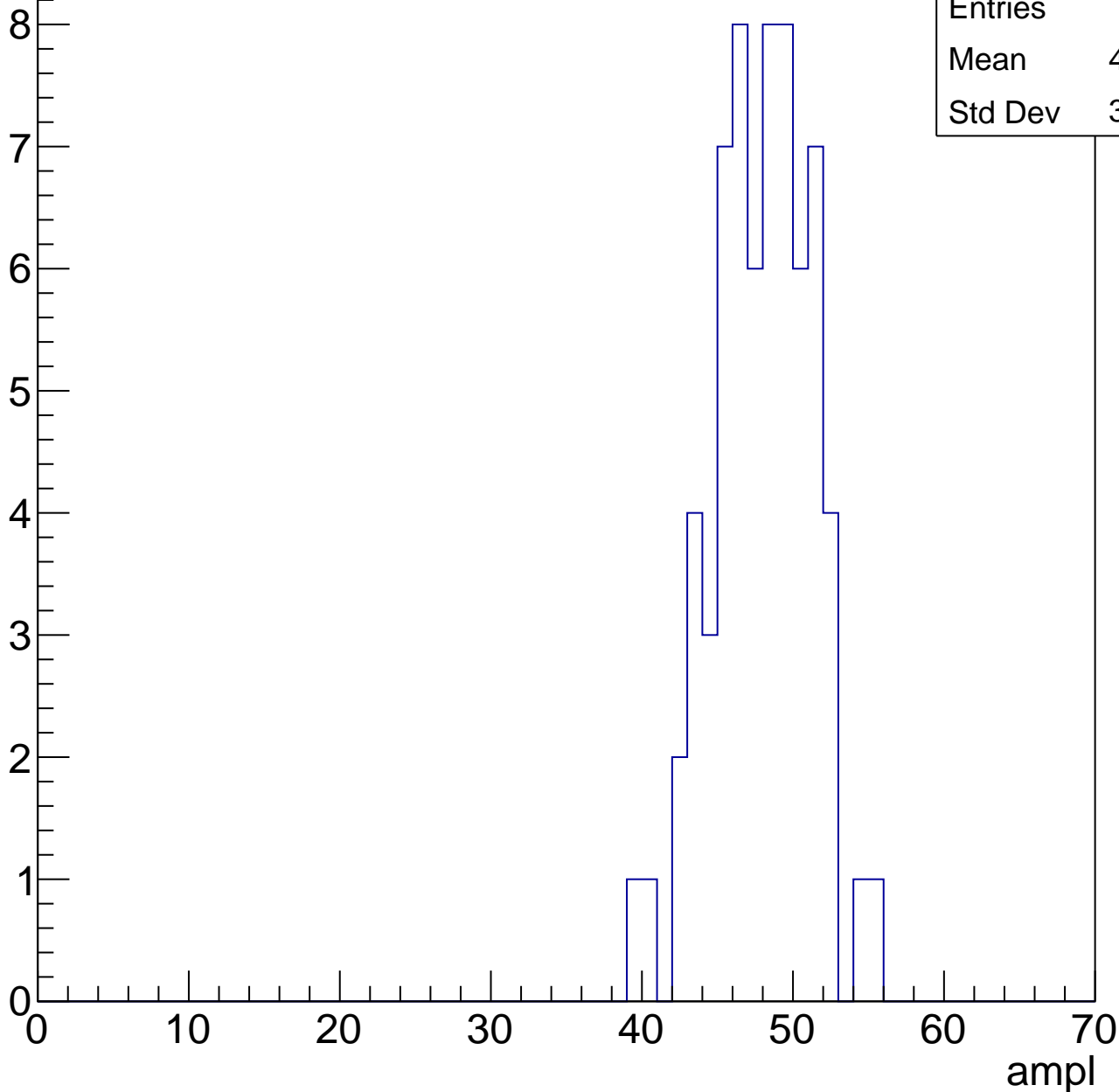


B1L101S, U1-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	47.49
Std Dev	3.216

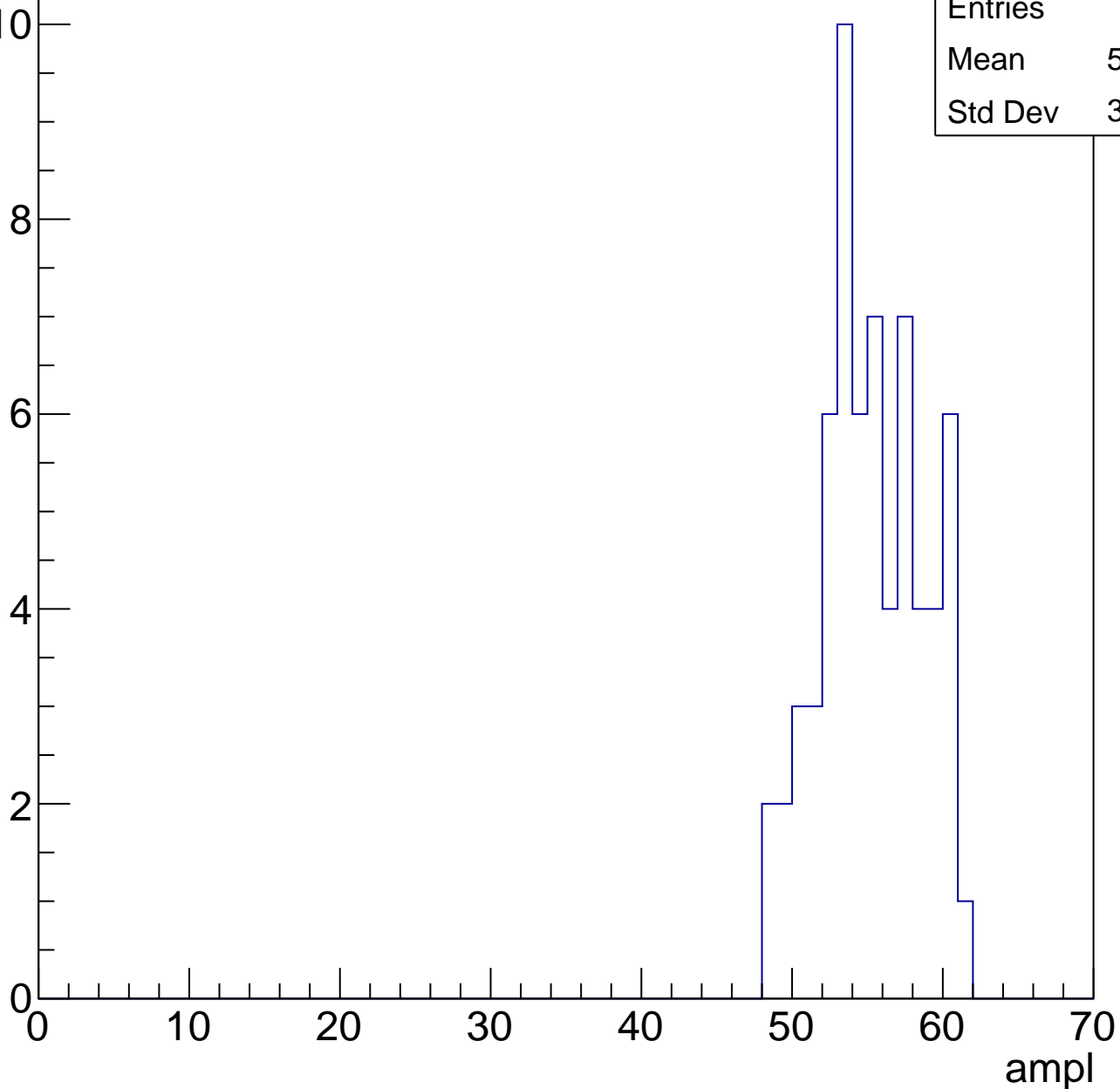


B1L101S, U1-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.77
Std Dev	3.299

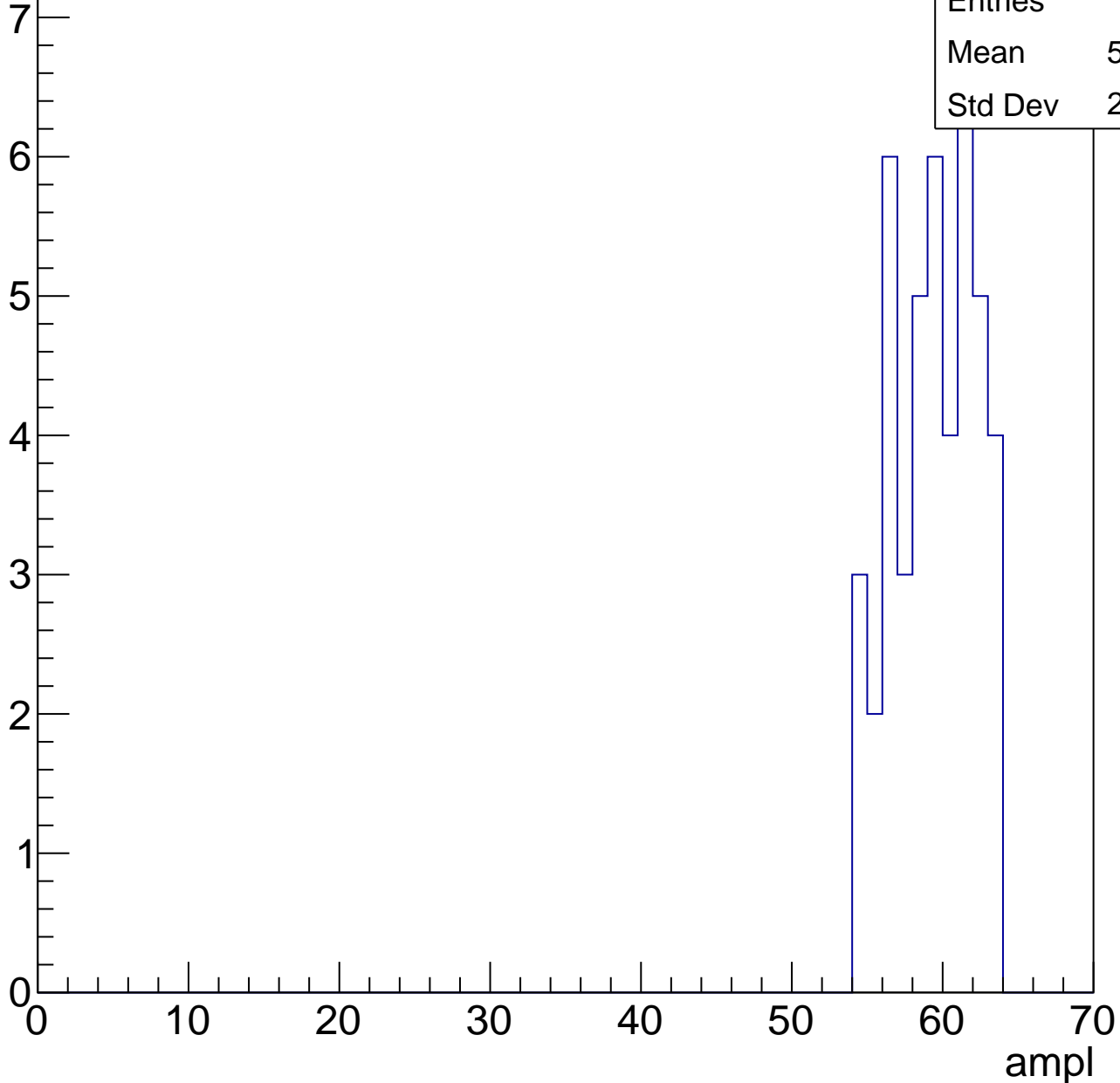


B1L101S, U1-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.93
Std Dev	2.662

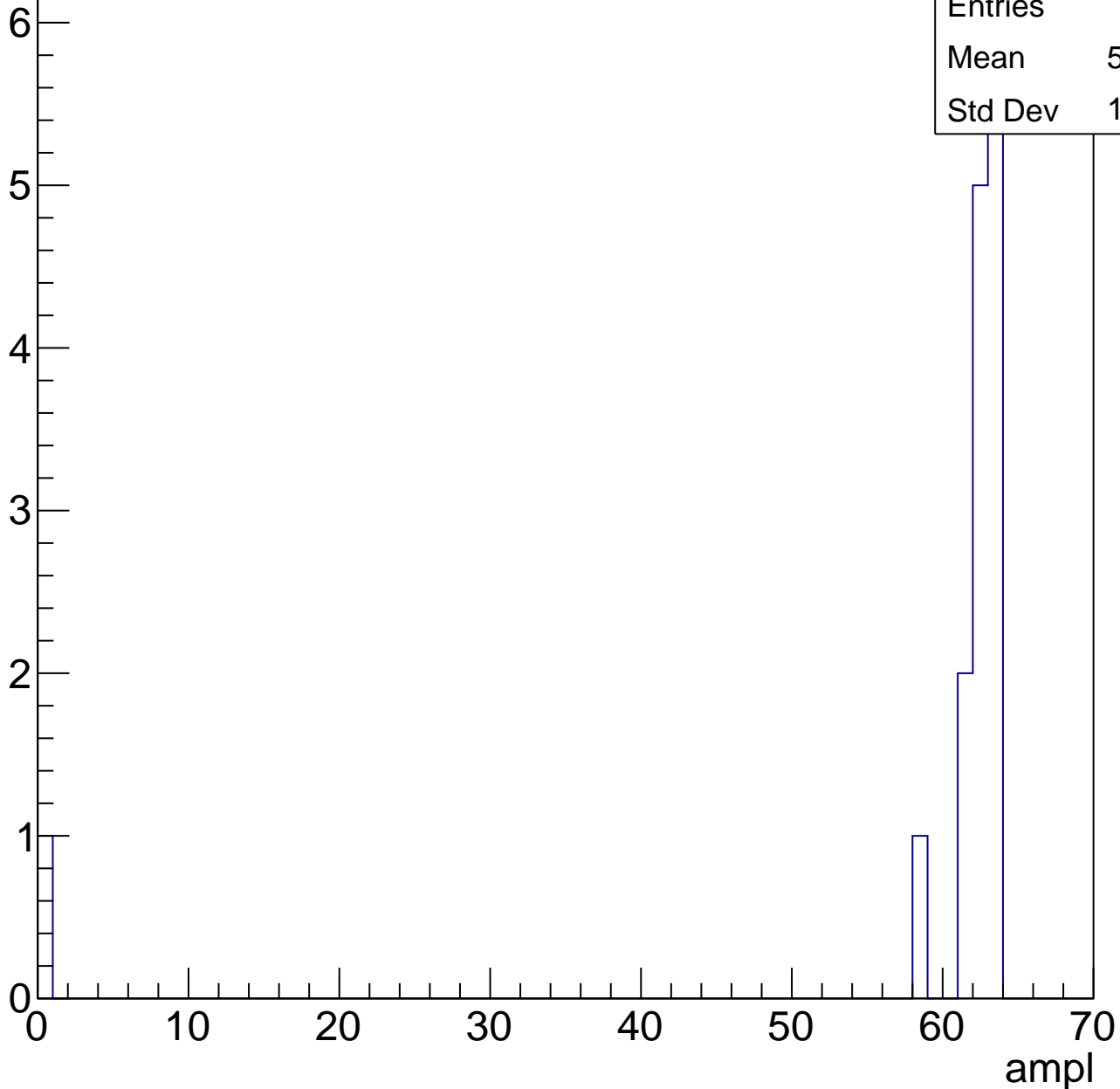


B1L101S, U1-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.87
Std Dev	15.52



B1L101S, U1-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch48, adc0

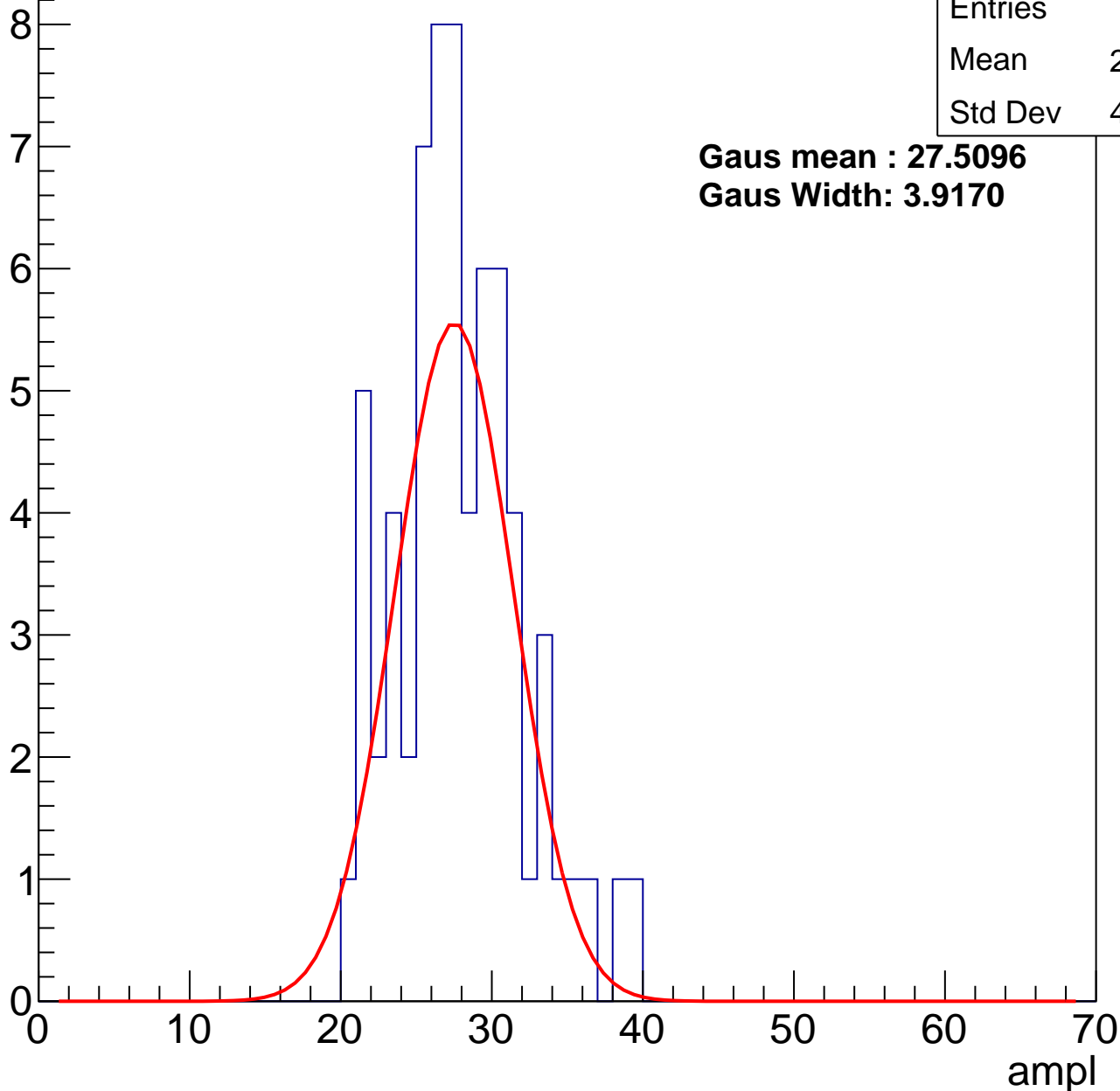
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.44
Std Dev	4.124

Gaus mean : 27.5096

Gaus Width: 3.9170



B1L101S, U1-ch48, adc1

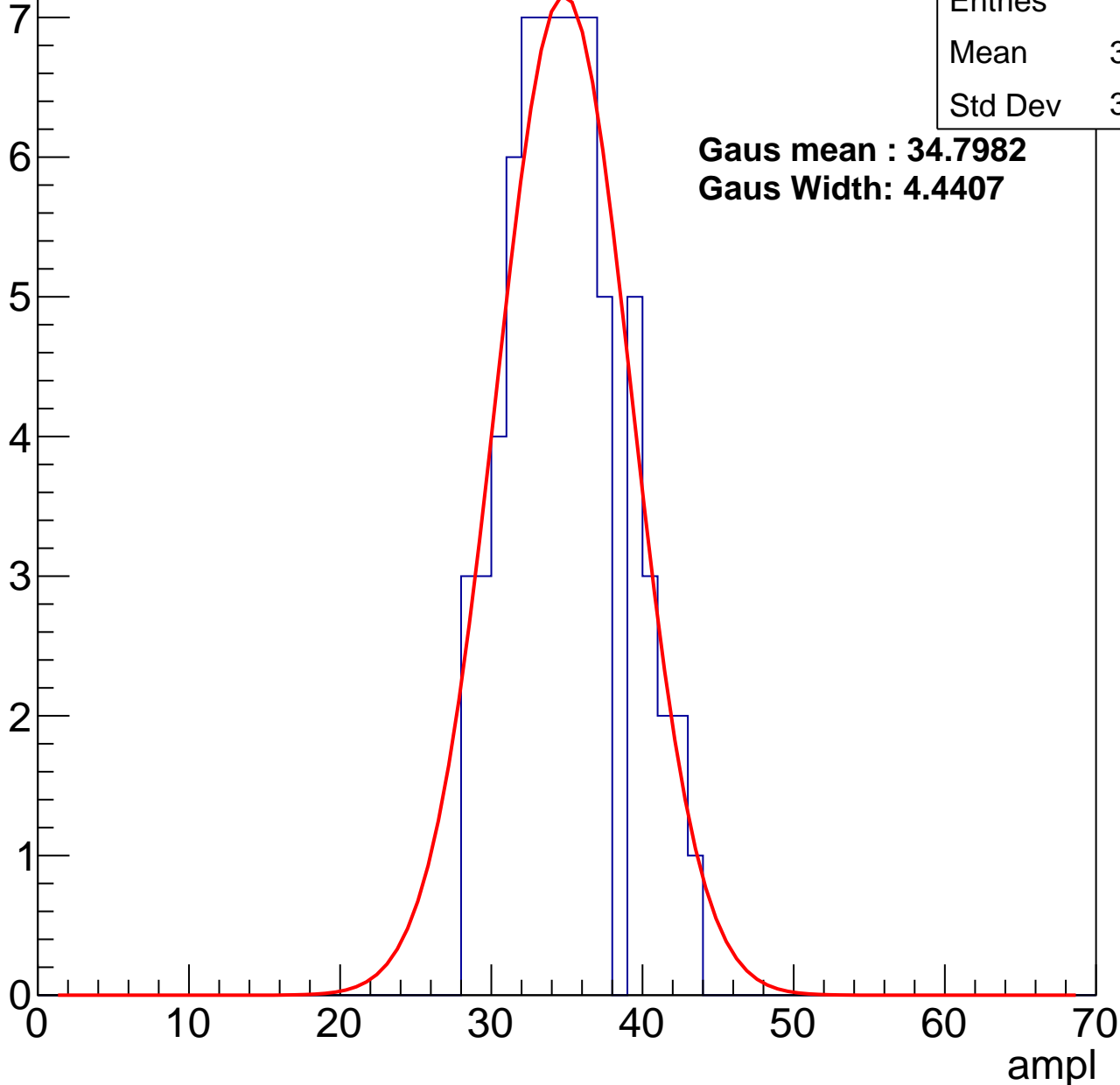
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	34.43
Std Dev	3.697

Gaus mean : 34.7982

Gaus Width: 4.4407



B1L101S, U1-ch48, adc2

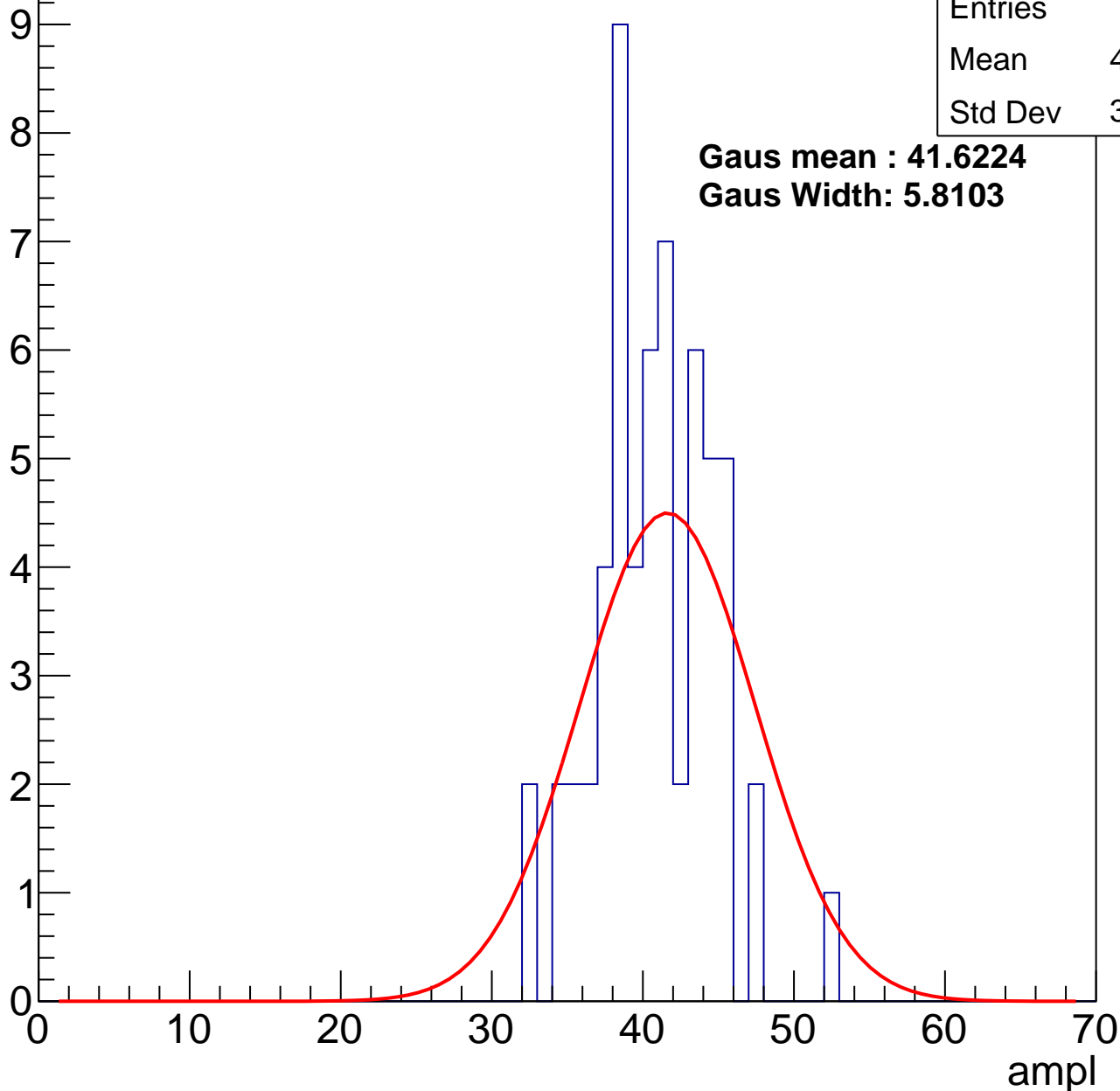
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	40.34
Std Dev	3.834

Gaus mean : 41.6224

Gaus Width: 5.8103

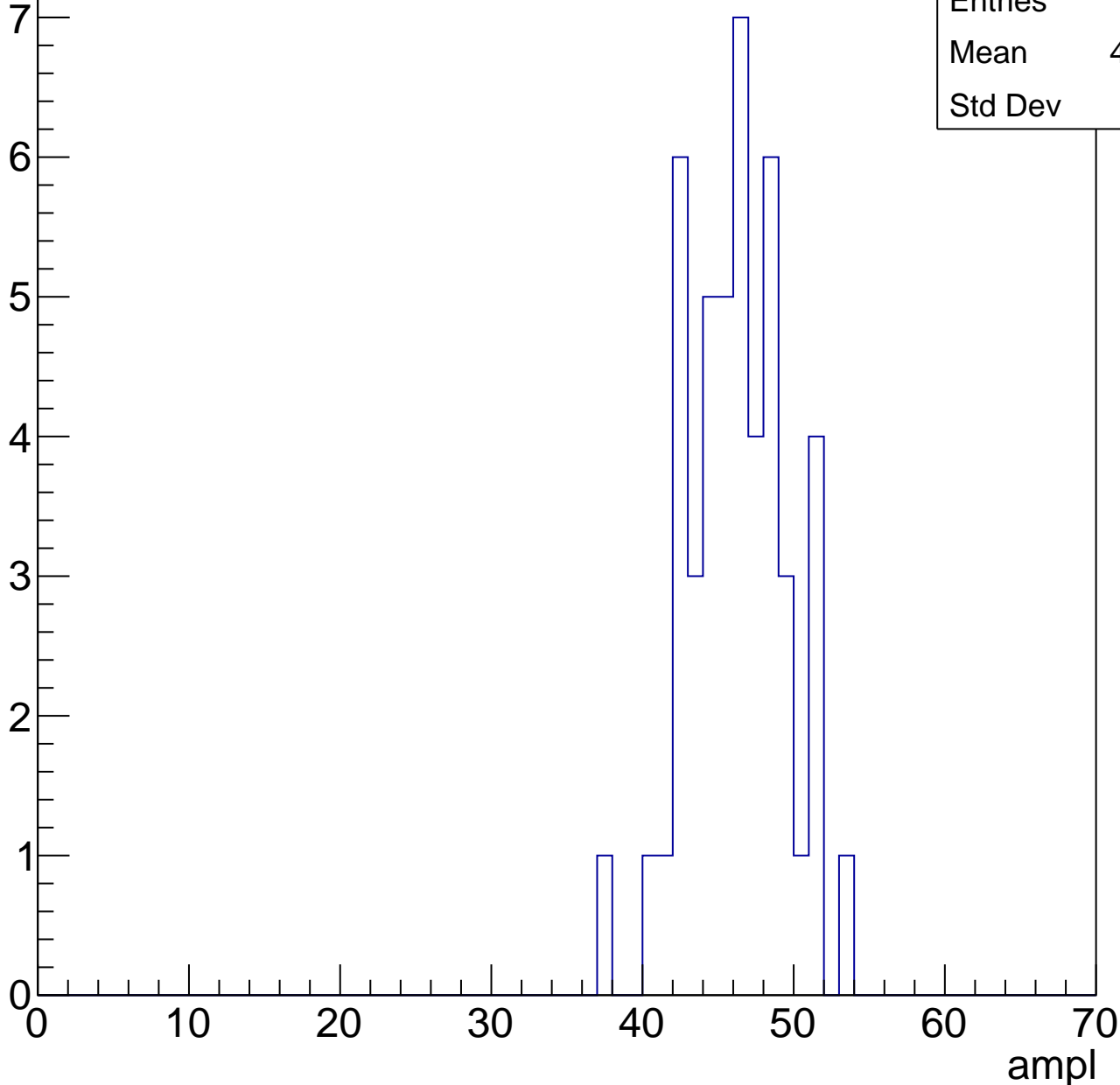


B1L101S, U1-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

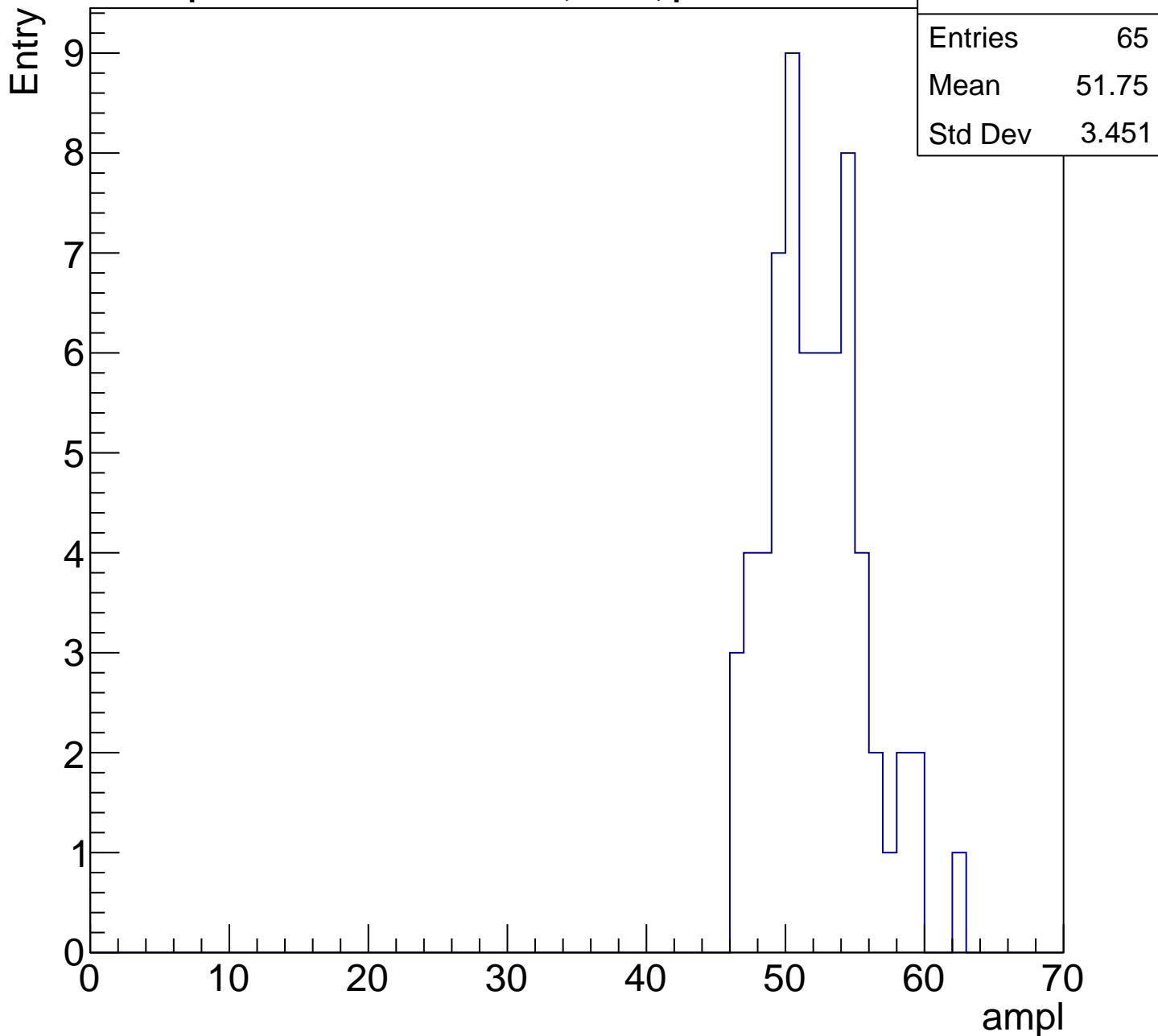
Entry

Entries	48
Mean	45.75
Std Dev	3.25



B1L101S, U1-ch48, adc4

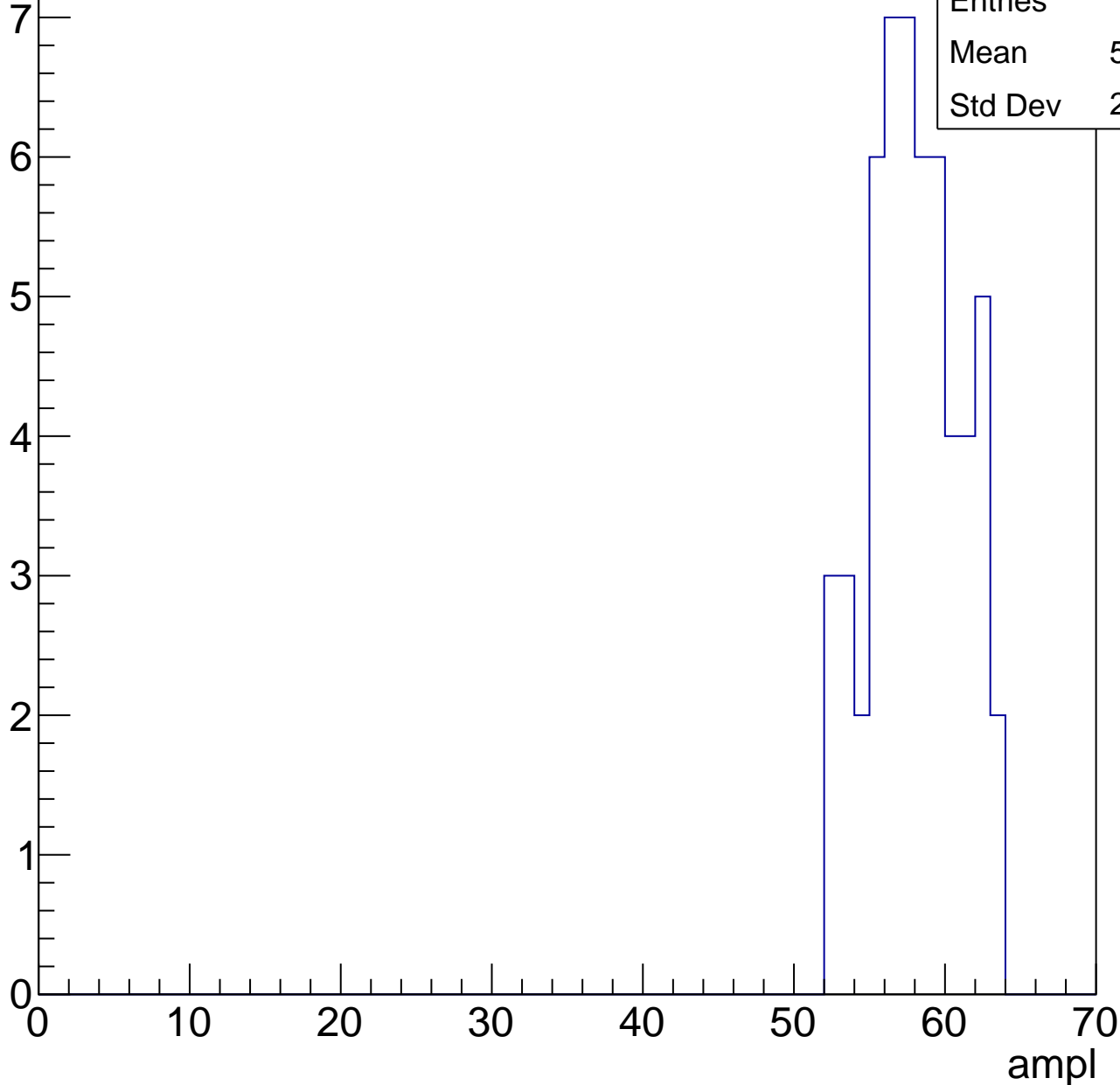
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

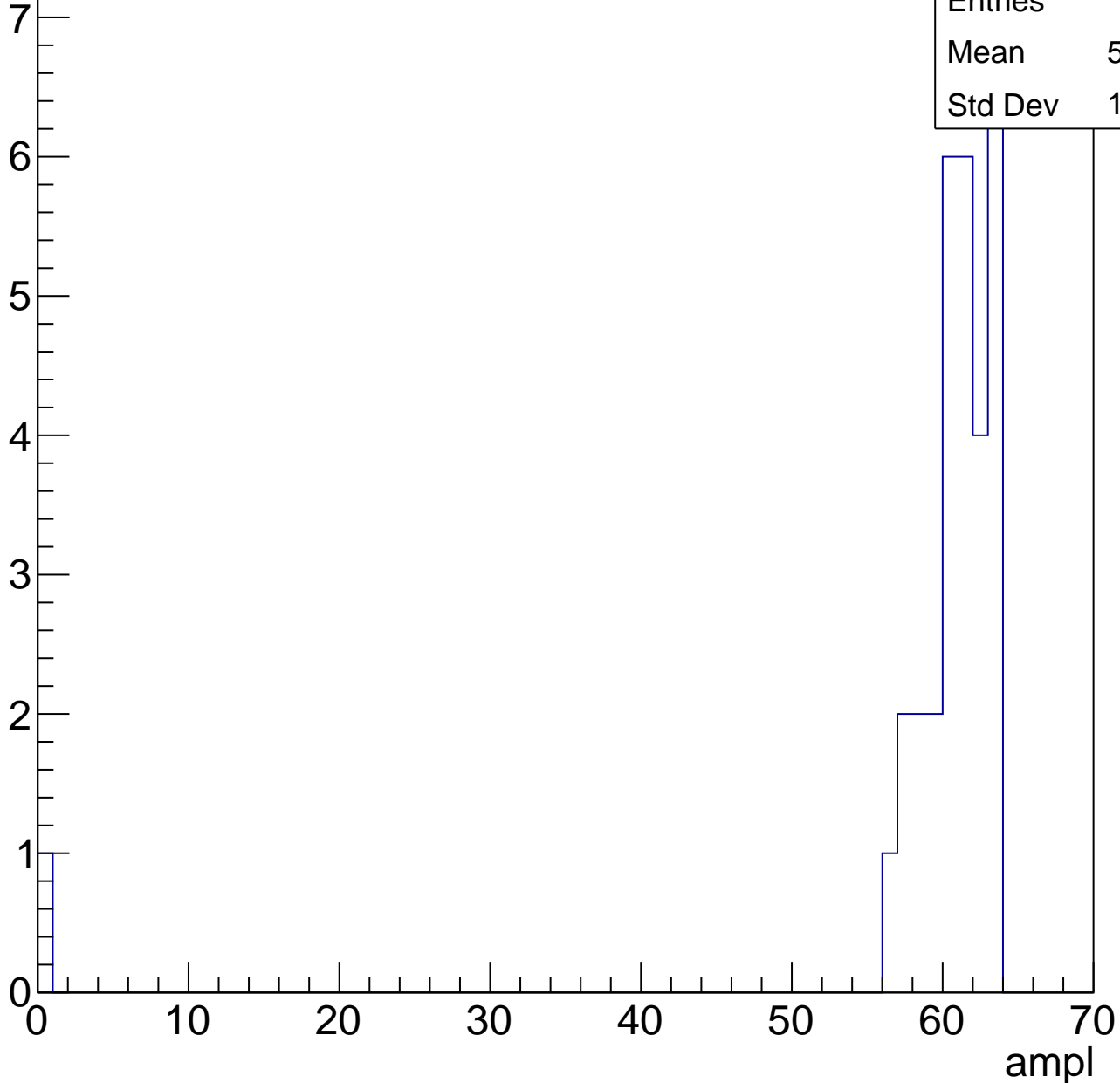


B1L101S, U1-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.68
Std Dev	10.89



B1L101S, U1-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch49, adc0

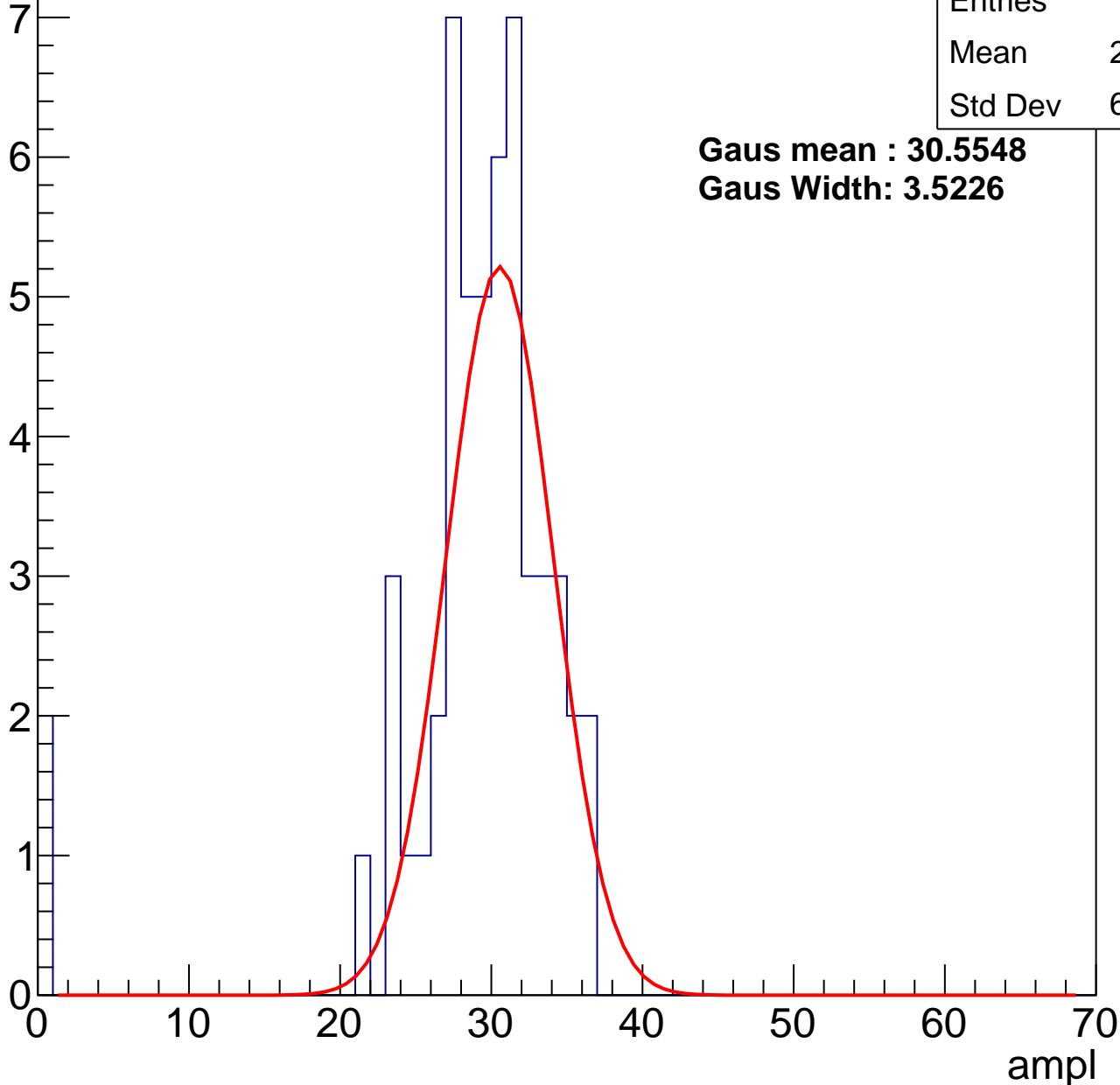
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	28.32
Std Dev	6.552

Gaus mean : 30.5548

Gaus Width: 3.5226



B1L101S, U1-ch49, adc1

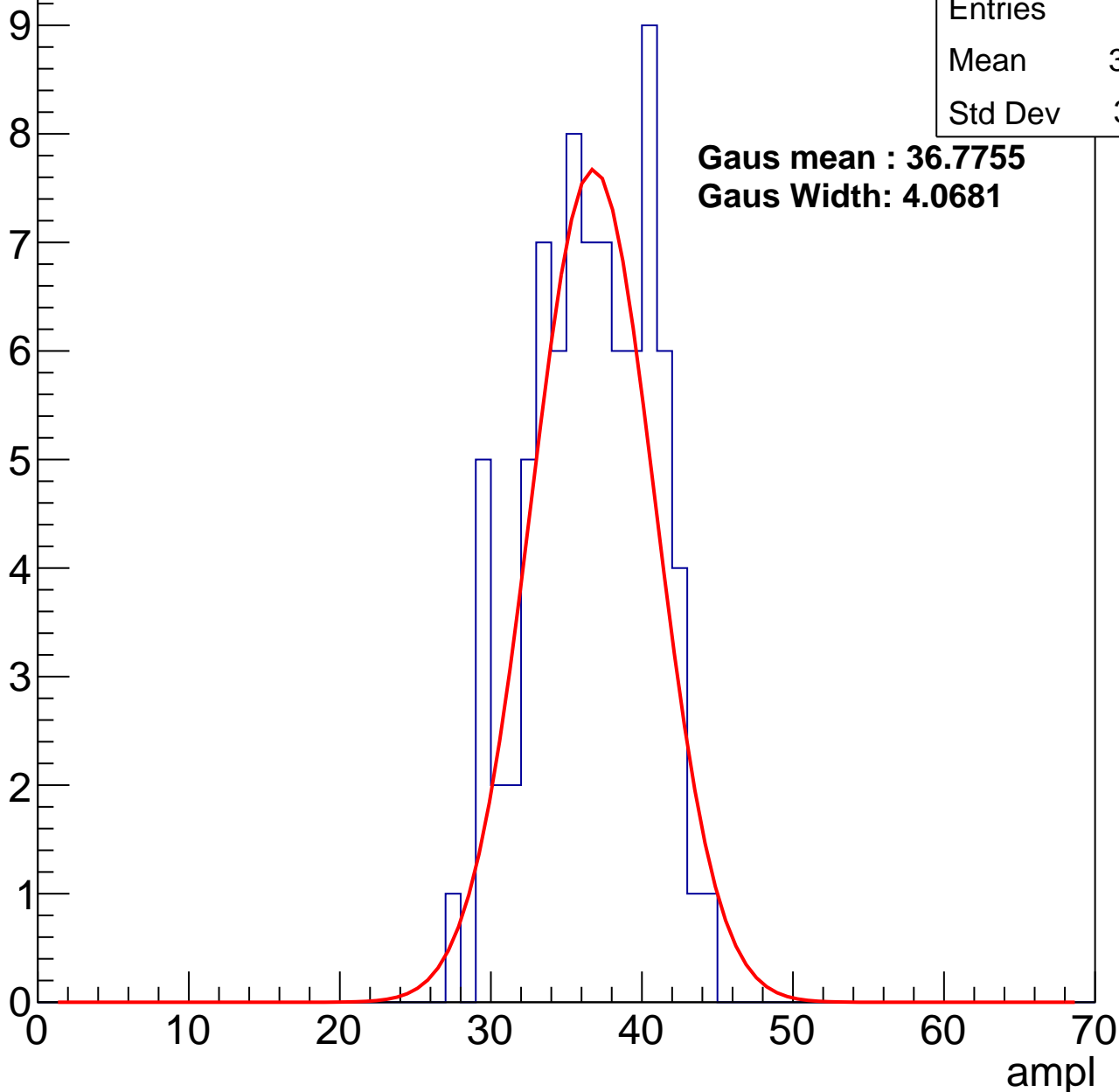
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.18
Std Dev	3.891

Gaus mean : 36.7755

Gaus Width: 4.0681



B1L101S, U1-ch49, adc2

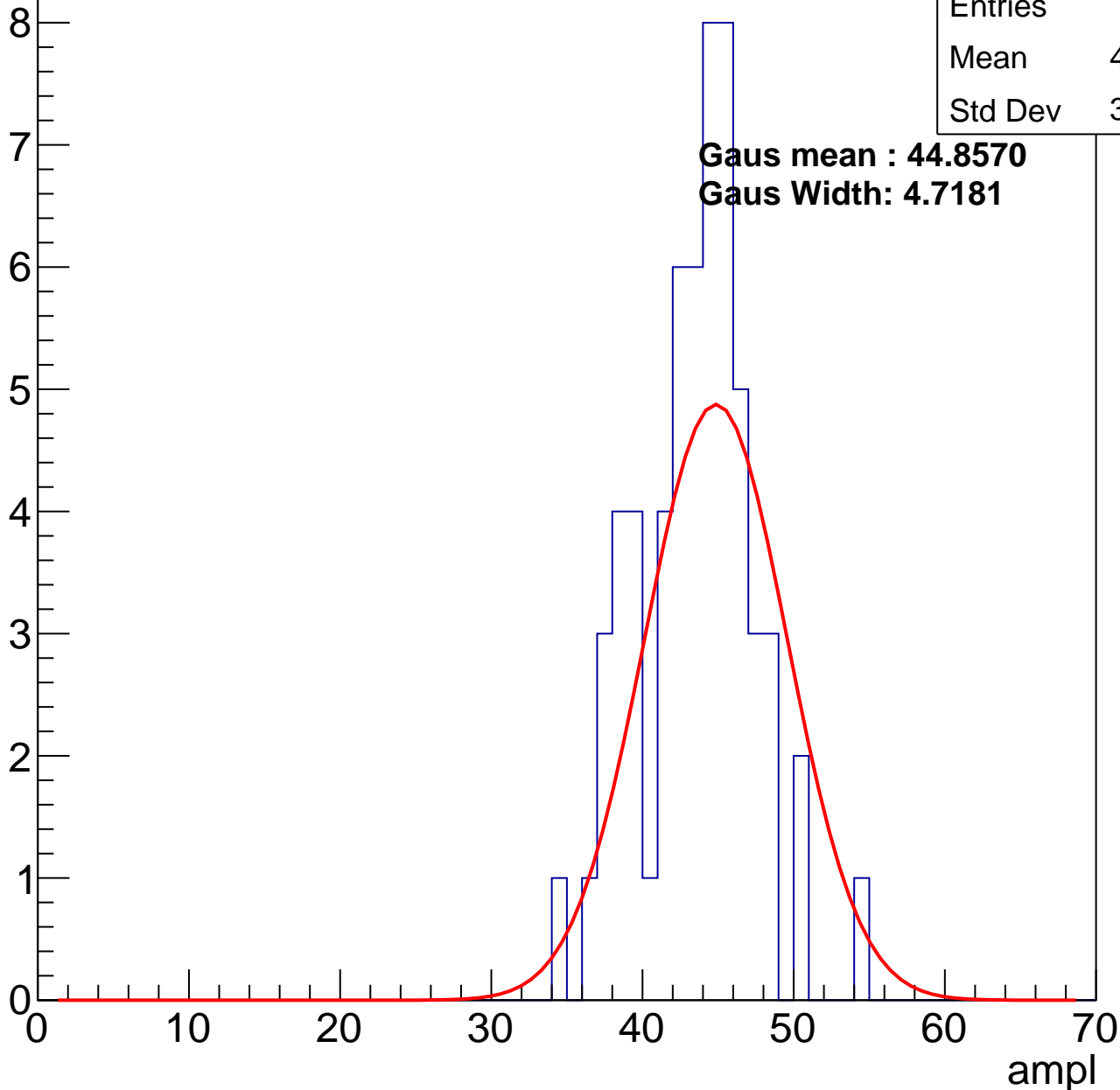
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.07
Std Dev	3.785

Gaus mean : 44.8570

Gaus Width: 4.7181

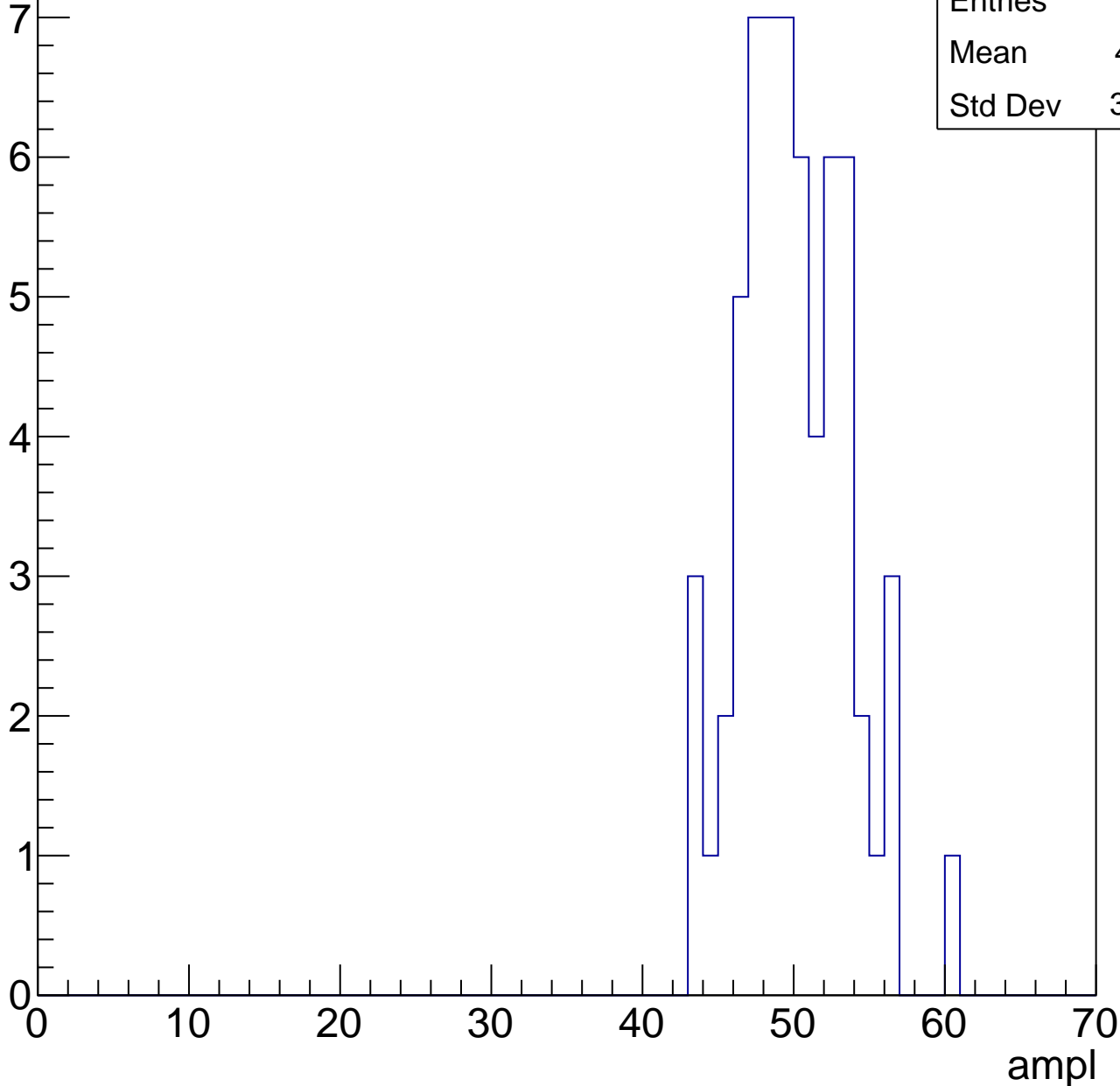


B1L101S, U1-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

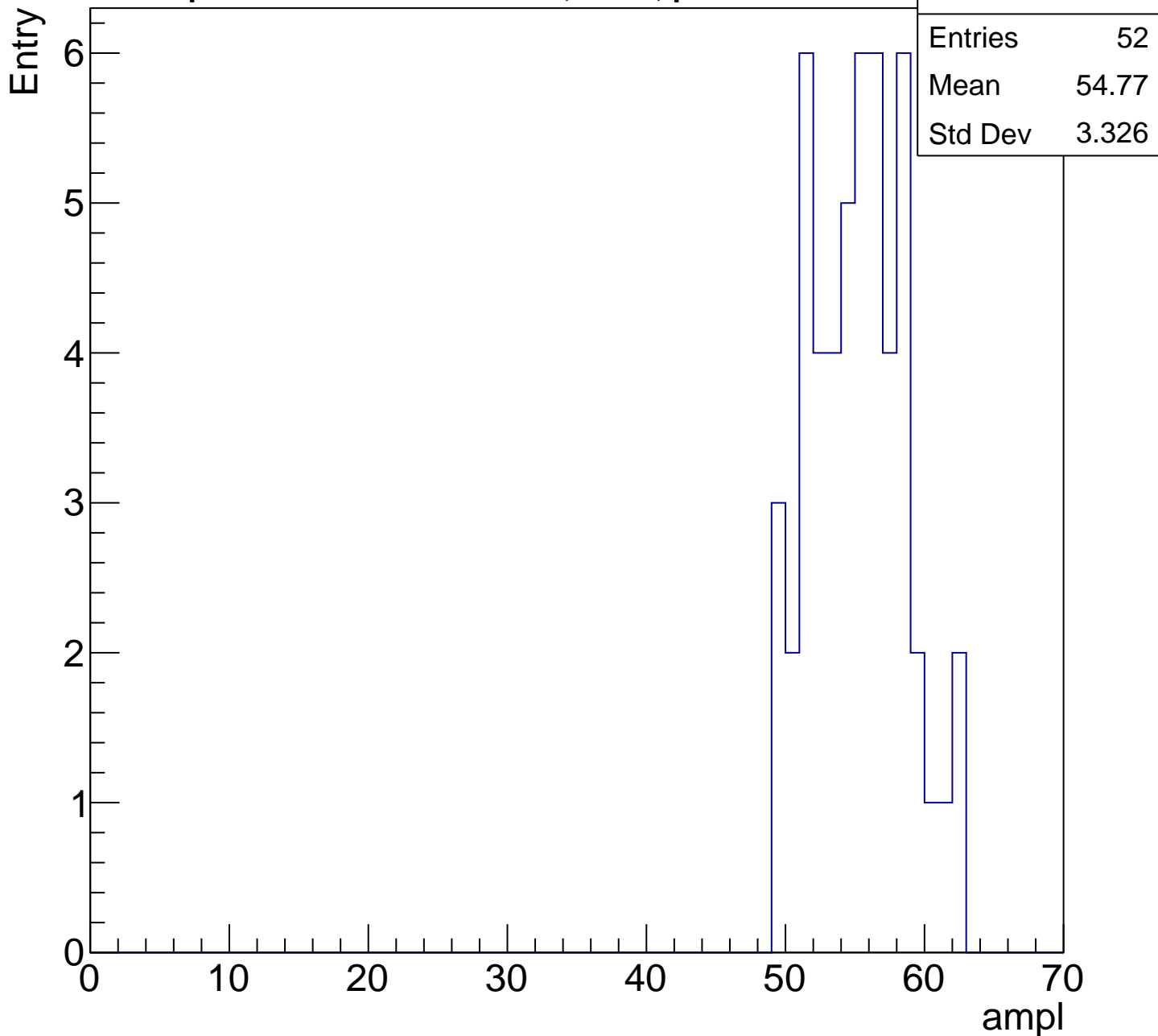
Entry

Entries	61
Mean	49.61
Std Dev	3.503



B1L101S, U1-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

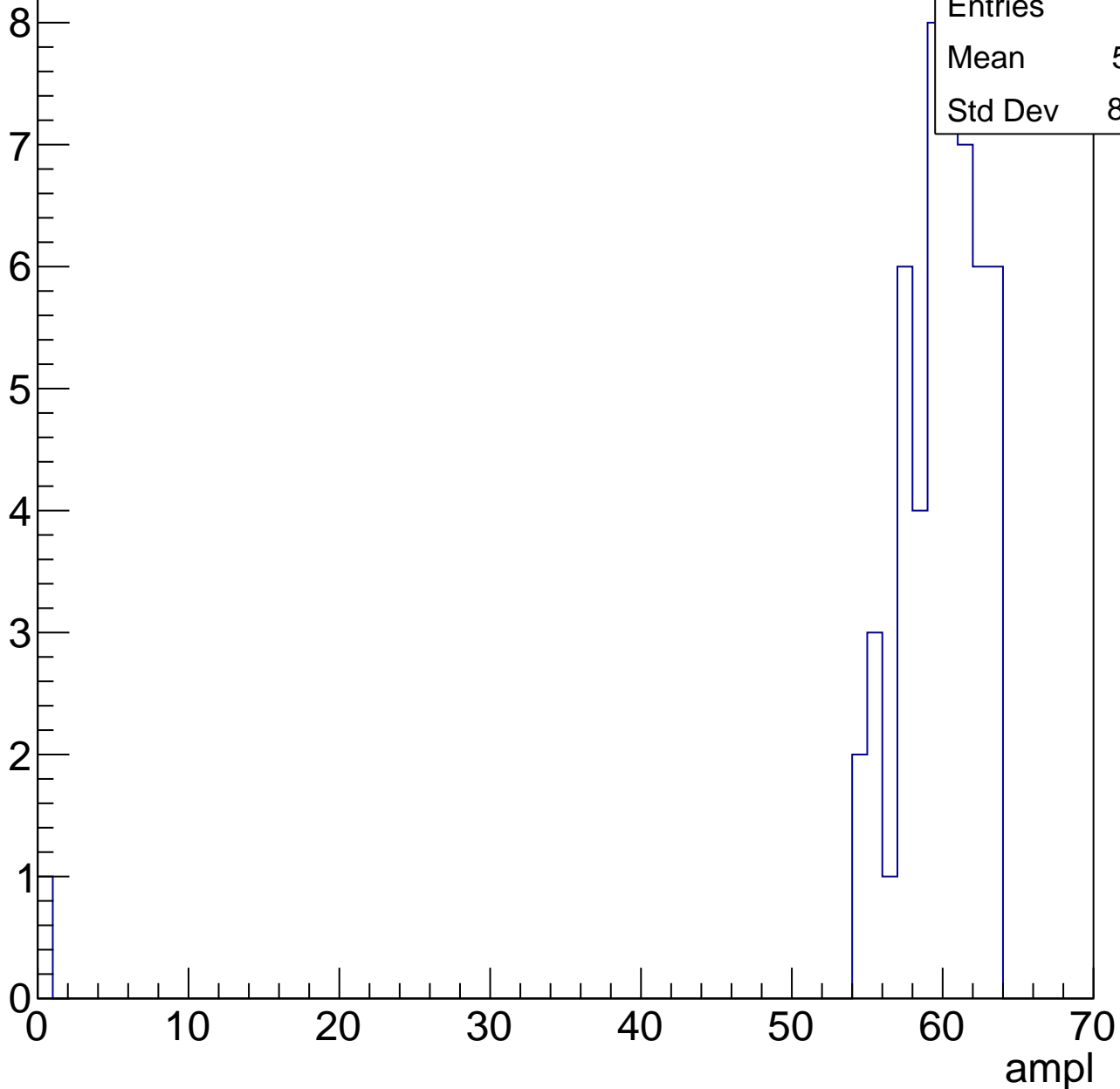


B1L101S, U1-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

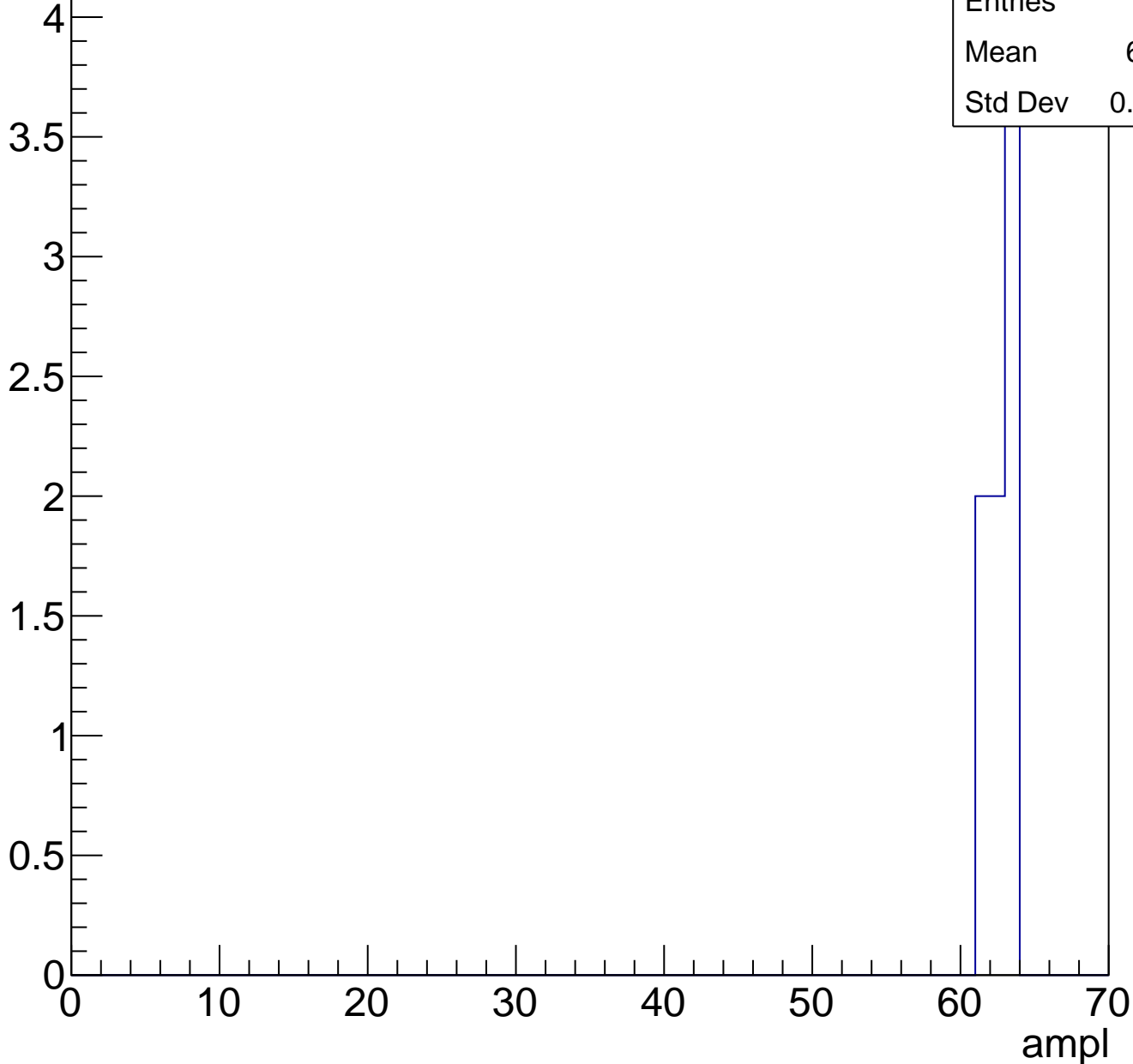
Entries	52
Mean	58.31
Std Dev	8.523



B1L101S, U1-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch50, adc0

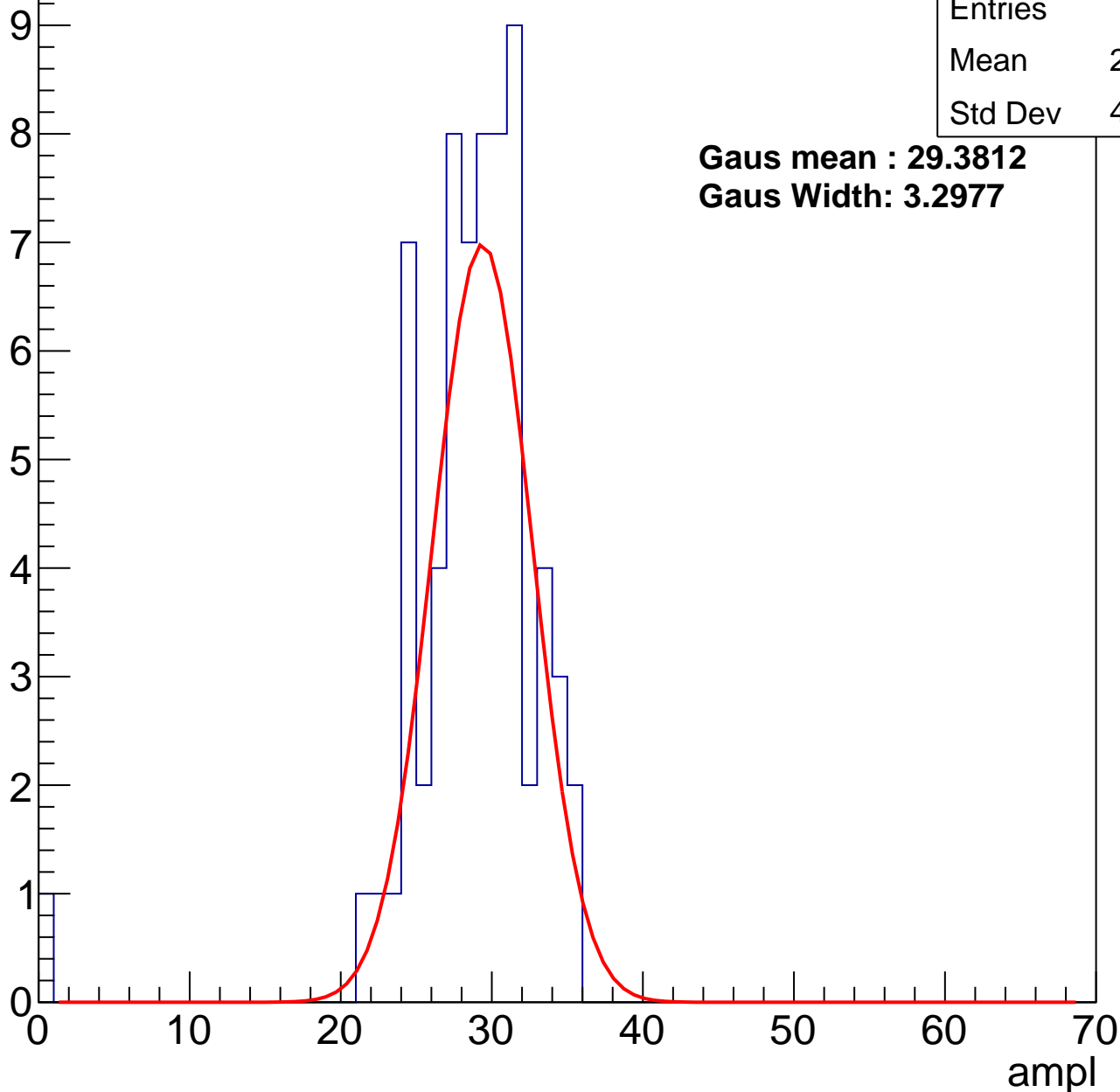
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.22
Std Dev	4.702

Gaus mean : 29.3812

Gaus Width: 3.2977



B1L101S, U1-ch50, adc1

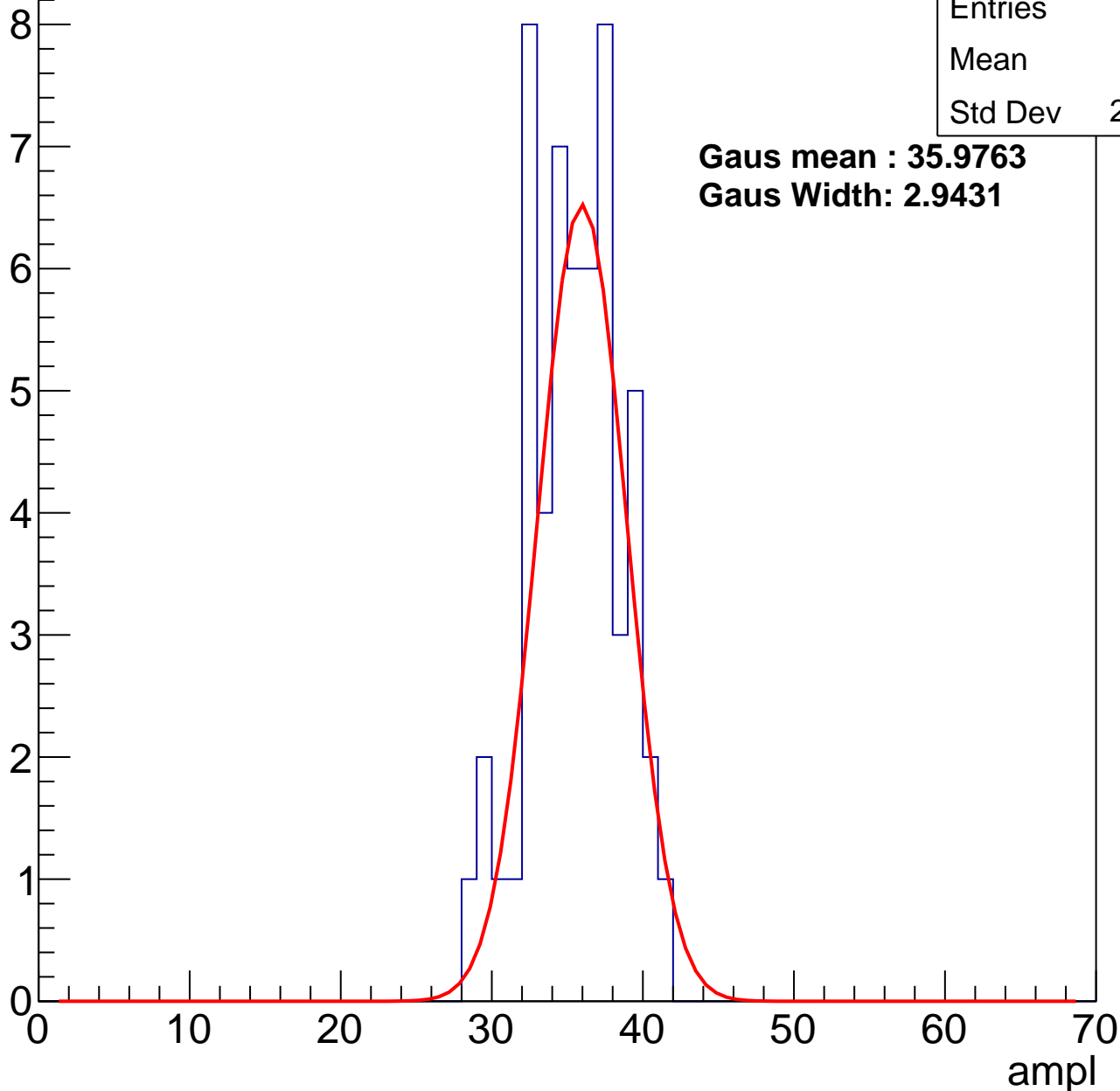
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35
Std Dev	2.979

Gaus mean : 35.9763

Gaus Width: 2.9431



B1L101S, U1-ch50, adc2

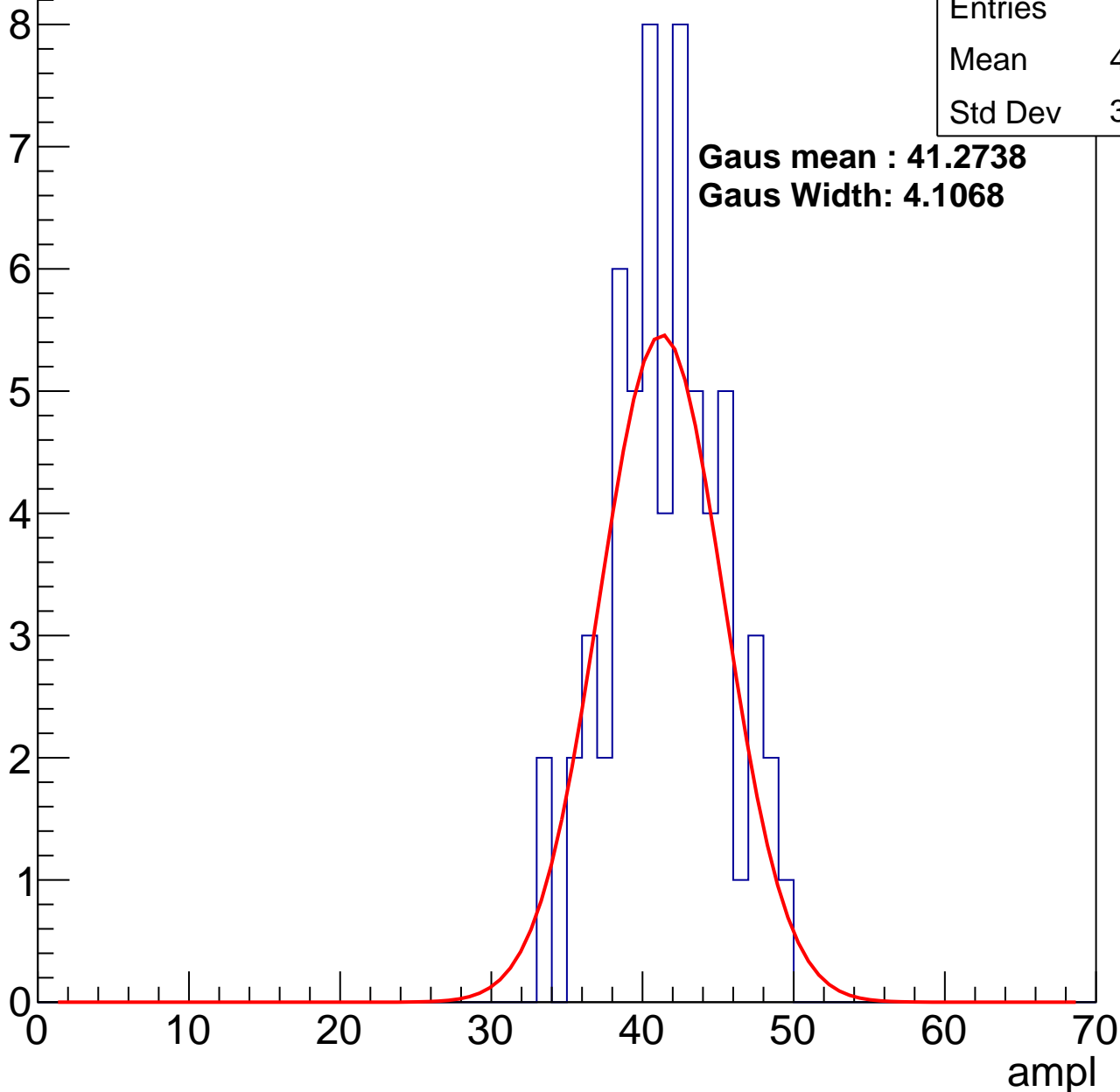
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.13
Std Dev	3.682

Gaus mean : 41.2738

Gaus Width: 4.1068

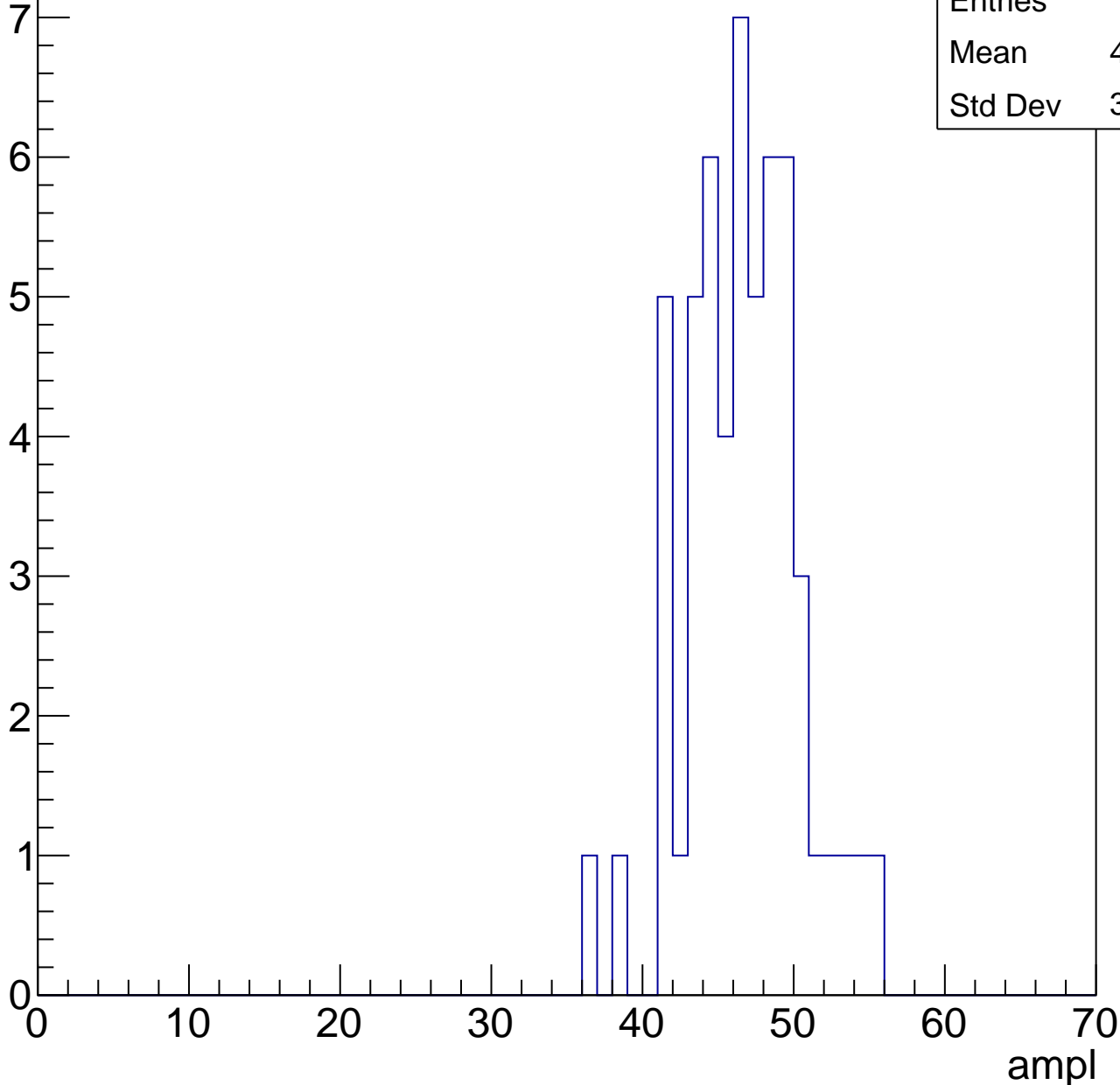


B1L101S, U1-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	46.07
Std Dev	3.726

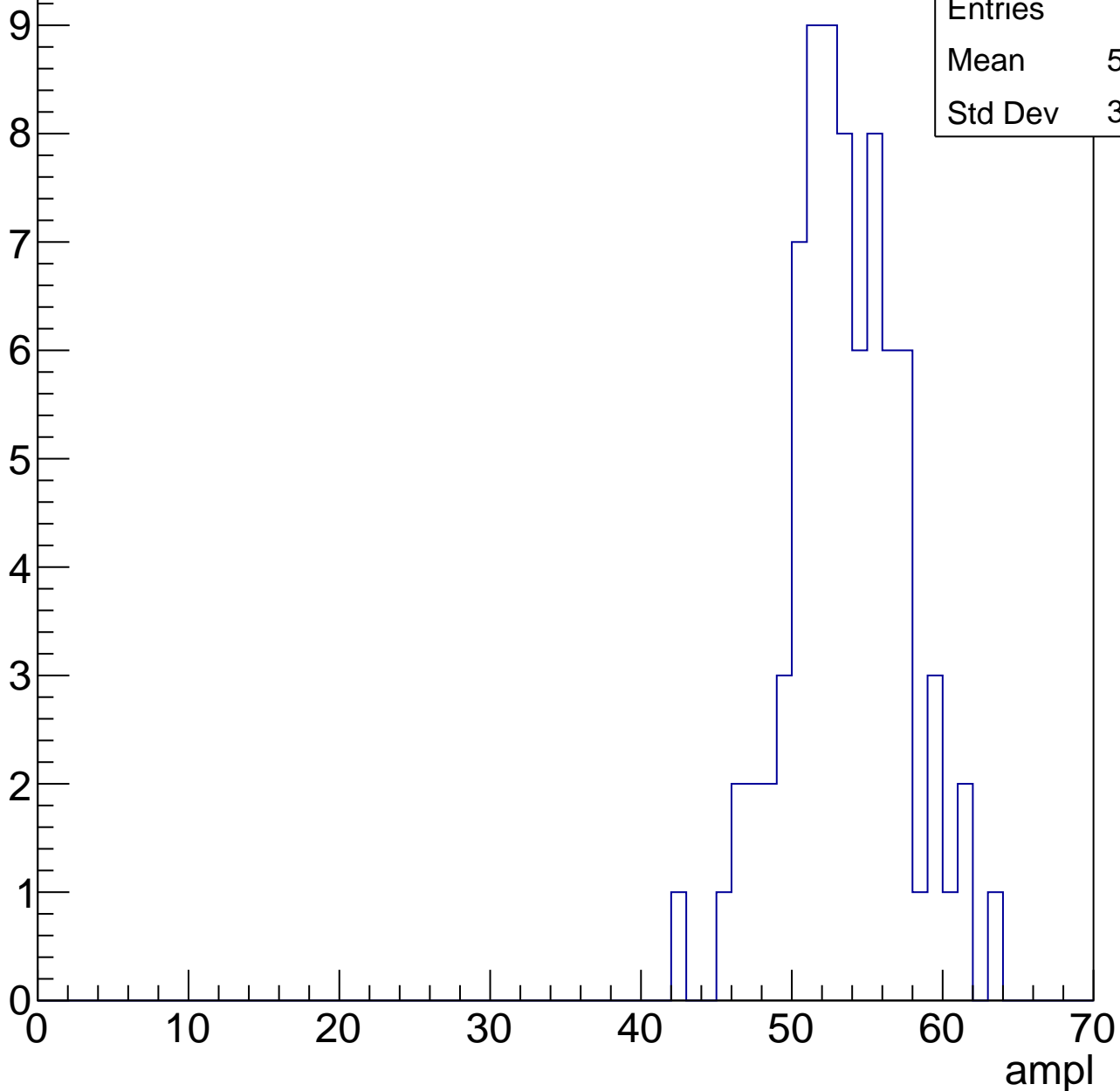


B1L101S, U1-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	53.06
Std Dev	3.854

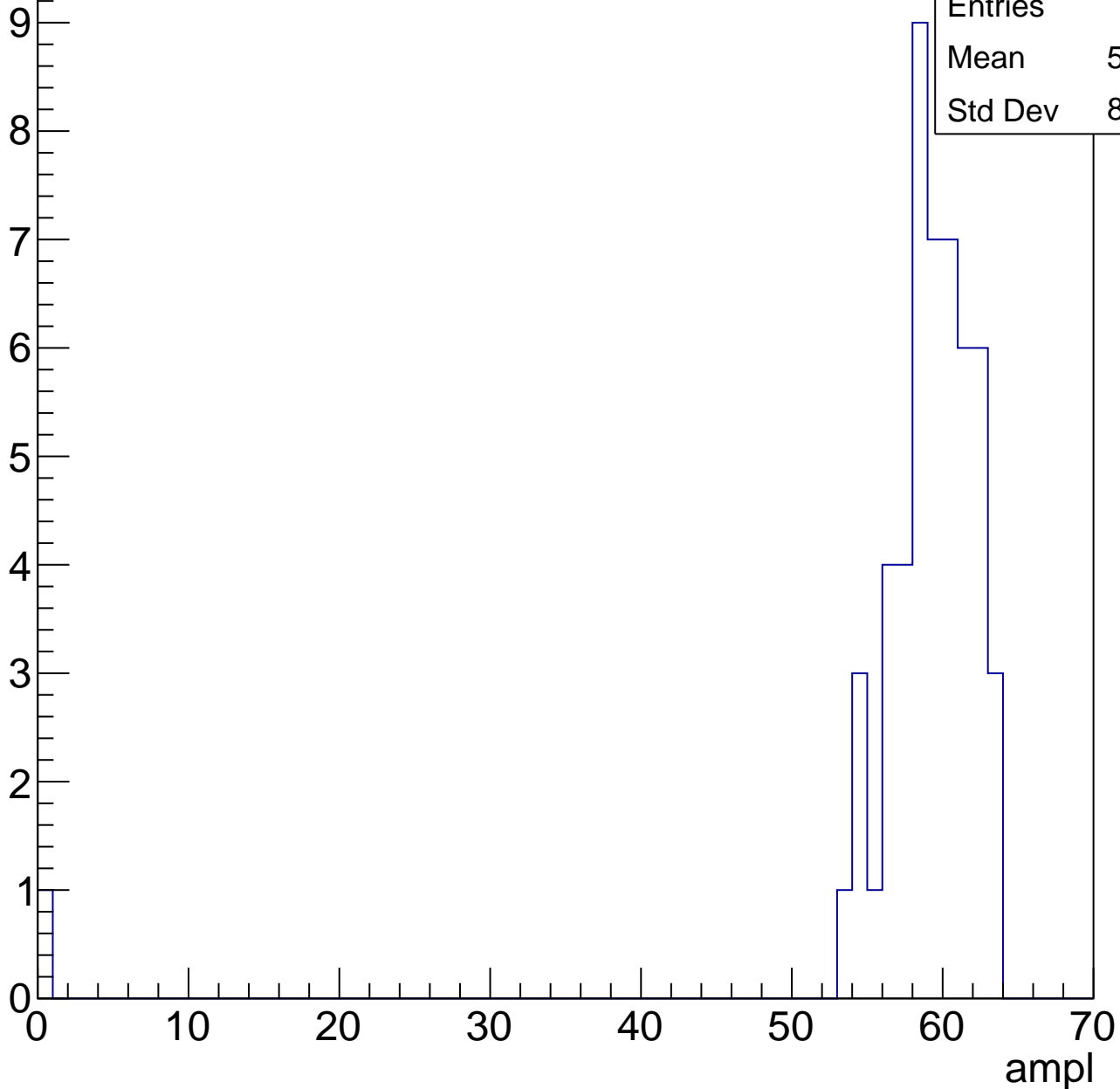


B1L101S, U1-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.77
Std Dev	8.462

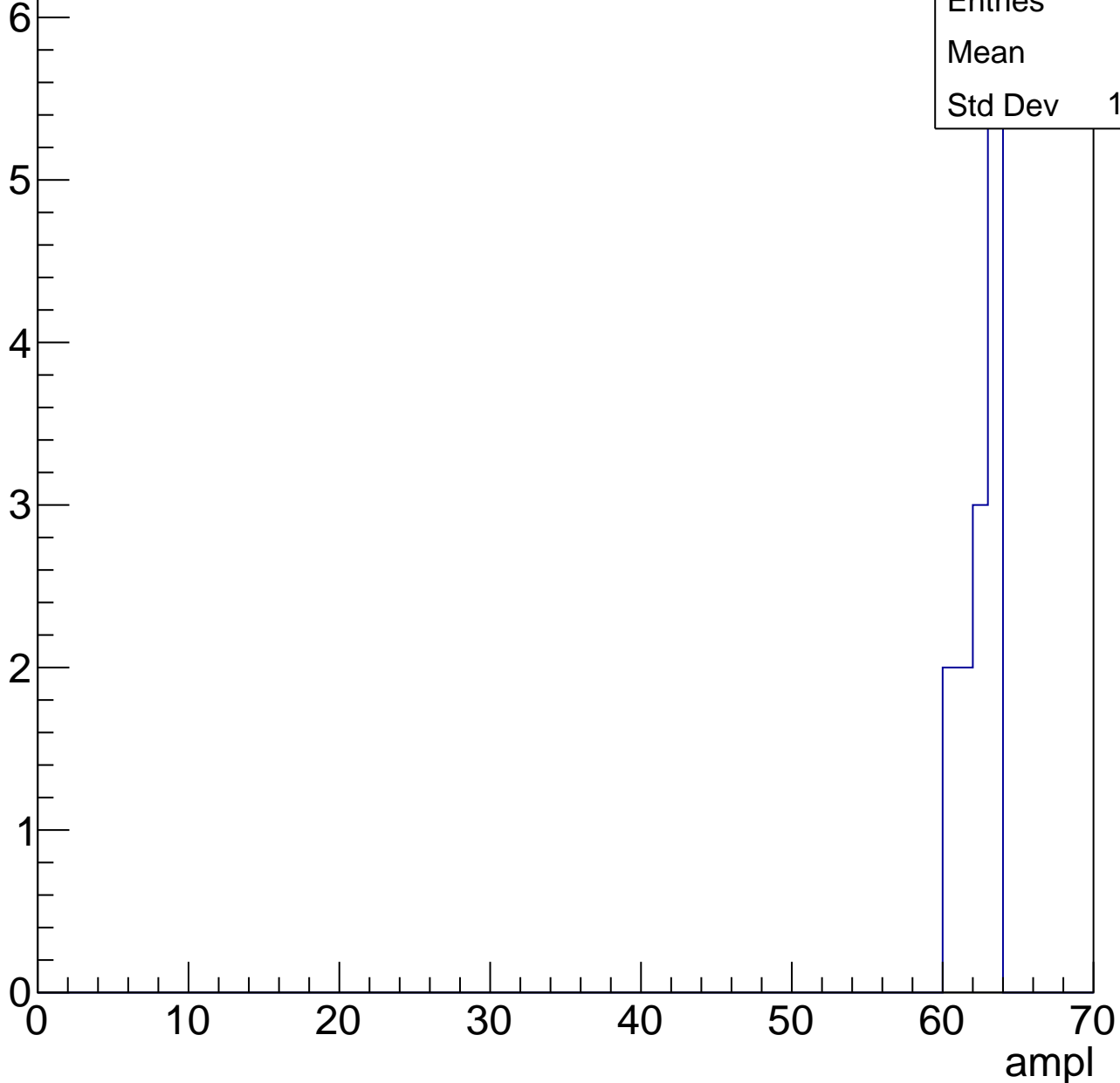


B1L101S, U1-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	62
Std Dev	1.109



B1L101S, U1-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch51, adc0

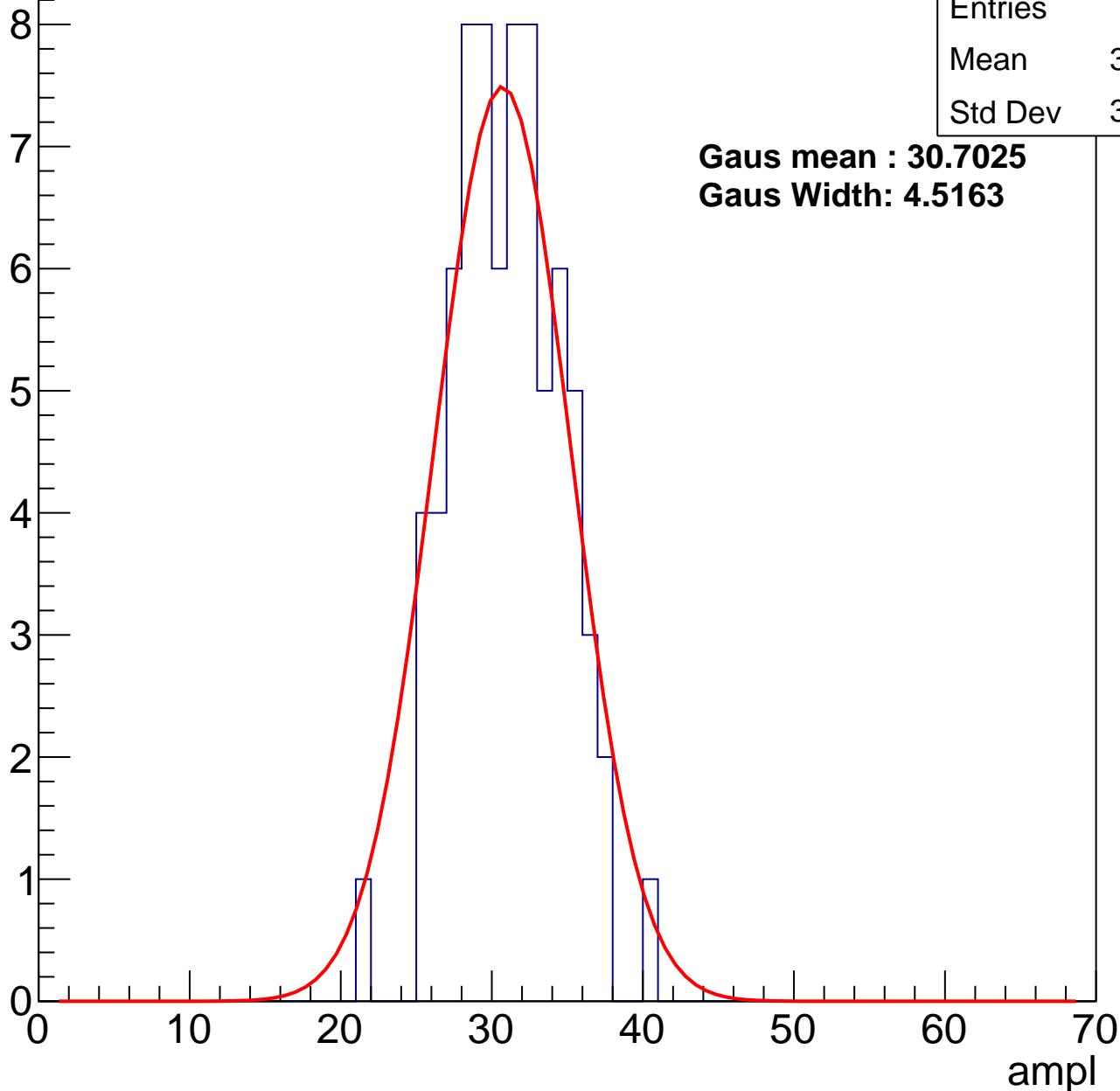
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.57
Std Dev	3.514

Gaus mean : 30.7025

Gaus Width: 4.5163



B1L101S, U1-ch51, adc1

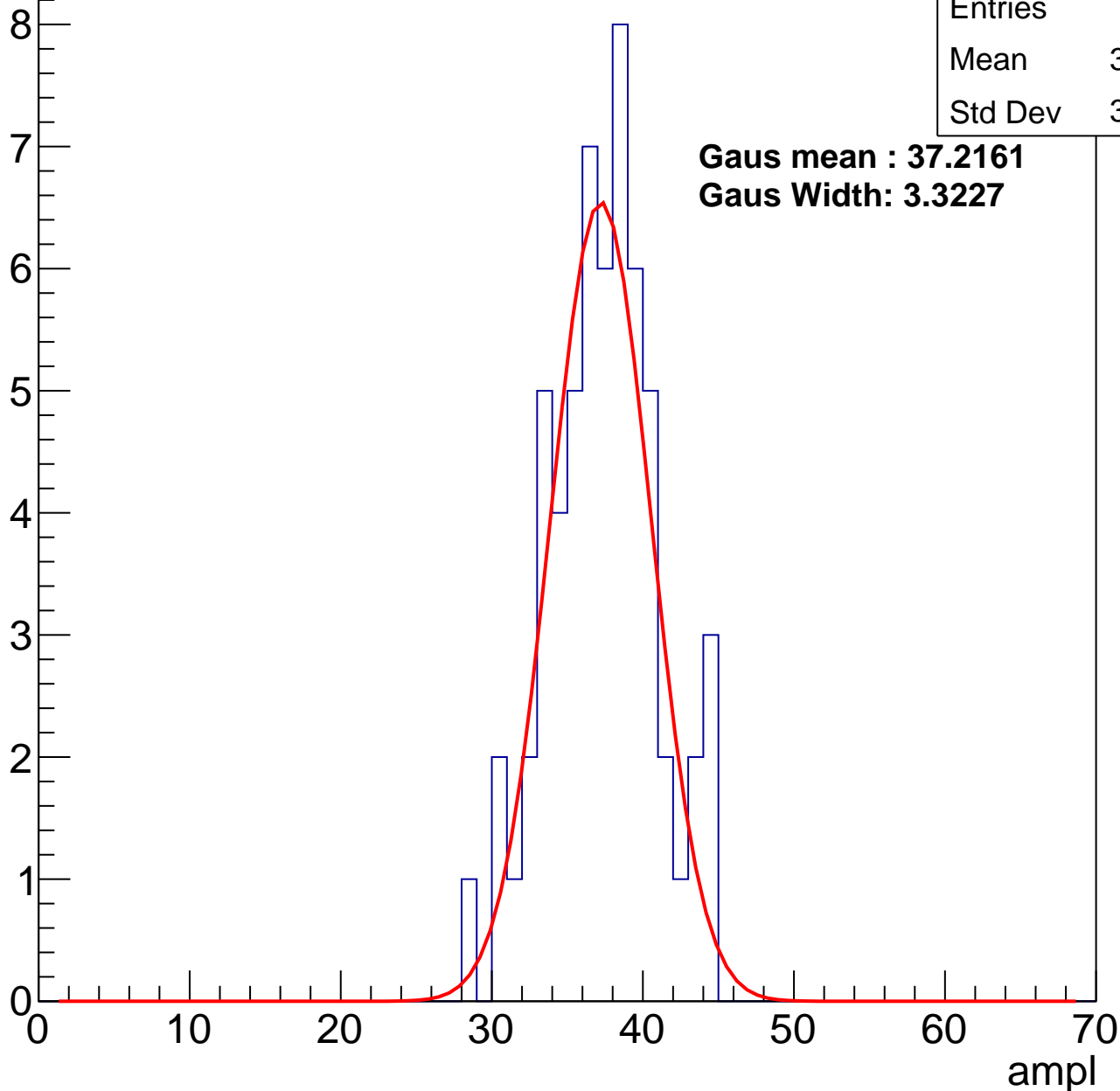
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.88
Std Dev	3.559

Gaus mean : 37.2161

Gaus Width: 3.3227



B1L101S, U1-ch51, adc2

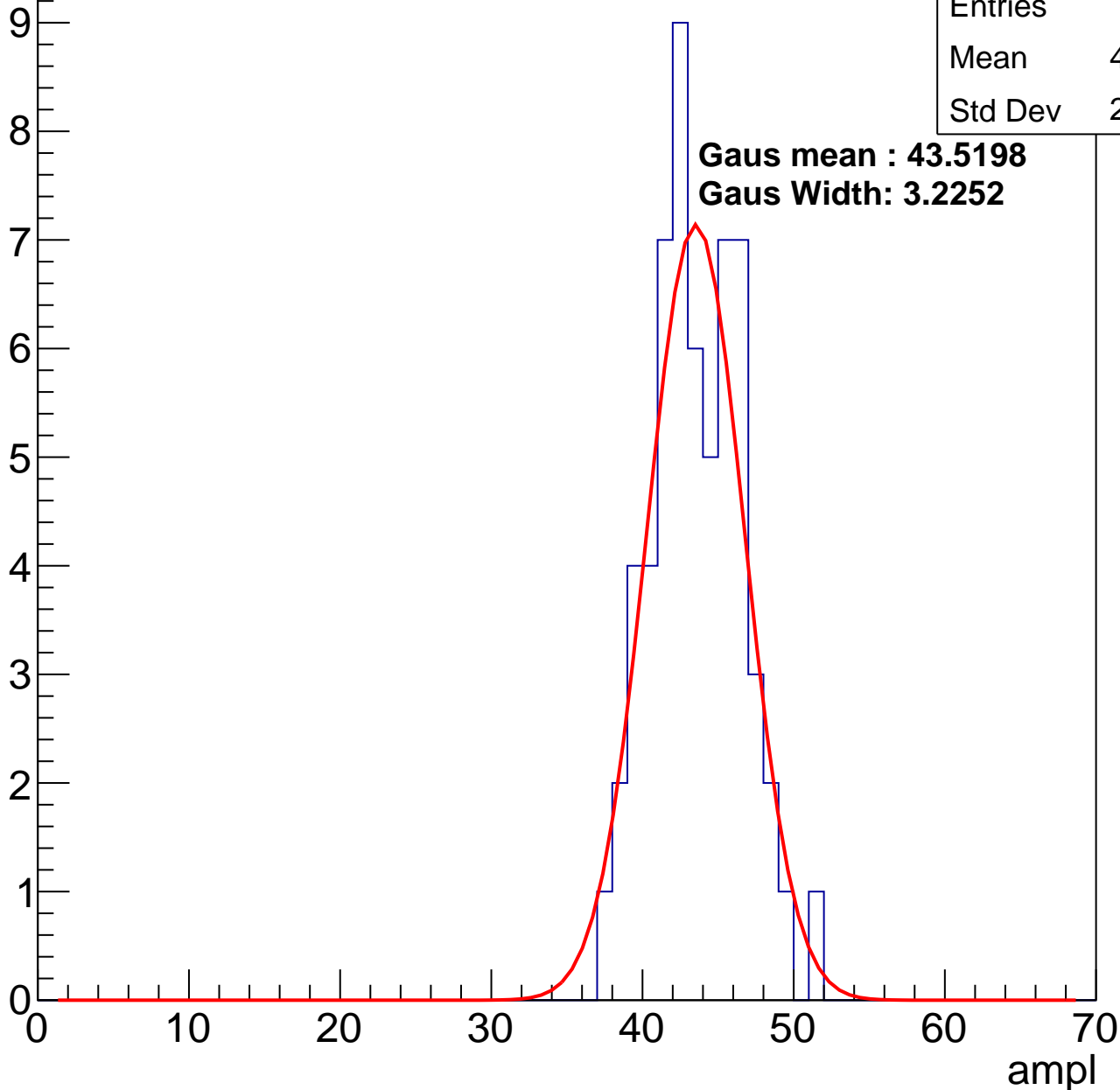
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.15
Std Dev	2.956

Gaus mean : 43.5198

Gaus Width: 3.2252

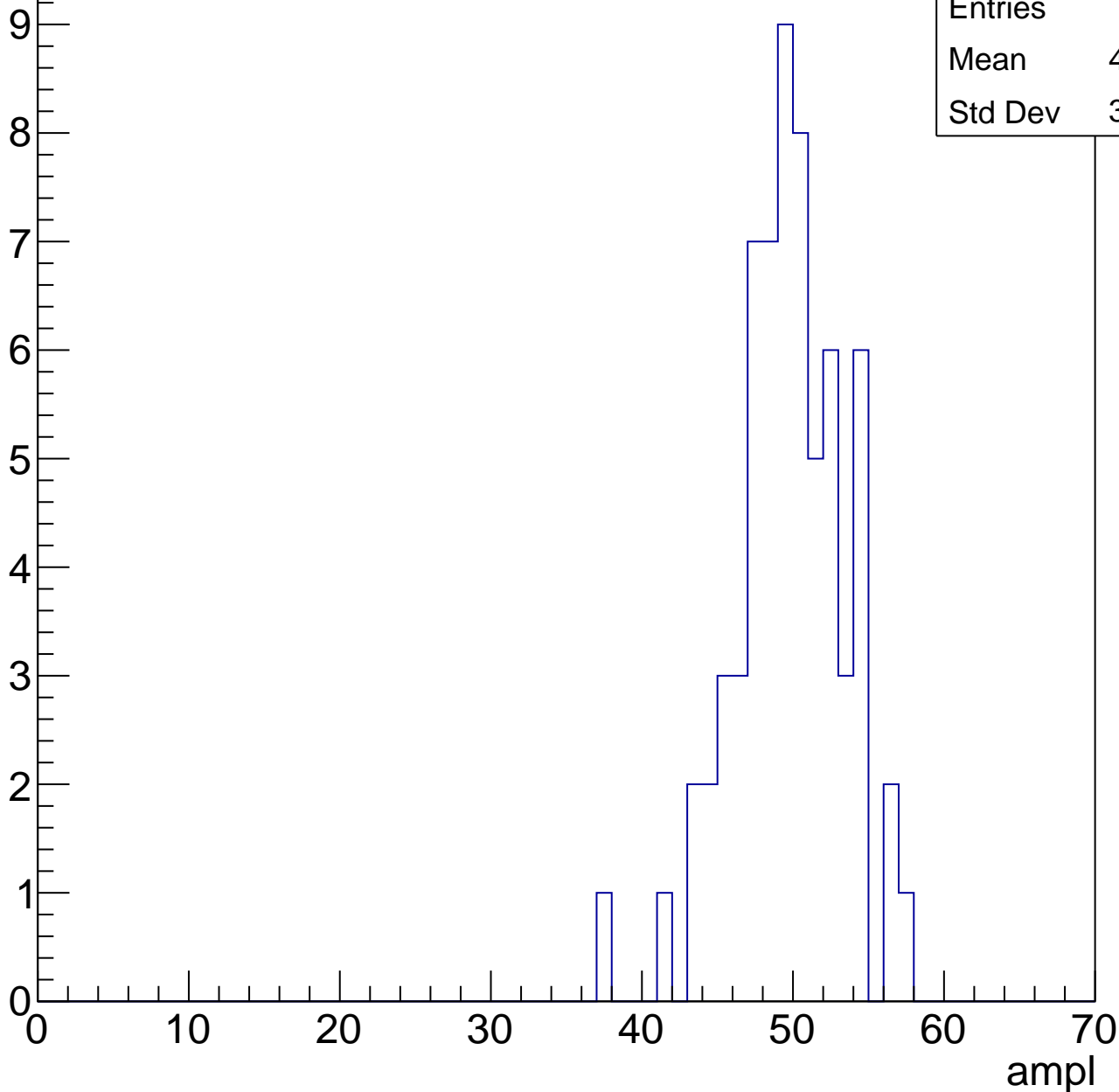


B1L101S, U1-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	49.24
Std Dev	3.648

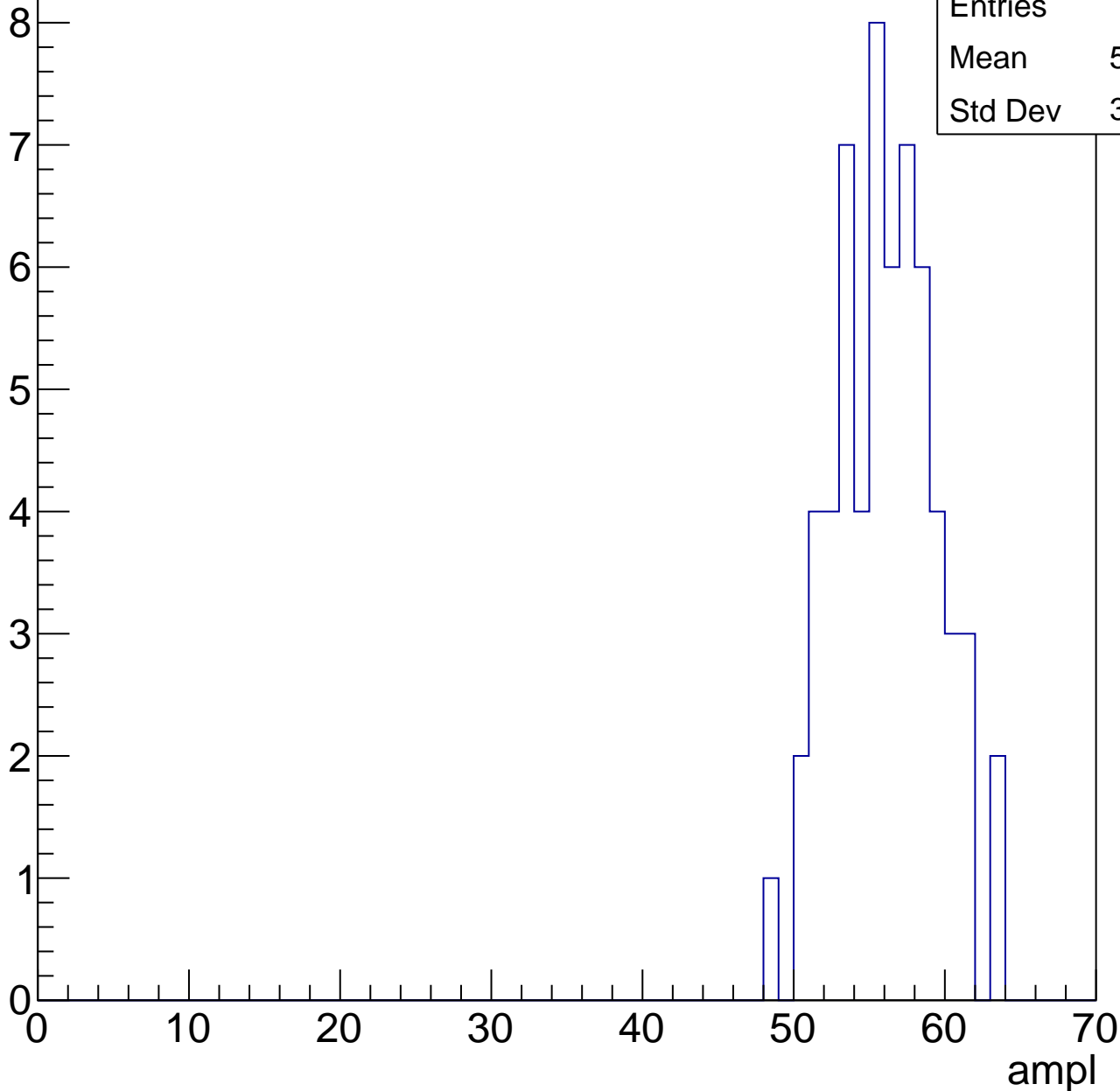


B1L101S, U1-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.66
Std Dev	3.309

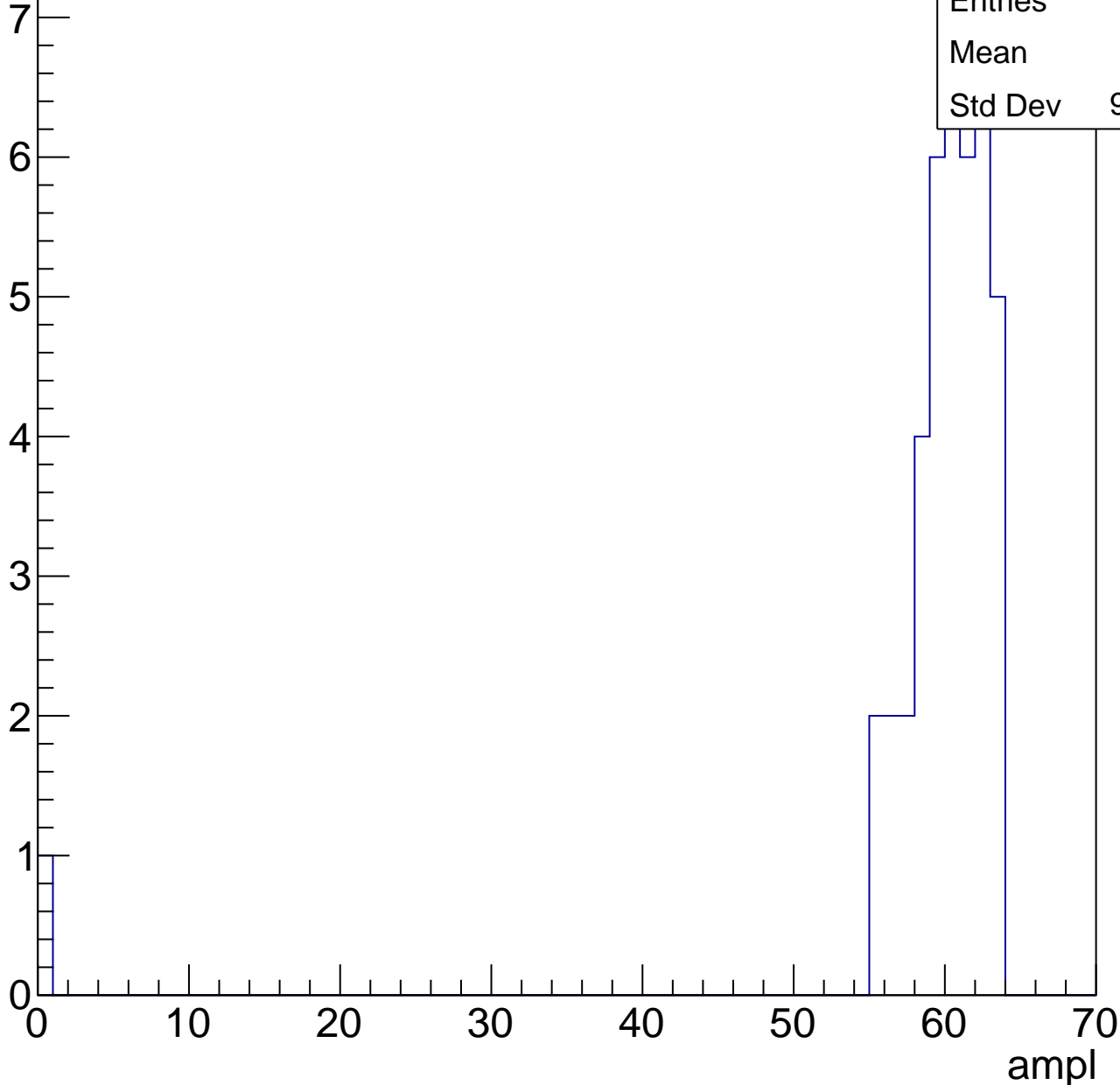


B1L101S, U1-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.5
Std Dev	9.394



B1L101S, U1-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch52, adc0

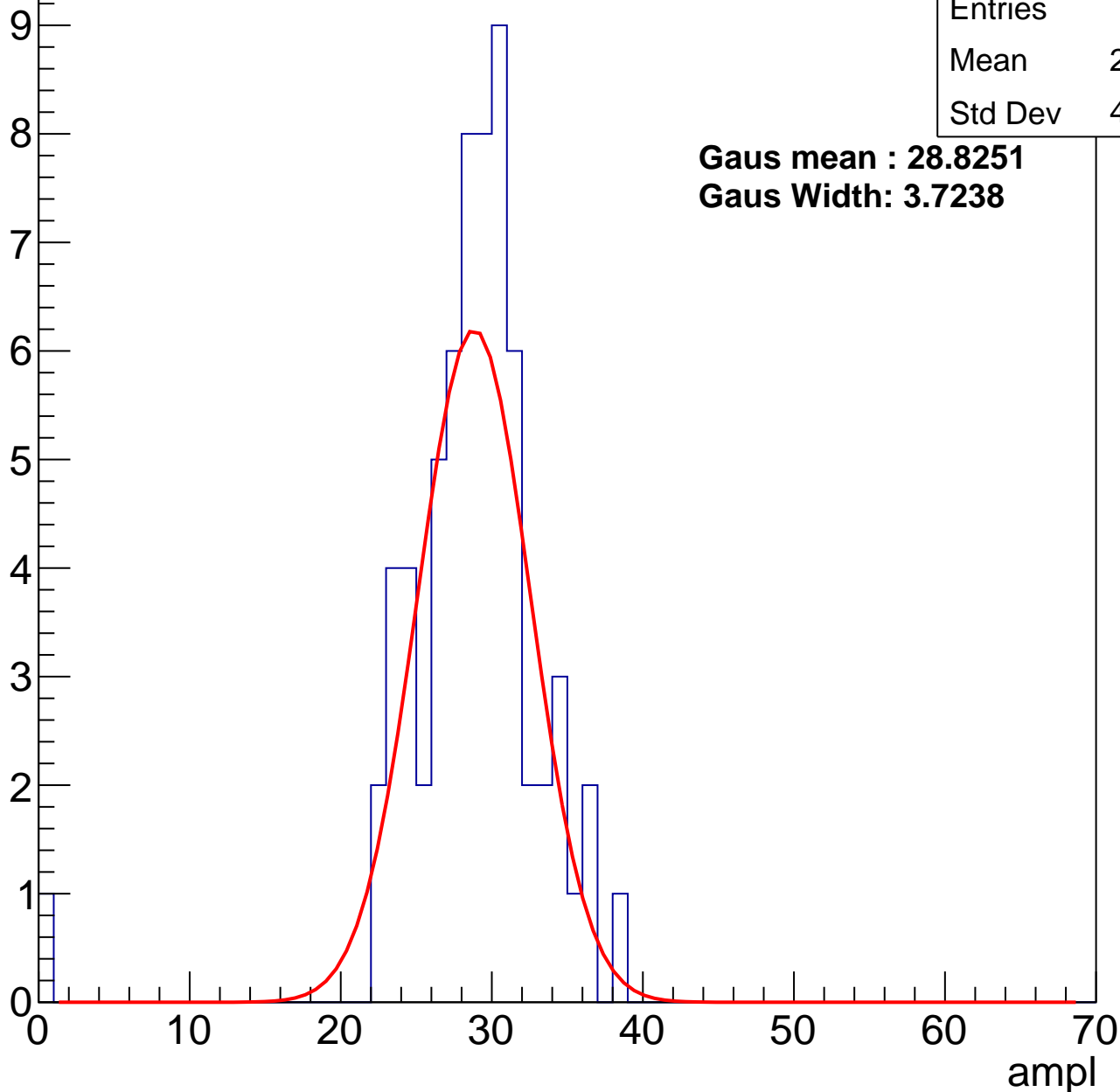
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.23
Std Dev	4.963

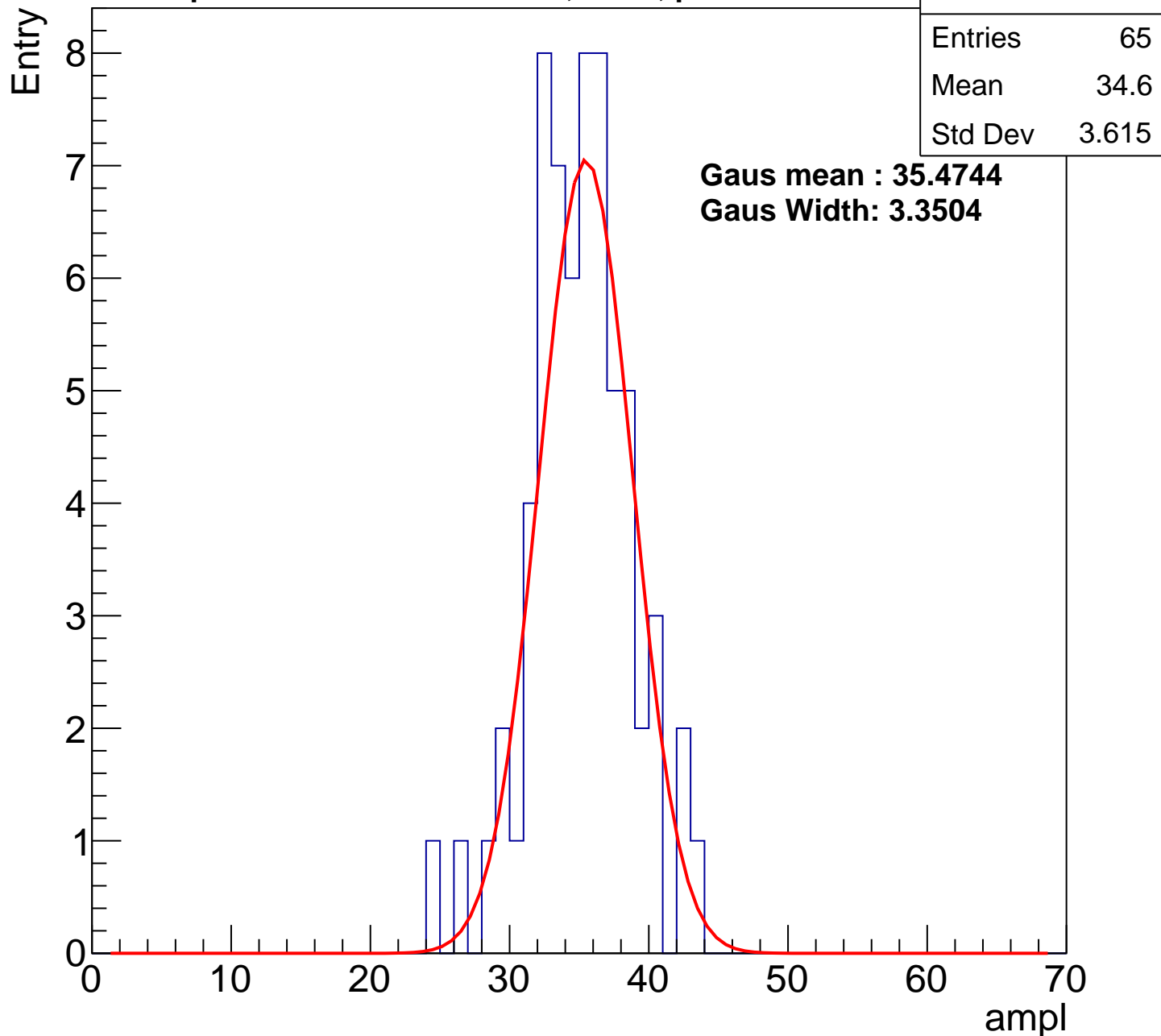
Gaus mean : 28.8251

Gaus Width: 3.7238



B1L101S, U1-ch52, adc1

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch52, adc2

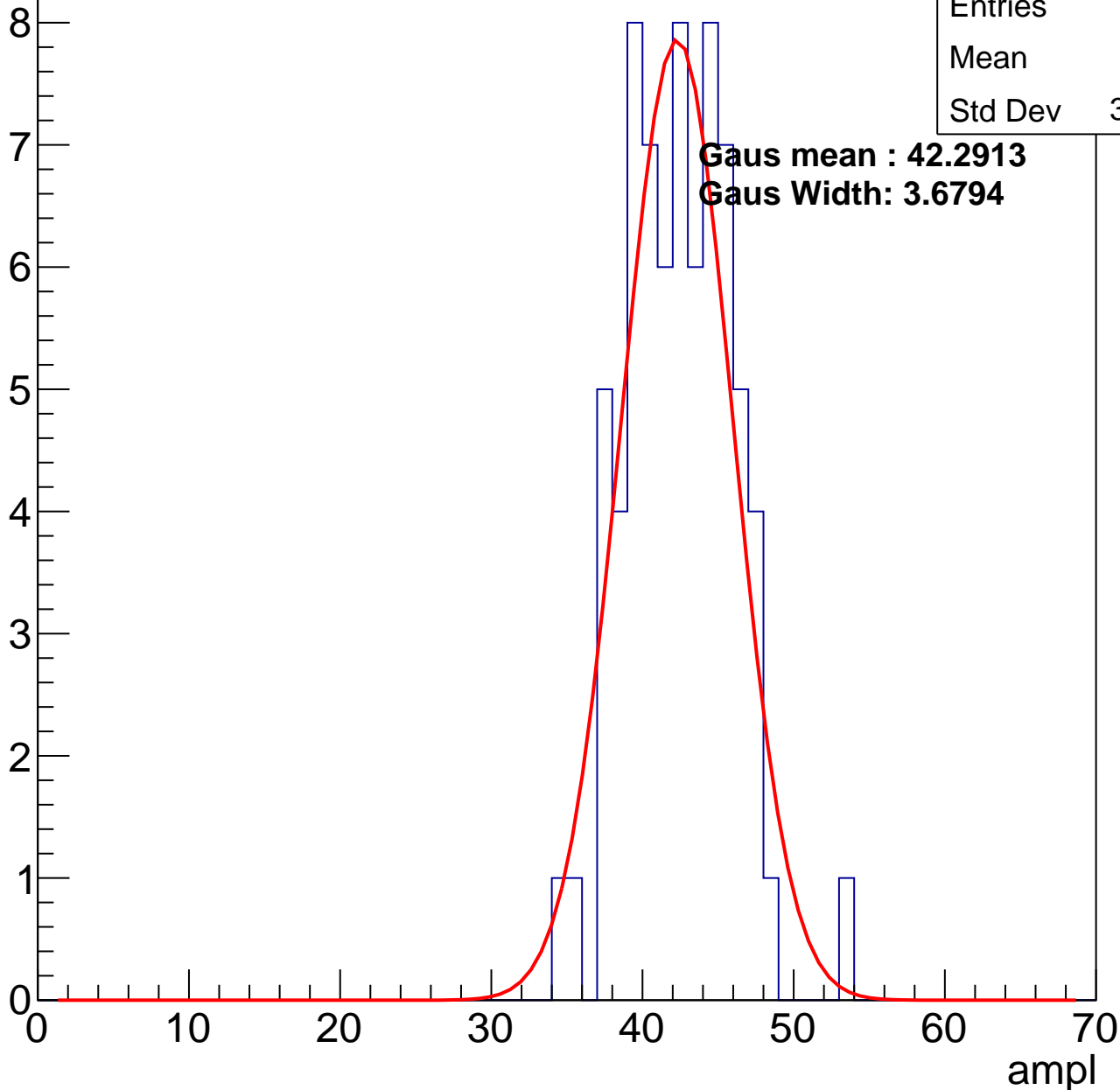
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42
Std Dev	3.428

Gaus mean : 42.2913

Gaus Width: 3.6794



B1L101S, U1-ch52, adc3

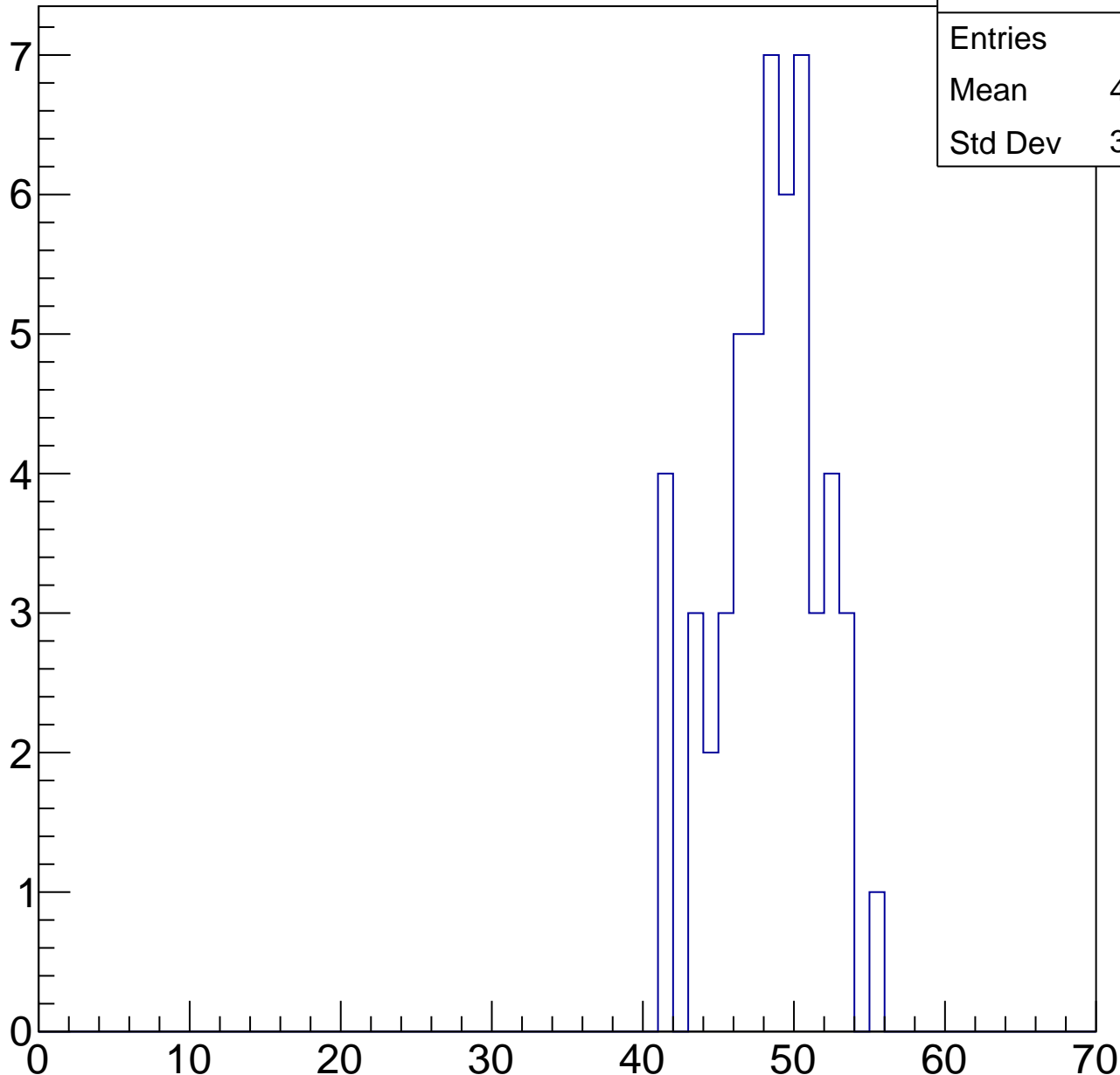
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

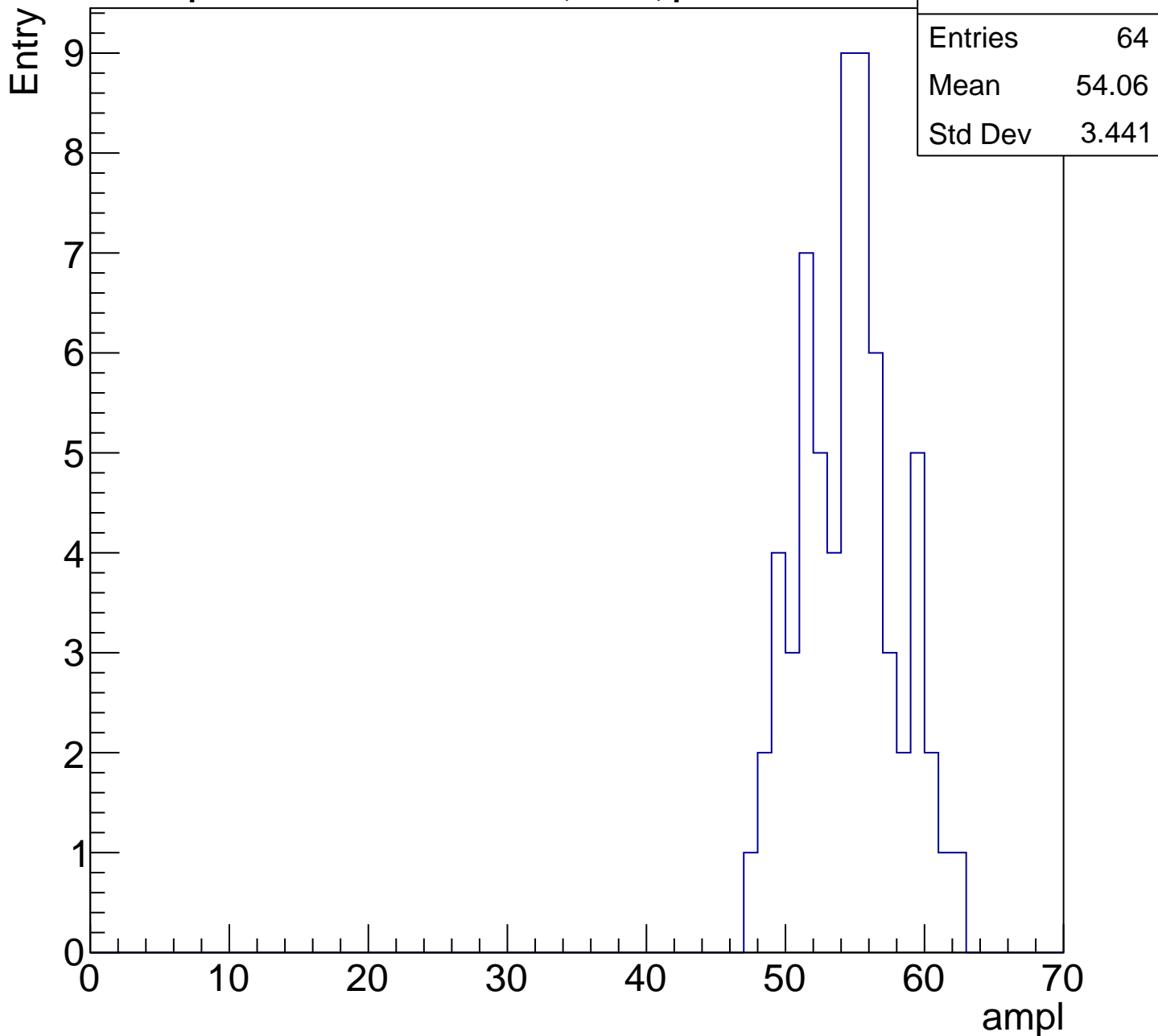
Entries	53
Mean	47.85
Std Dev	3.372

ampl



B1L101S, U1-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

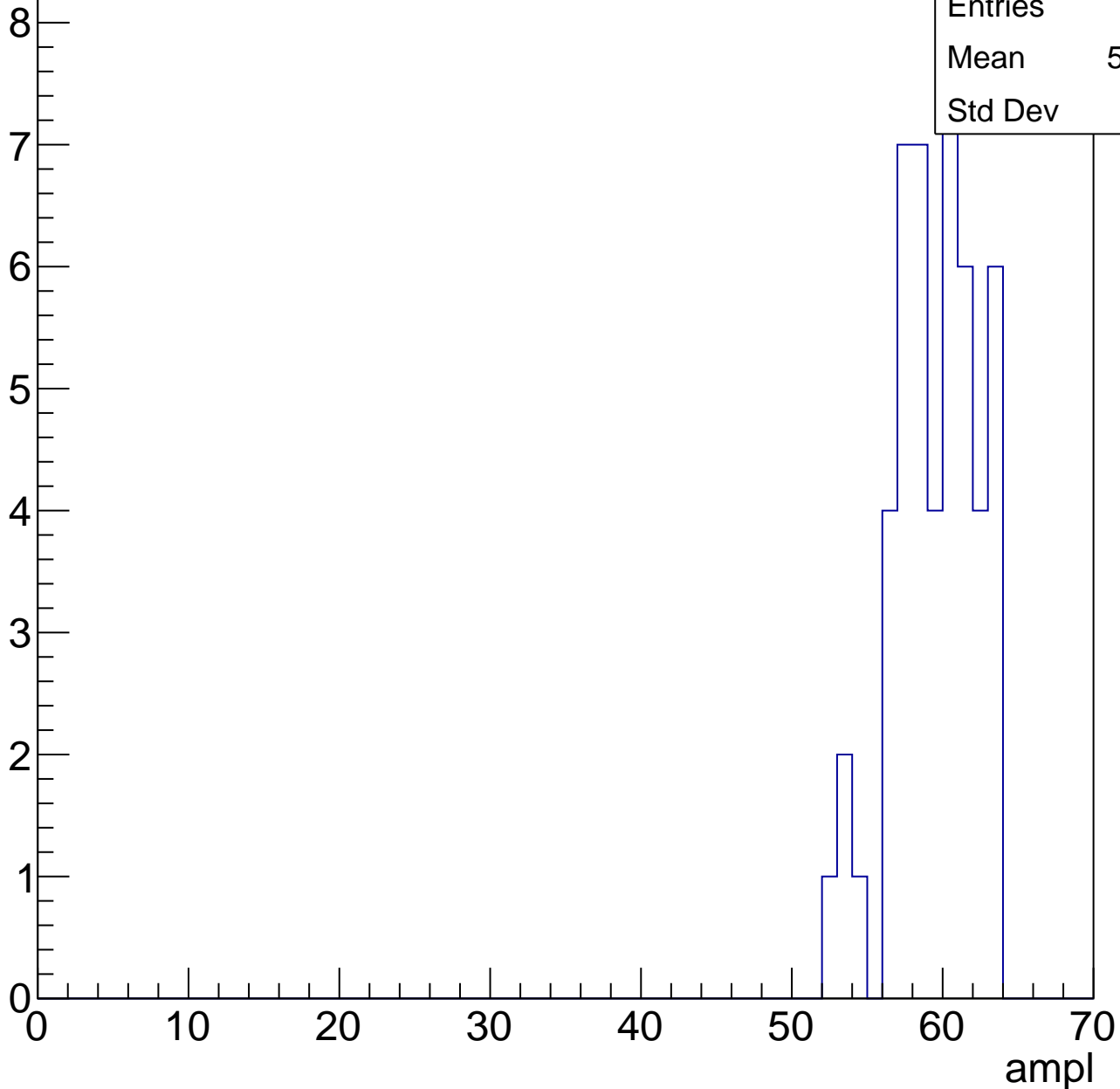


B1L101S, U1-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.98
Std Dev	2.76

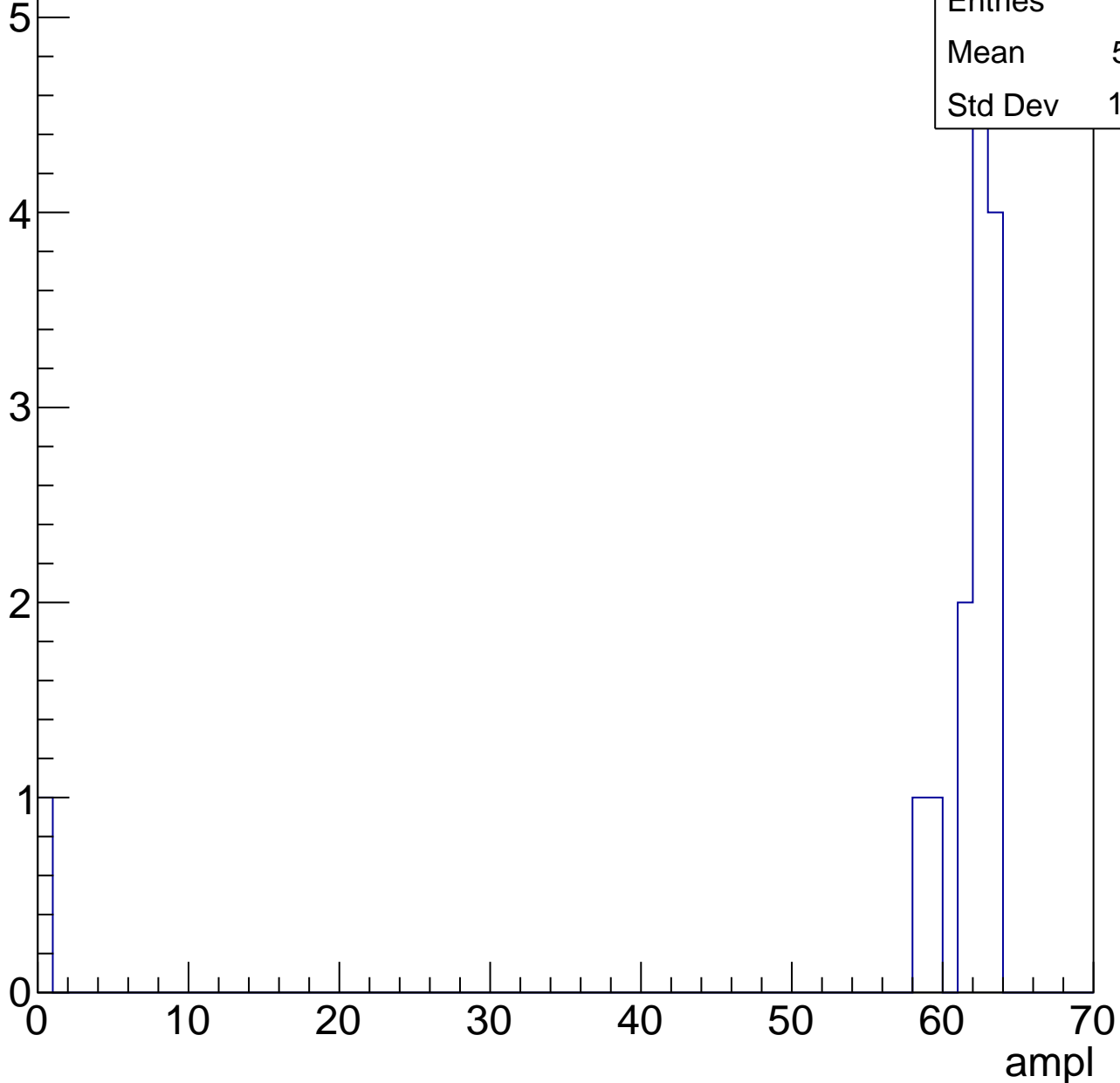


B1L101S, U1-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

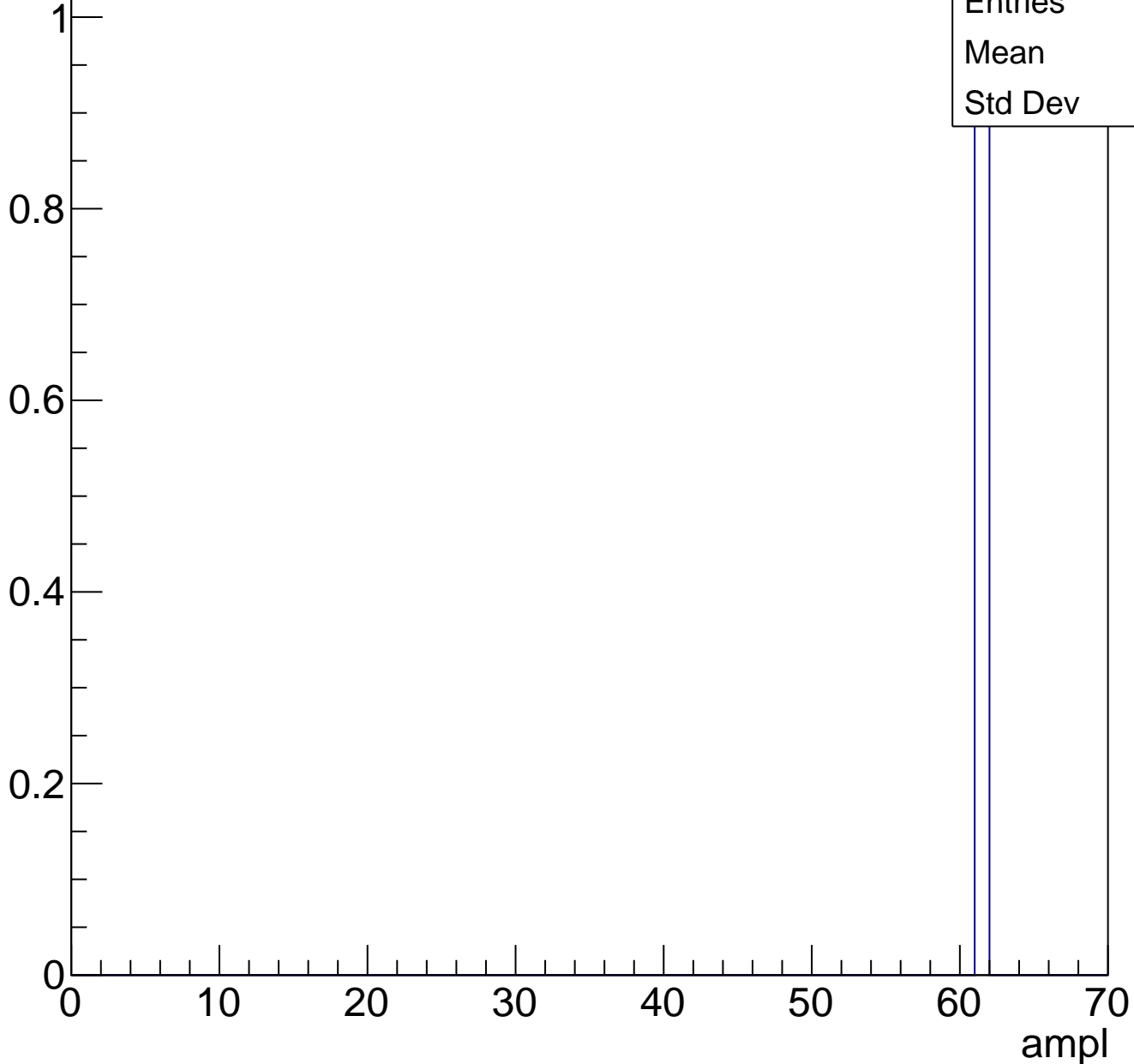
Entries	14
Mean	57.21
Std Dev	15.93



B1L101S, U1-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch53, adc0

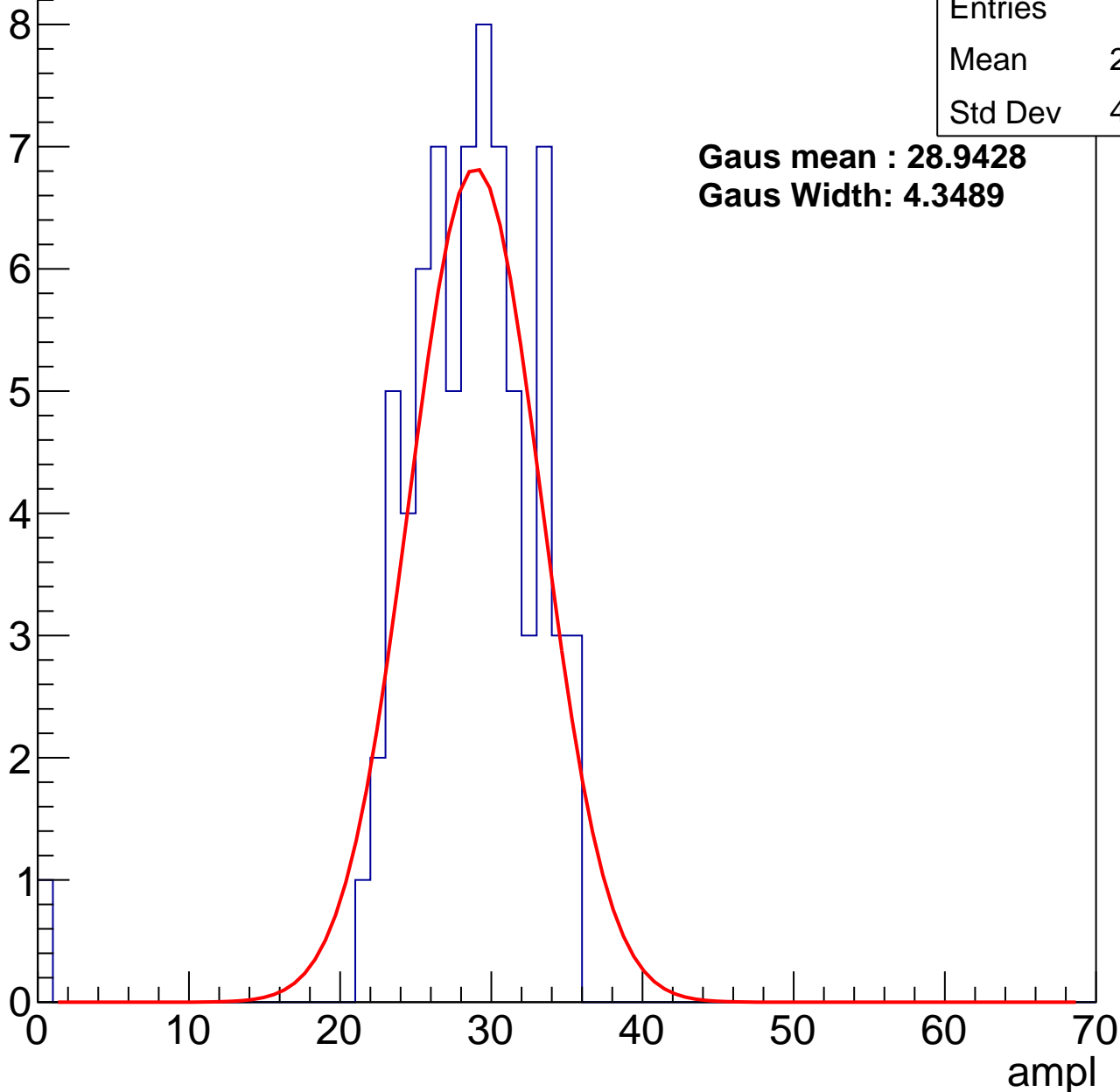
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	27.97
Std Dev	4.849

Gaus mean : 28.9428

Gaus Width: 4.3489



B1L101S, U1-ch53, adc1

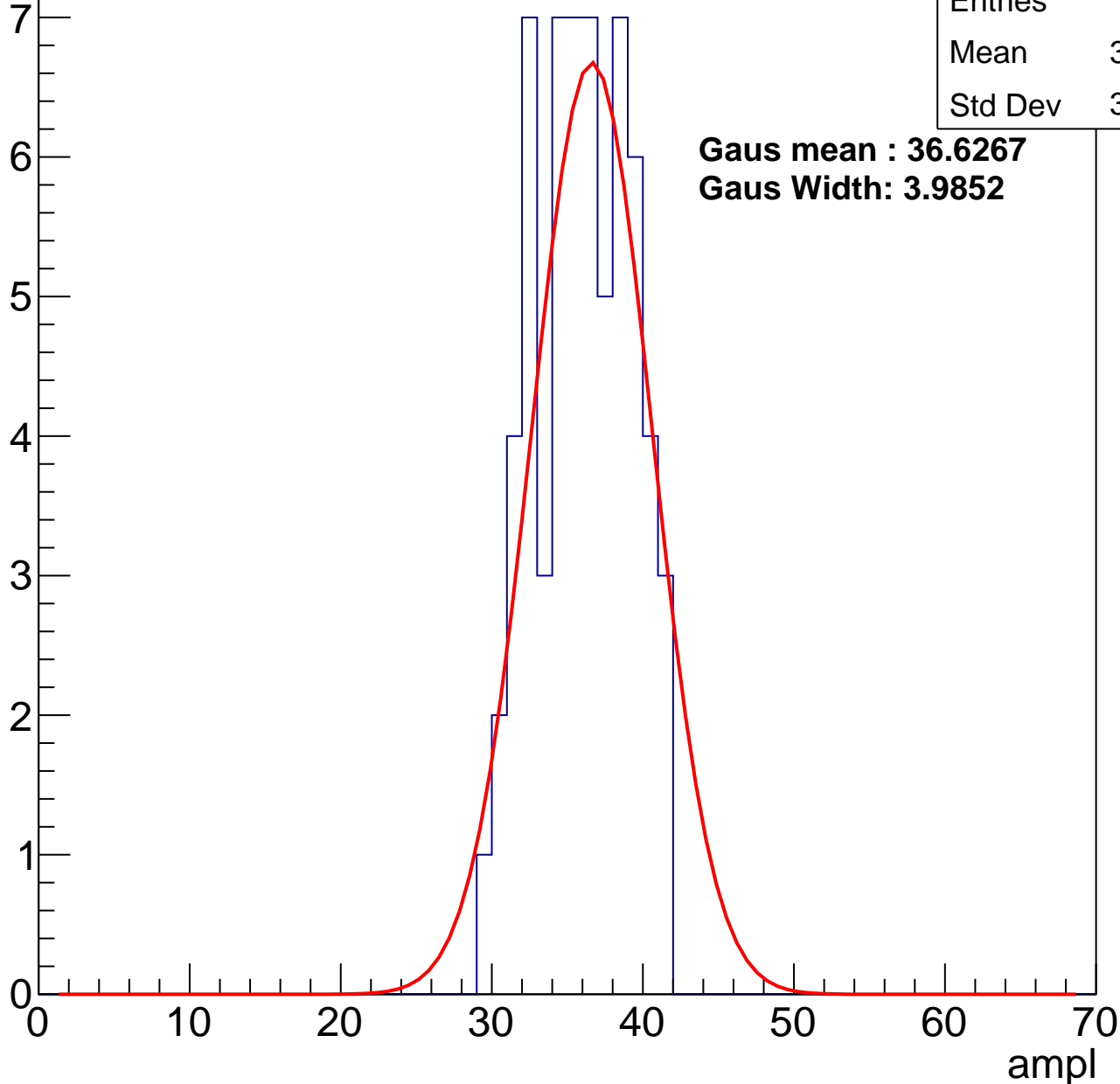
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.54
Std Dev	3.106

Gaus mean : 36.6267

Gaus Width: 3.9852



B1L101S, U1-ch53, adc2

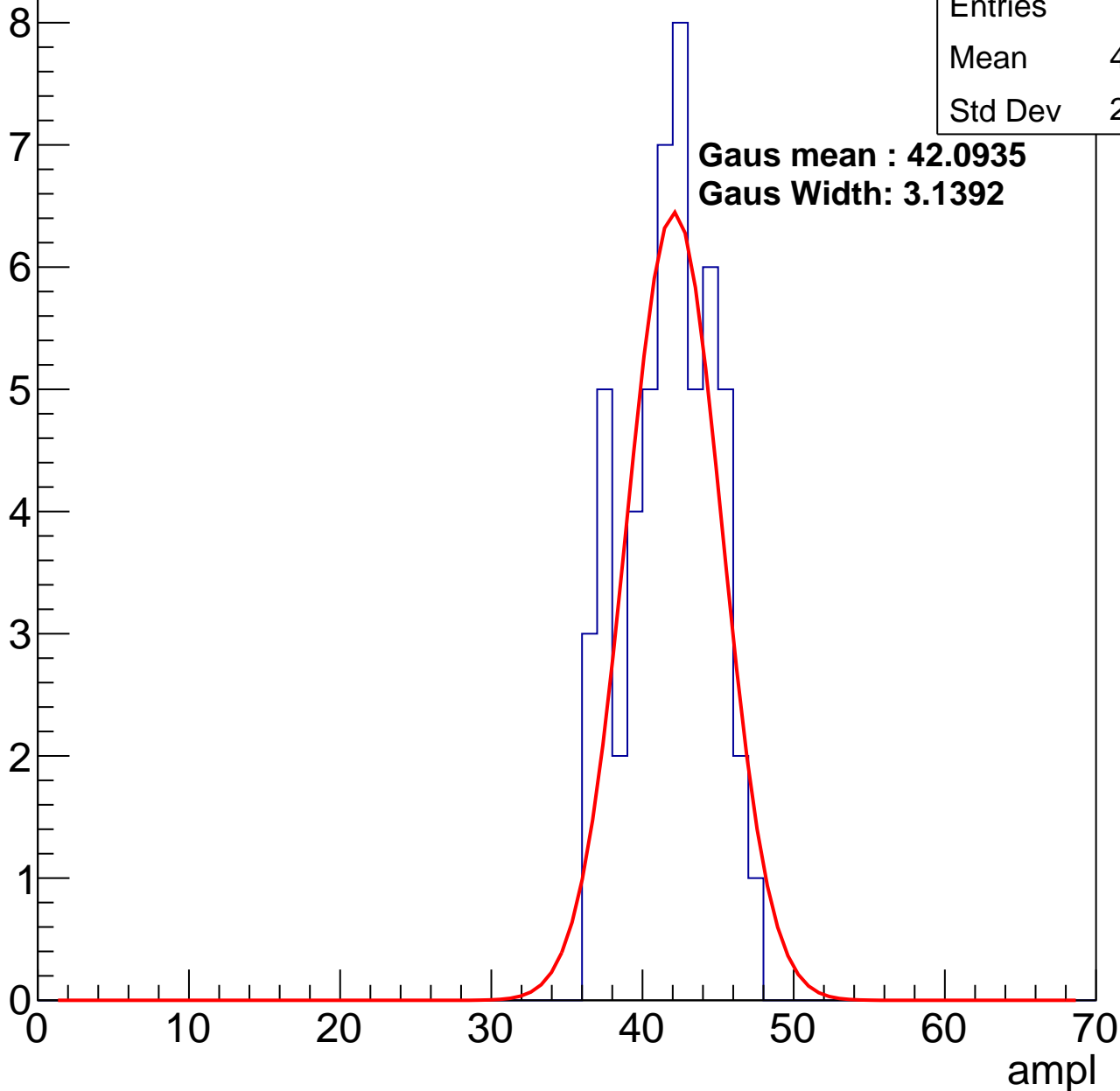
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	41.34
Std Dev	2.868

Gaus mean : 42.0935

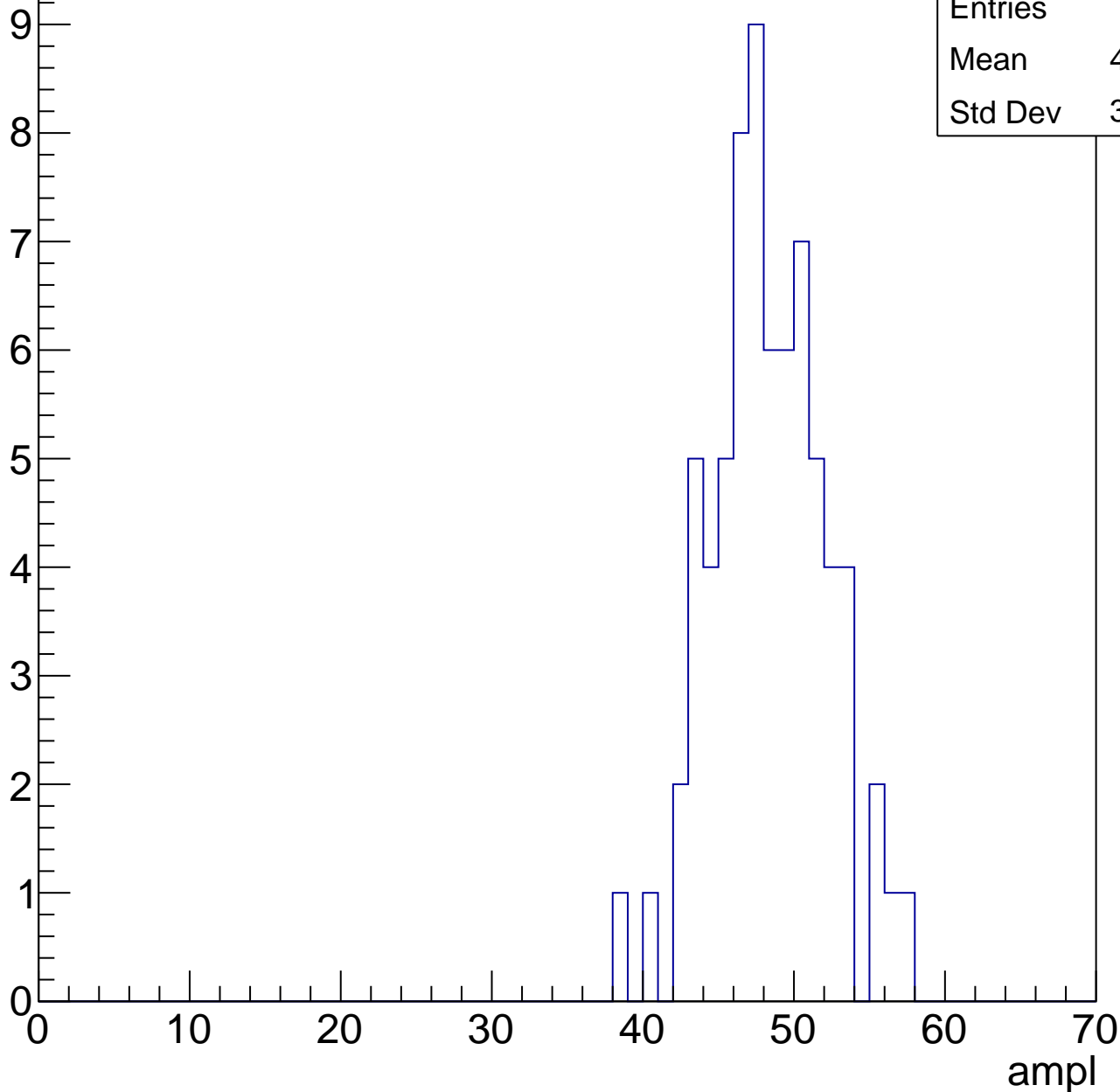
Gaus Width: 3.1392



B1L101S, U1-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

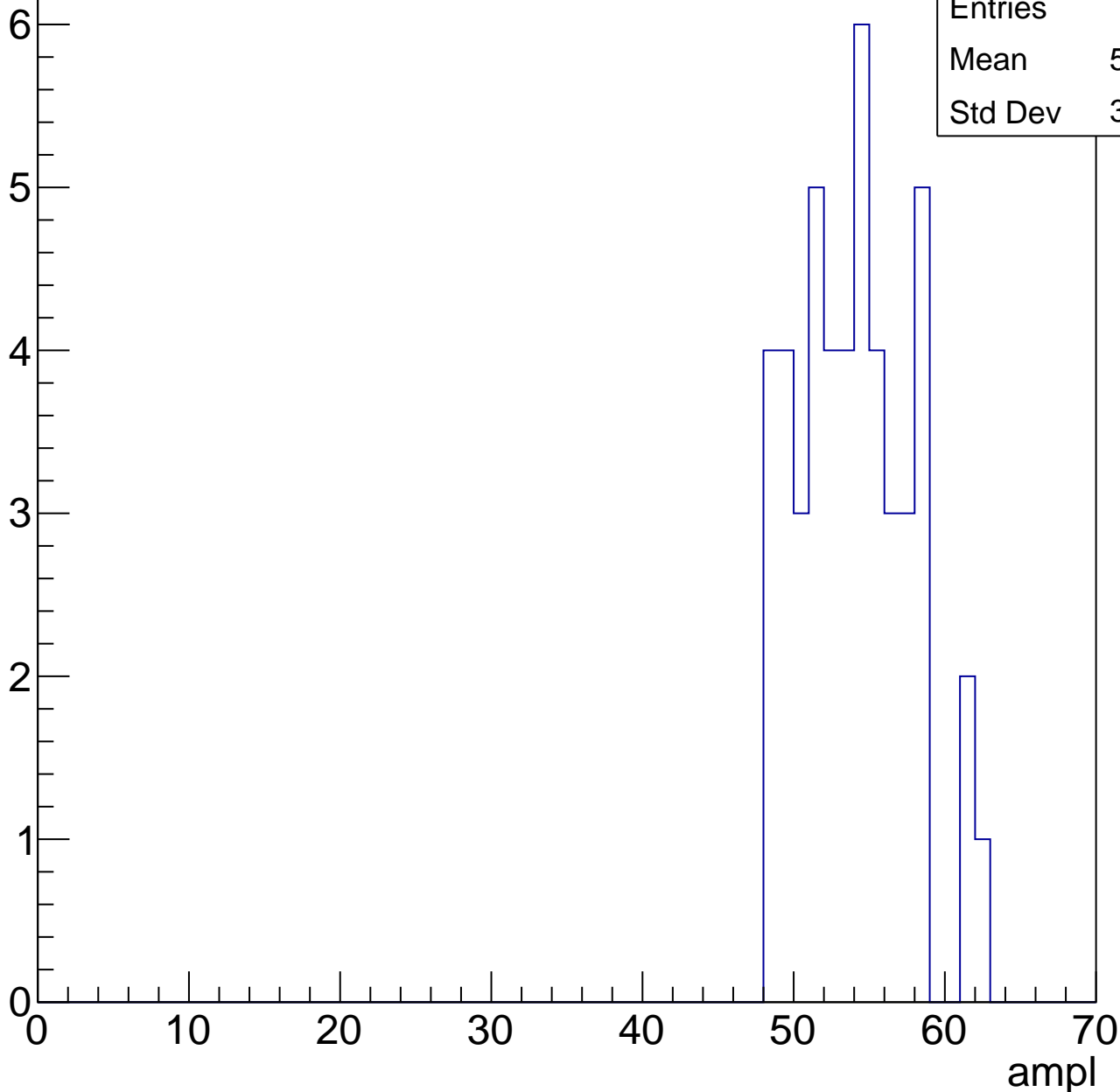


B1L101S, U1-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

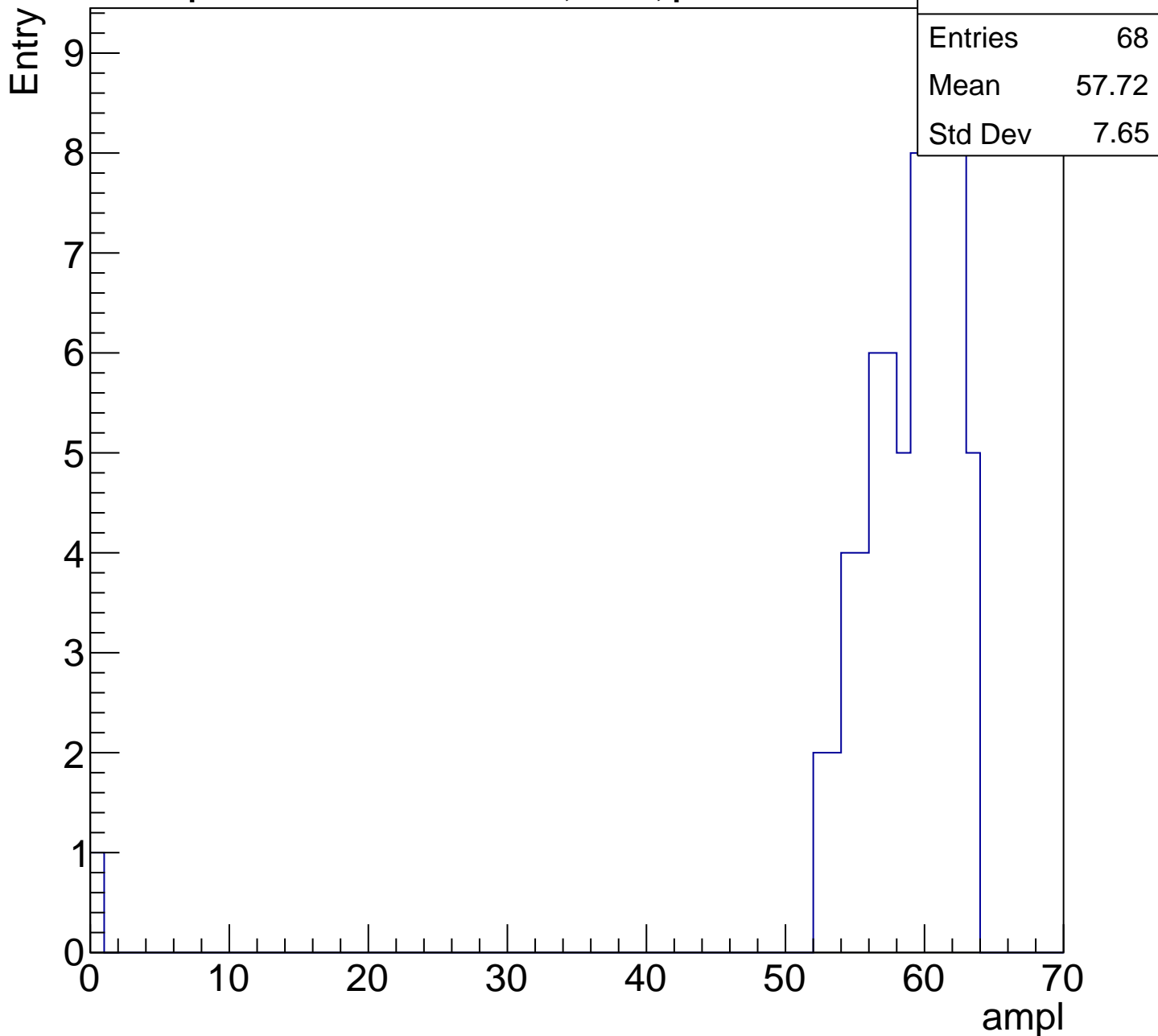
Entry

Entries	48
Mean	53.54
Std Dev	3.628



B1L101S, U1-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

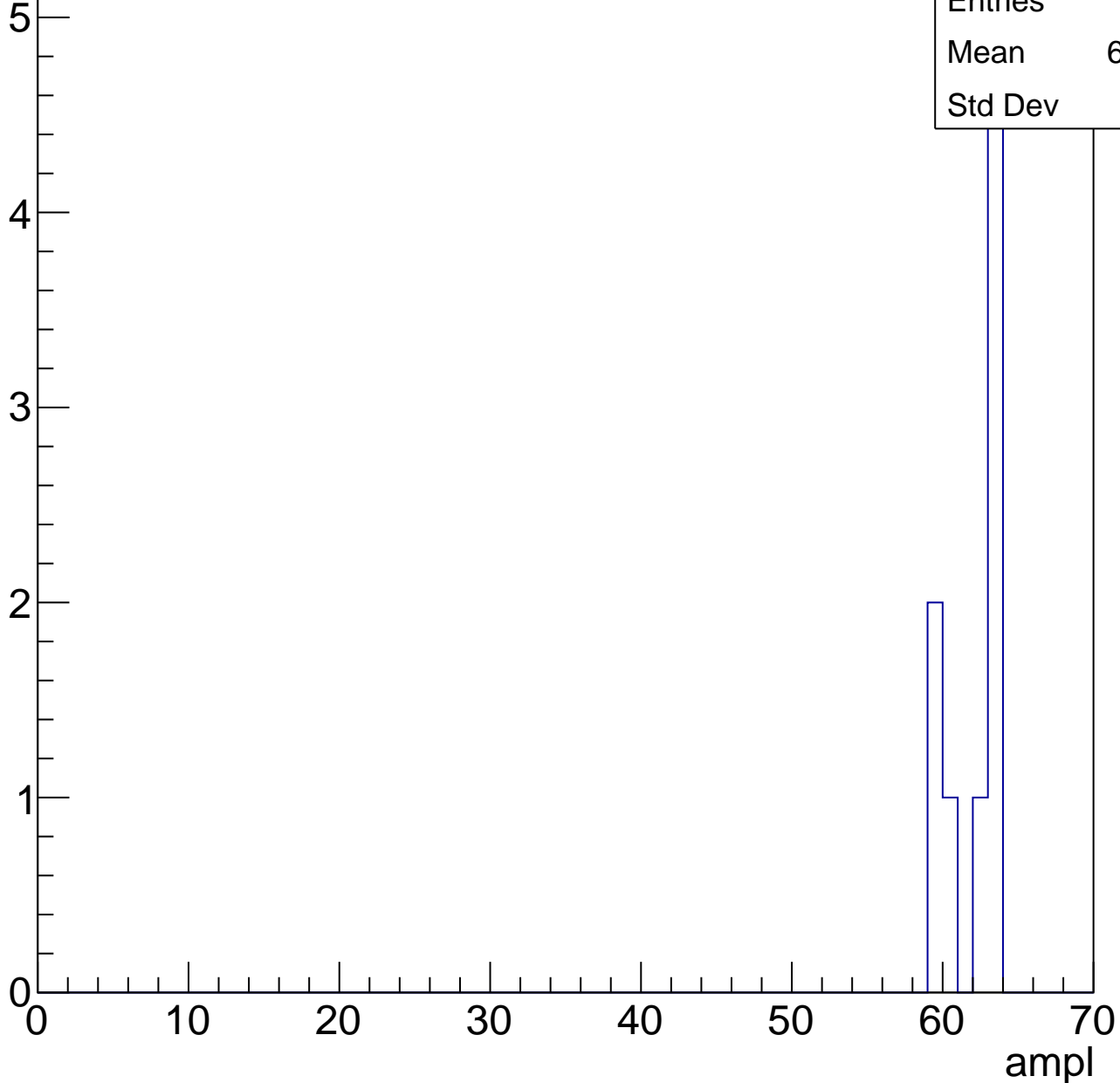


B1L101S, U1-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	61.67
Std Dev	1.7



B1L101S, U1-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch54, adc0

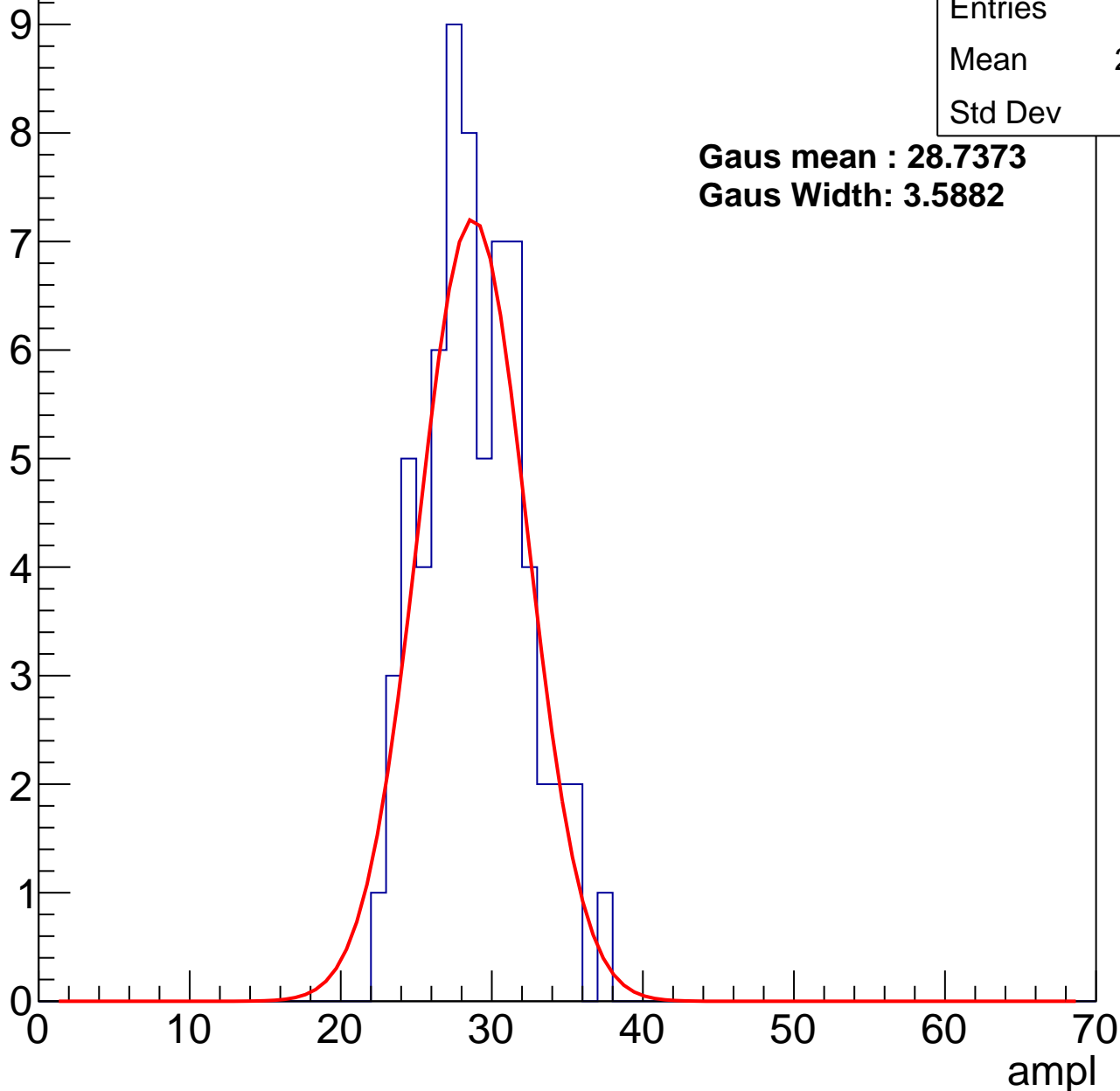
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.41
Std Dev	3.28

Gaus mean : 28.7373

Gaus Width: 3.5882



B1L101S, U1-ch54, adc1

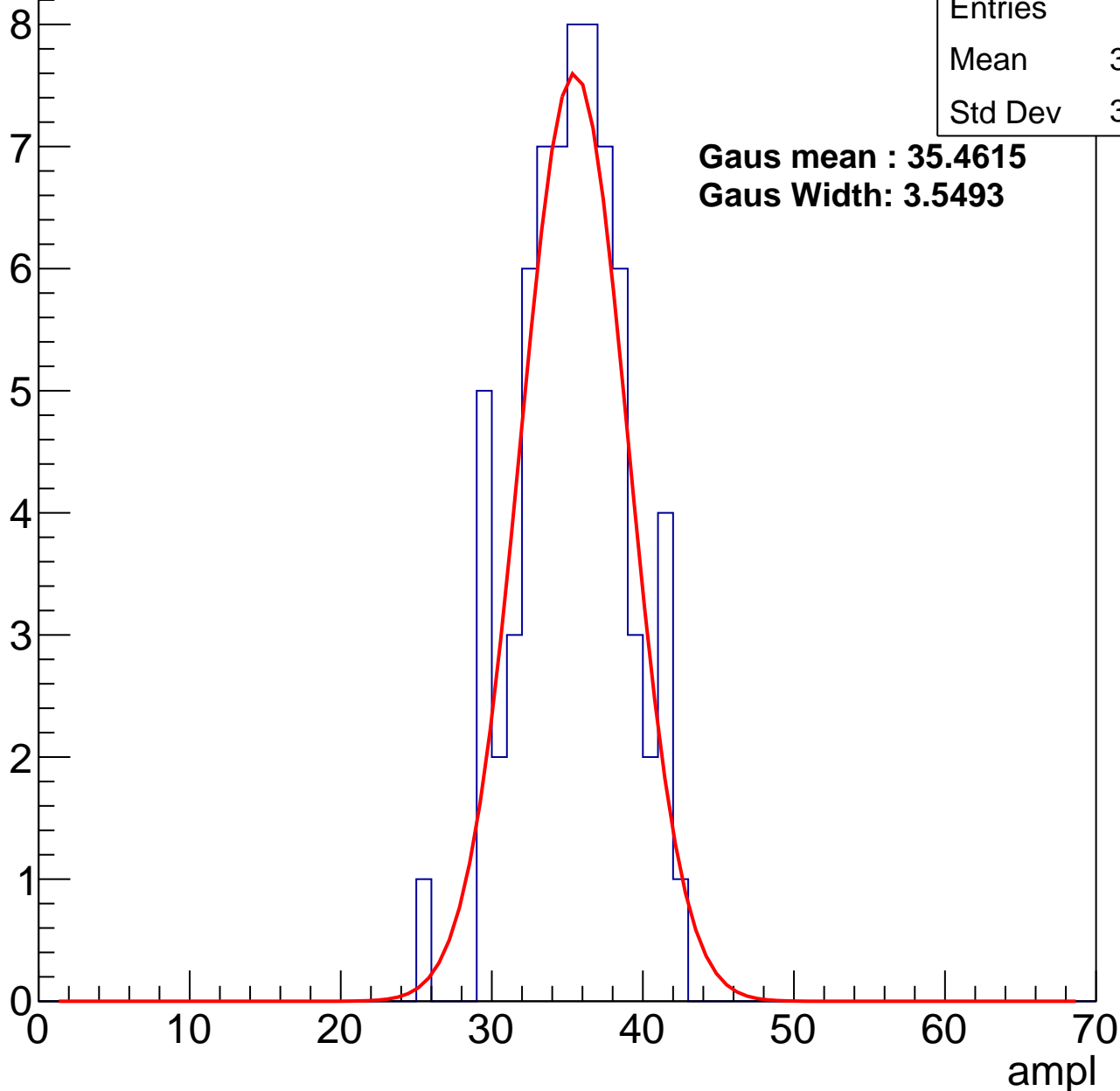
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	34.89
Std Dev	3.479

Gaus mean : 35.4615

Gaus Width: 3.5493



B1L101S, U1-ch54, adc2

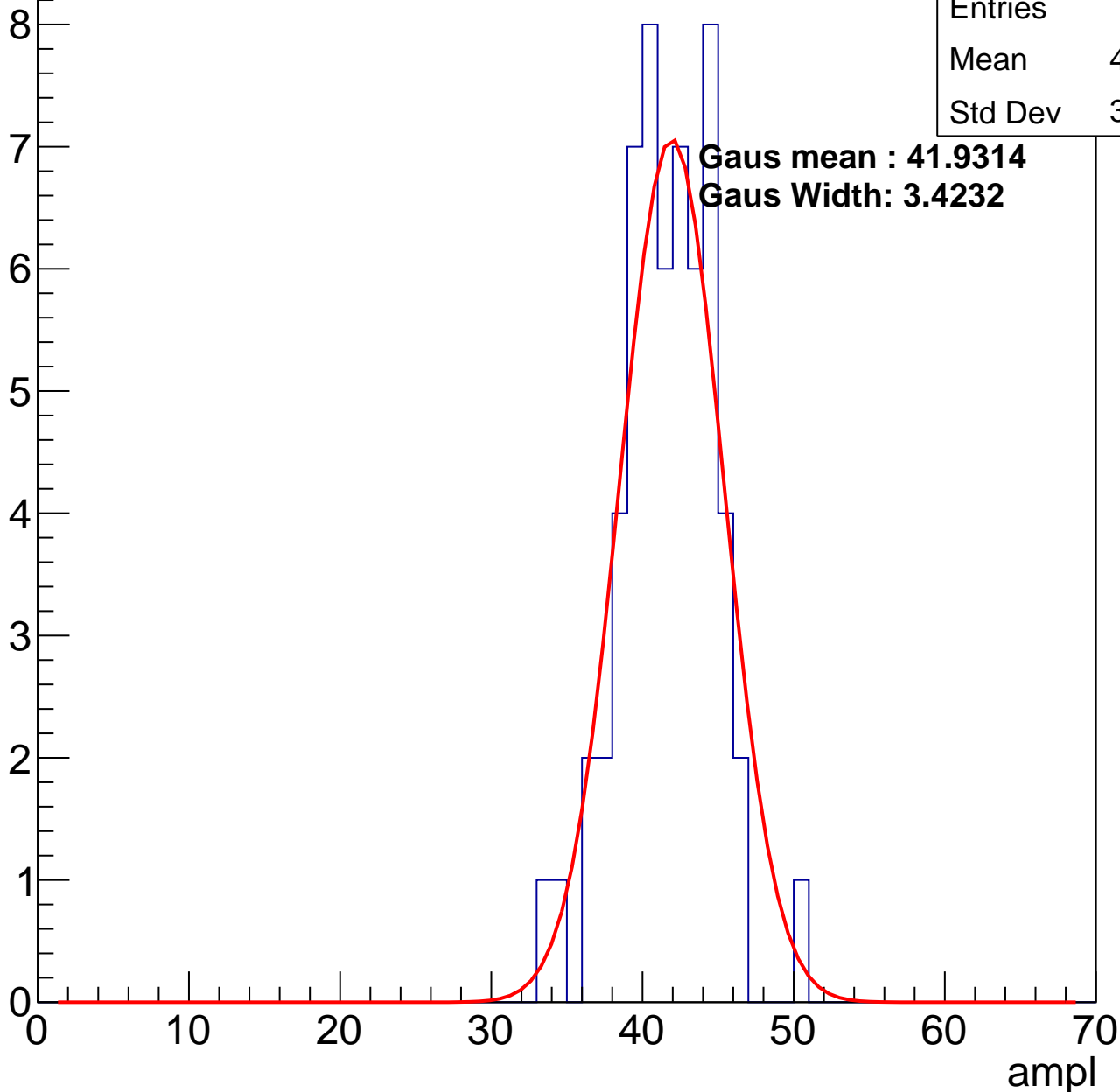
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	41.19
Std Dev	3.089

Gaus mean : 41.9314

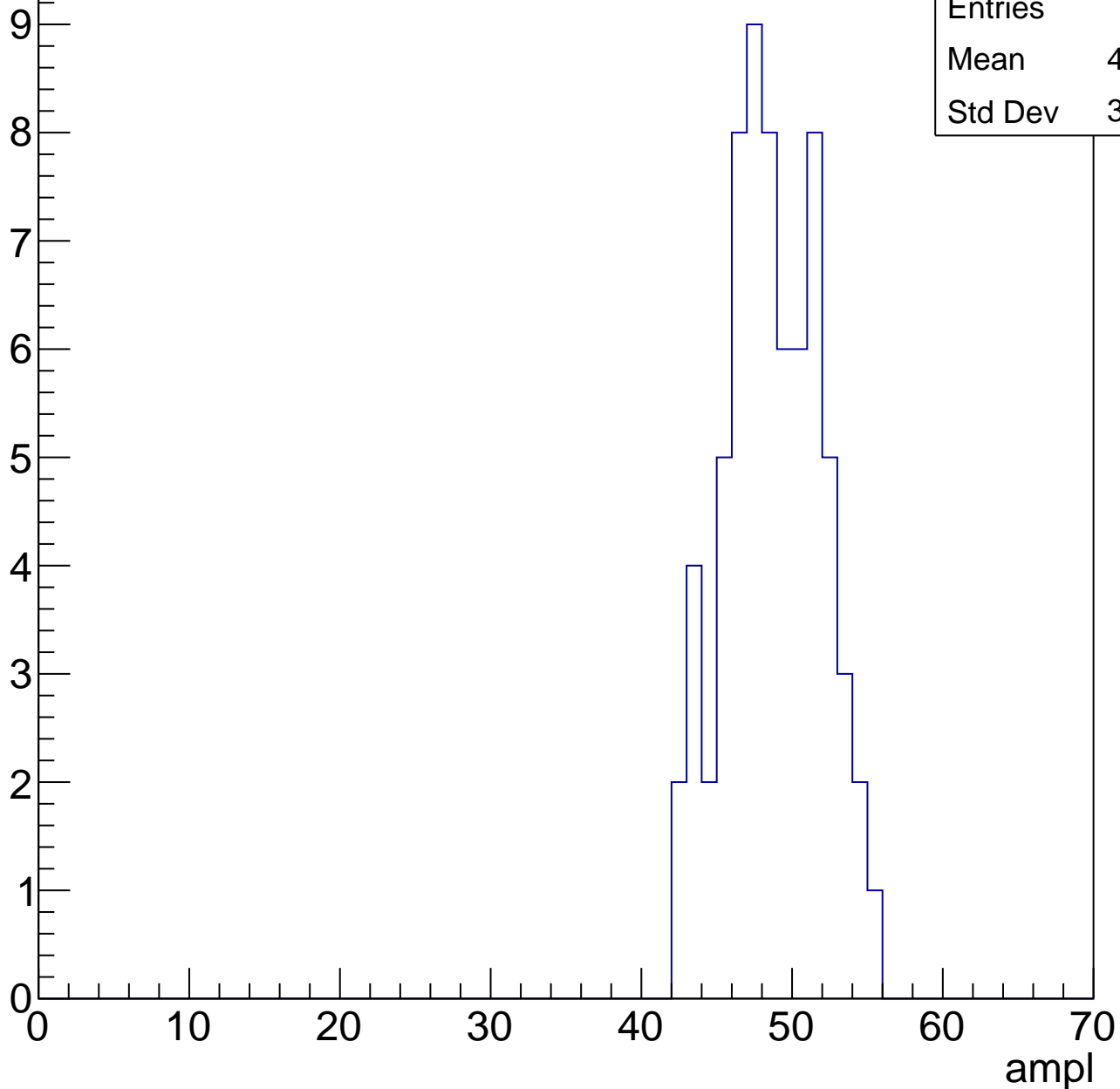
Gaus Width: 3.4232



B1L101S, U1-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

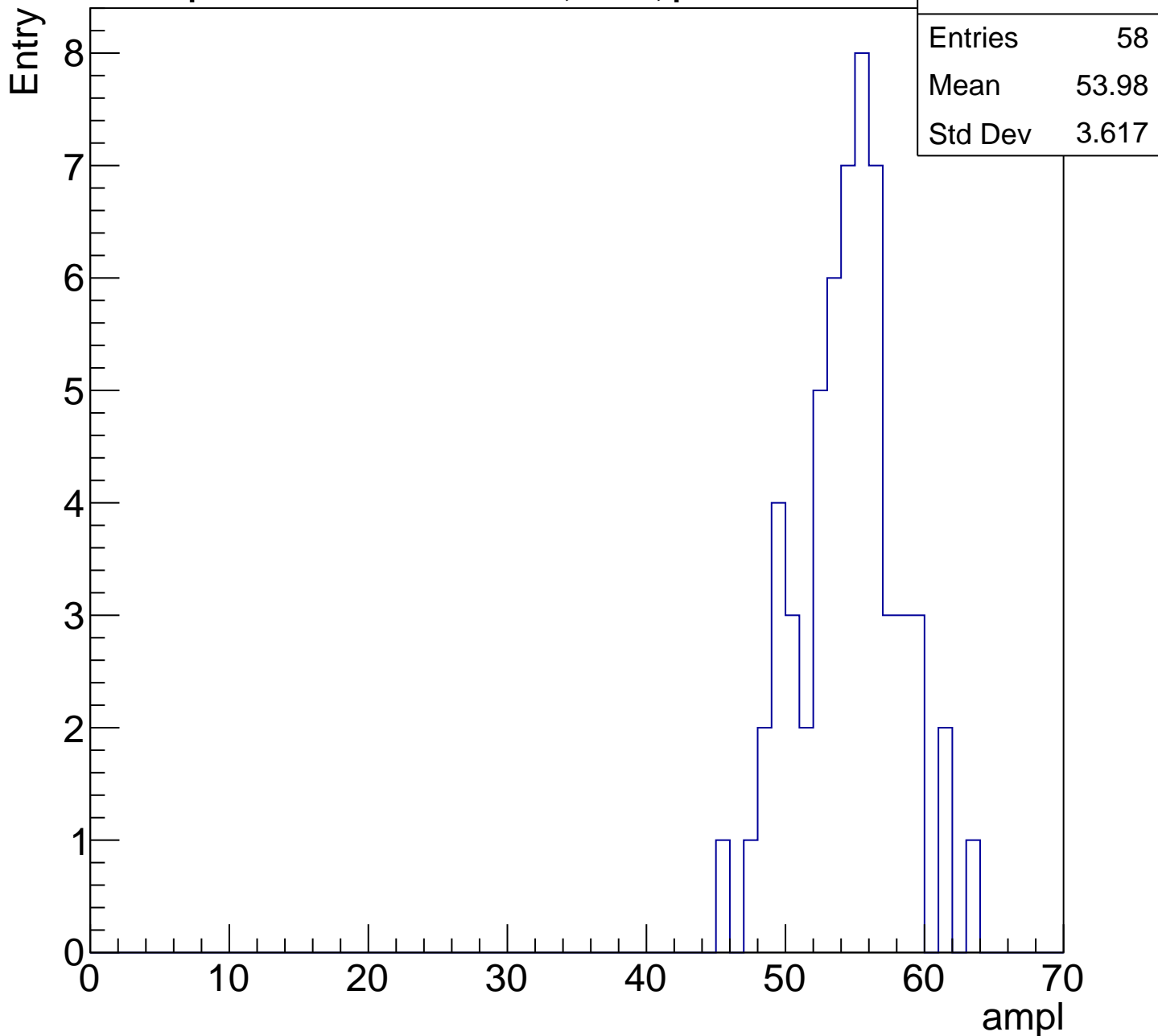
Entry



Entries	69
Mean	48.23
Std Dev	3.103

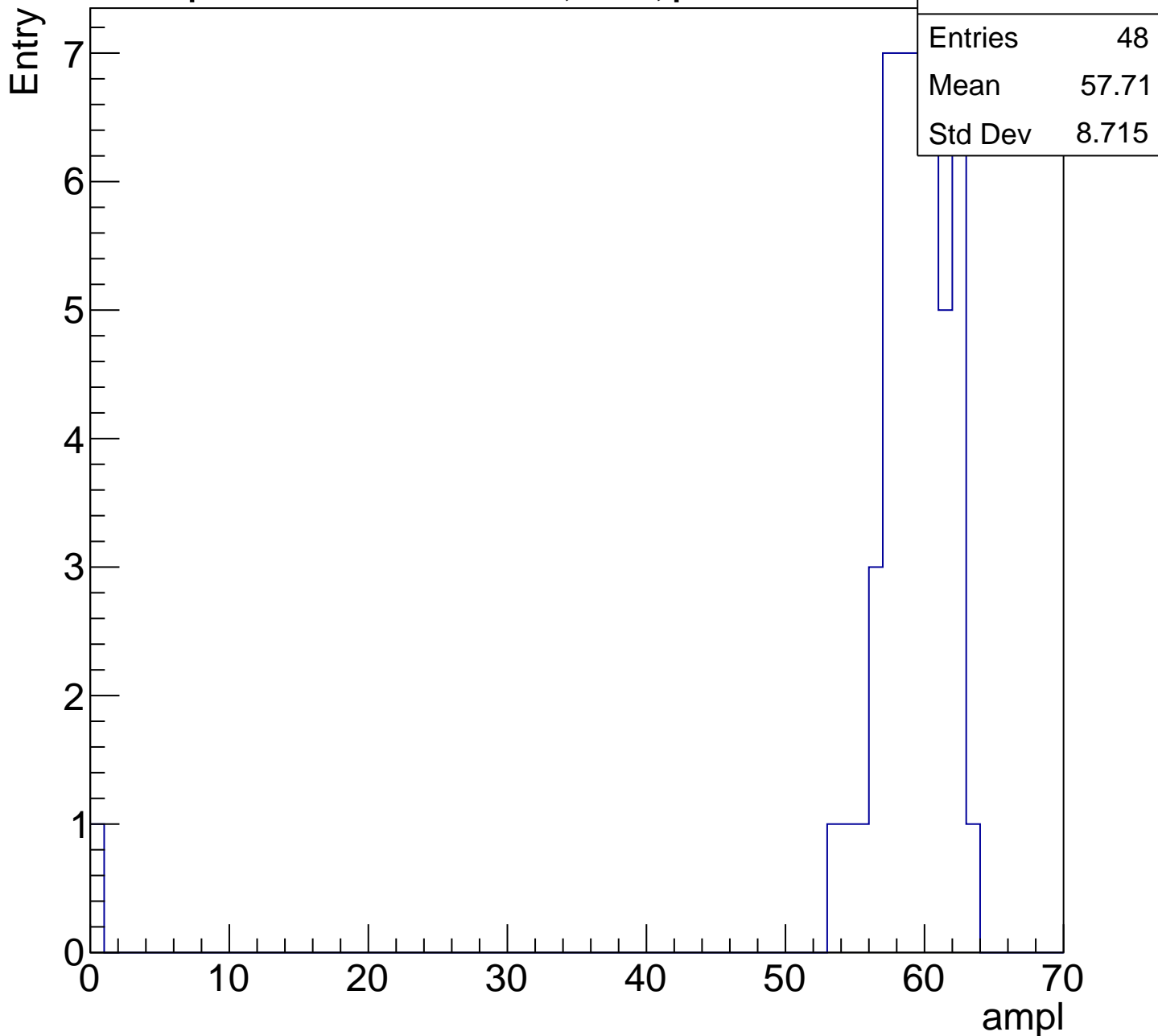
B1L101S, U1-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch54, adc5

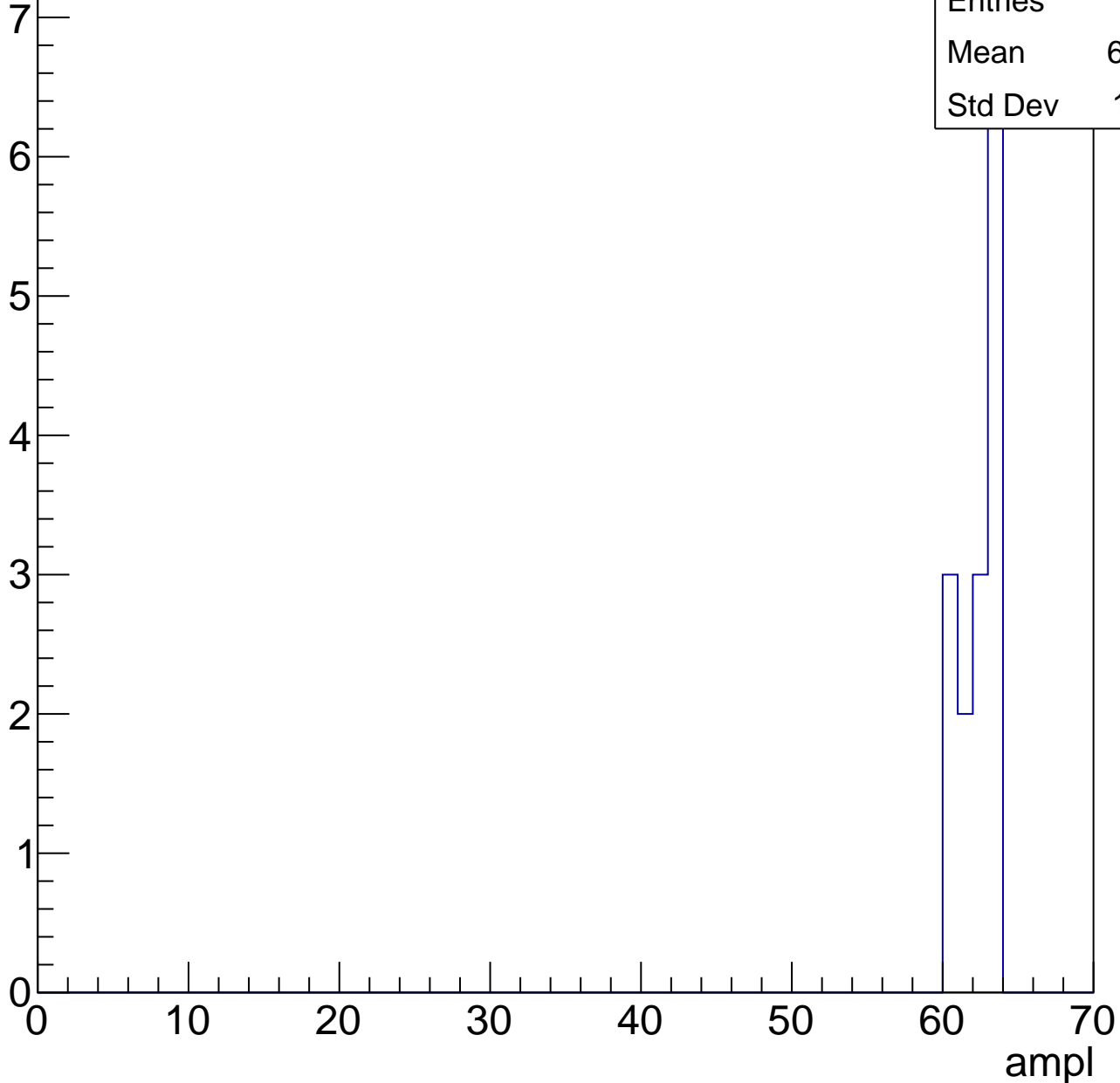
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

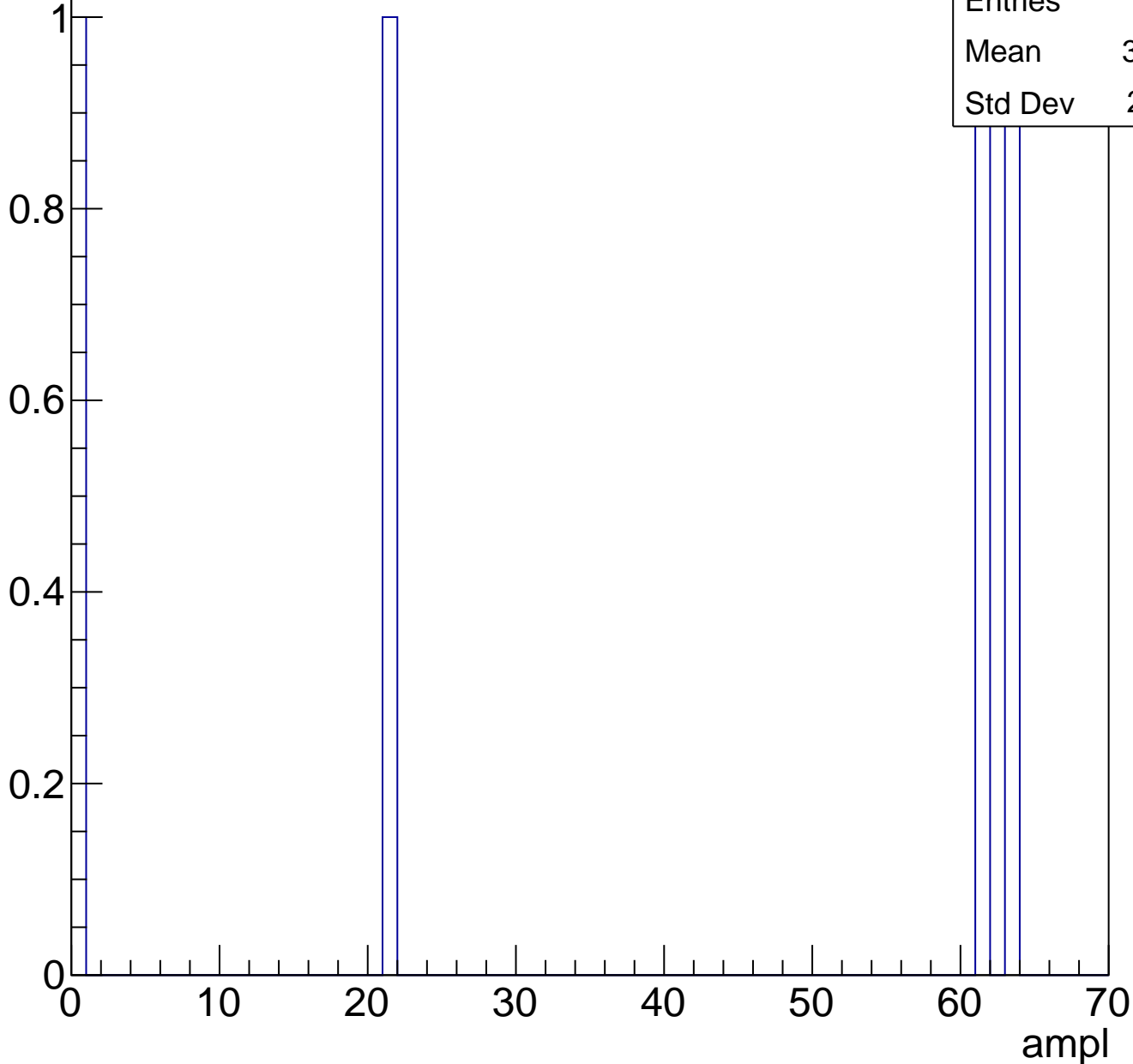
Entry



B1L101S, U1-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	36.25
Std Dev	26.81

B1L101S, U1-ch55, adc0

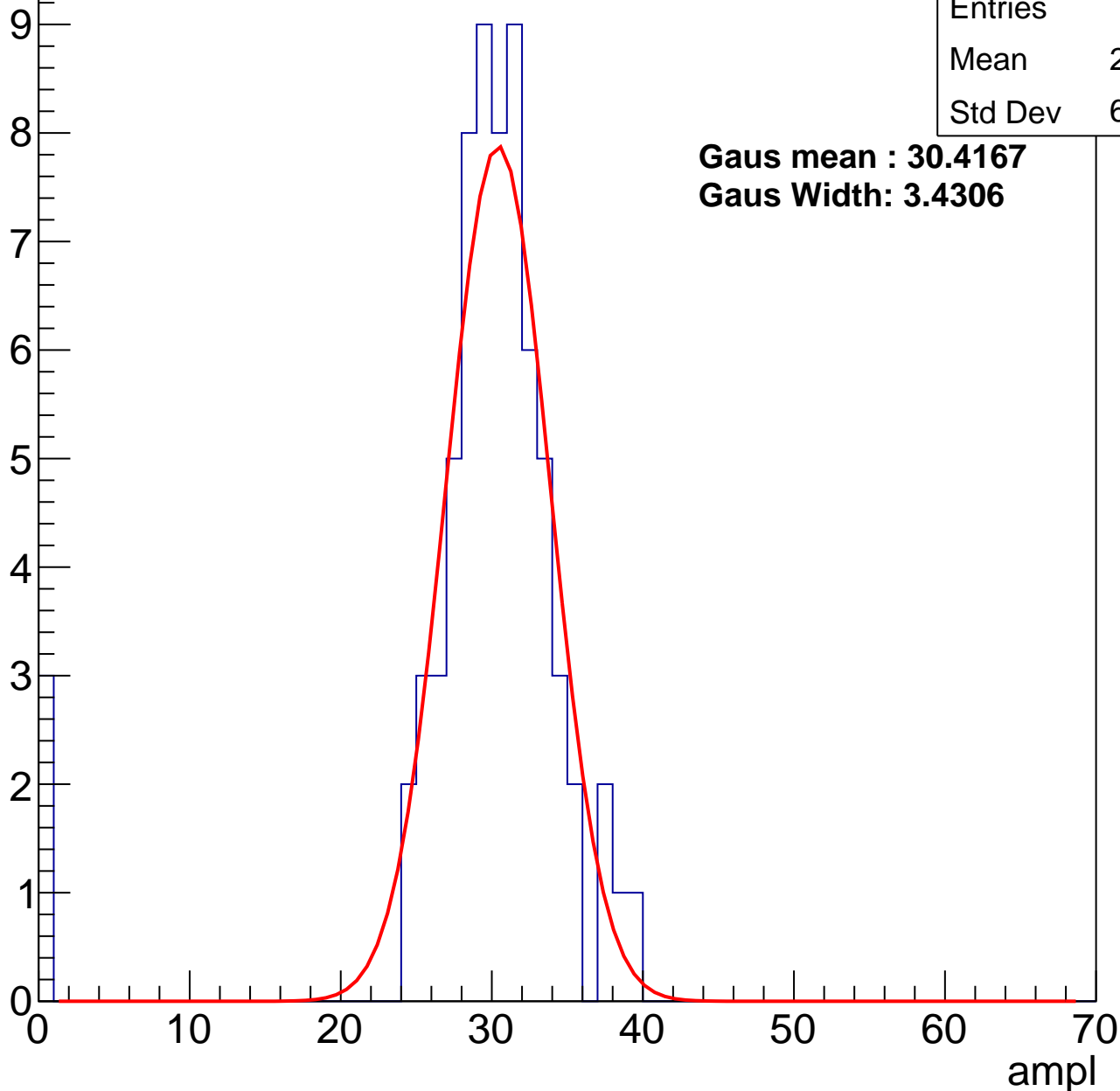
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.86
Std Dev	6.875

Gaus mean : 30.4167

Gaus Width: 3.4306



B1L101S, U1-ch55, adc1

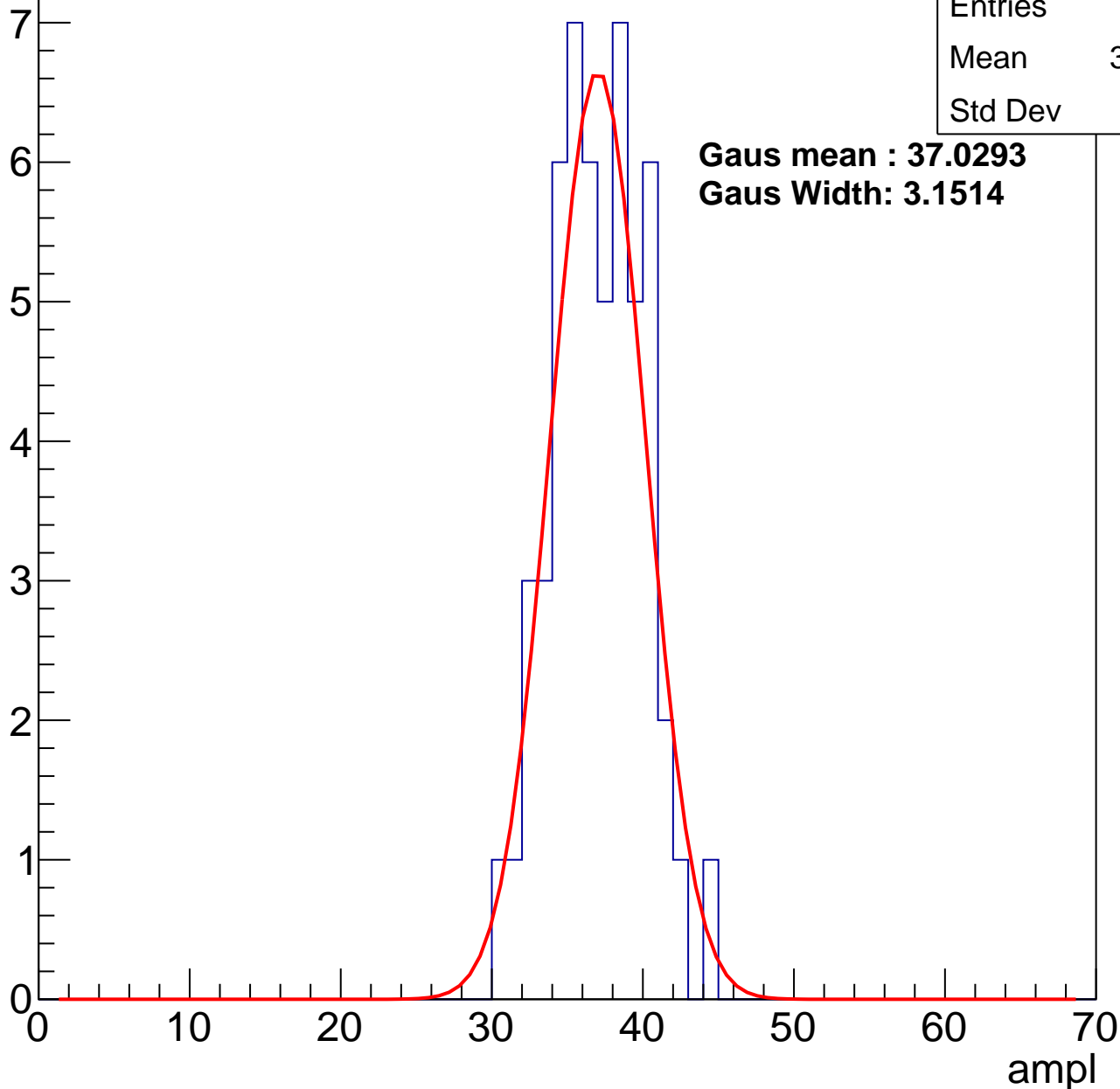
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.57
Std Dev	2.96

Gaus mean : 37.0293

Gaus Width: 3.1514



B1L101S, U1-ch55, adc2

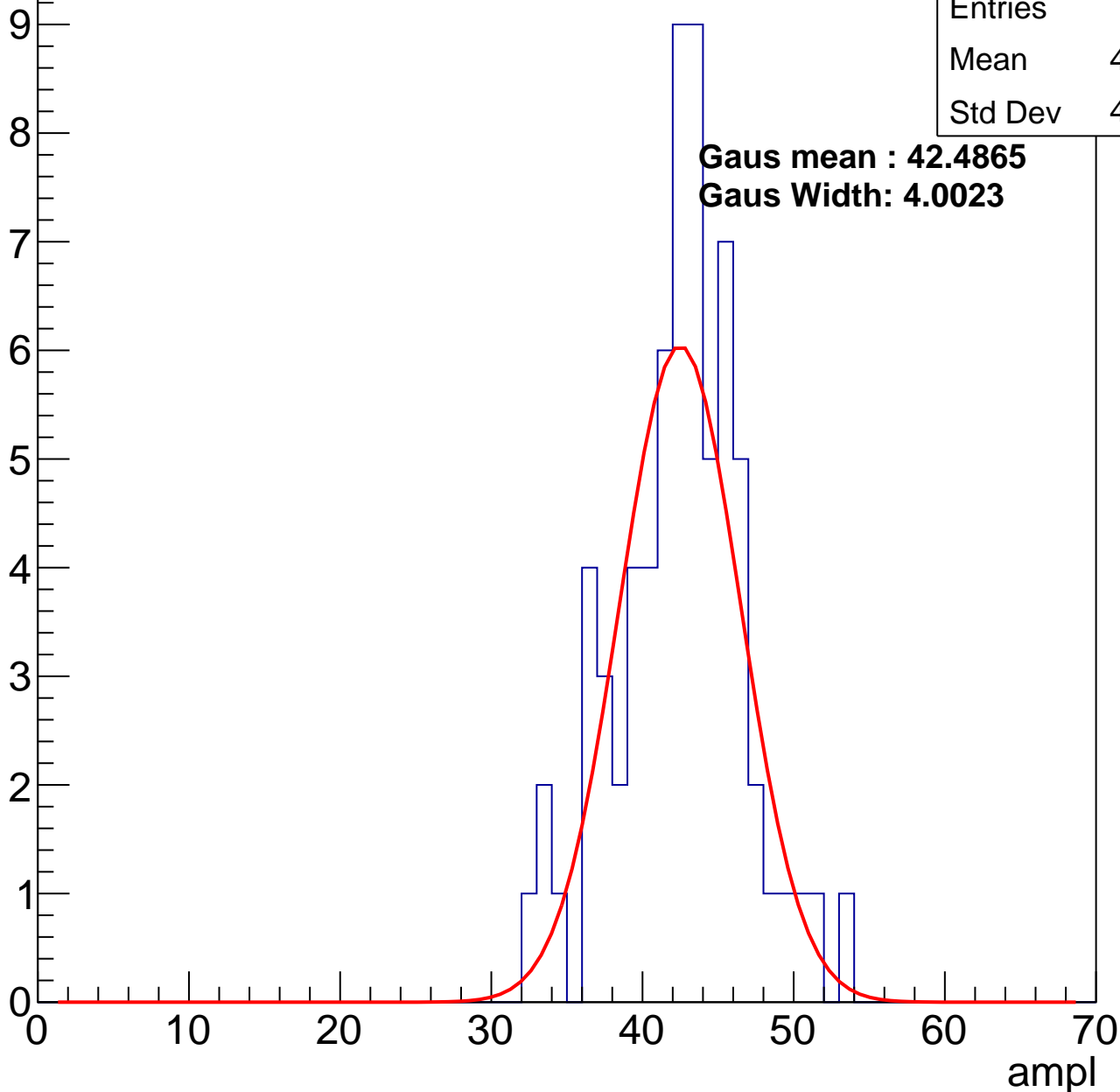
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.03
Std Dev	4.184

Gaus mean : 42.4865

Gaus Width: 4.0023

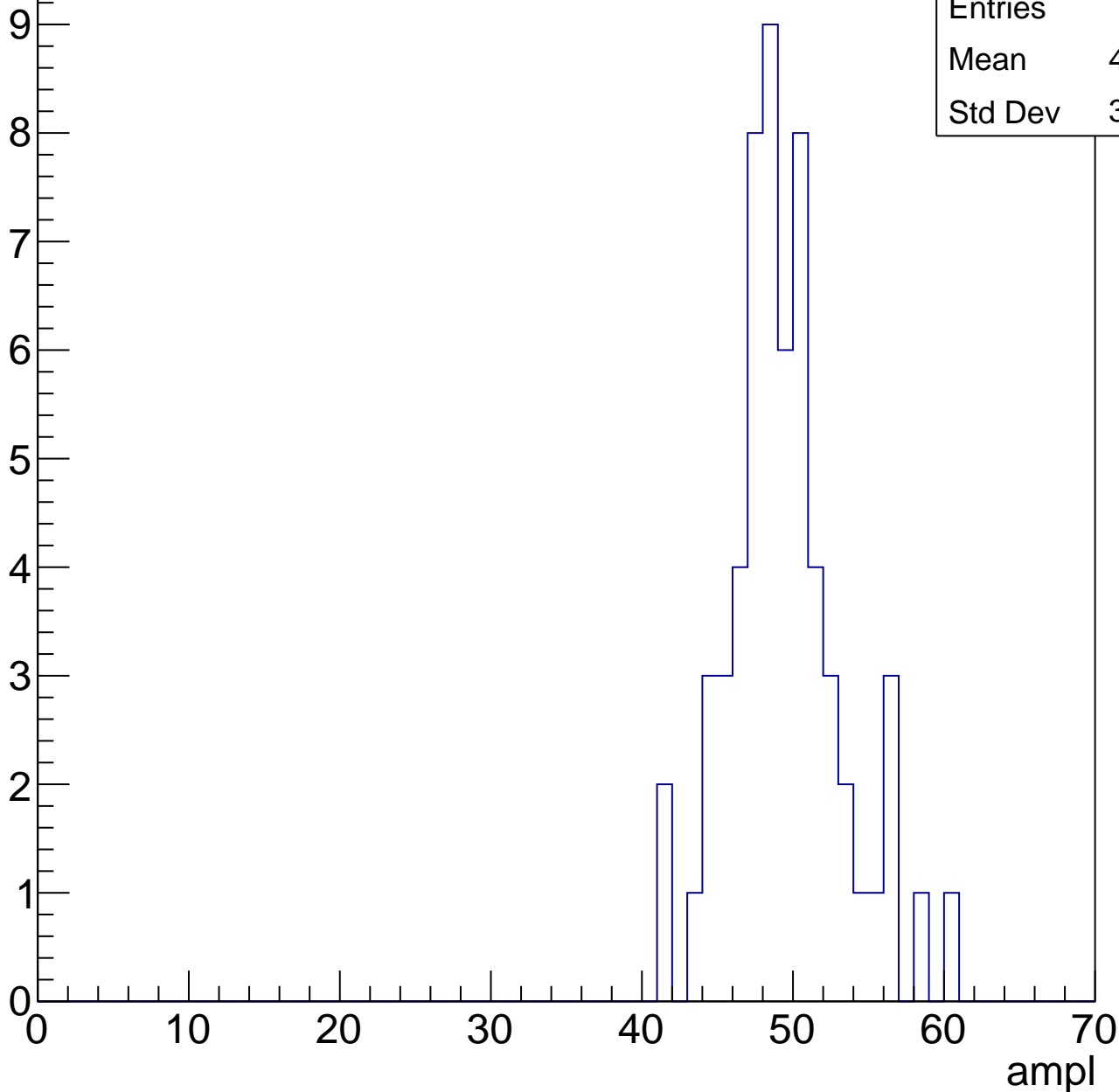


B1L101S, U1-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.98
Std Dev	3.779

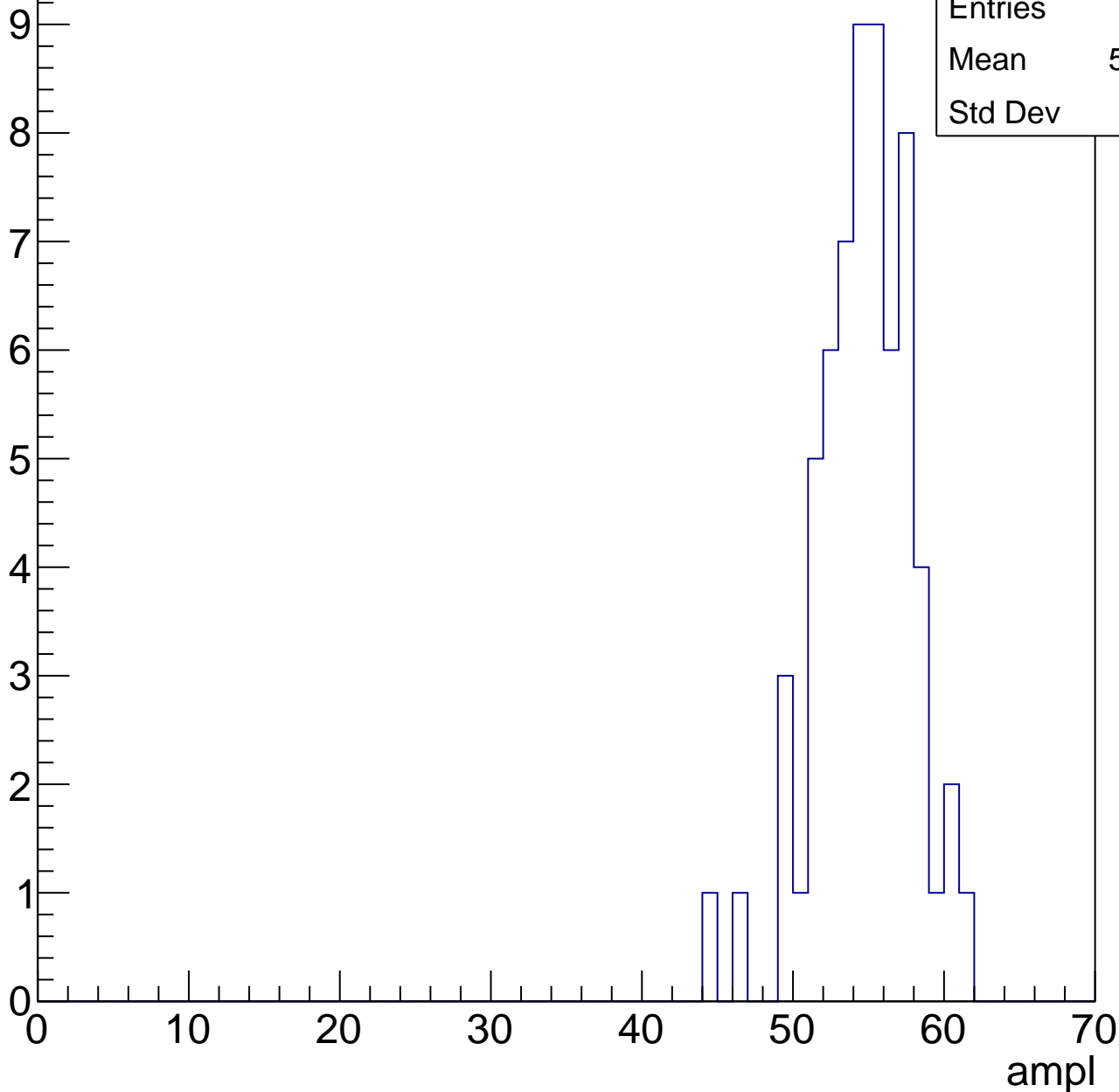


B1L101S, U1-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

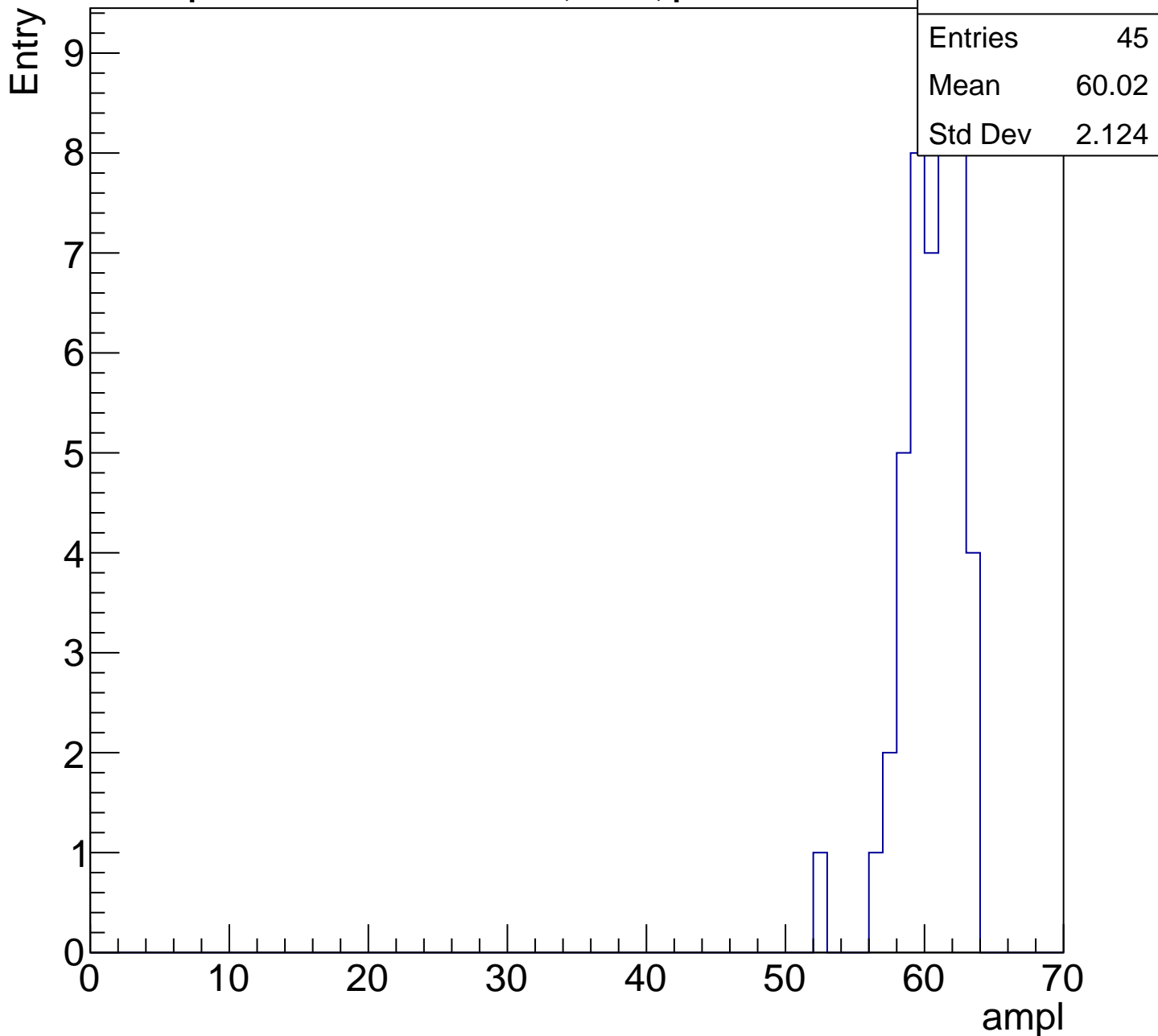
Entry

Entries	64
Mean	54.22
Std Dev	3.17



B1L101S, U1-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

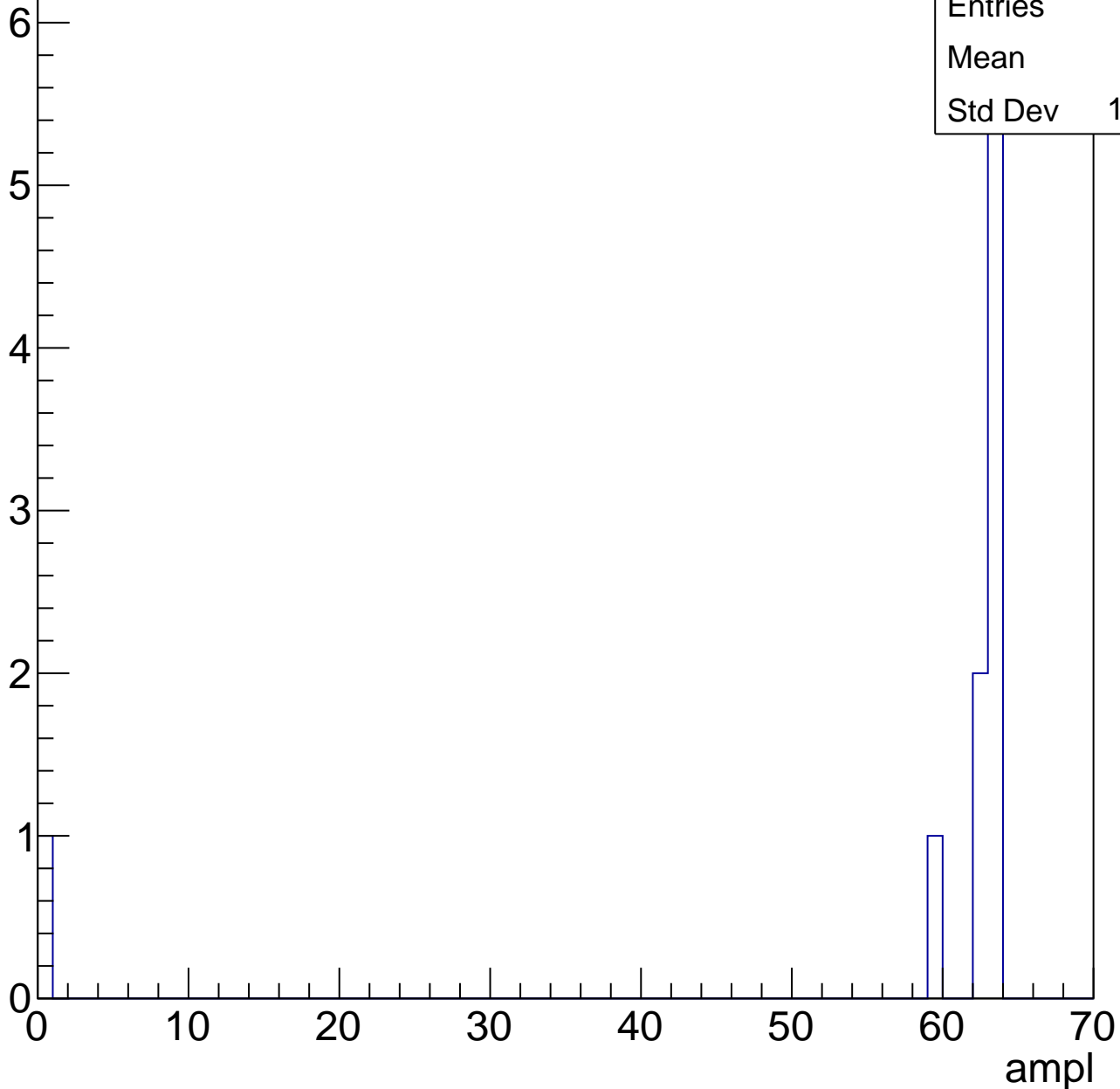


B1L101S, U1-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	56.1
Std Dev	18.74



B1L101S, U1-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch56, adc0

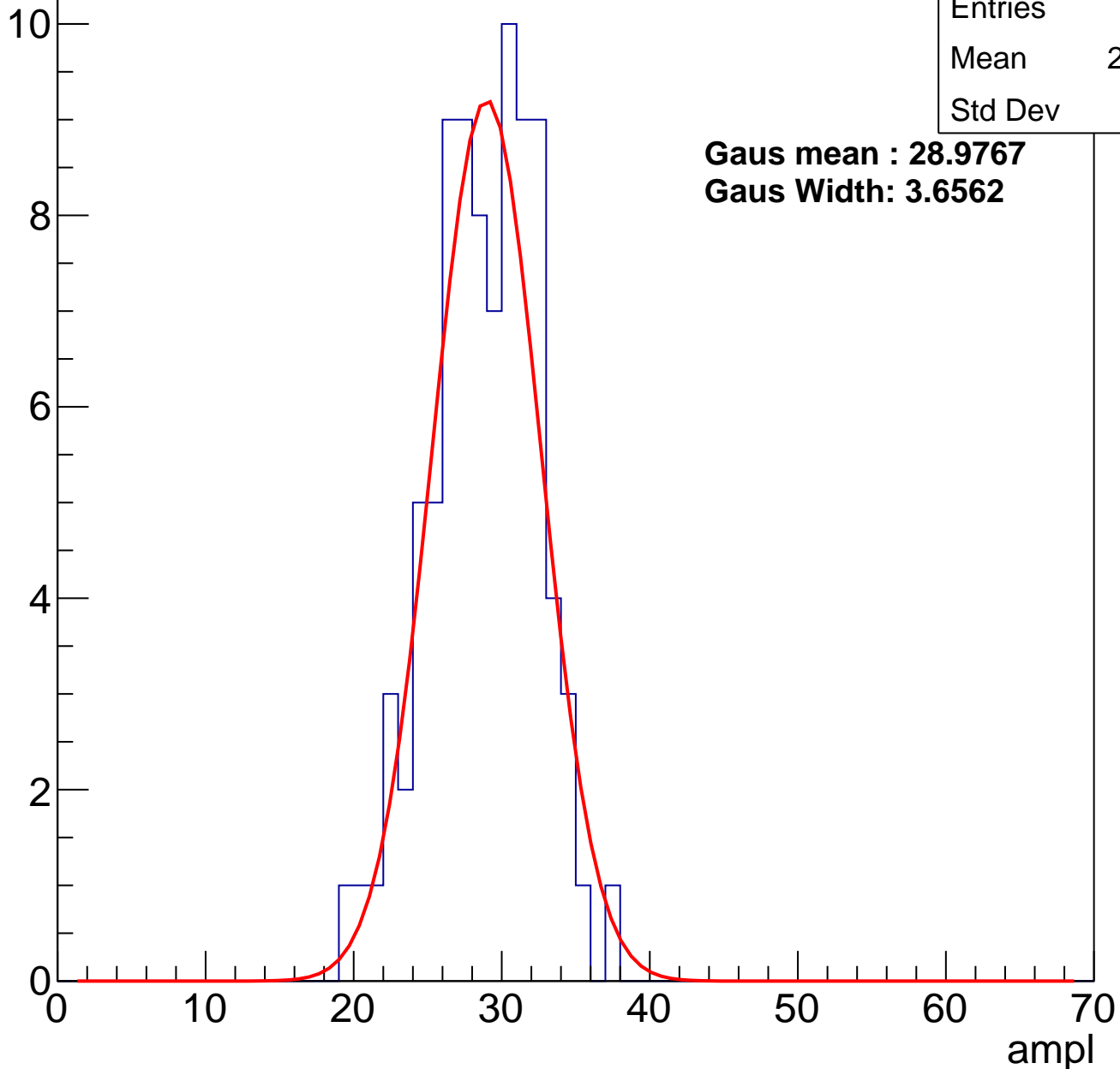
calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	28.34
Std Dev	3.57

Gaus mean : 28.9767

Gaus Width: 3.6562

Entry



B1L101S, U1-ch56, adc1

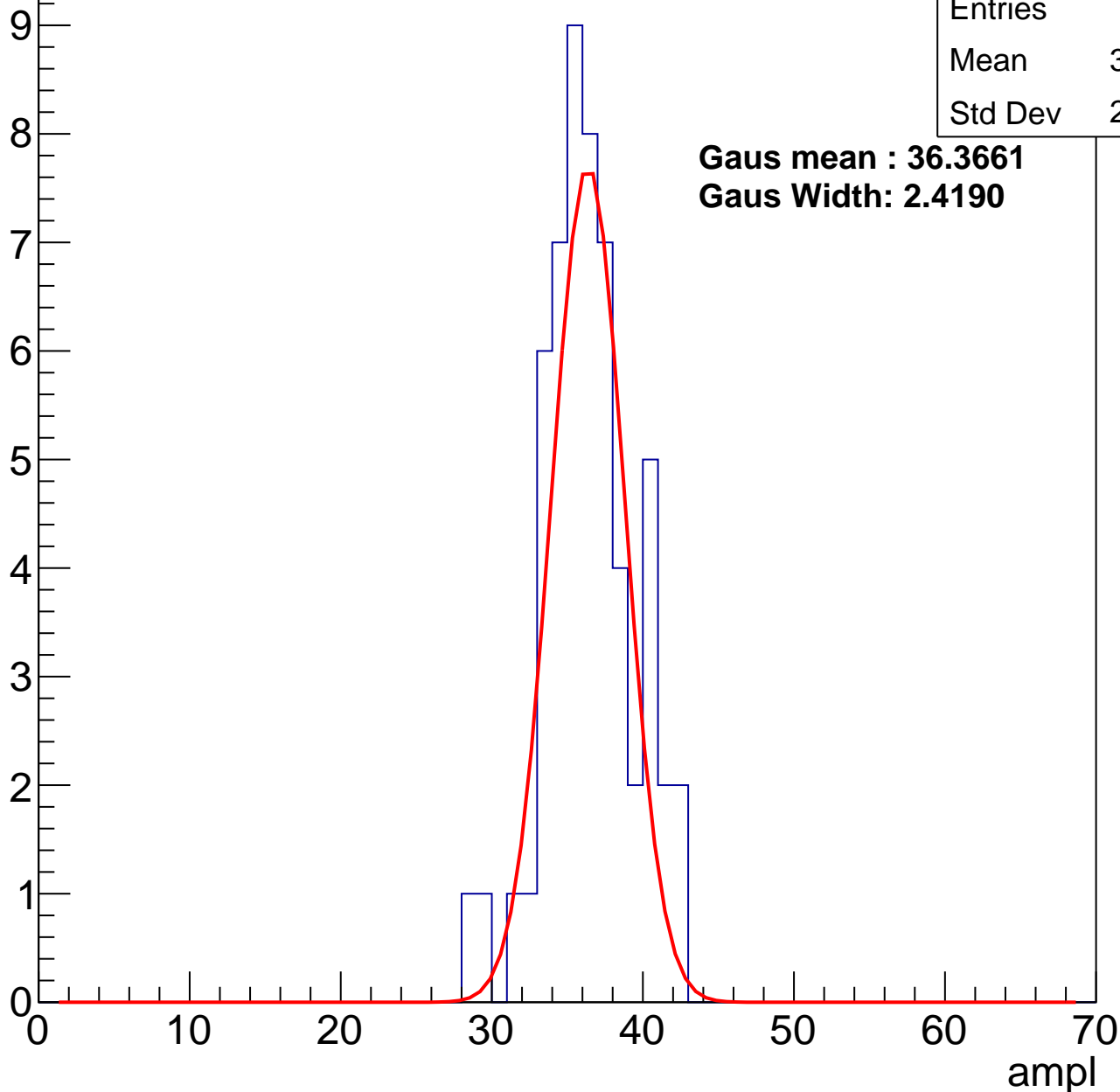
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.96
Std Dev	2.952

Gaus mean : 36.3661

Gaus Width: 2.4190



B1L101S, U1-ch56, adc2

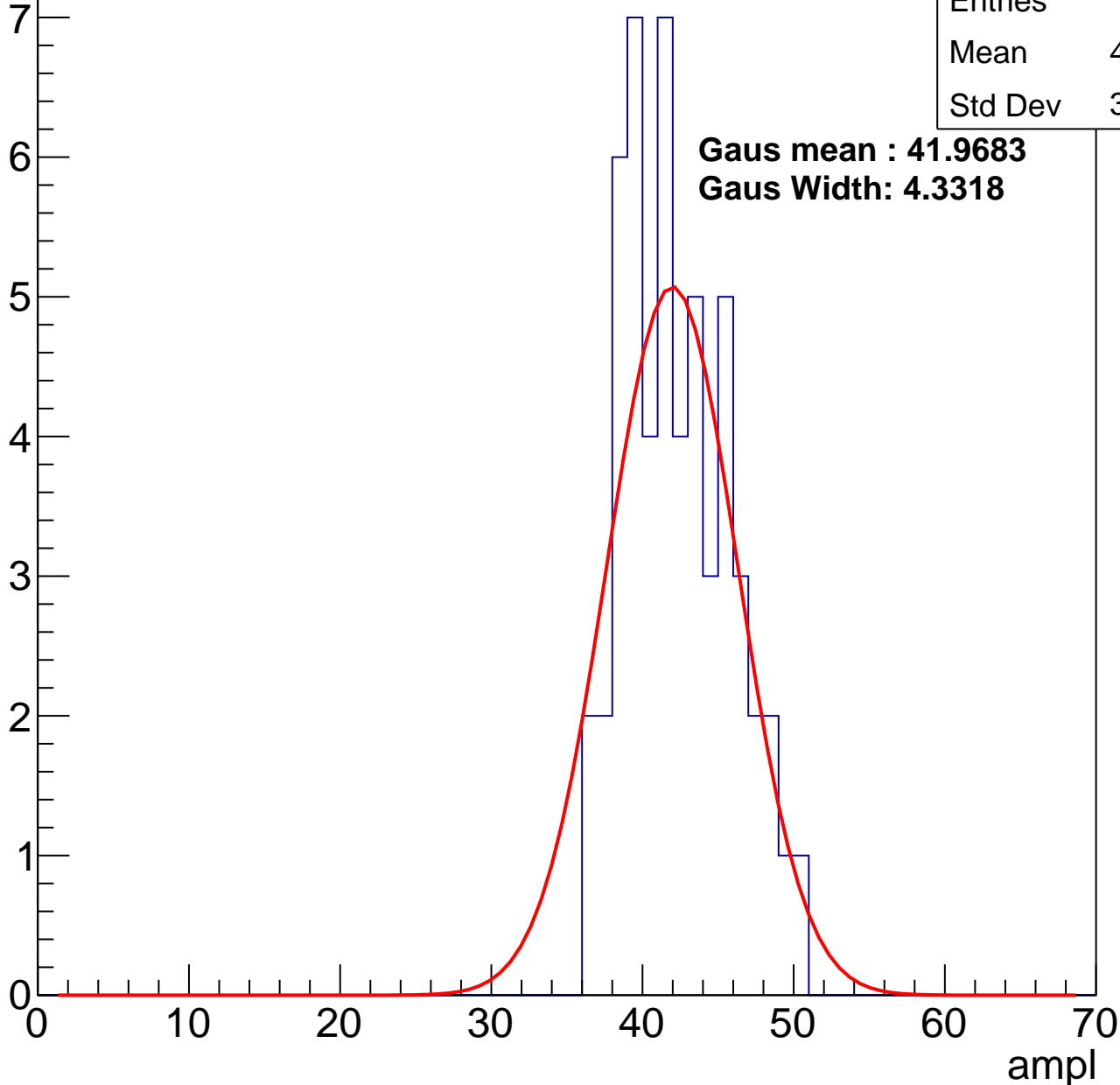
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.87
Std Dev	3.464

Gaus mean : 41.9683

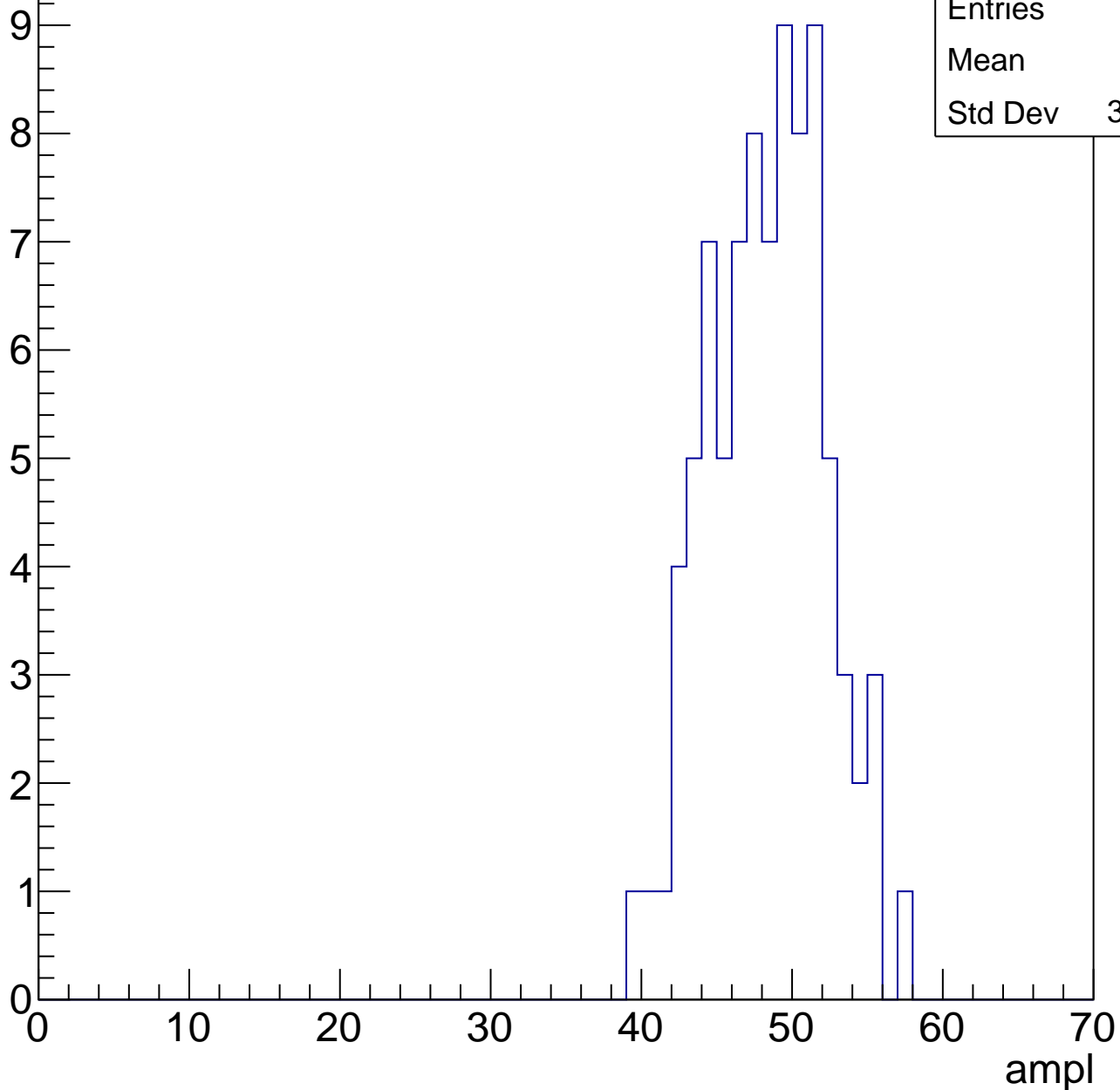
Gaus Width: 4.3318



B1L101S, U1-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

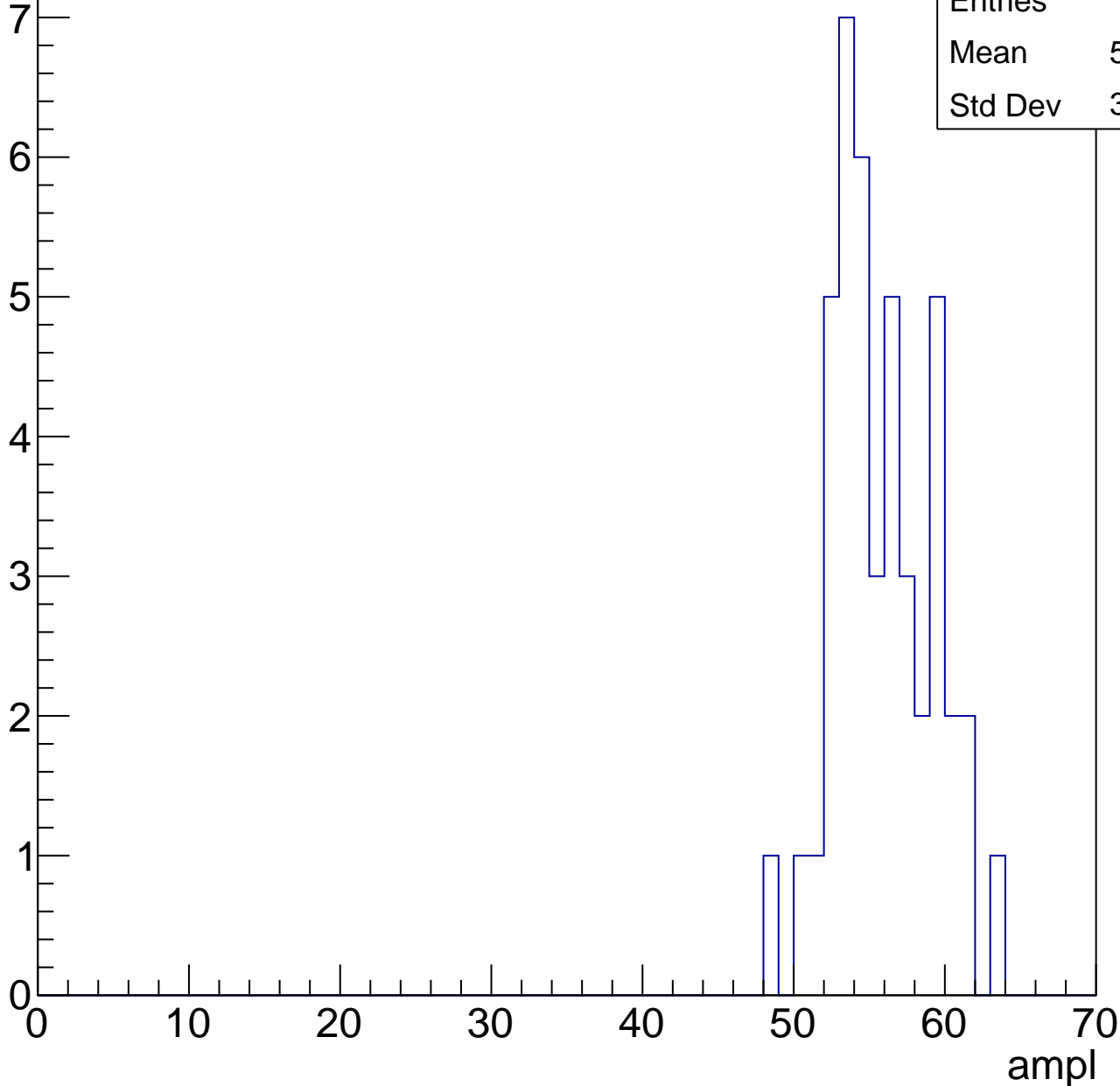


B1L101S, U1-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

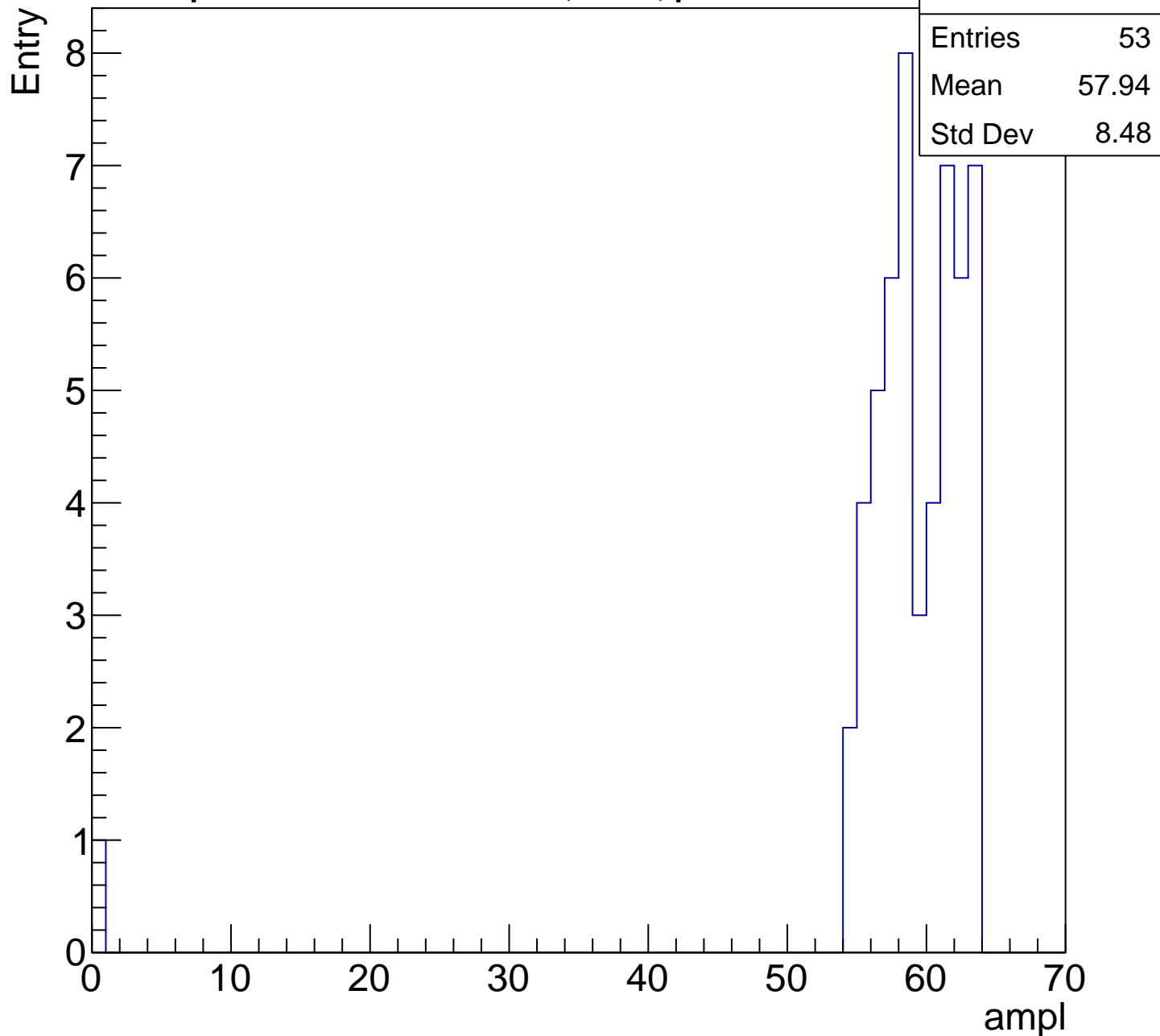
Entry

Entries	44
Mean	55.36
Std Dev	3.248



B1L101S, U1-ch56, adc5

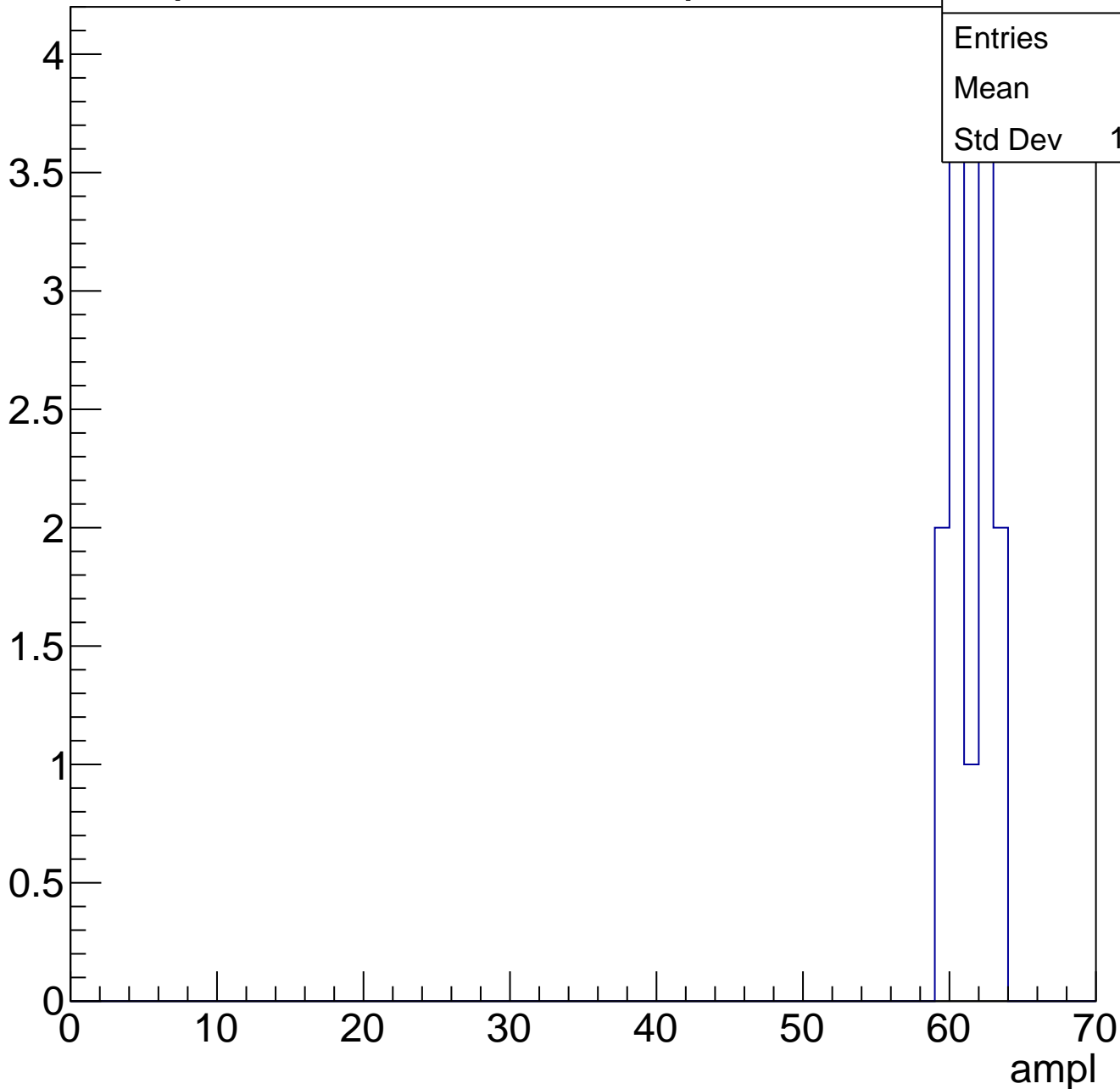
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch57, adc0

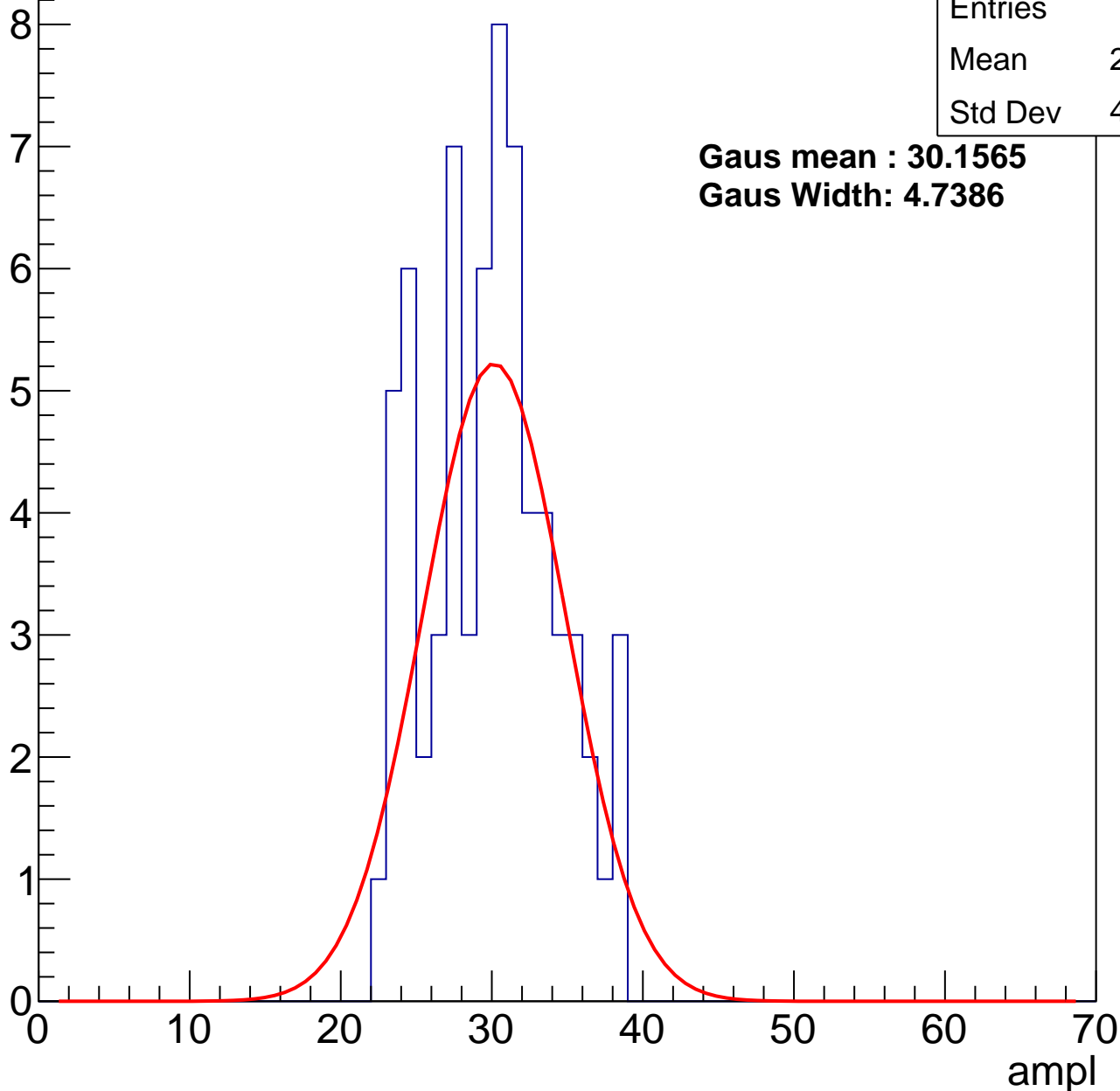
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.46
Std Dev	4.164

Gaus mean : 30.1565

Gaus Width: 4.7386



B1L101S, U1-ch57, adc1

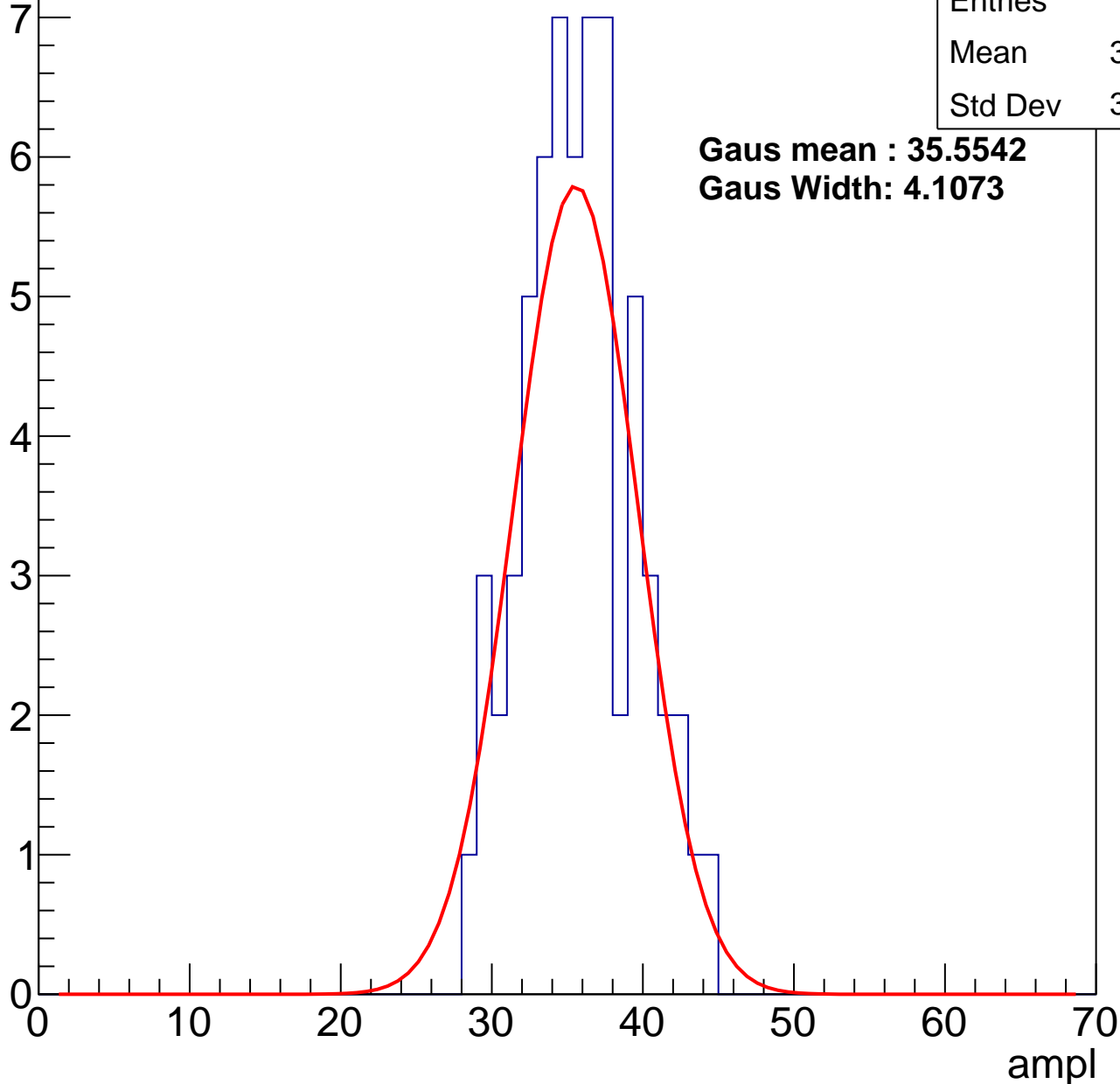
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.38
Std Dev	3.662

Gaus mean : 35.5542

Gaus Width: 4.1073



B1L101S, U1-ch57, adc2

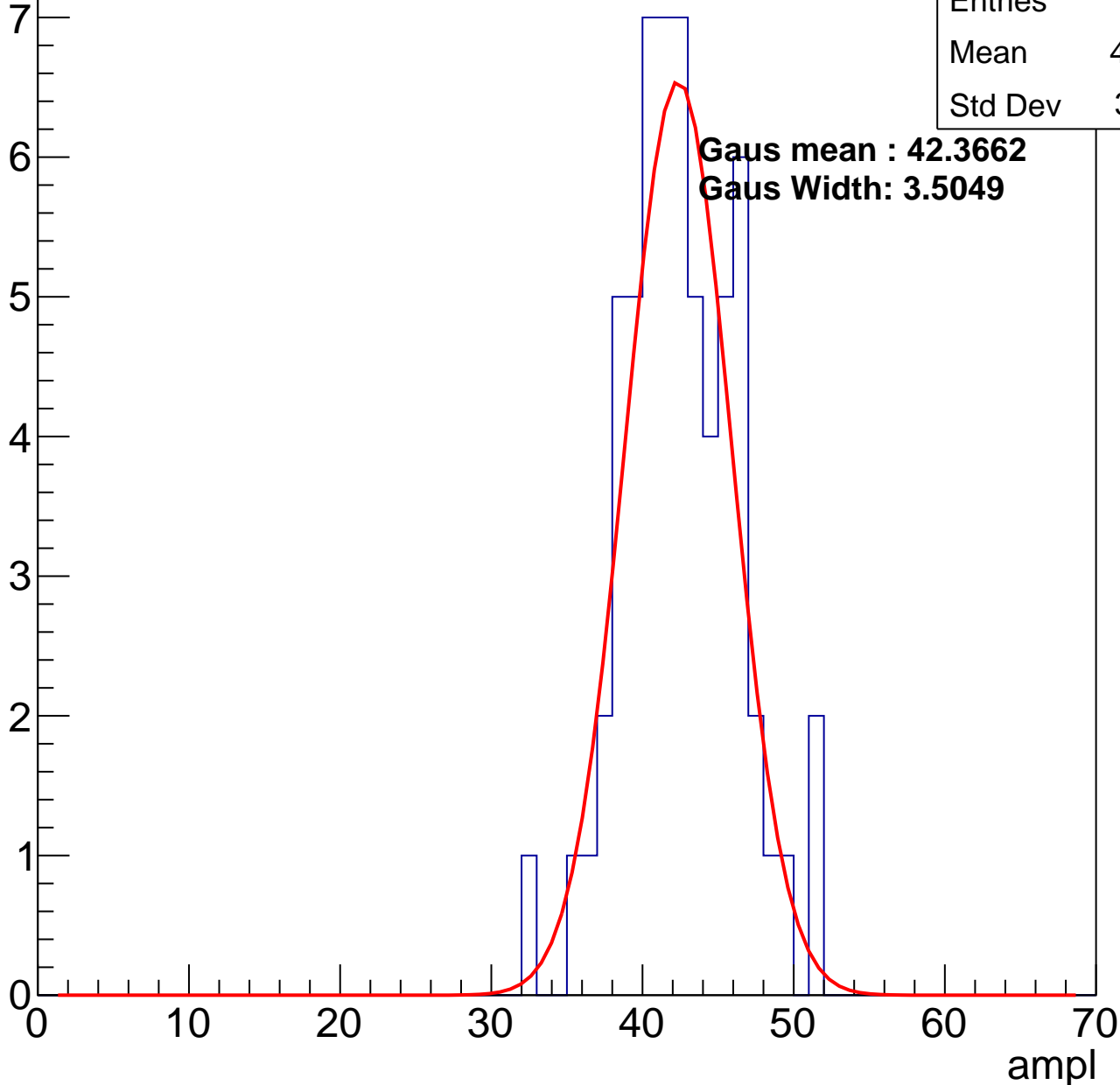
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.06
Std Dev	3.711

Gaus mean : 42.3662

Gaus Width: 3.5049

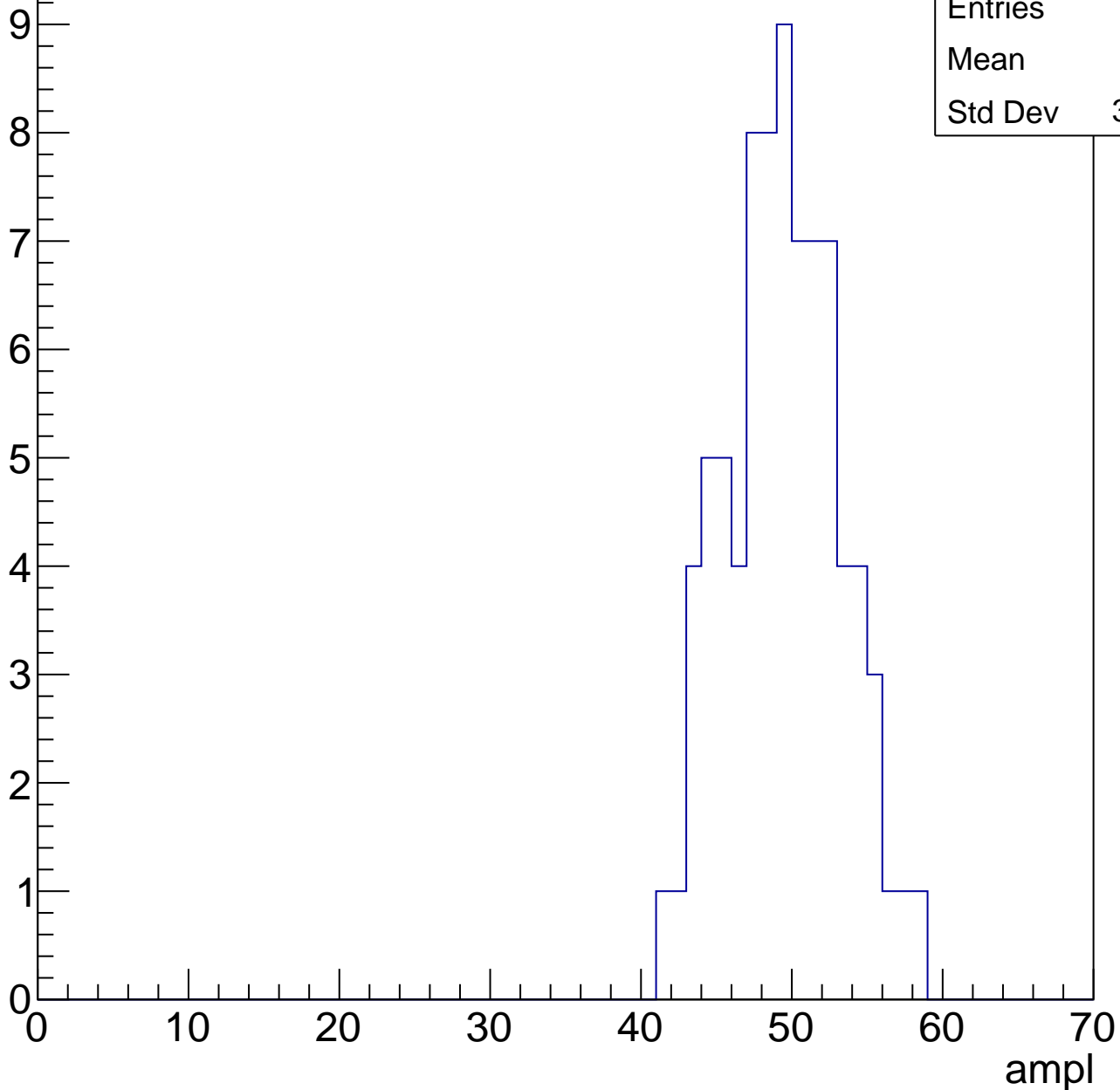


B1L101S, U1-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	49
Std Dev	3.711

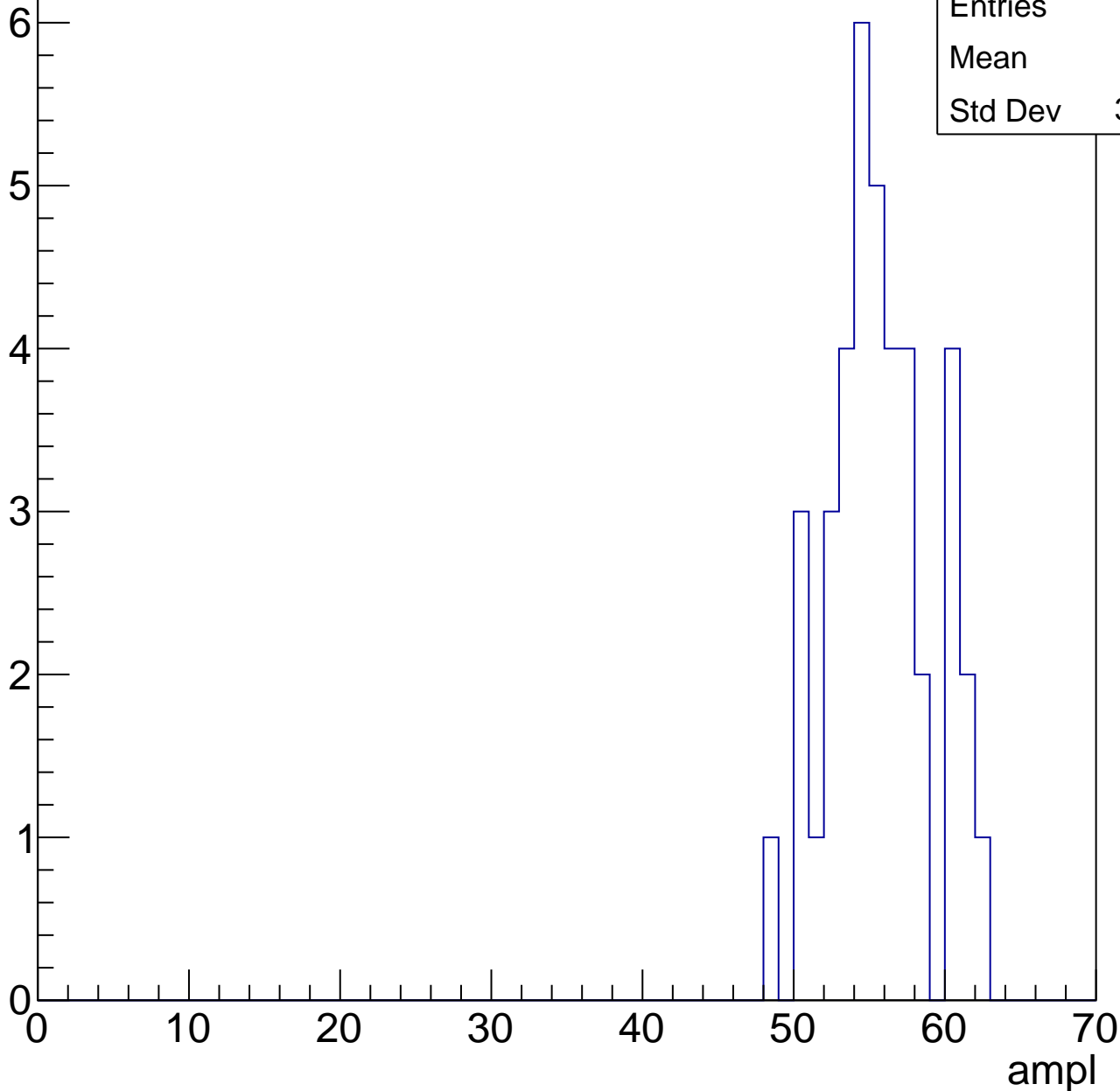


B1L101S, U1-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	55.2
Std Dev	3.341

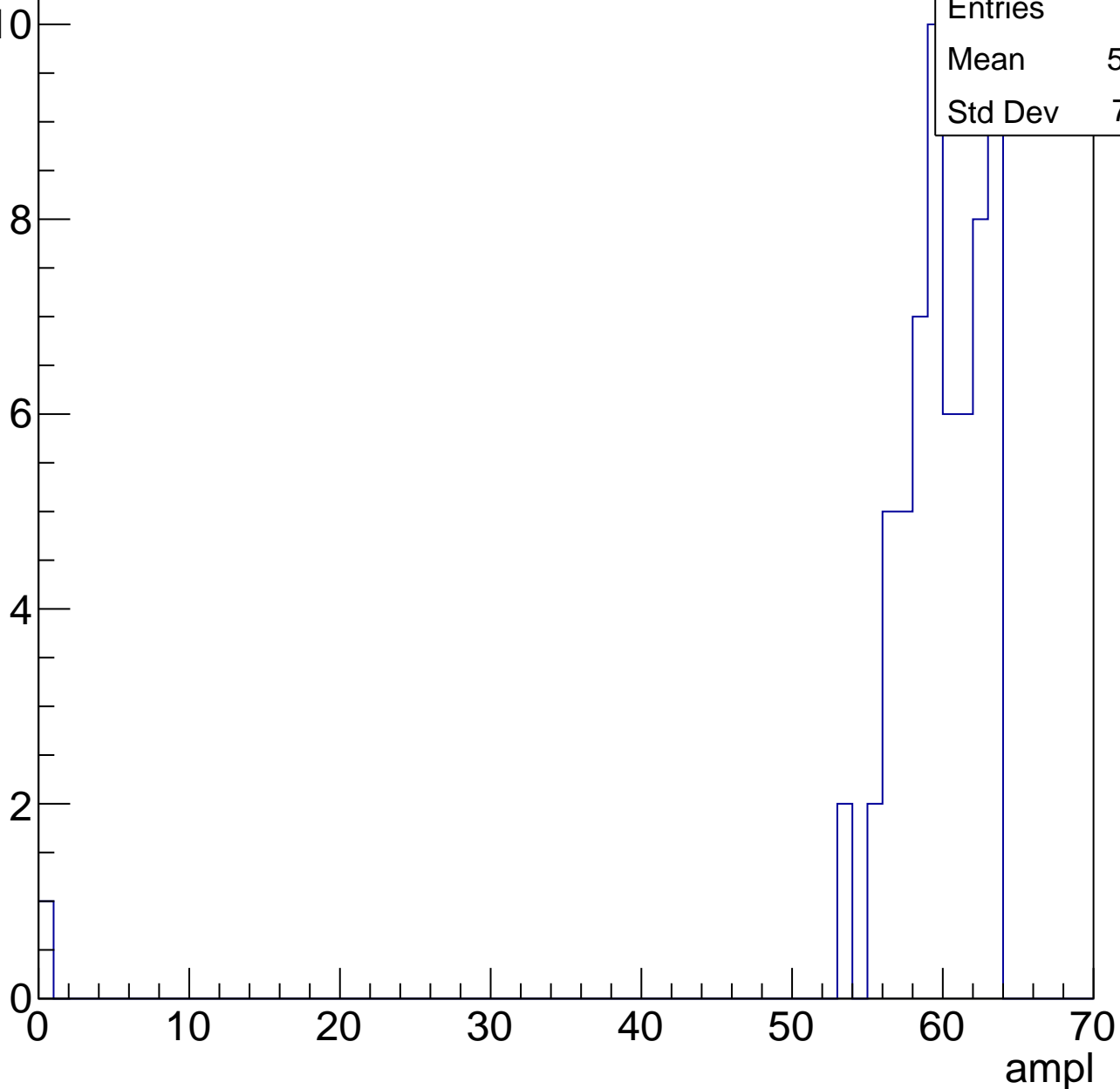


B1L101S, U1-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

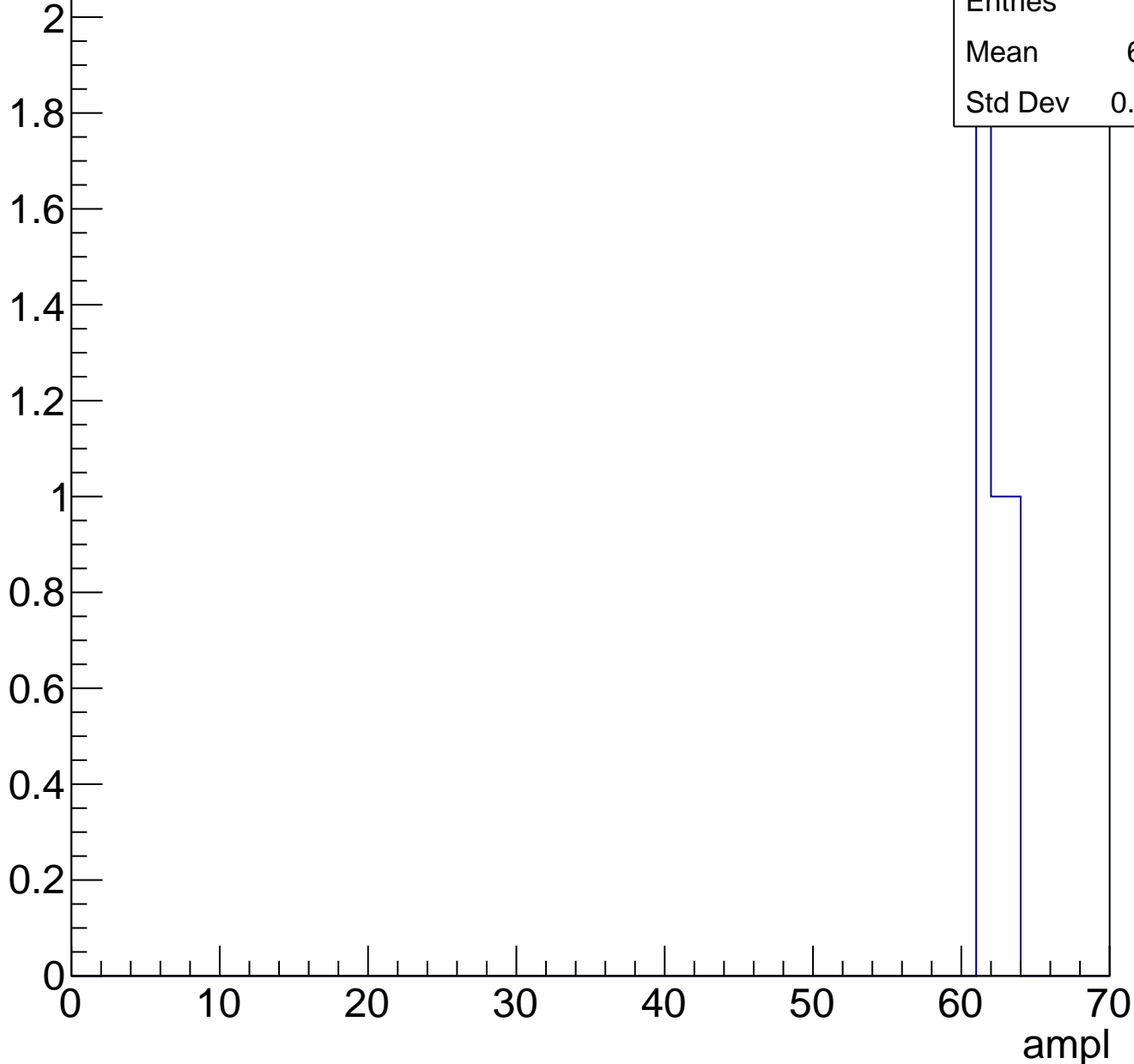
Entries	61
Mean	58.46
Std Dev	7.981



B1L101S, U1-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch58, adc0

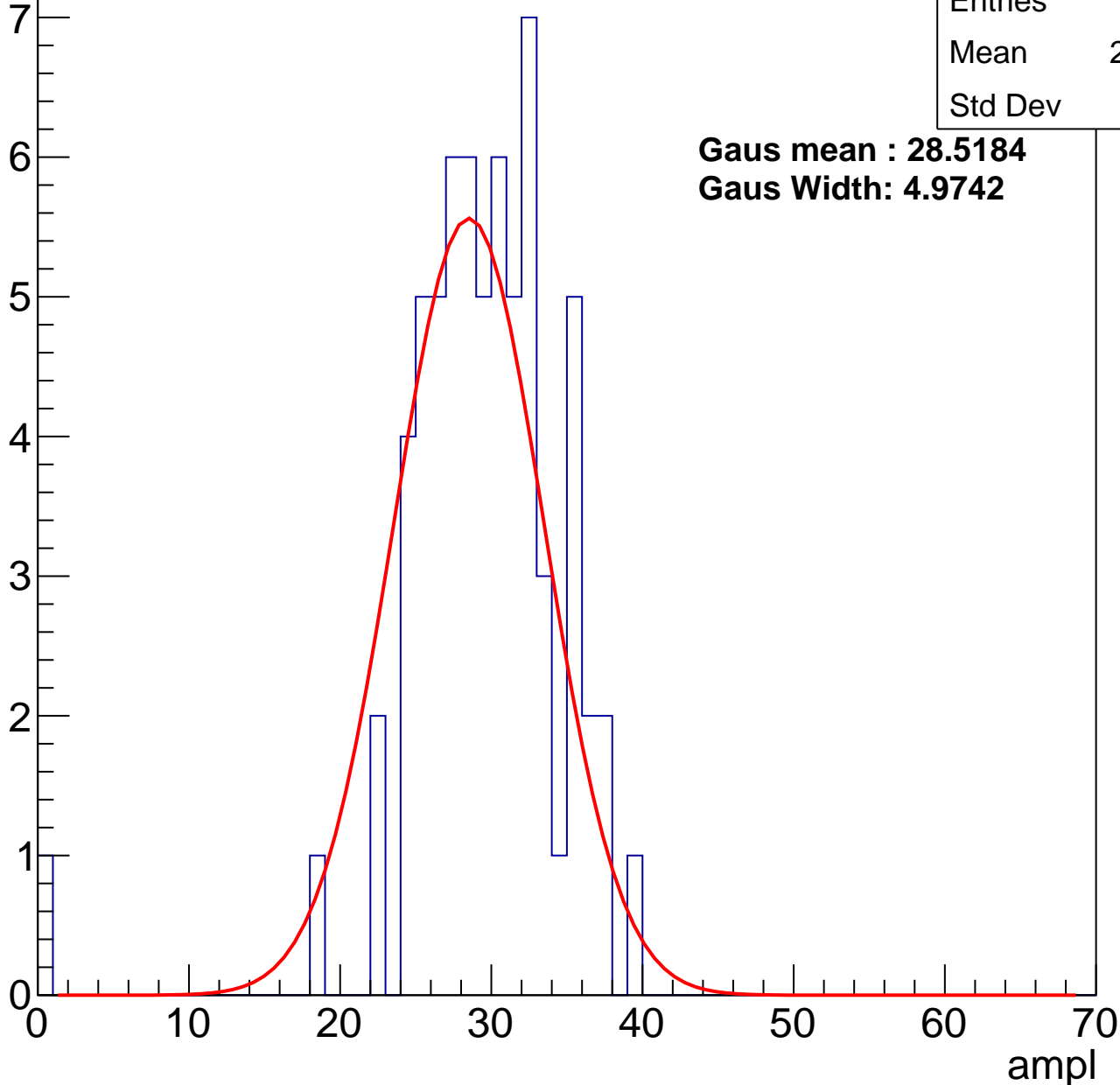
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.96
Std Dev	5.44

Gaus mean : 28.5184

Gaus Width: 4.9742



B1L101S, U1-ch58, adc1

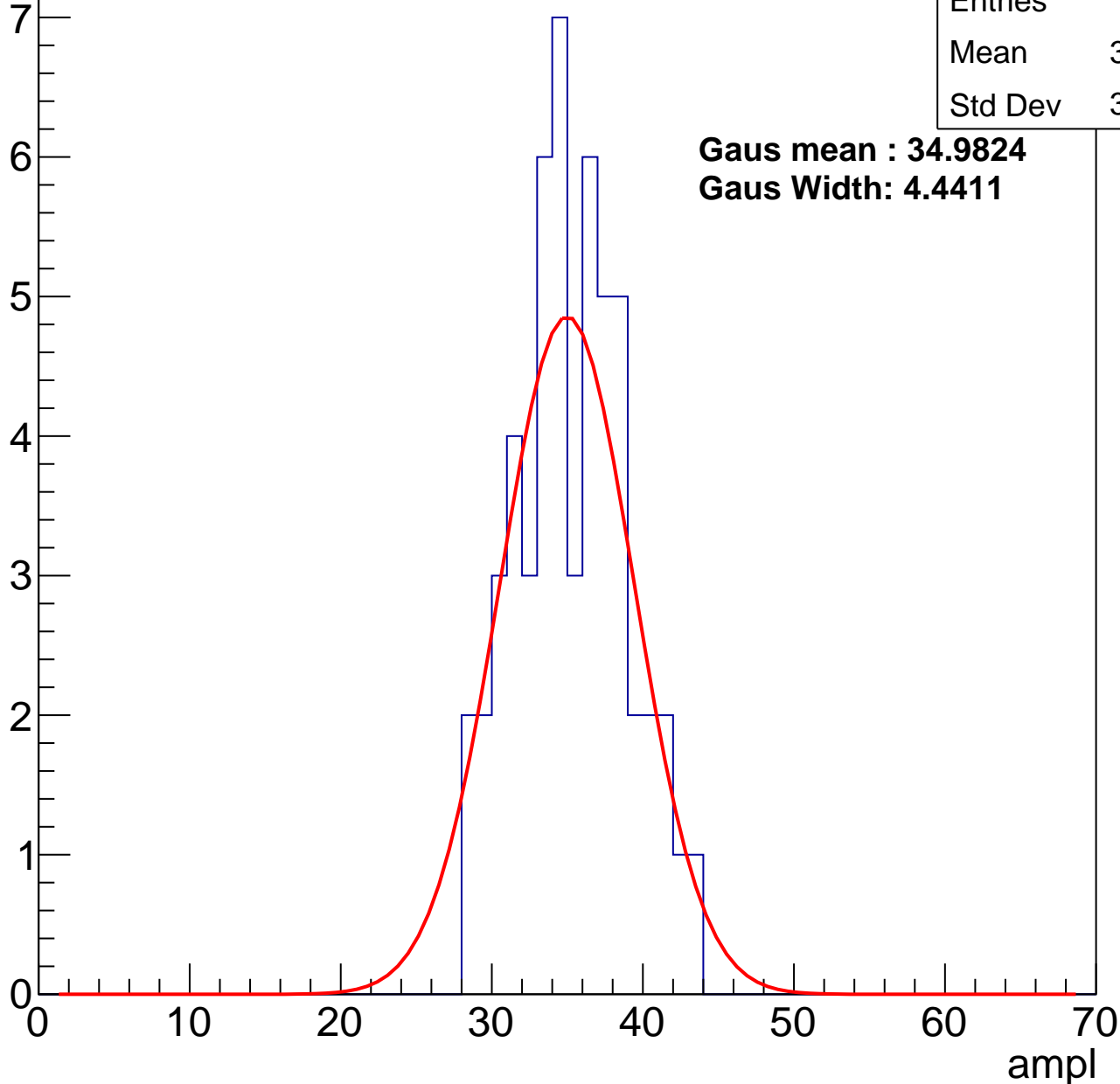
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	34.83
Std Dev	3.609

Gaus mean : 34.9824

Gaus Width: 4.4411



B1L101S, U1-ch58, adc2

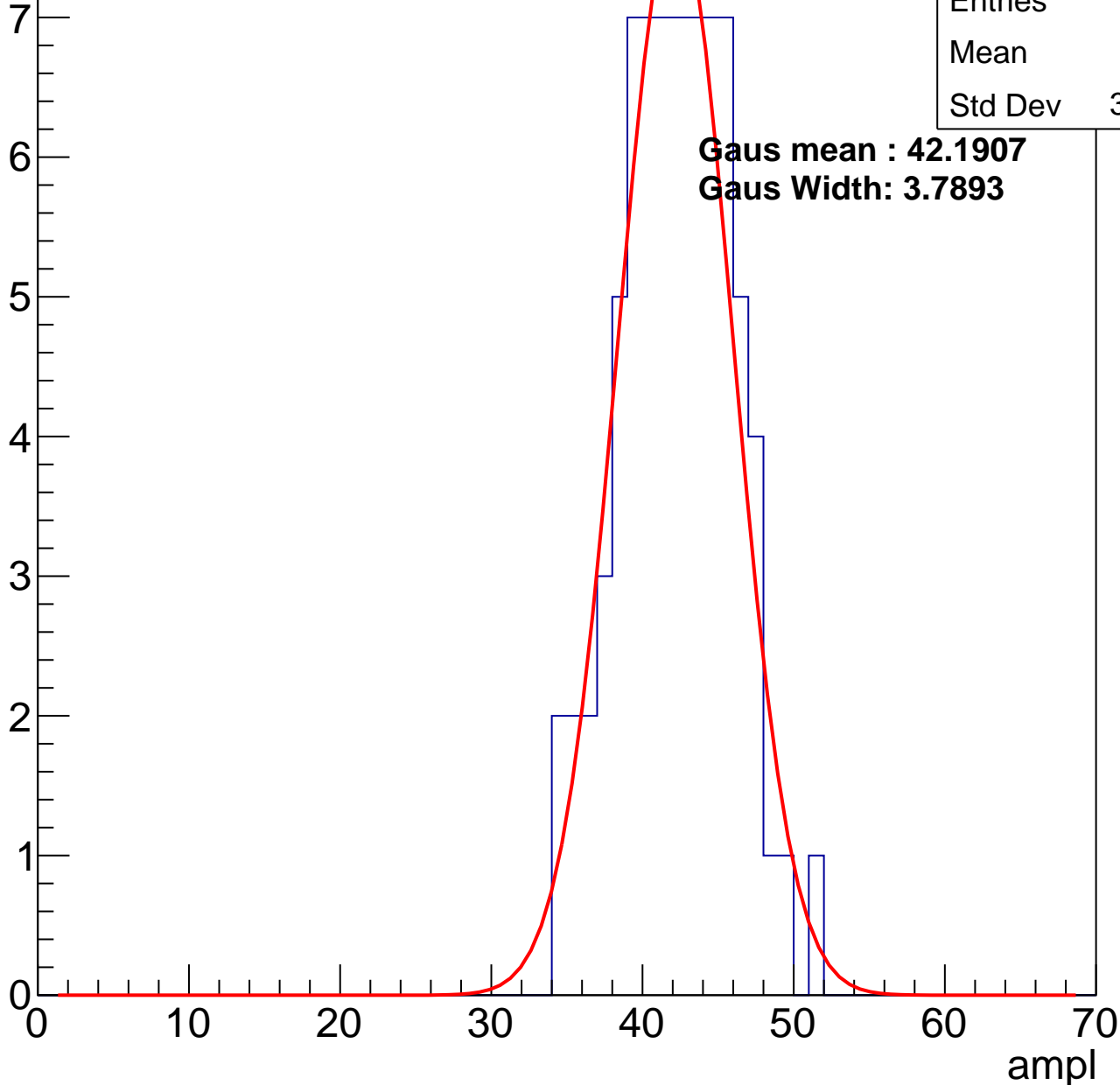
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	41.8
Std Dev	3.637

Gaus mean : 42.1907

Gaus Width: 3.7893

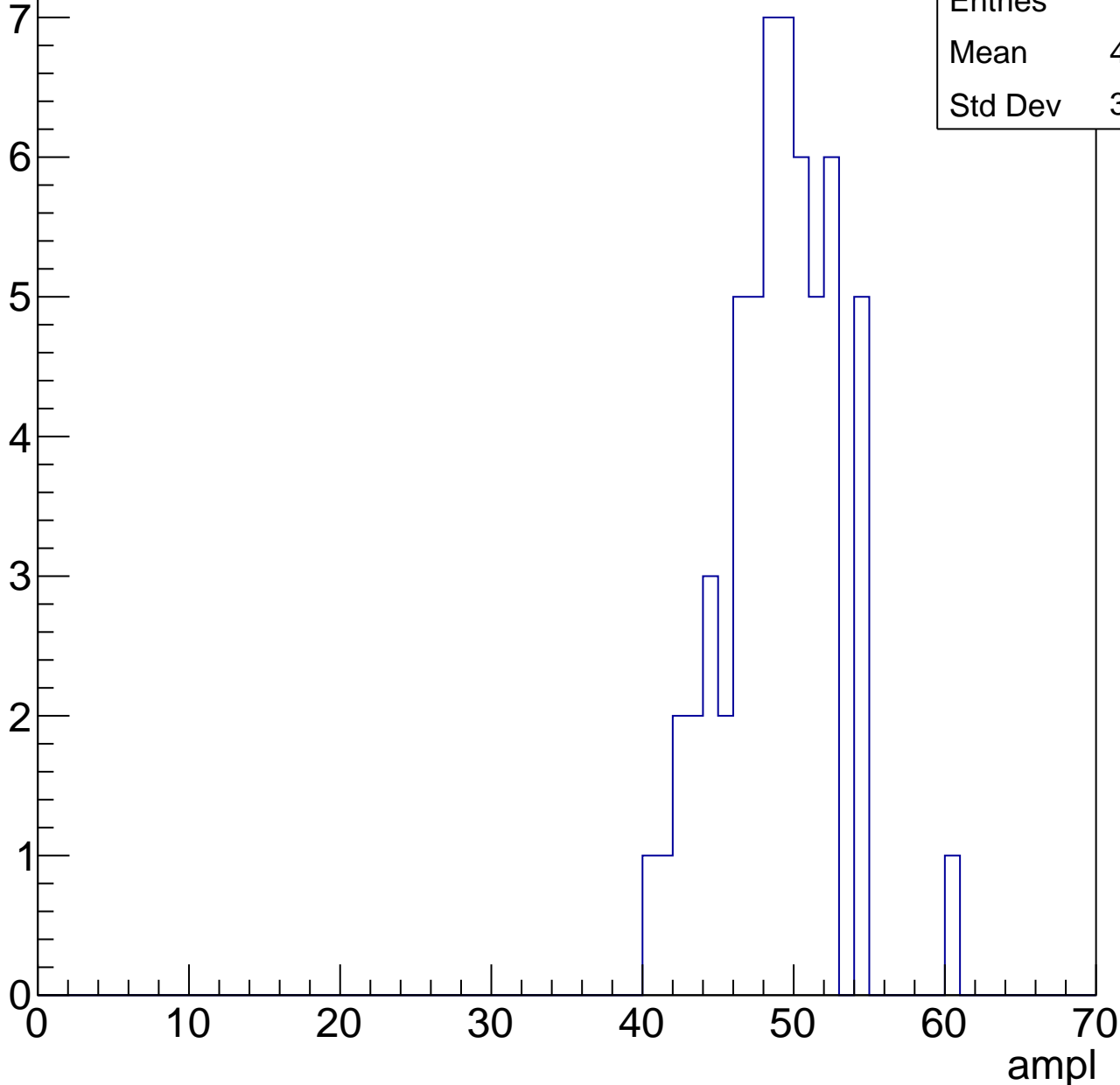


B1L101S, U1-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.52
Std Dev	3.729

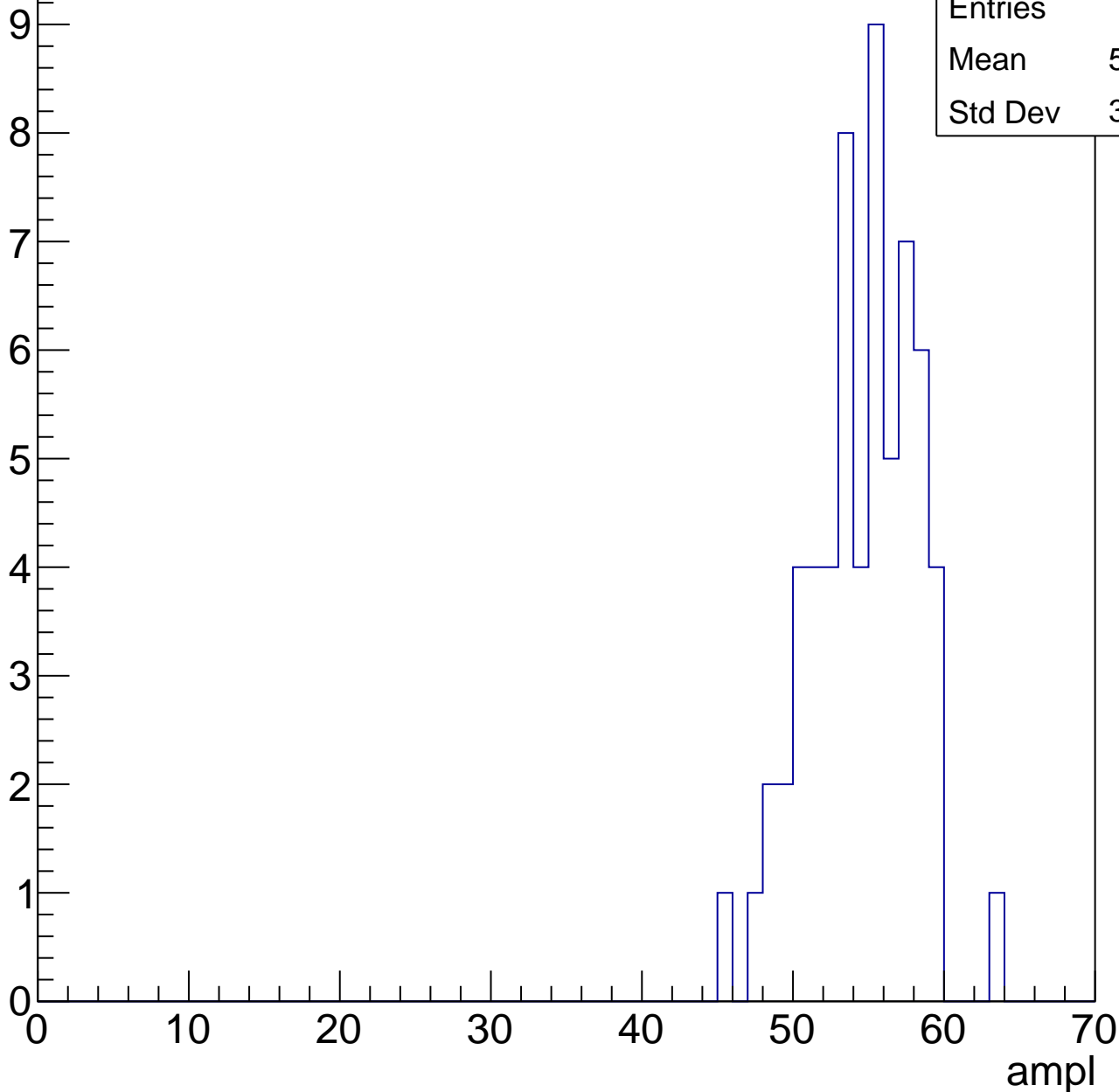


B1L101S, U1-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.18
Std Dev	3.462

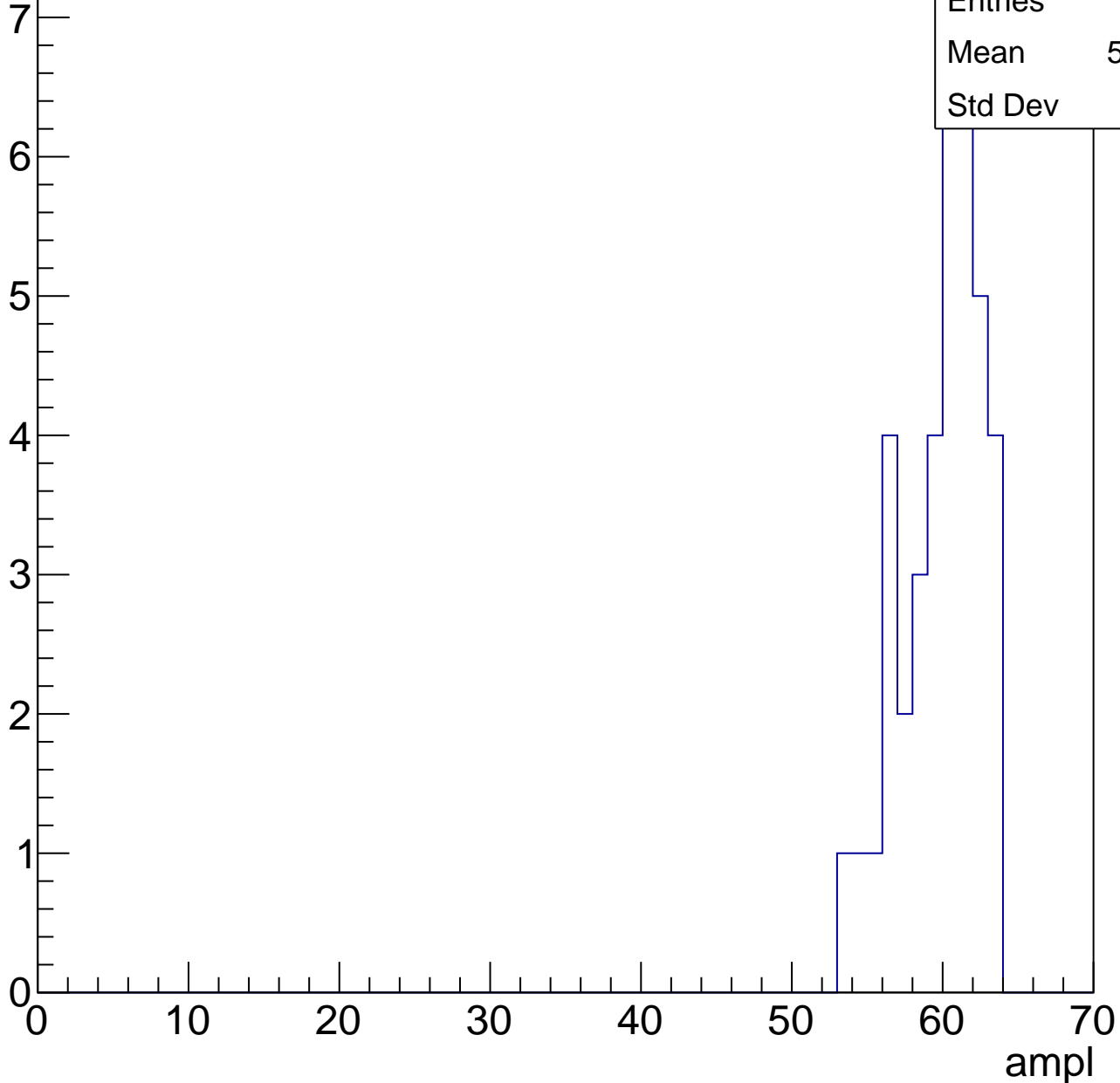


B1L101S, U1-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	59.46
Std Dev	2.58

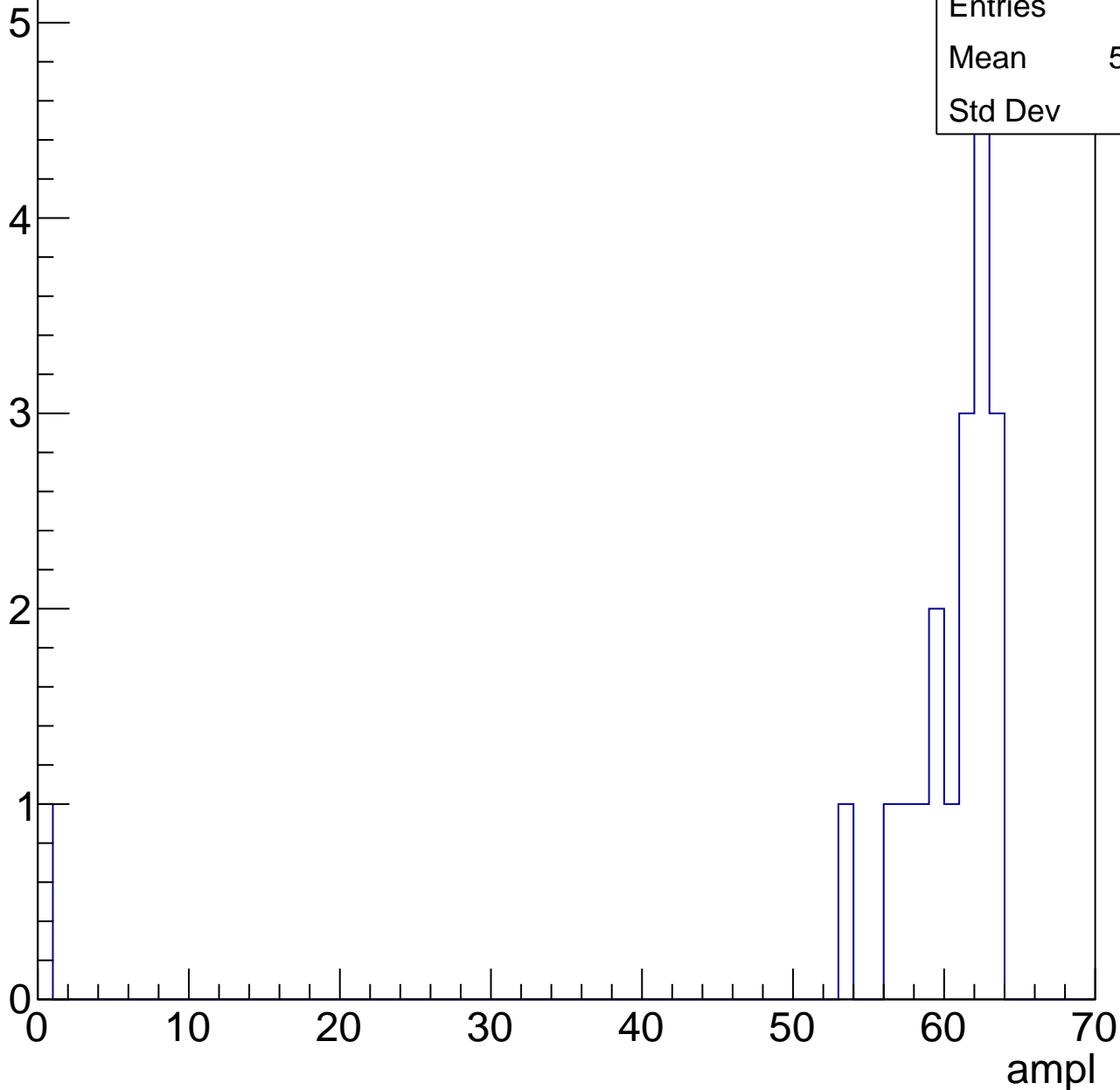


B1L101S, U1-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

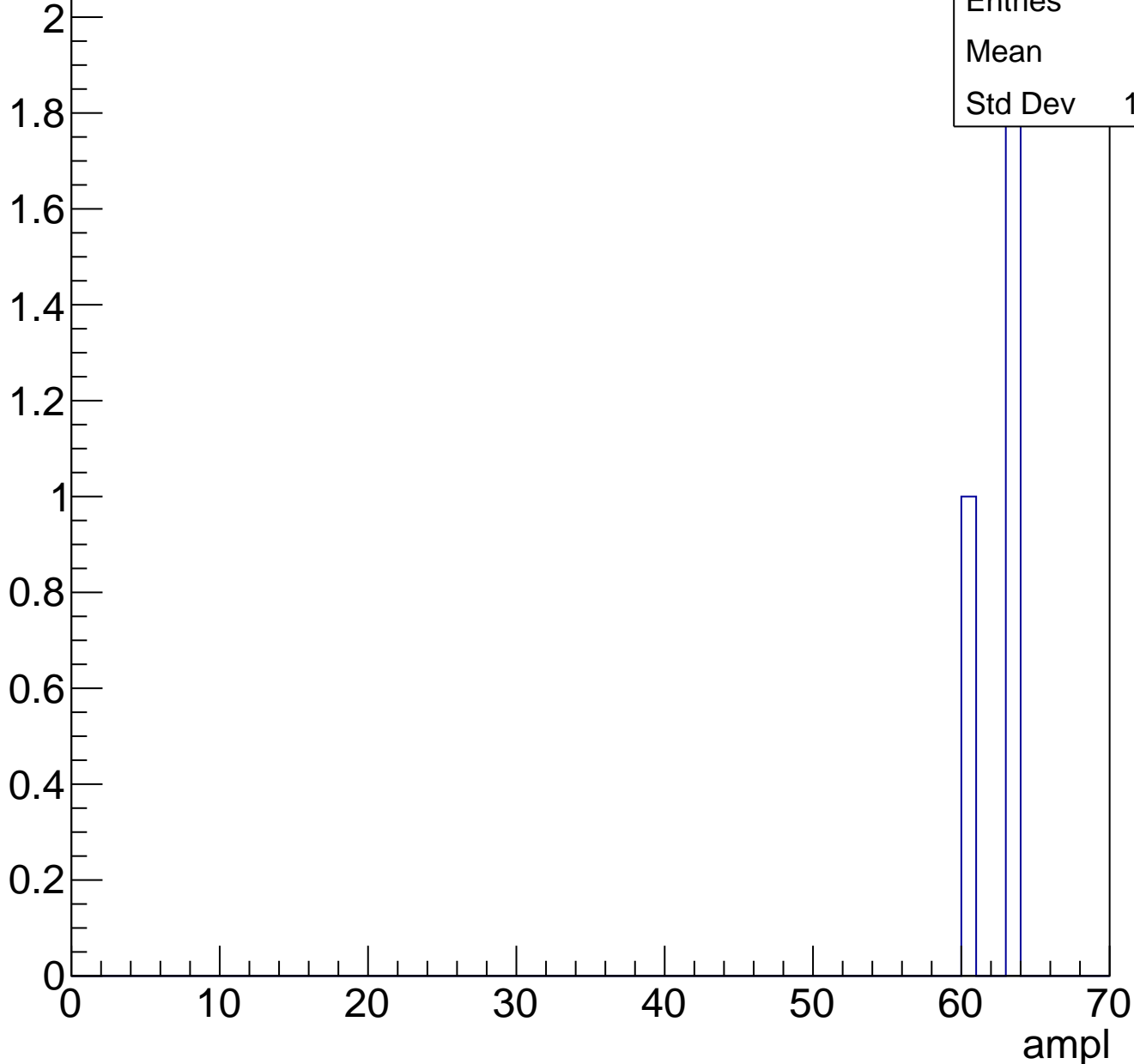
Entries	19
Mean	57.05
Std Dev	13.7



B1L101S, U1-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch59, adc0

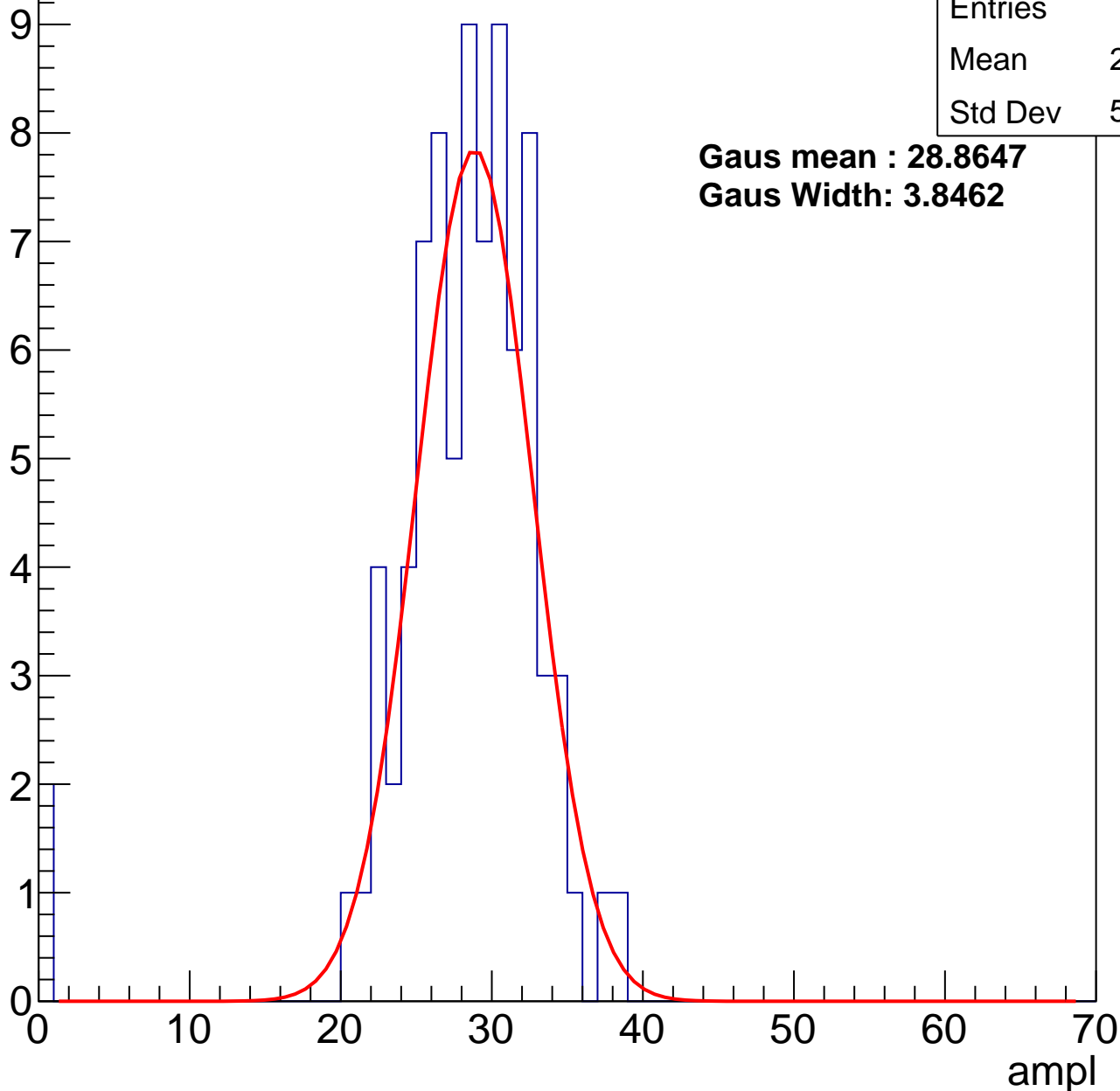
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	27.65
Std Dev	5.703

Gaus mean : 28.8647

Gaus Width: 3.8462



B1L101S, U1-ch59, adc1

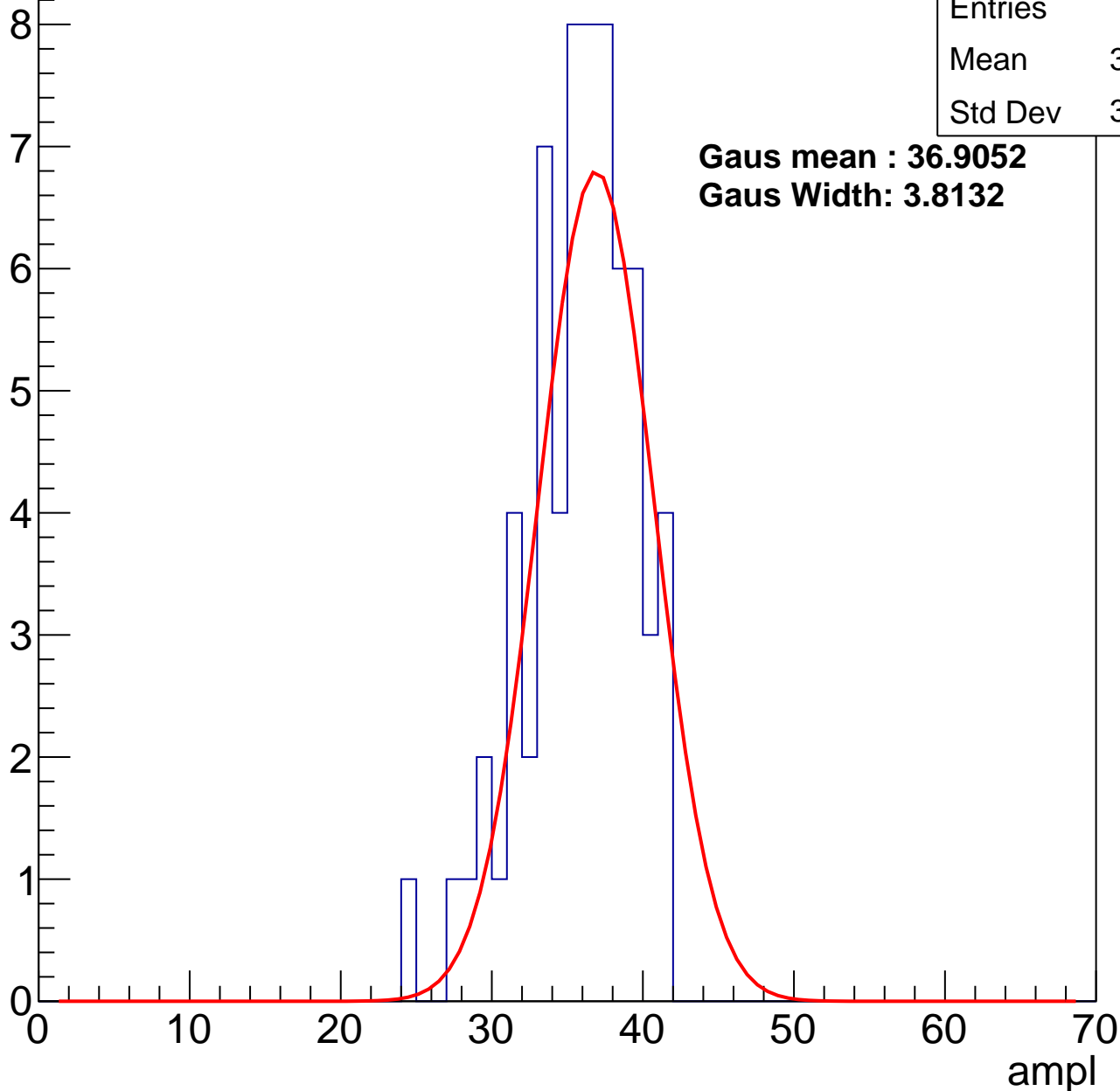
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.33
Std Dev	3.582

Gaus mean : 36.9052

Gaus Width: 3.8132



B1L101S, U1-ch59, adc2

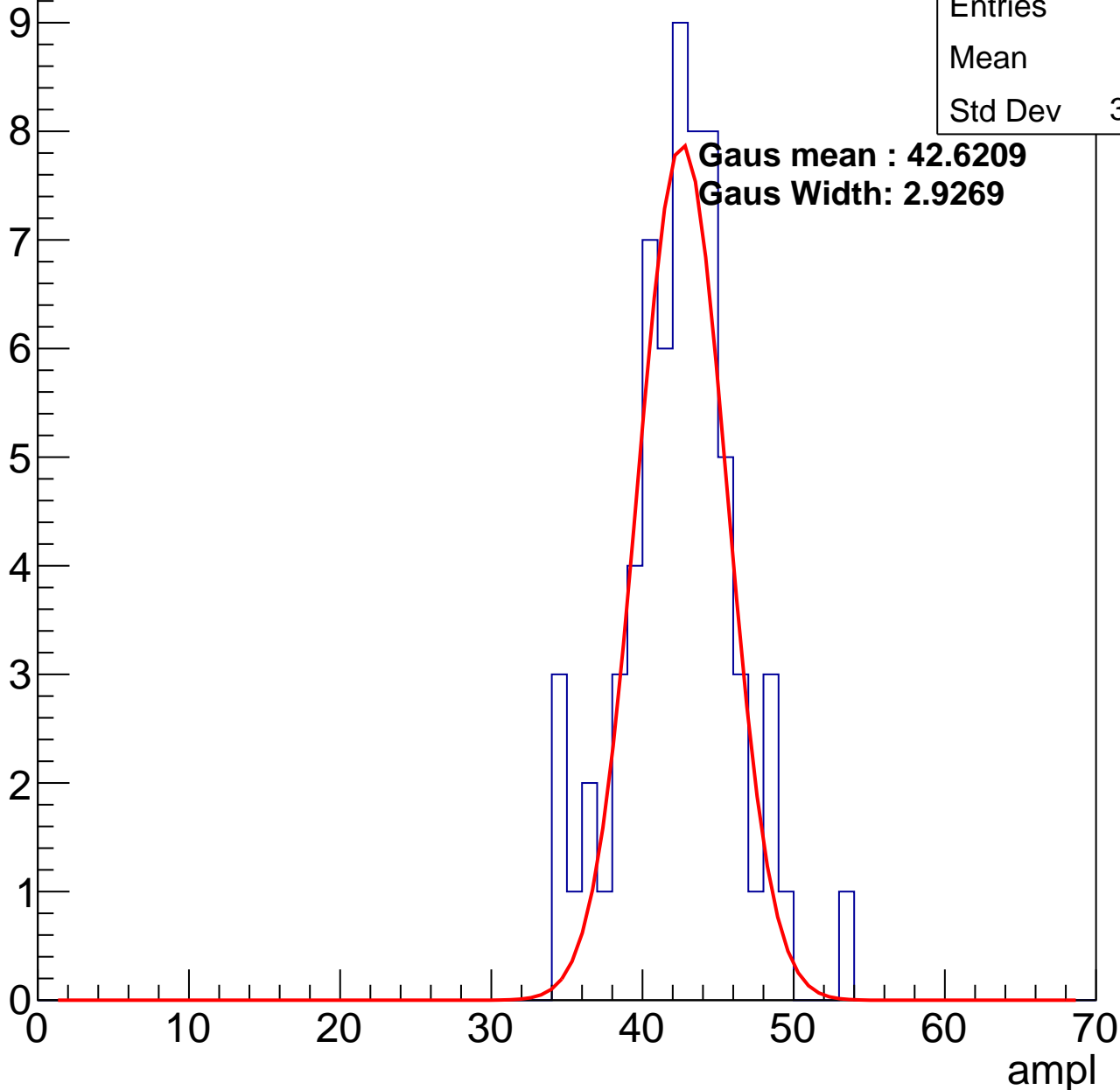
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42
Std Dev	3.676

Gaus mean : 42.6209

Gaus Width: 2.9269

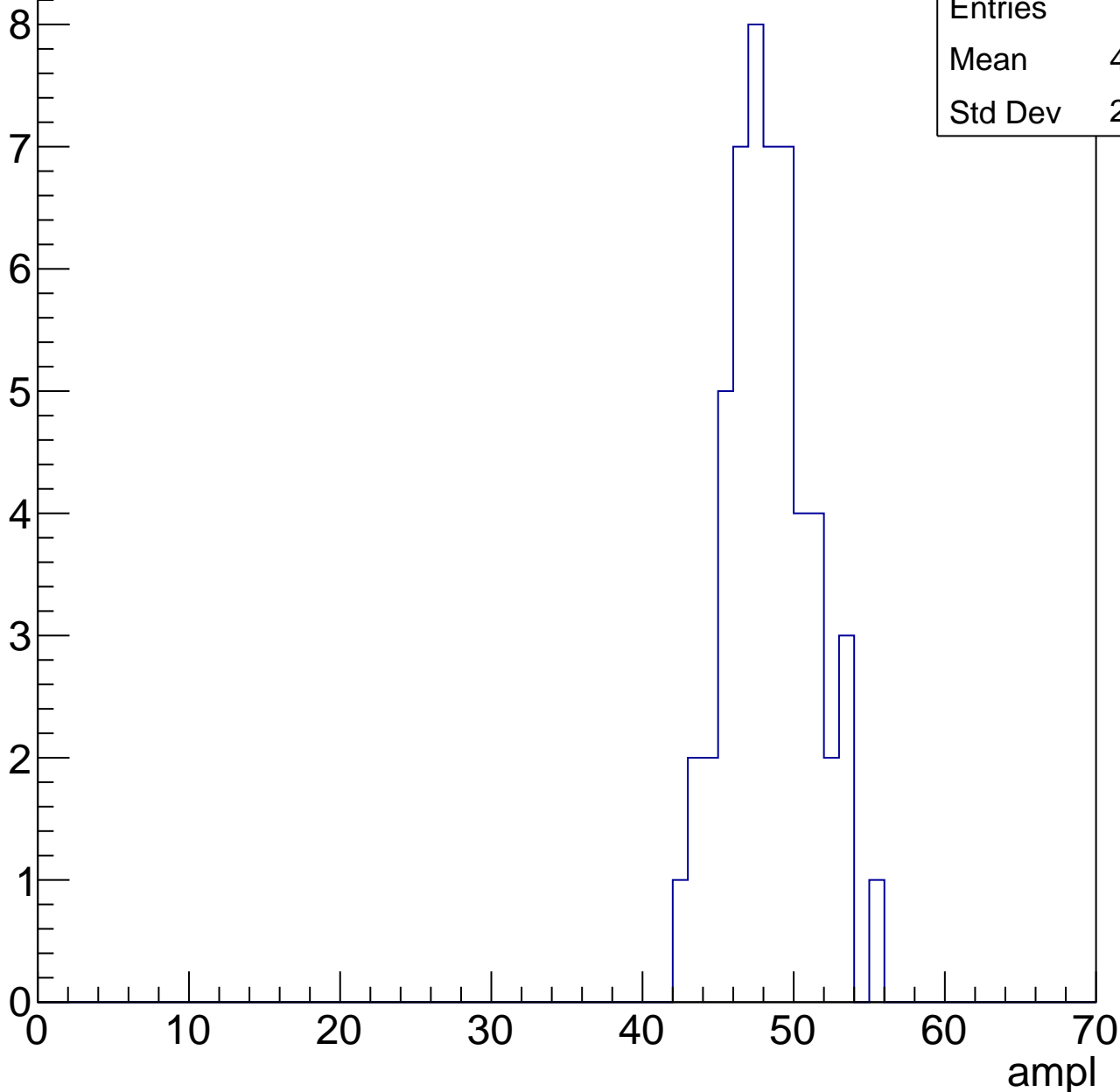


B1L101S, U1-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

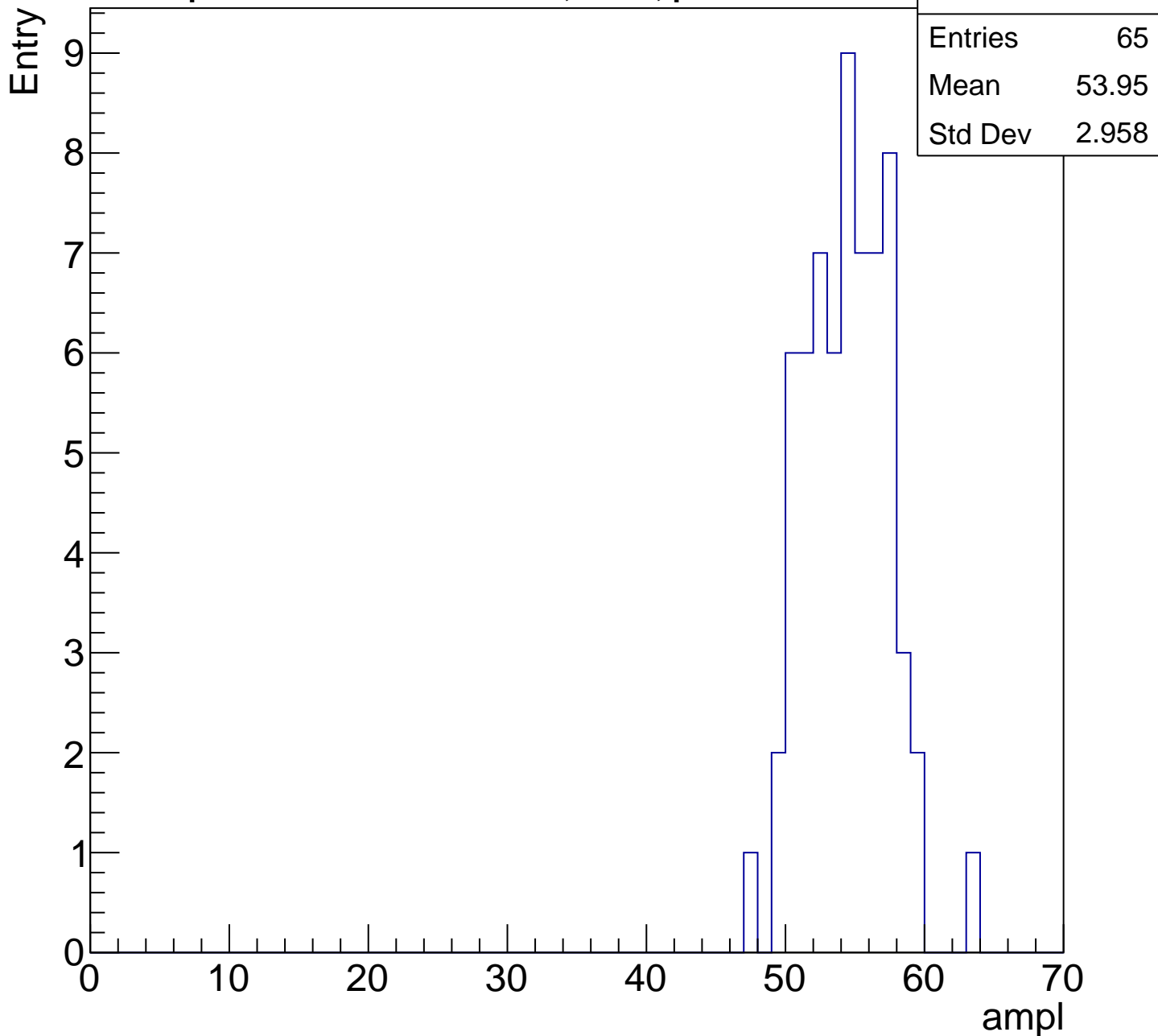
Entry

Entries	53
Mean	47.92
Std Dev	2.794



B1L101S, U1-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

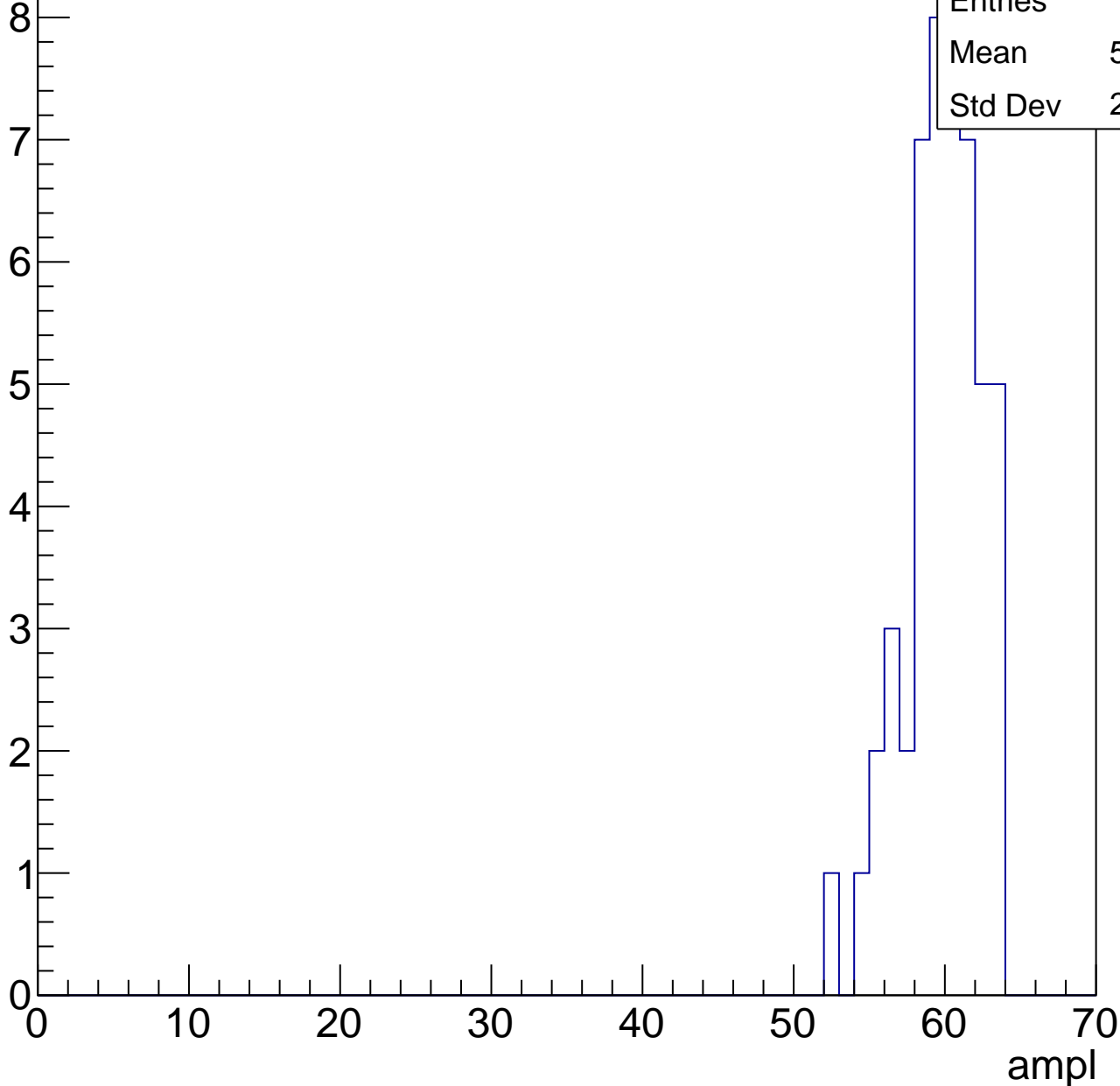


B1L101S, U1-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.35
Std Dev	2.487

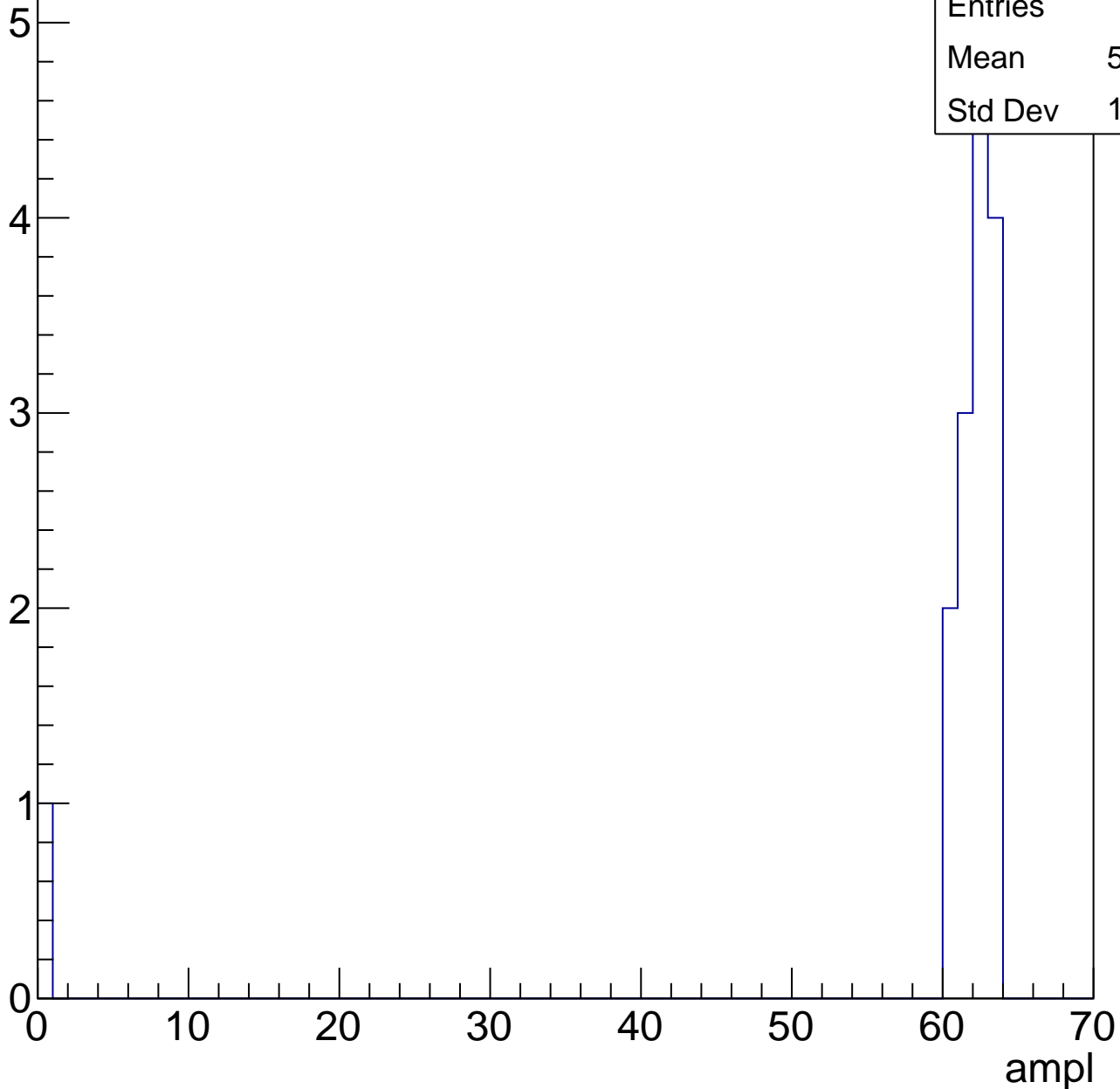


B1L101S, U1-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.67
Std Dev	15.44



B1L101S, U1-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U1-ch60, adc0

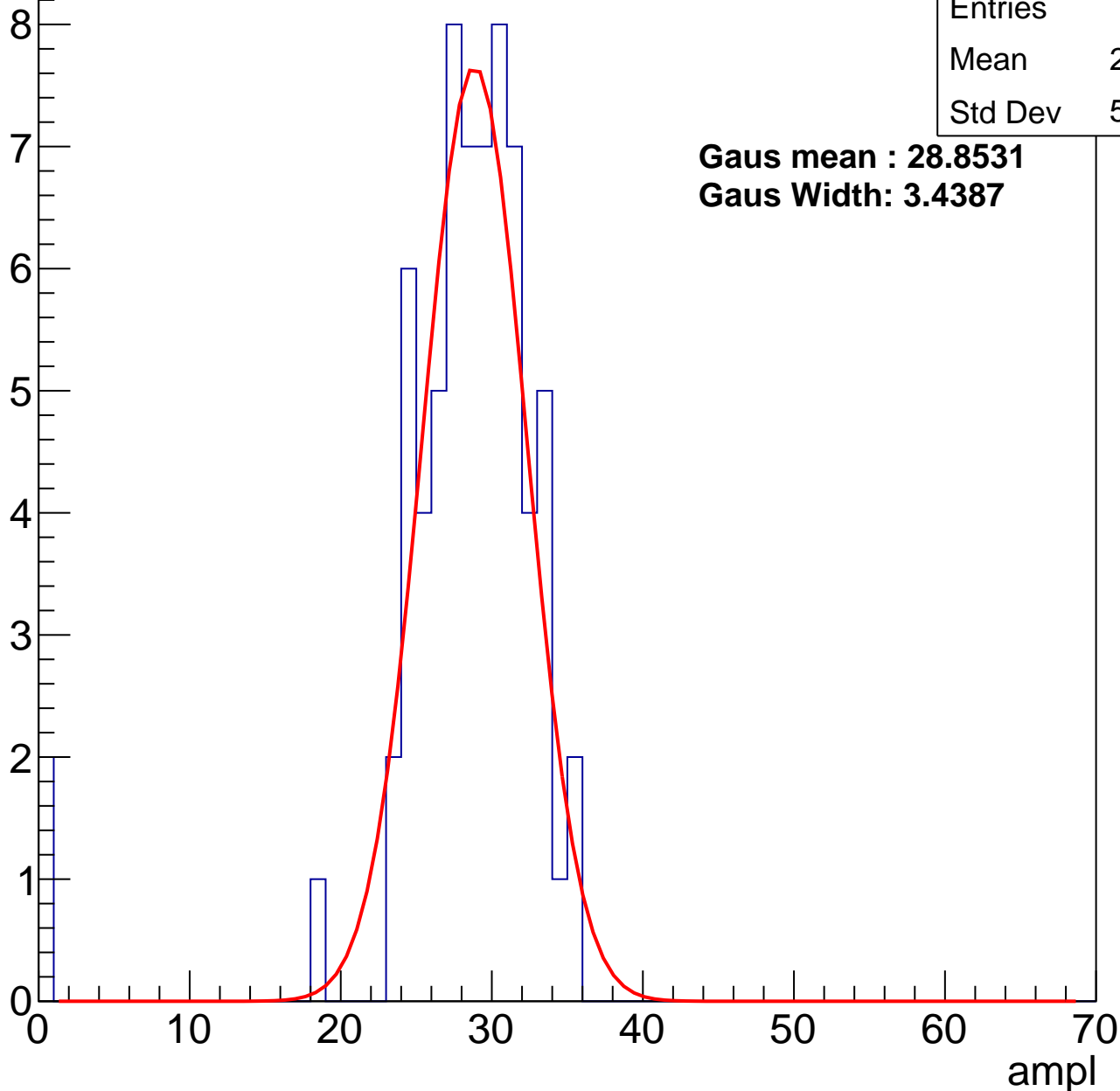
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	27.64
Std Dev	5.766

Gaus mean : 28.8531

Gaus Width: 3.4387



B1L101S, U1-ch60, adc1

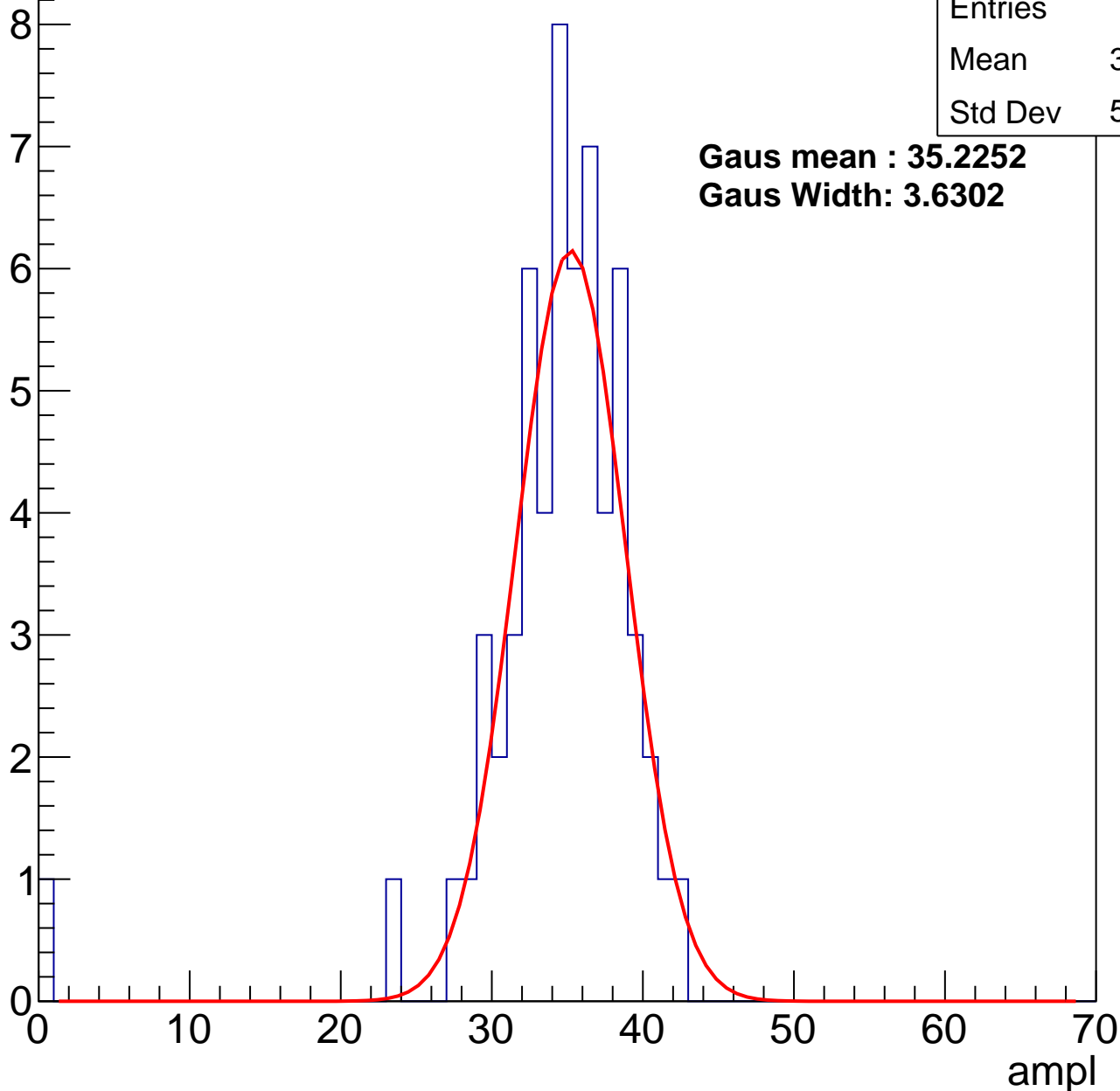
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	33.87
Std Dev	5.699

Gaus mean : 35.2252

Gaus Width: 3.6302



B1L101S, U1-ch60, adc2

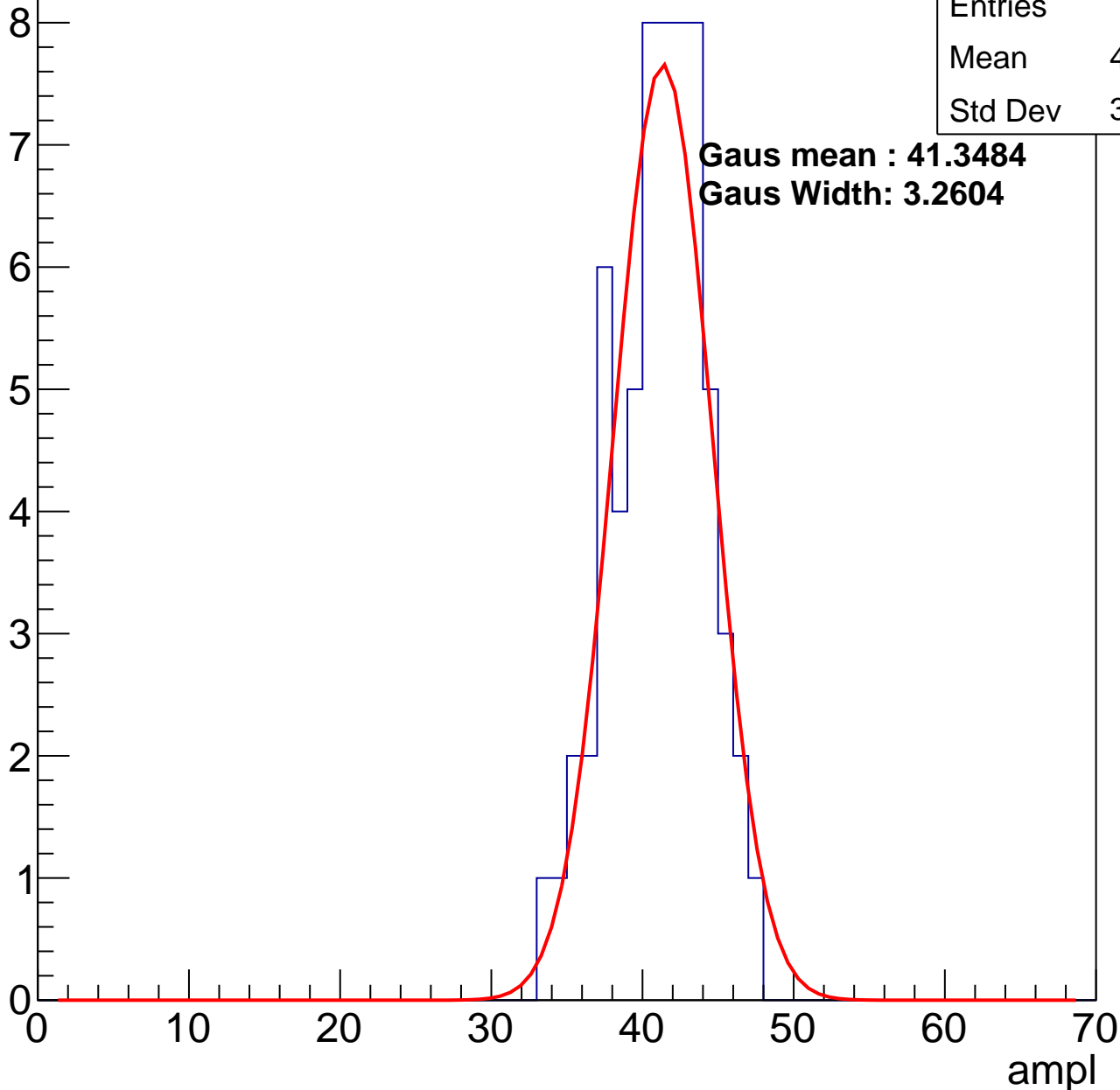
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	40.62
Std Dev	3.075

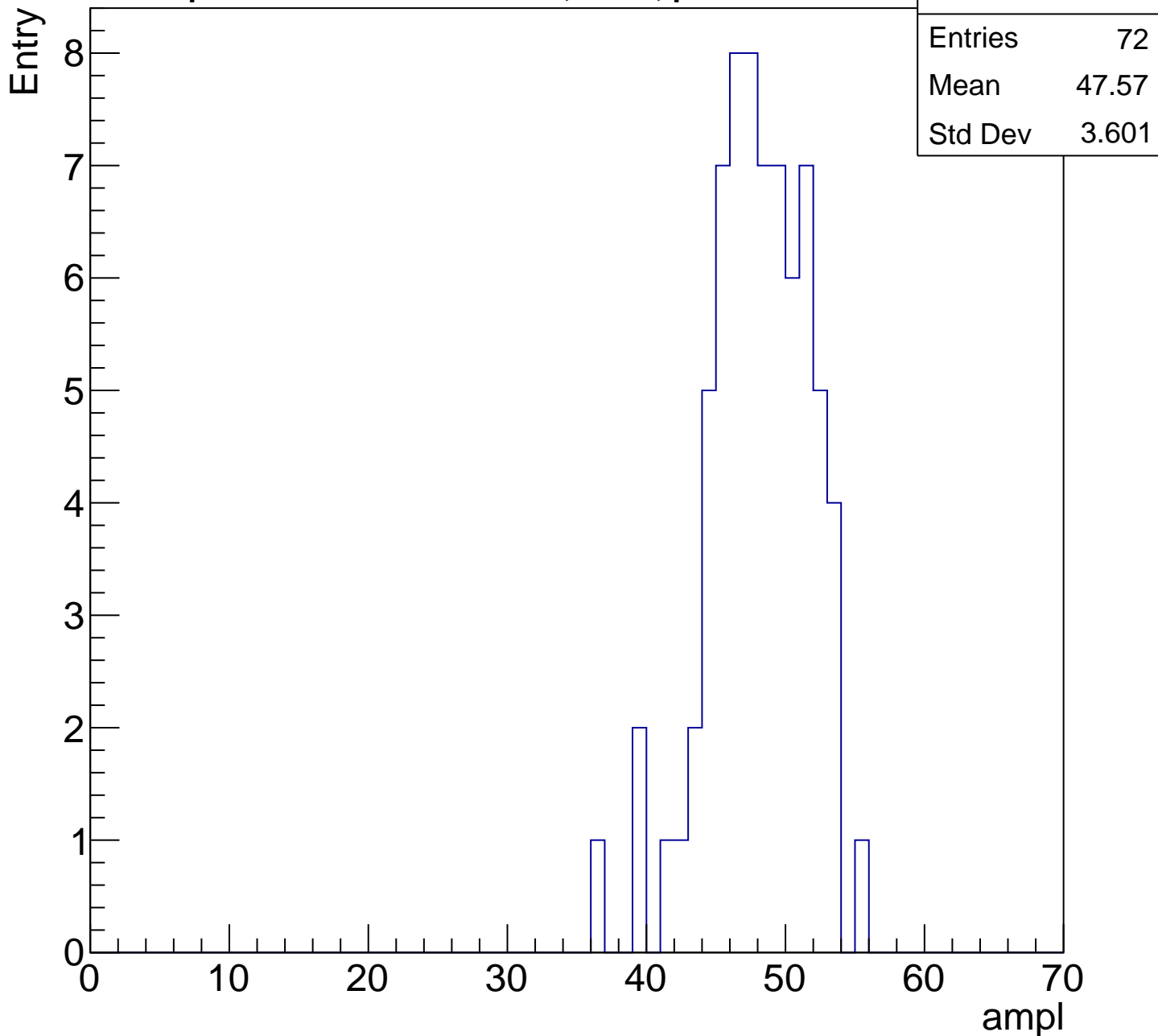
Gaus mean : 41.3484

Gaus Width: 3.2604



B1L101S, U1-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

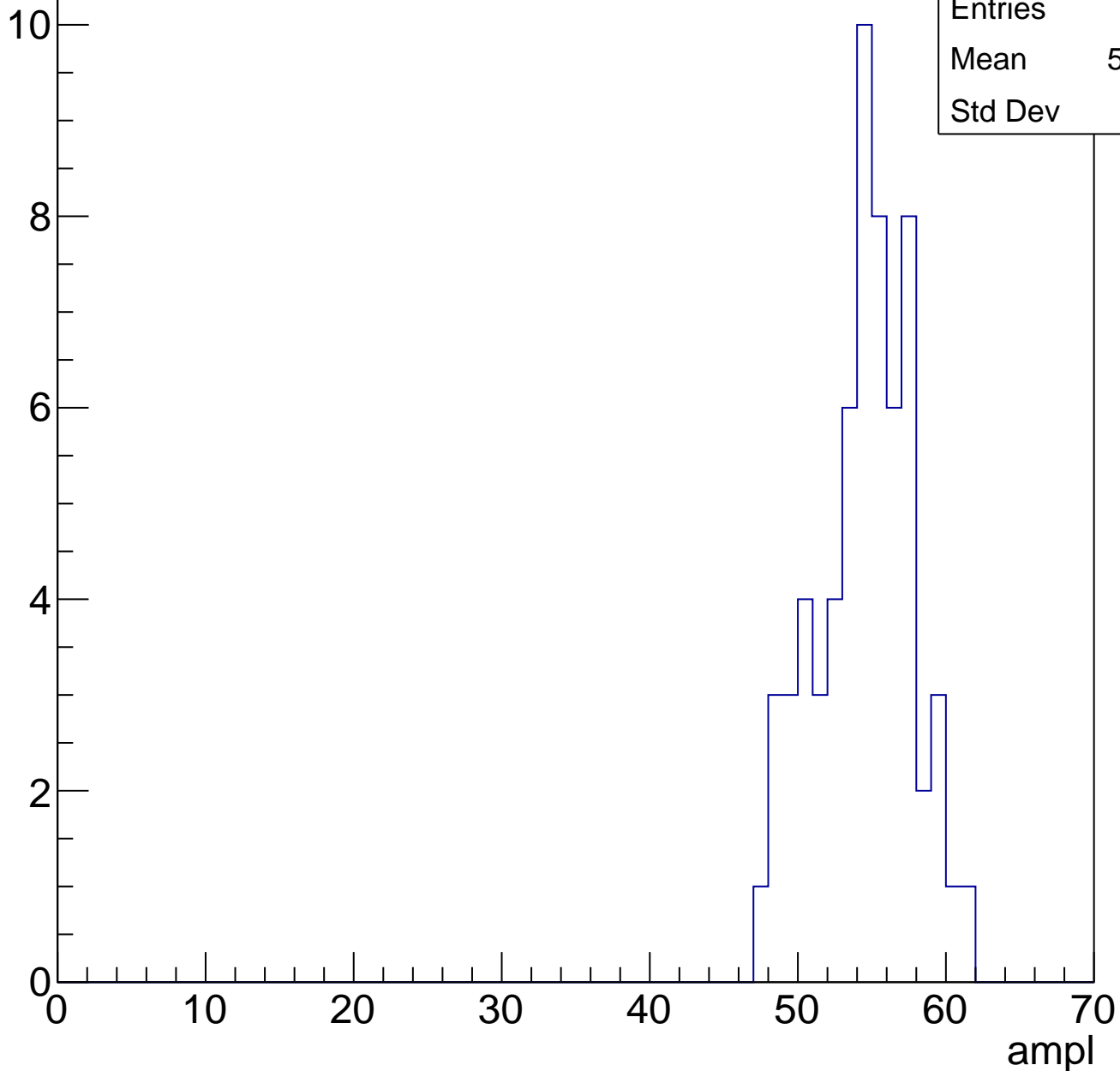


B1L101S, U1-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	54.02
Std Dev	3.19

Entry

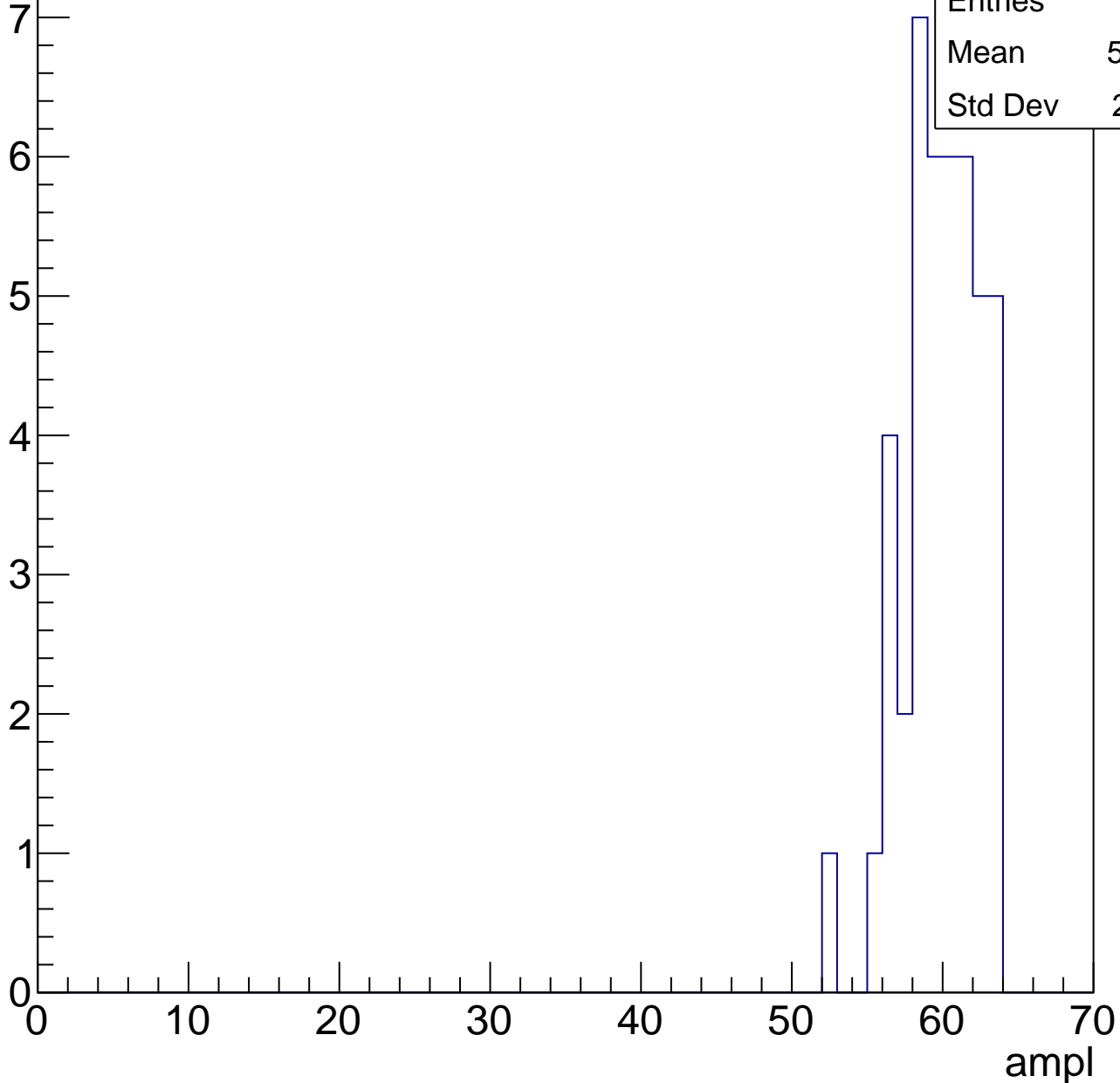


B1L101S, U1-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	59.44
Std Dev	2.471

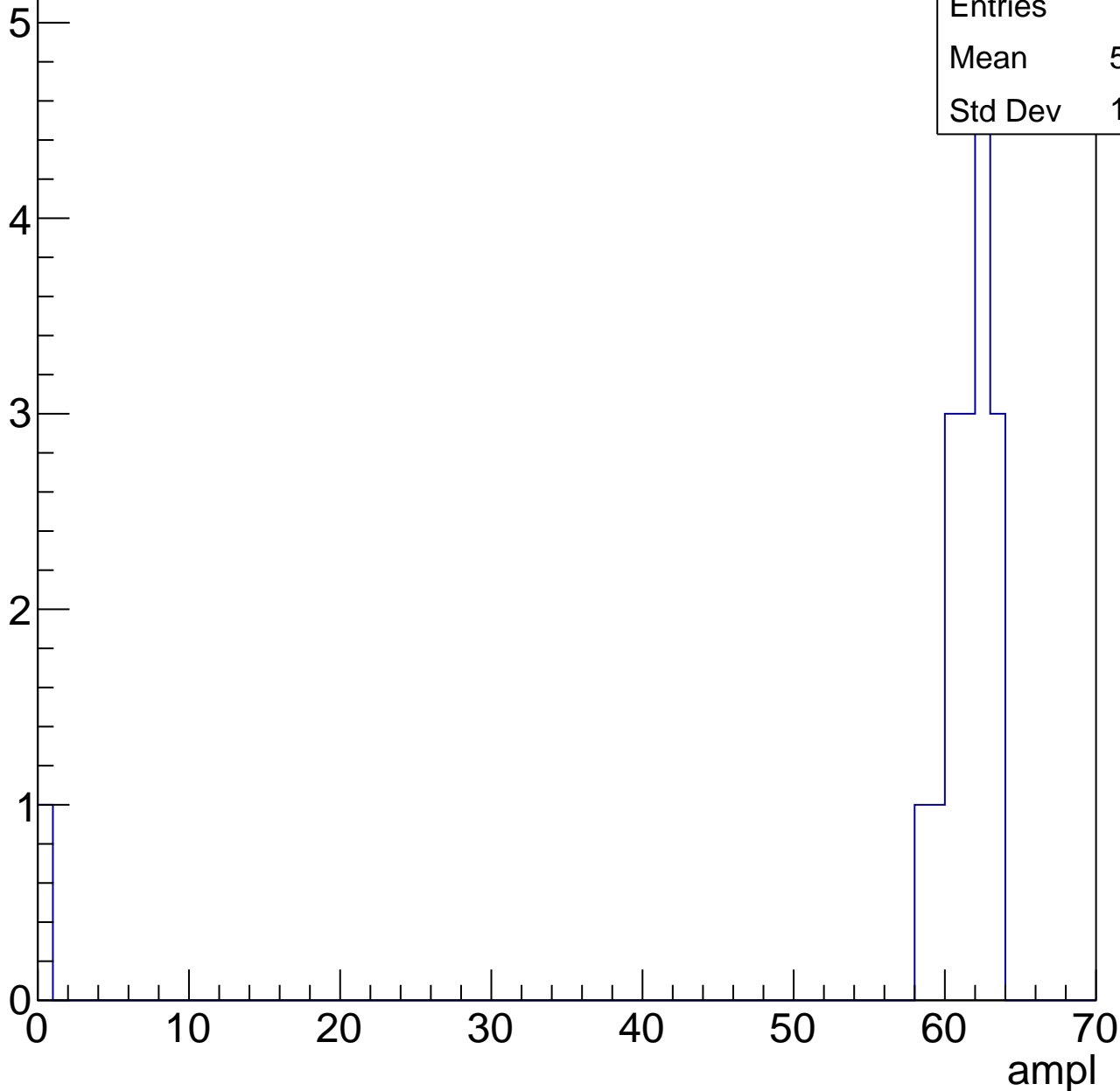


B1L101S, U1-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.59
Std Dev	14.46



B1L101S, U1-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch61, adc0

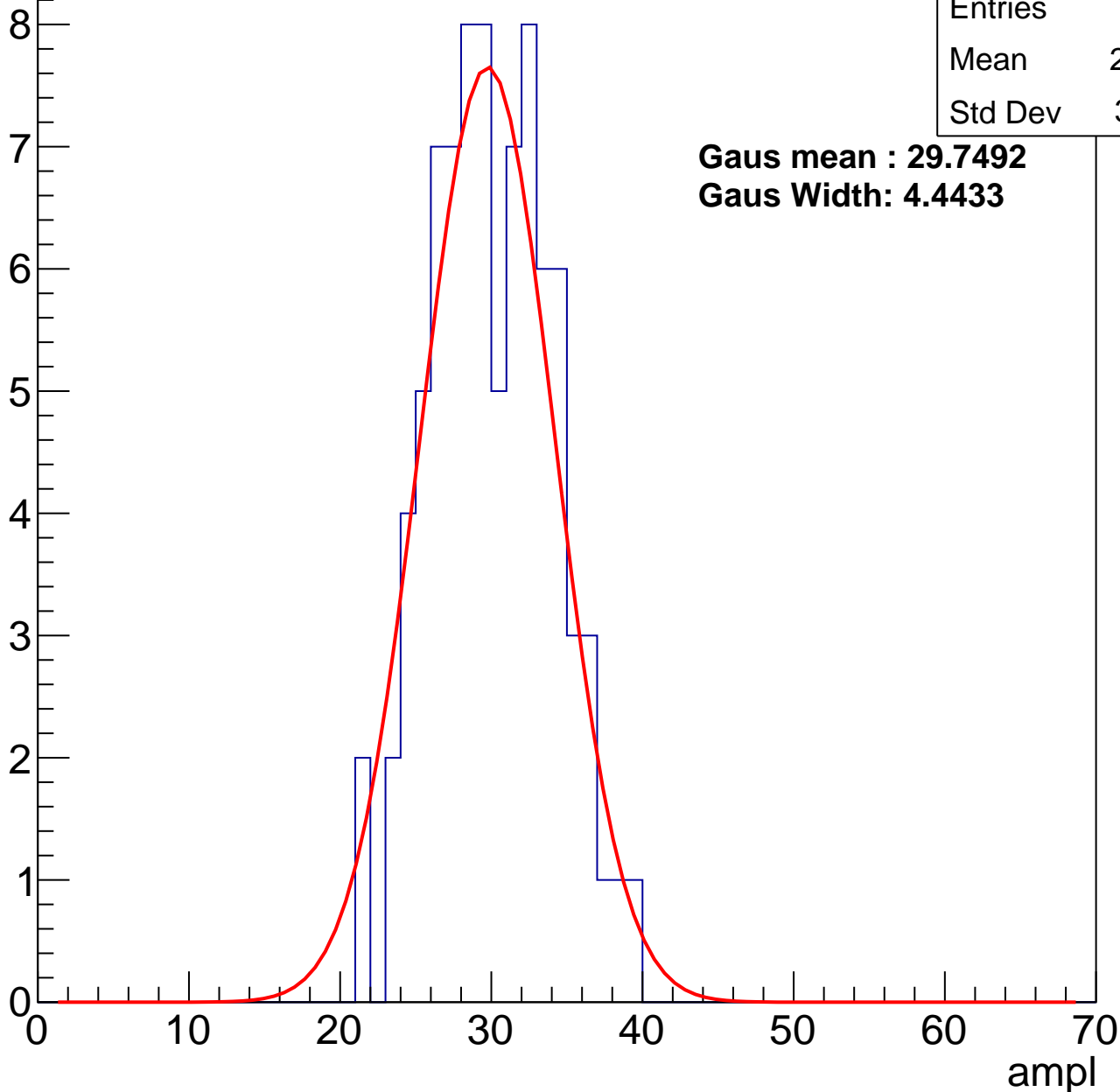
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.62
Std Dev	3.931

Gaus mean : 29.7492

Gaus Width: 4.4433



B1L101S, U1-ch61, adc1

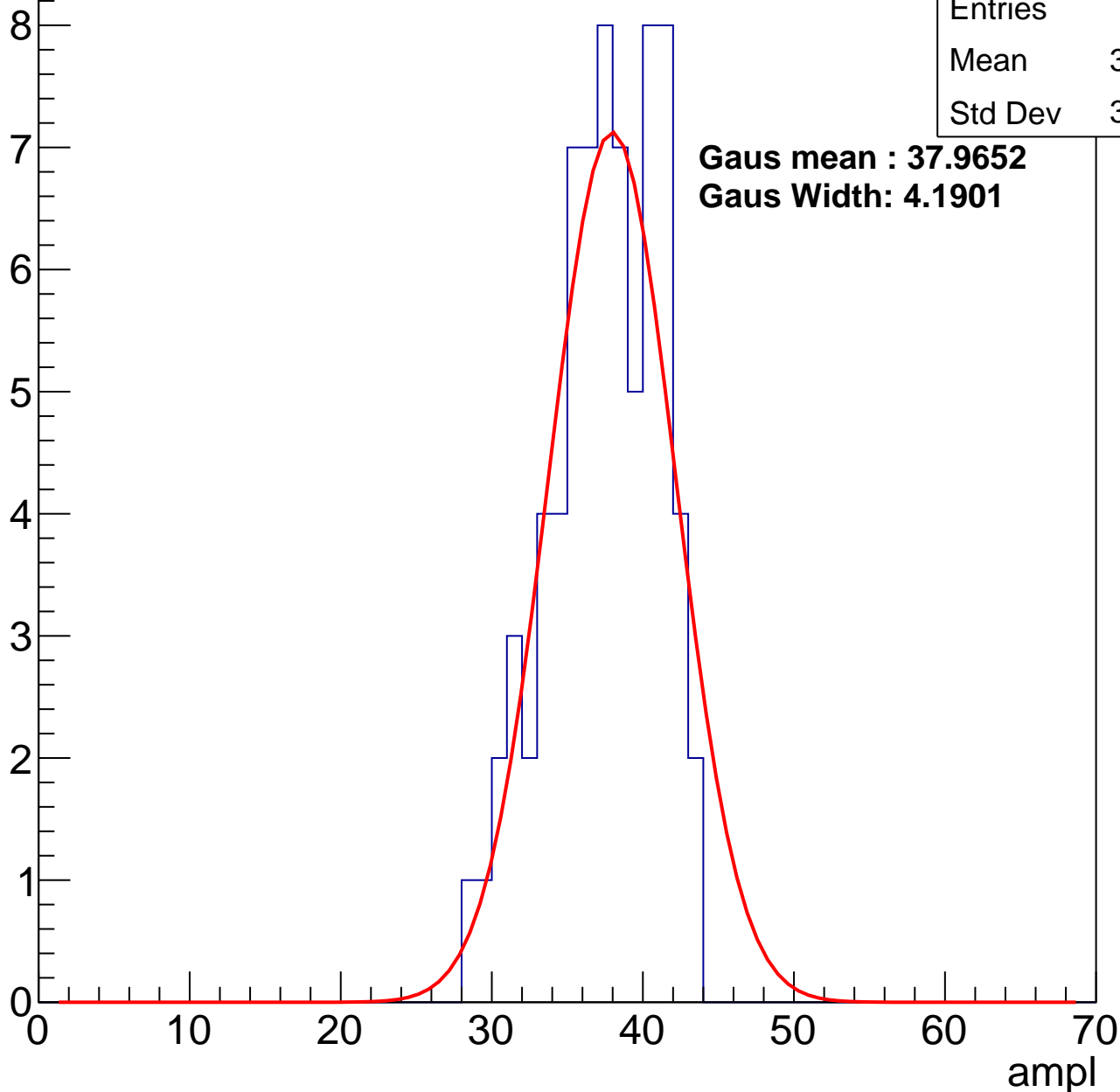
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.96
Std Dev	3.579

Gaus mean : 37.9652

Gaus Width: 4.1901



B1L101S, U1-ch61, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

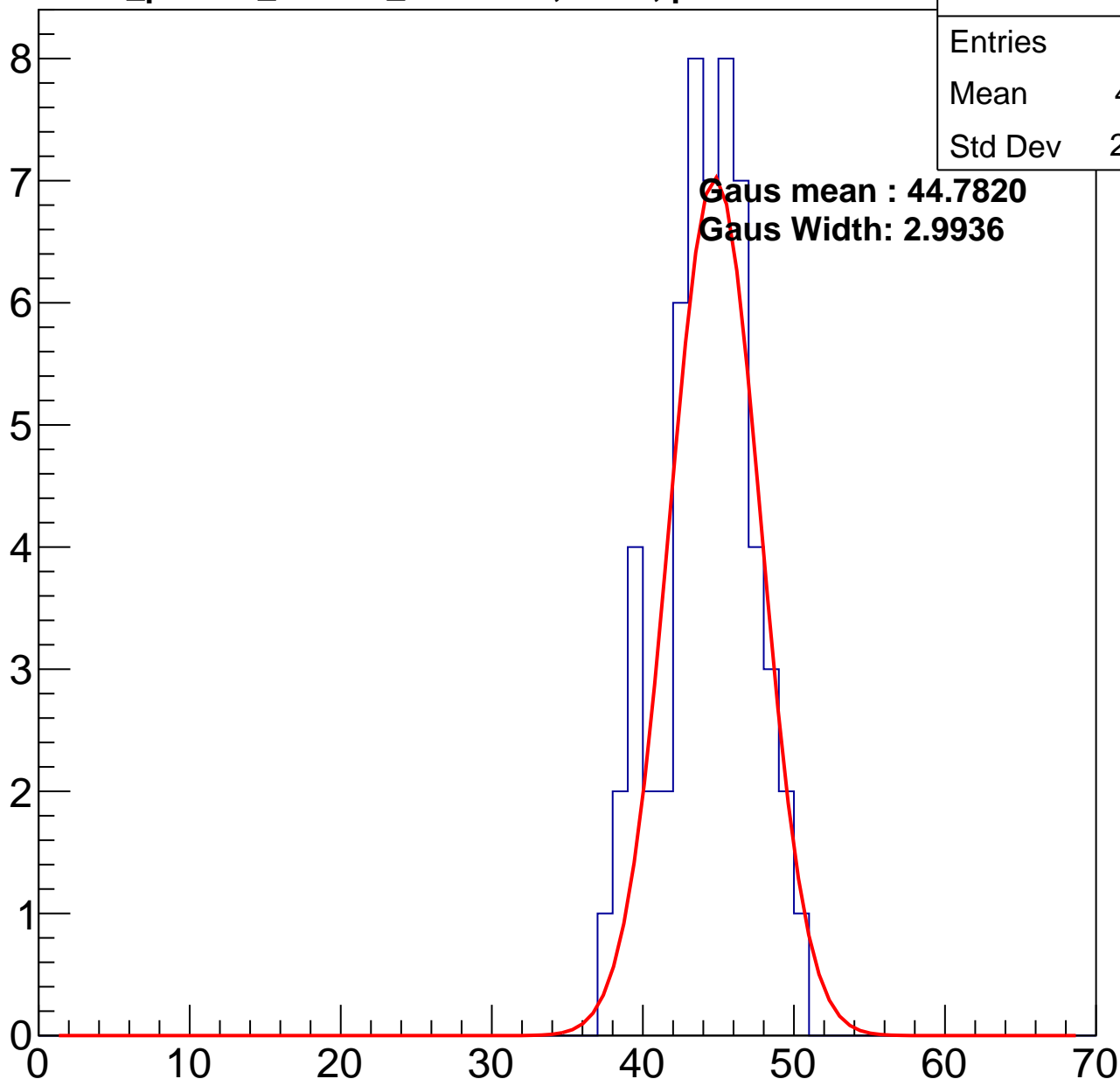
8
7
6
5
4
3
2
1
0

Entries	57
Mean	43.81
Std Dev	2.982

Gaus mean : 44.7820

Gaus Width: 2.9936

ampl

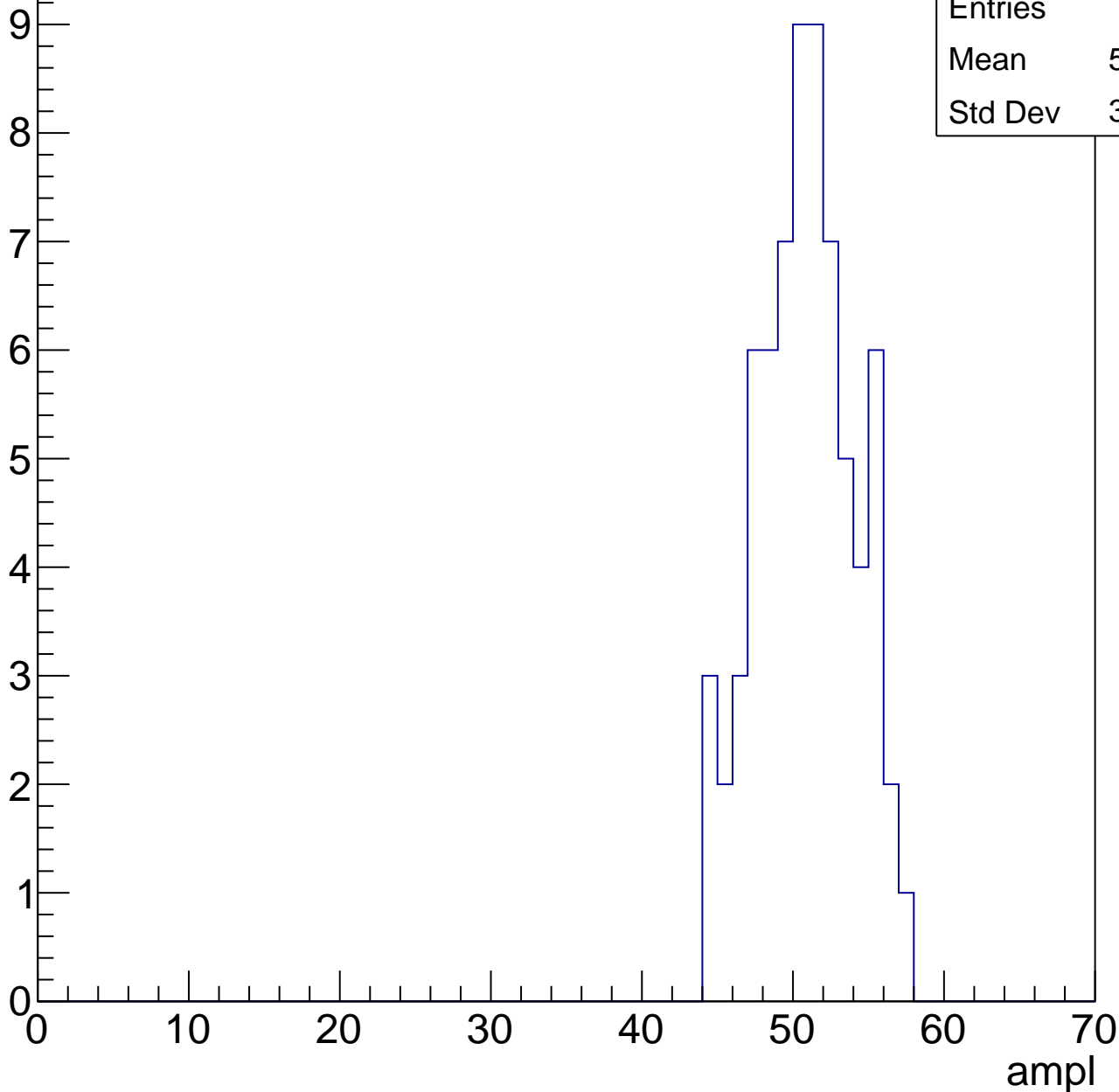


B1L101S, U1-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

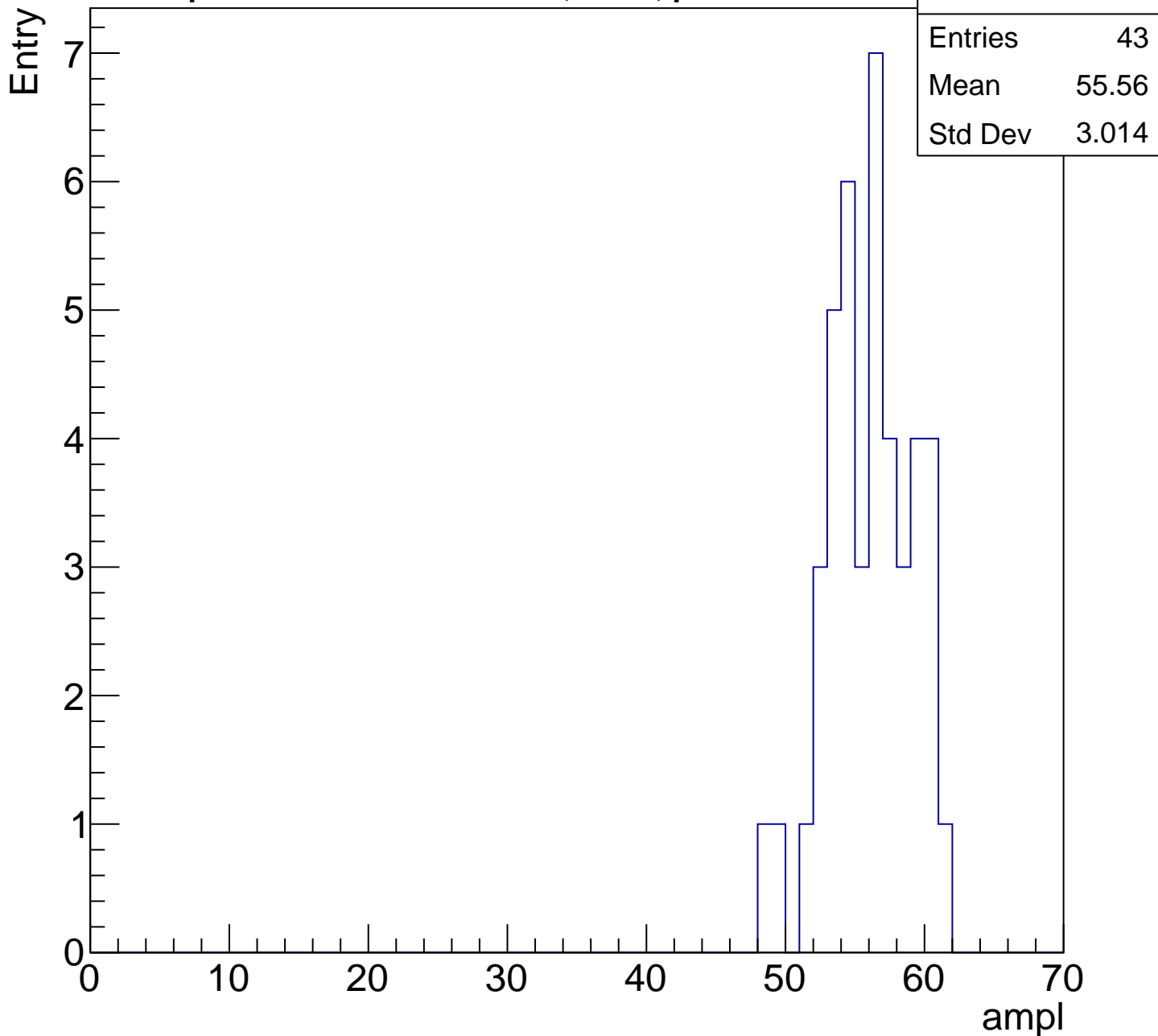
Entry

Entries	70
Mean	50.37
Std Dev	3.159



B1L101S, U1-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

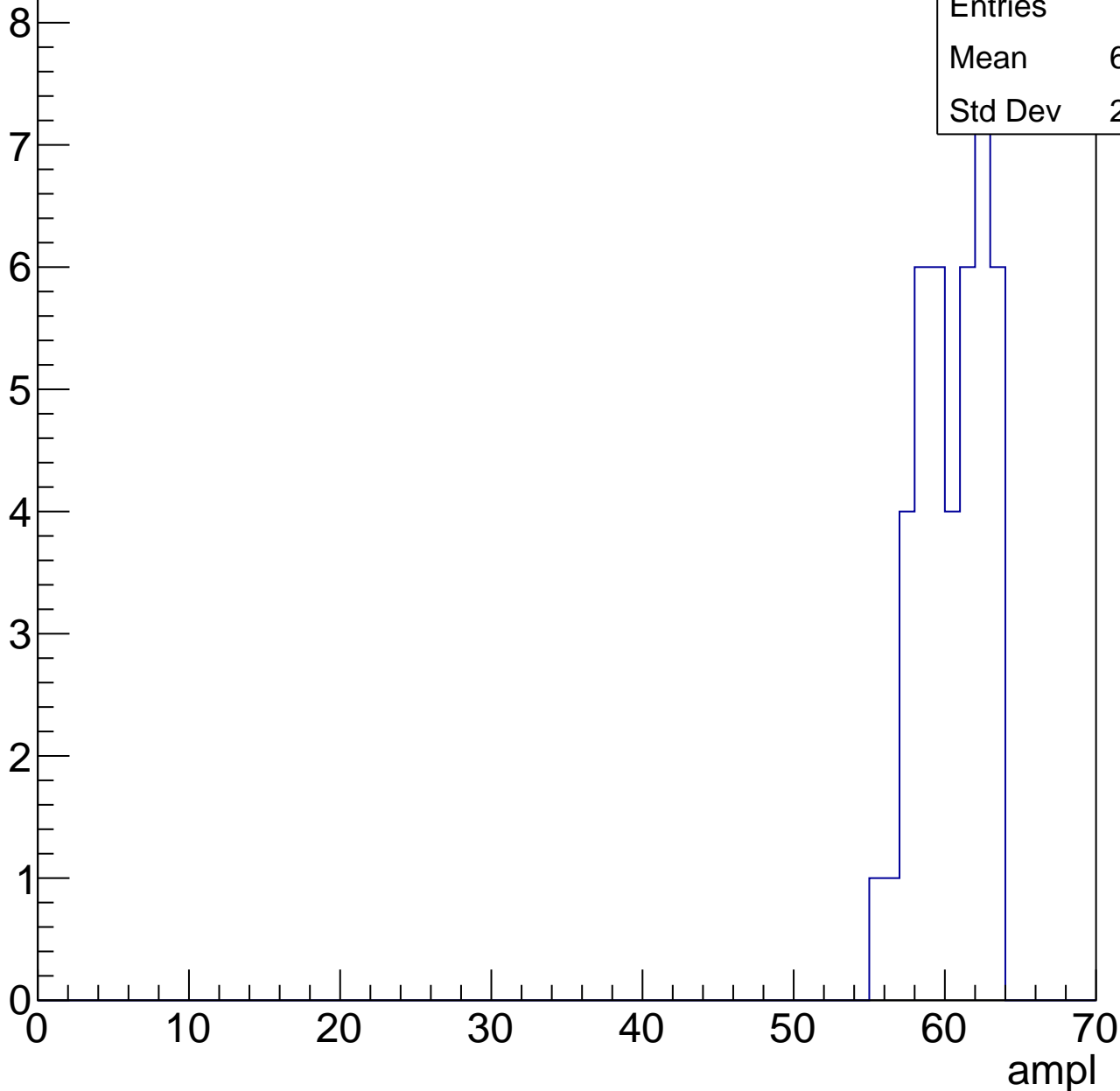


B1L101S, U1-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

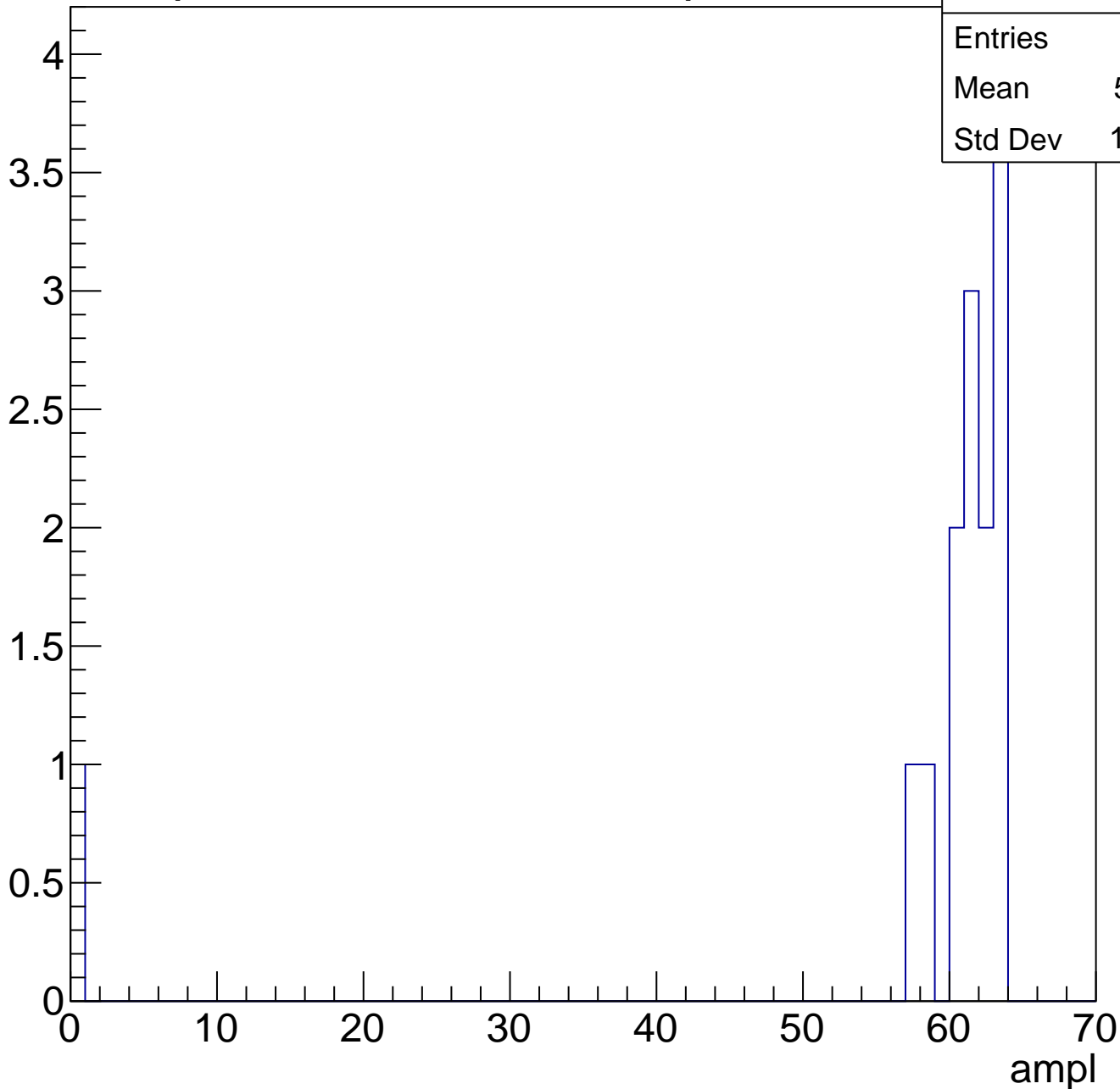
Entries	42
Mean	60.02
Std Dev	2.177



B1L101S, U1-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch61, adc7

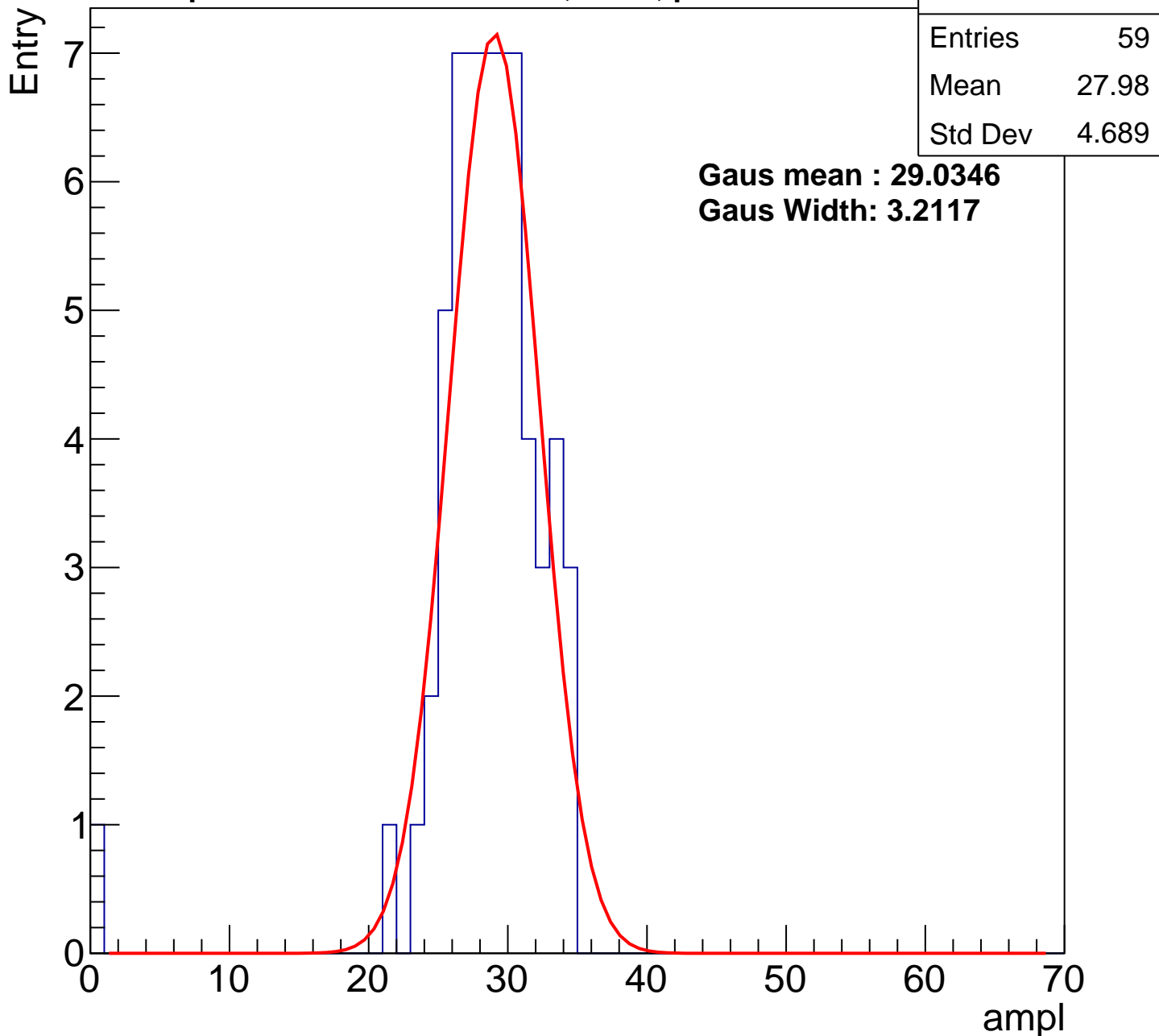
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch62, adc0

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch62, adc1

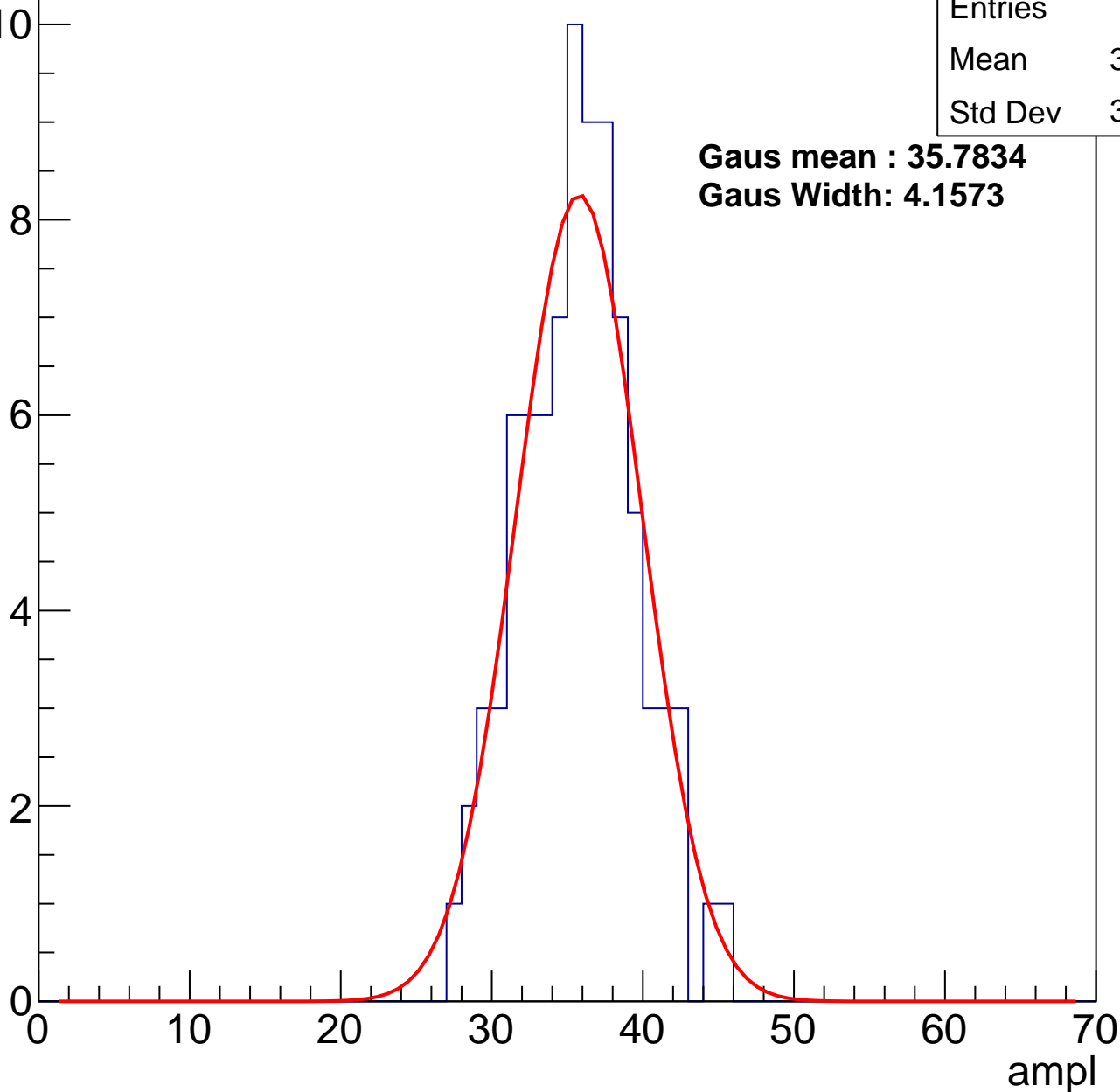
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	35.29
Std Dev	3.785

Gaus mean : 35.7834

Gaus Width: 4.1573

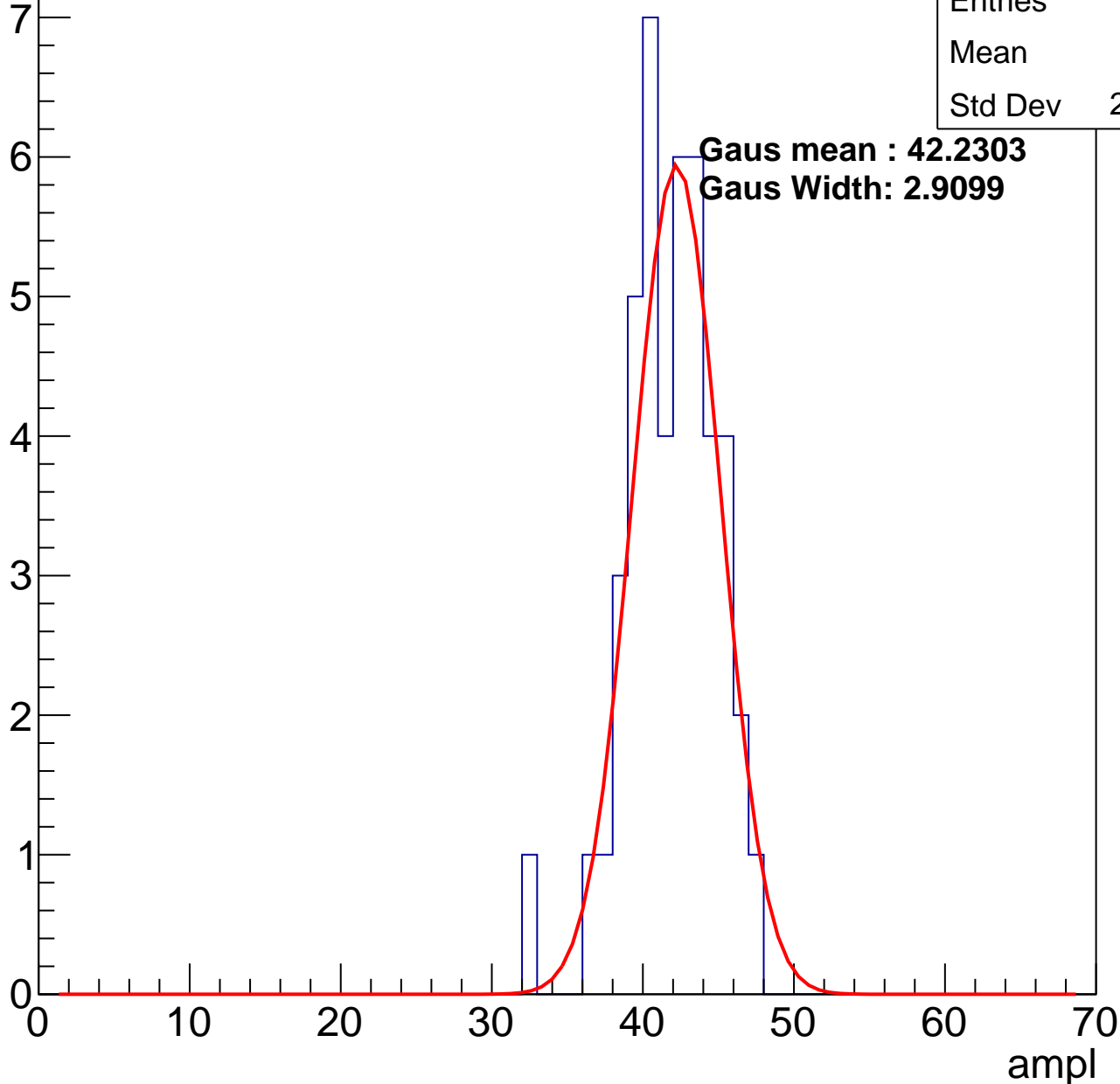


B1L101S, U1-ch62, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

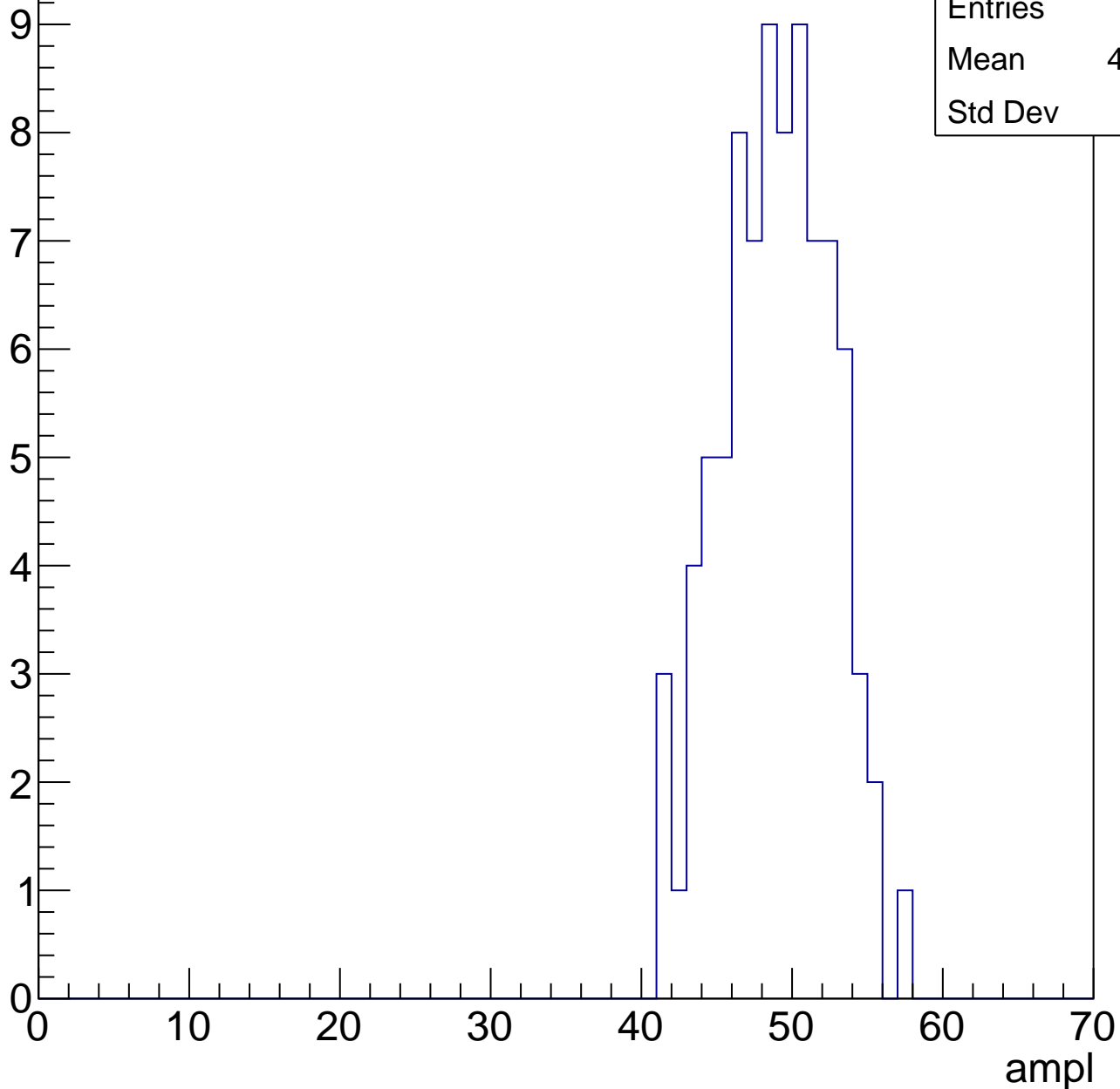
Entries	45
Mean	41.4
Std Dev	2.924



B1L101S, U1-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

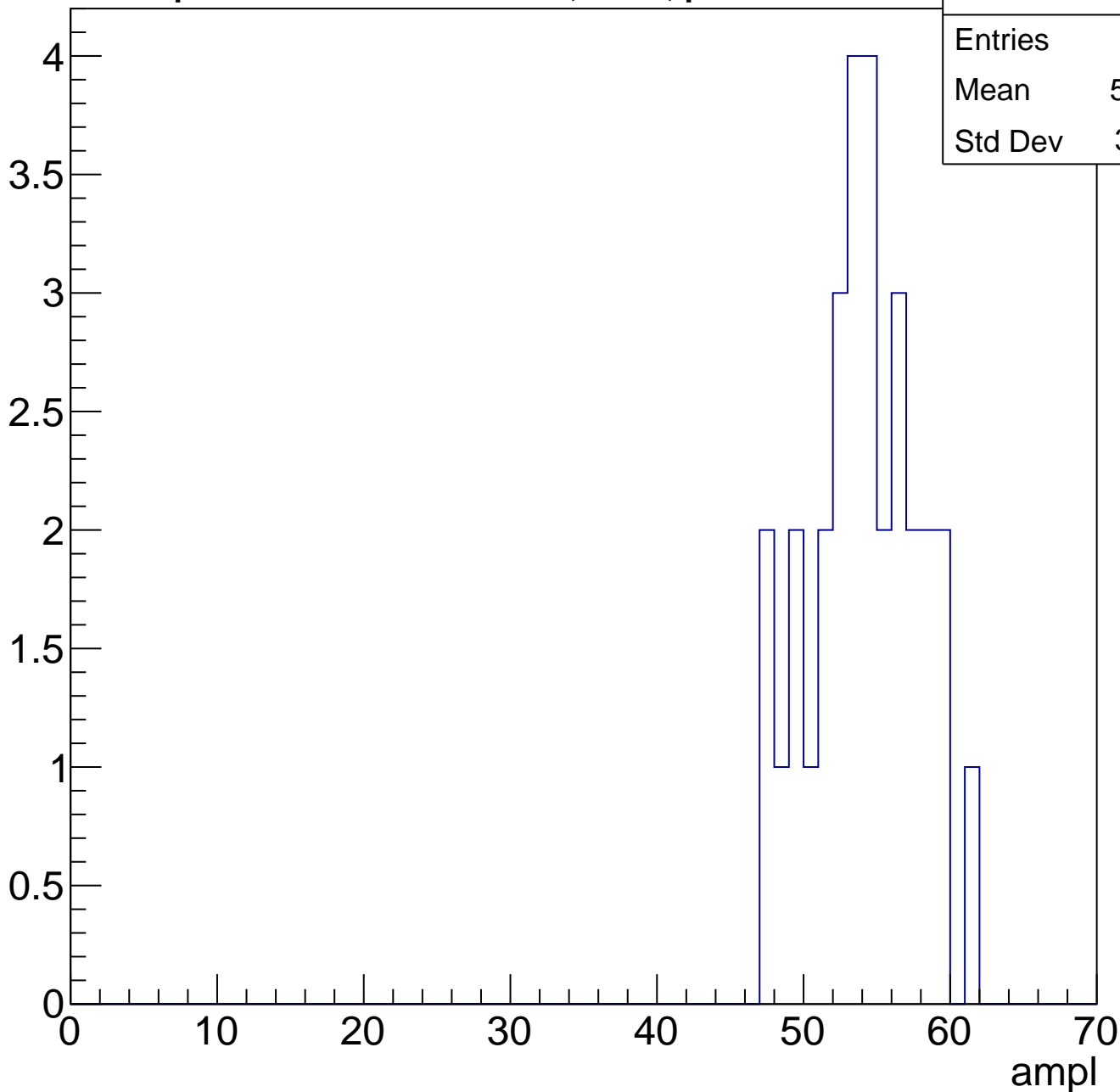


Entries	85
Mean	48.48
Std Dev	3.57

B1L101S, U1-ch62, adc4

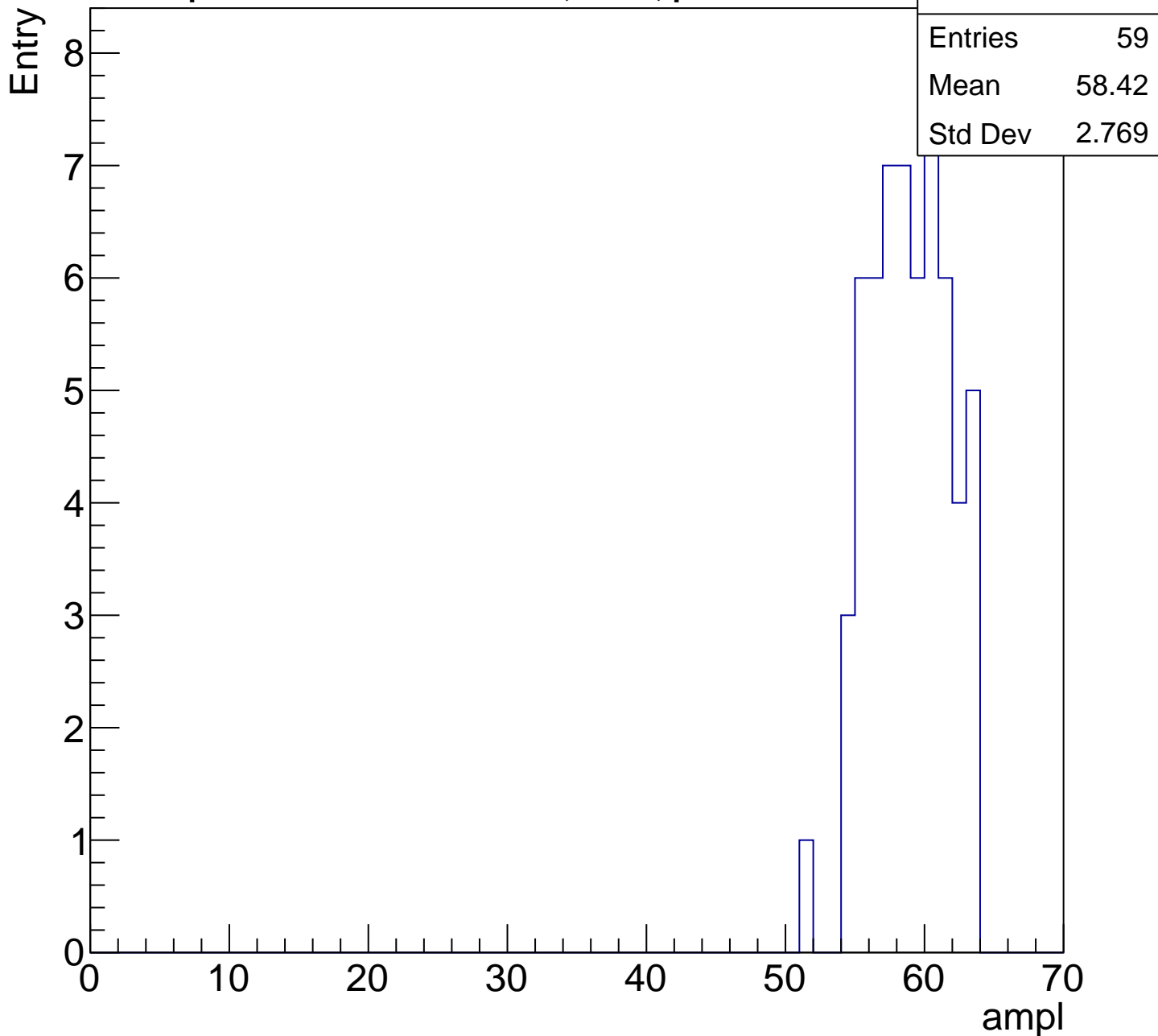
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

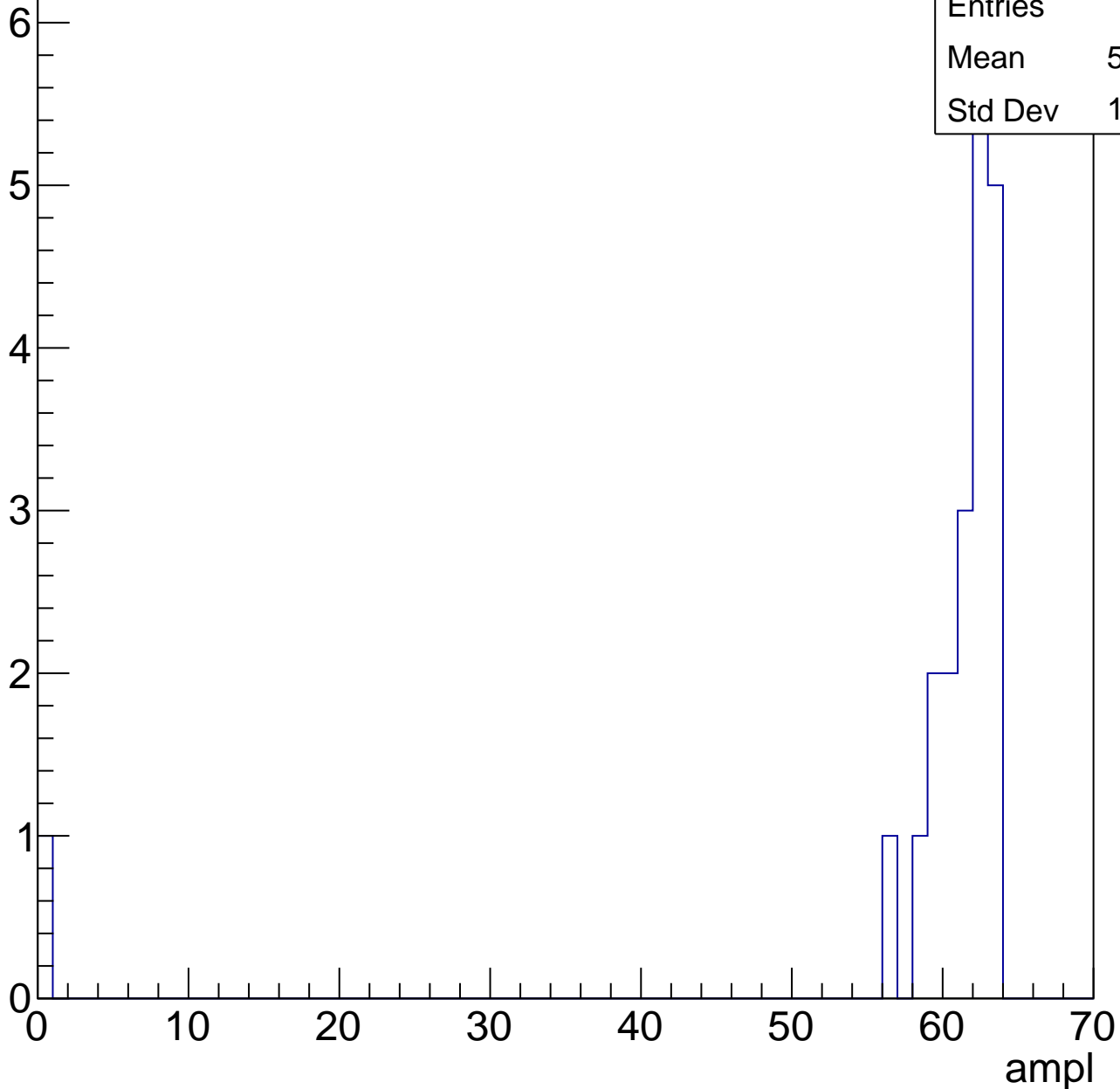


B1L101S, U1-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.19
Std Dev	13.14



B1L101S, U1-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch63, adc0

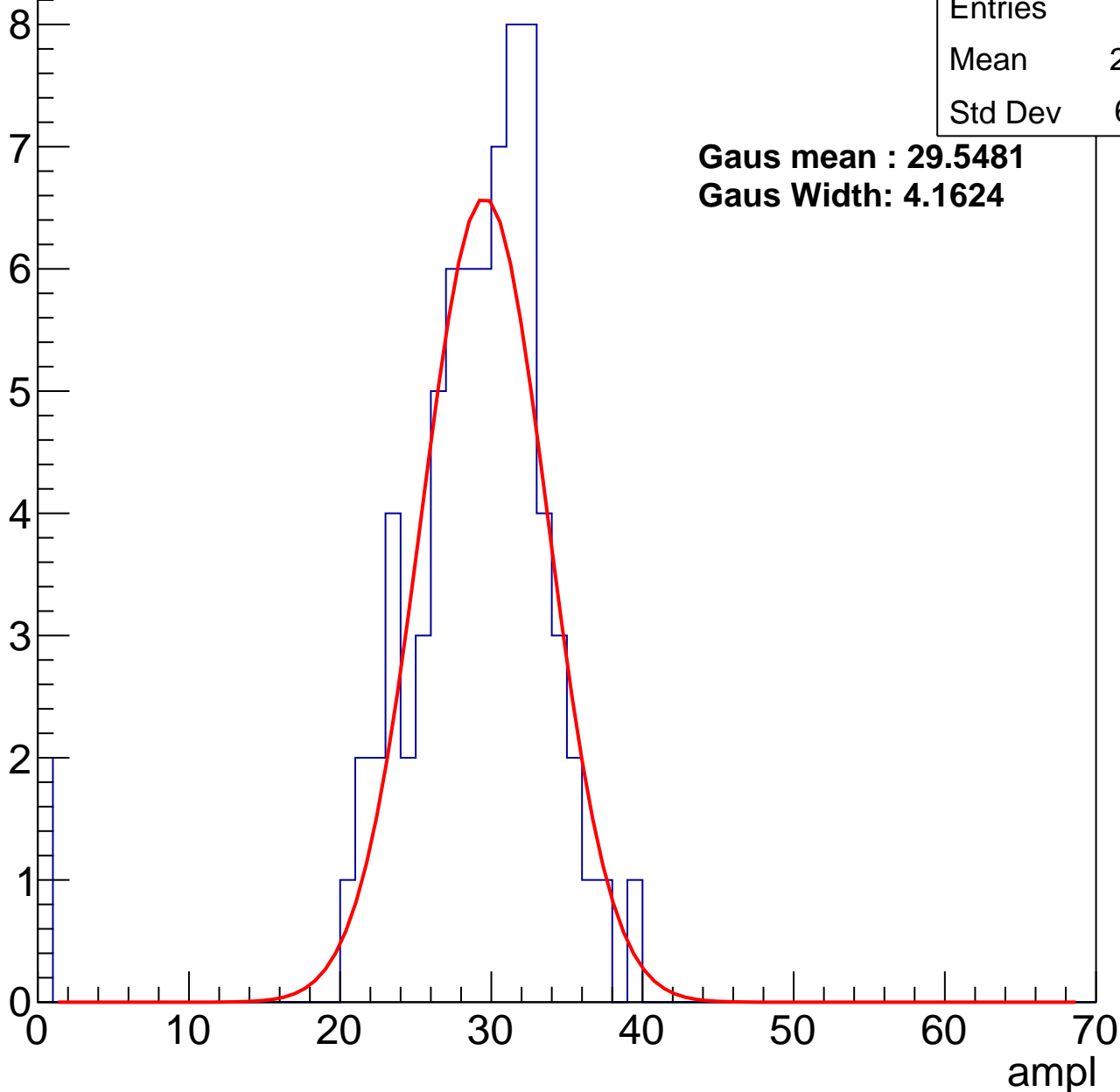
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.18
Std Dev	6.141

Gaus mean : 29.5481

Gaus Width: 4.1624



B1L101S, U1-ch63, adc1

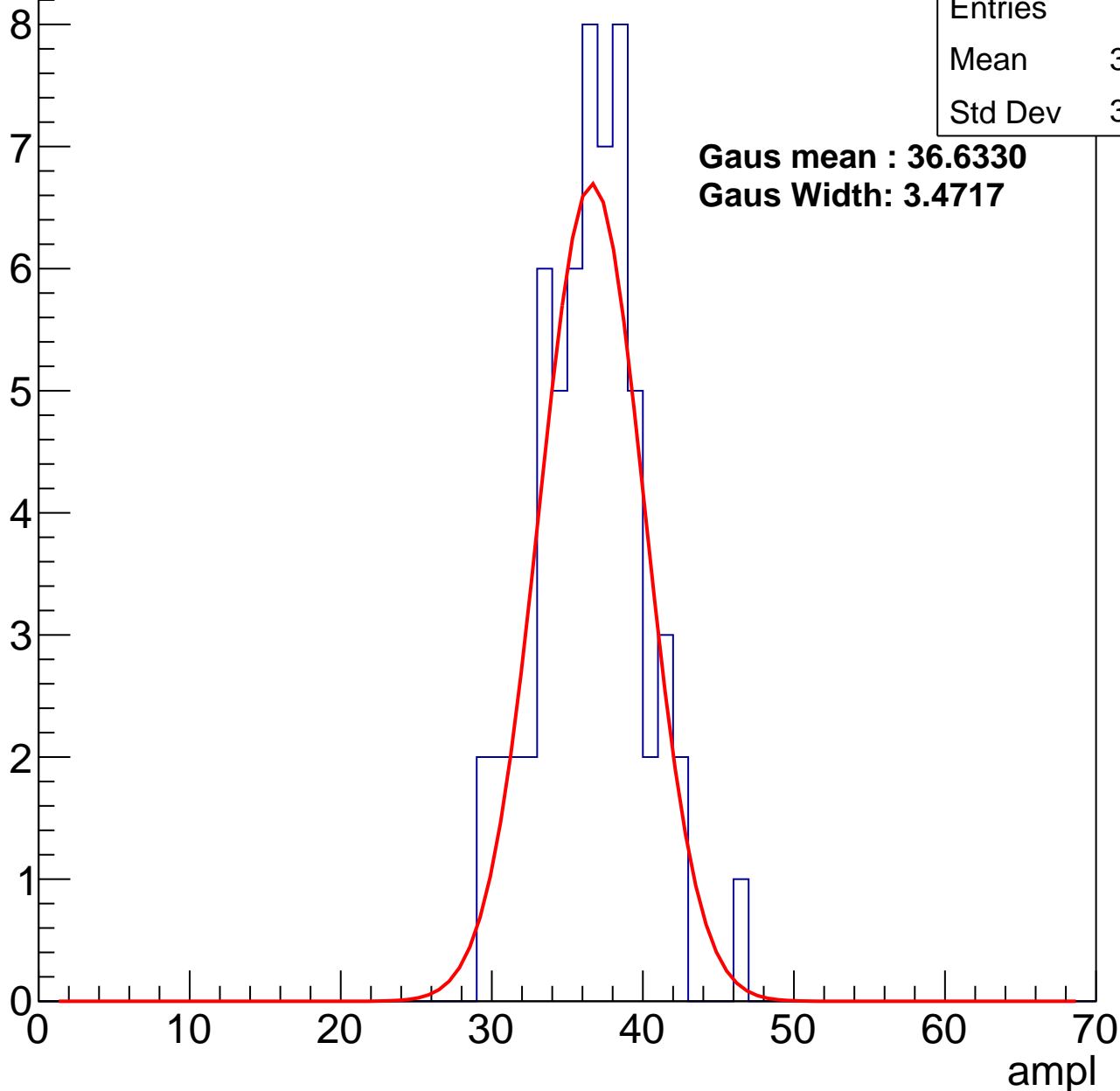
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.08
Std Dev	3.374

Gaus mean : 36.6330

Gaus Width: 3.4717



B1L101S, U1-ch63, adc2

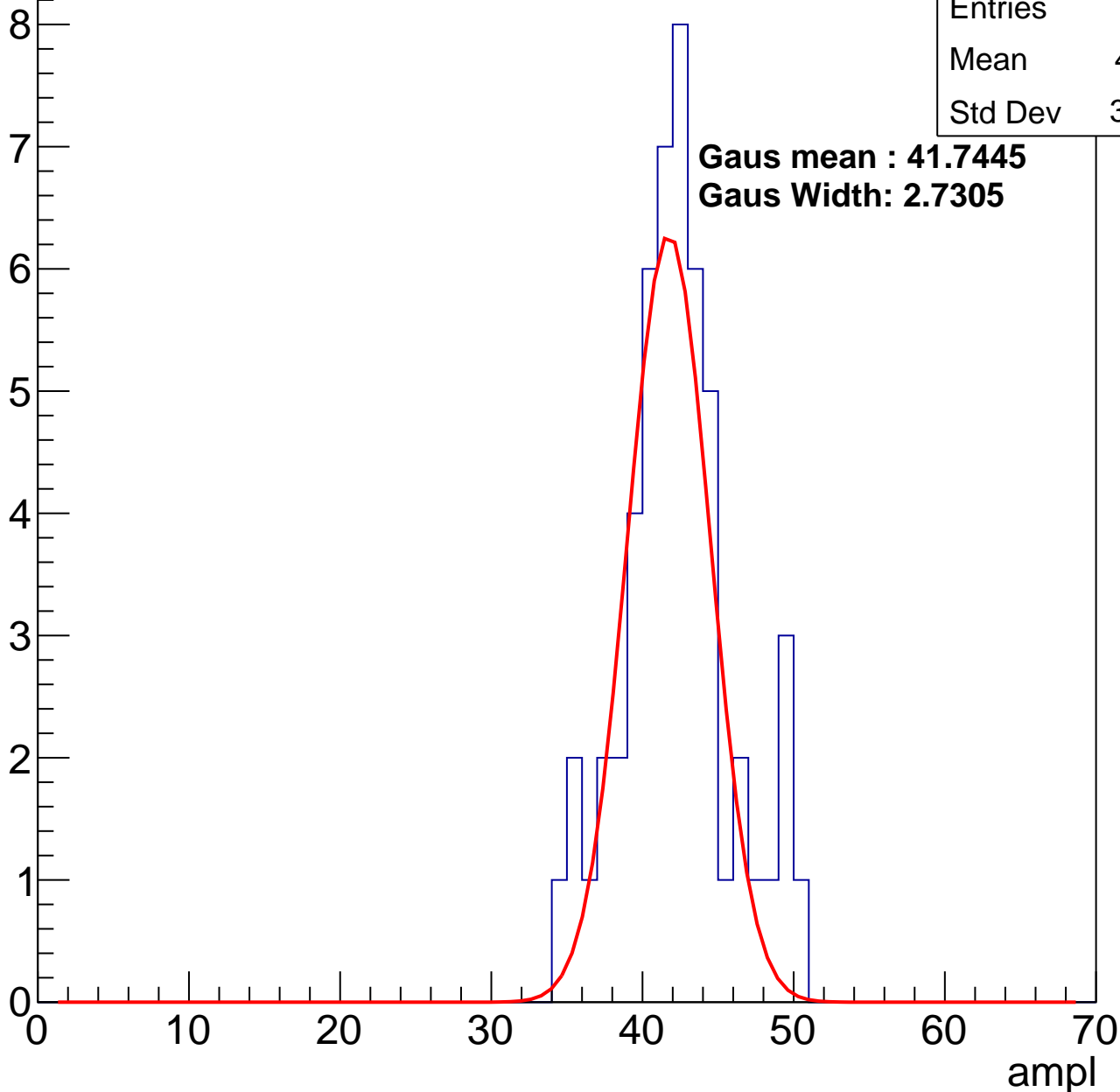
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	41.81
Std Dev	3.593

Gaus mean : 41.7445

Gaus Width: 2.7305

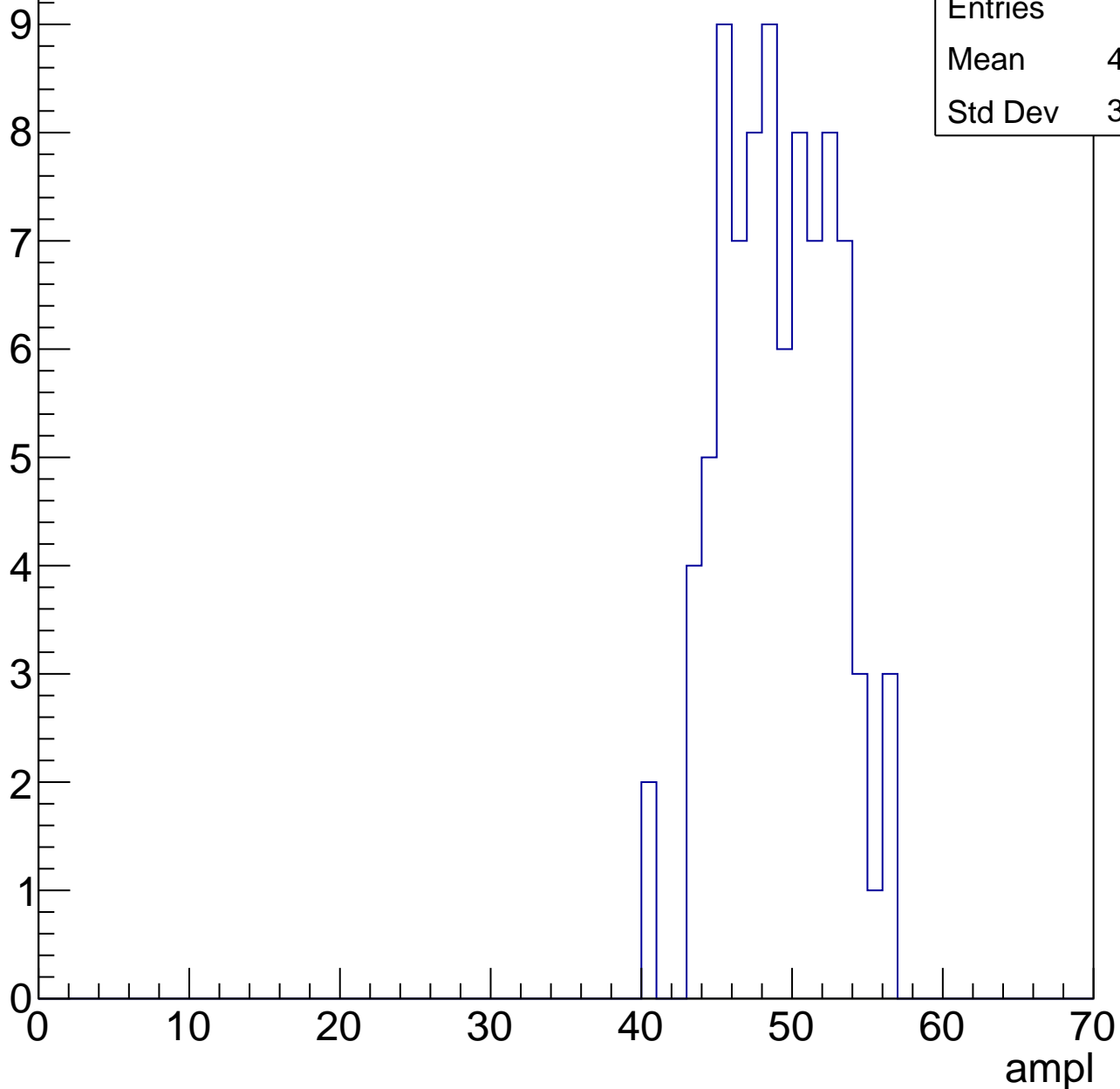


B1L101S, U1-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	48.62
Std Dev	3.627

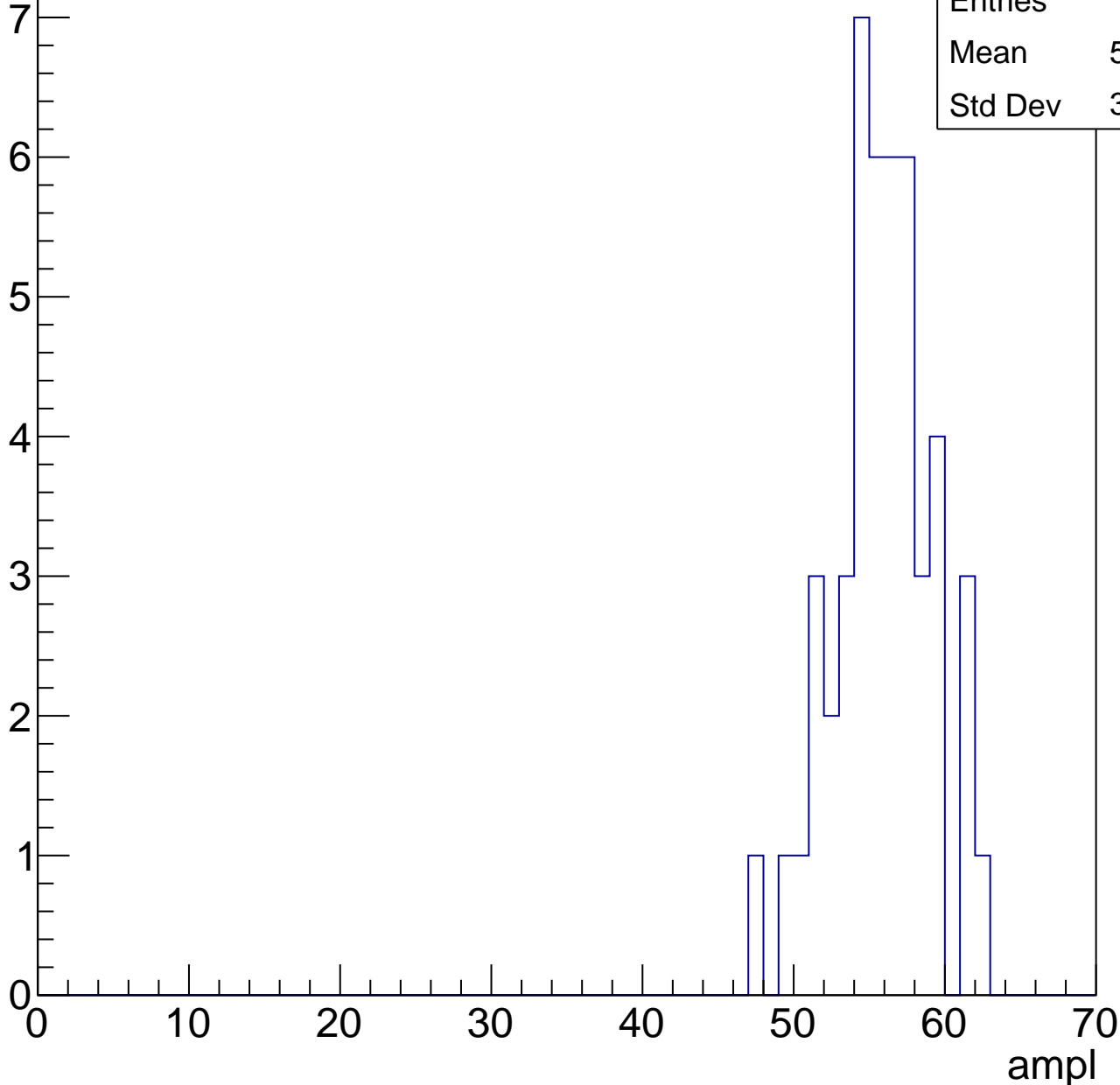


B1L101S, U1-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

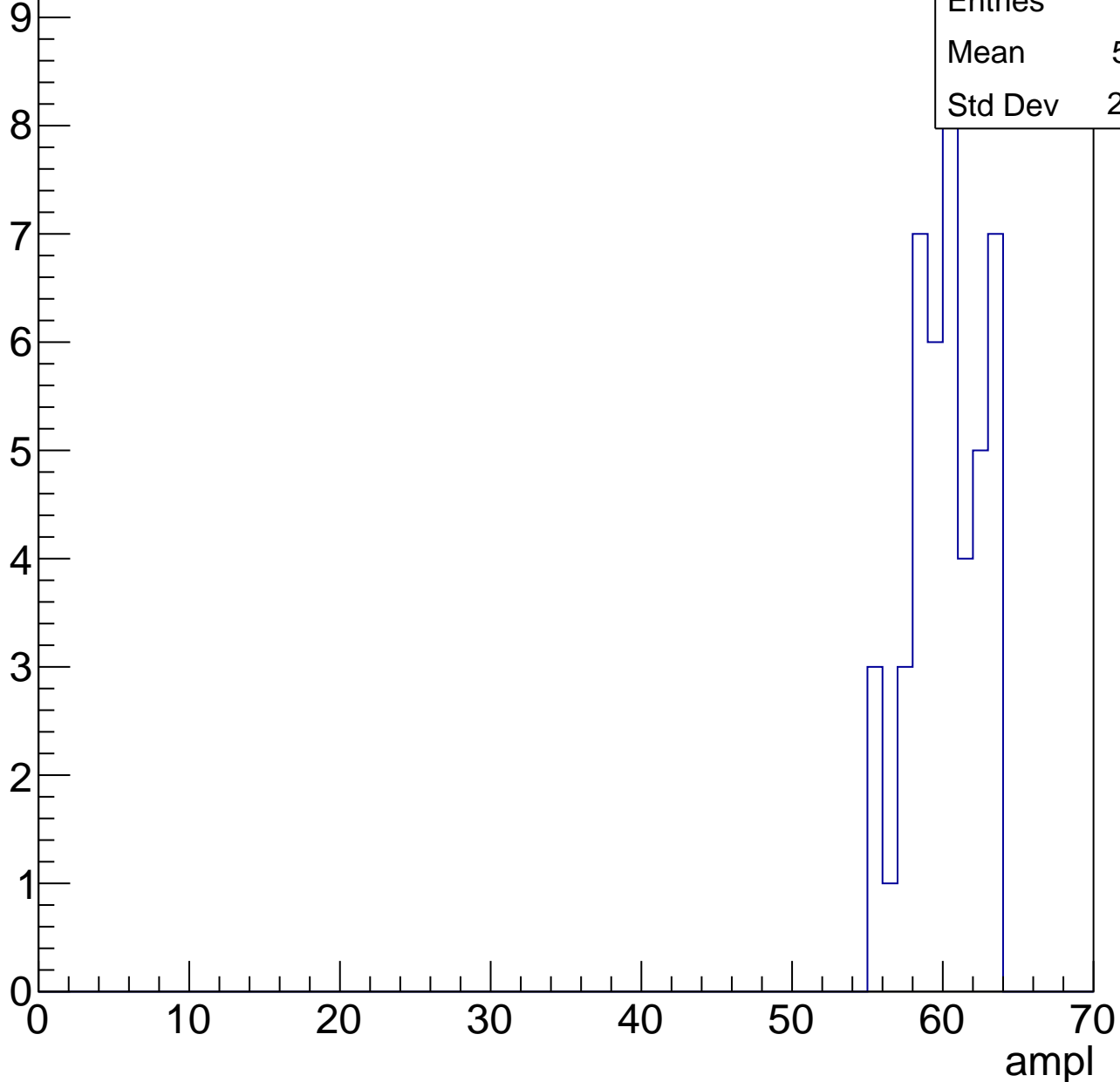
Entries	47
Mean	55.38
Std Dev	3.199



B1L101S, U1-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

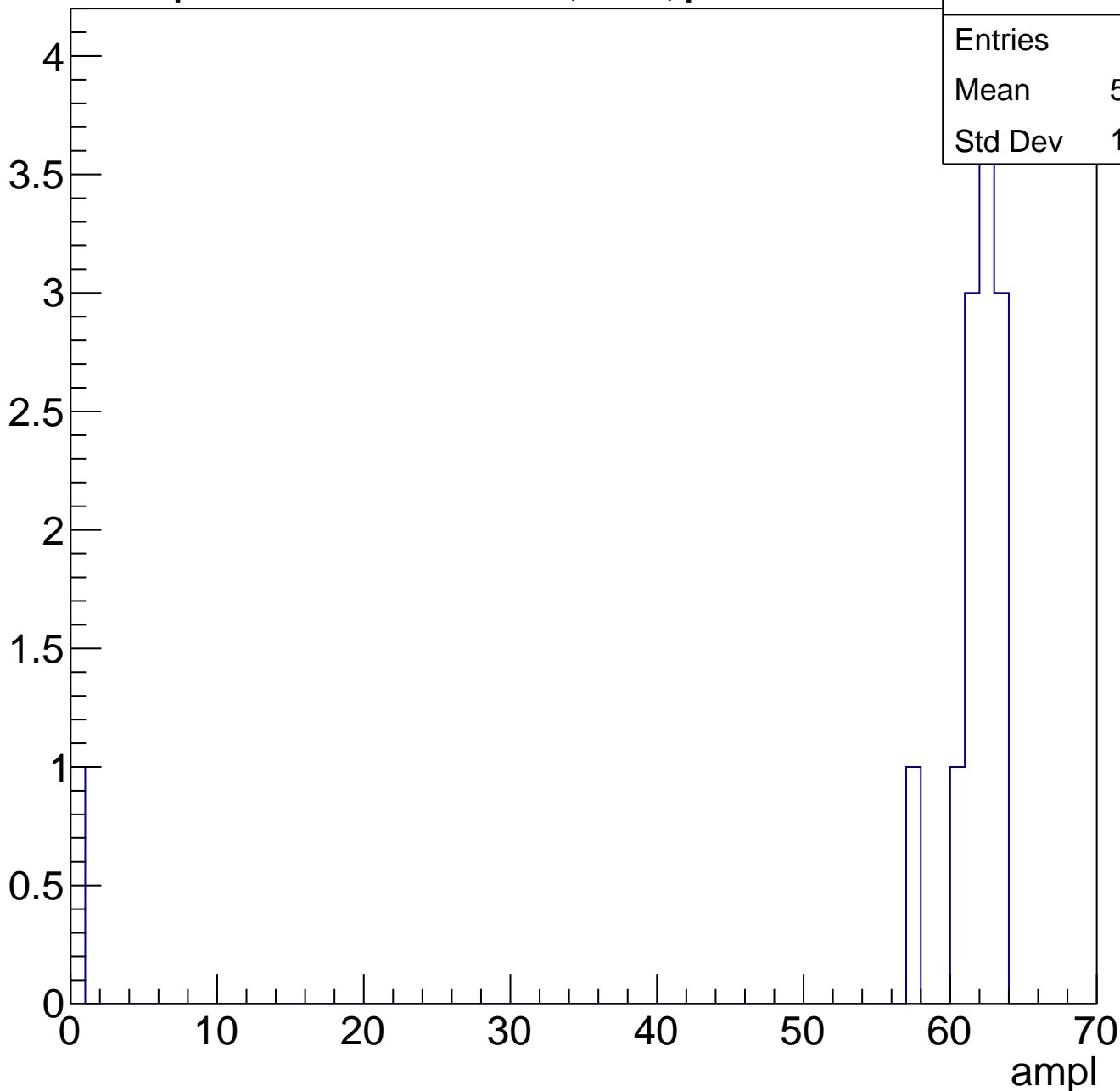
Entry



B1L101S, U1-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	13
Mean	56.69
Std Dev	16.44

B1L101S, U1-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch64, adc0

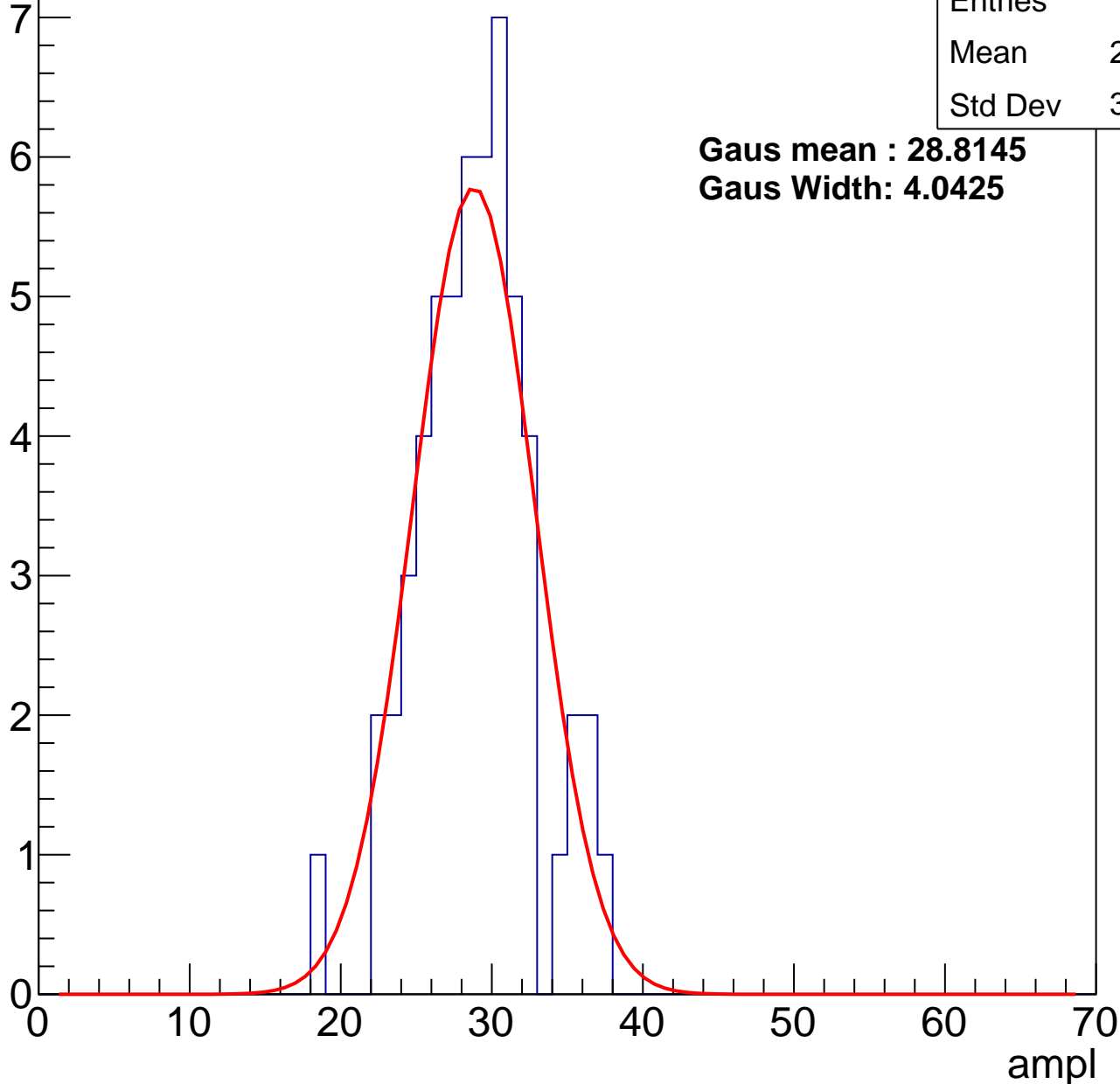
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.45
Std Dev	3.793

Gaus mean : 28.8145

Gaus Width: 4.0425



B1L101S, U1-ch64, adc1

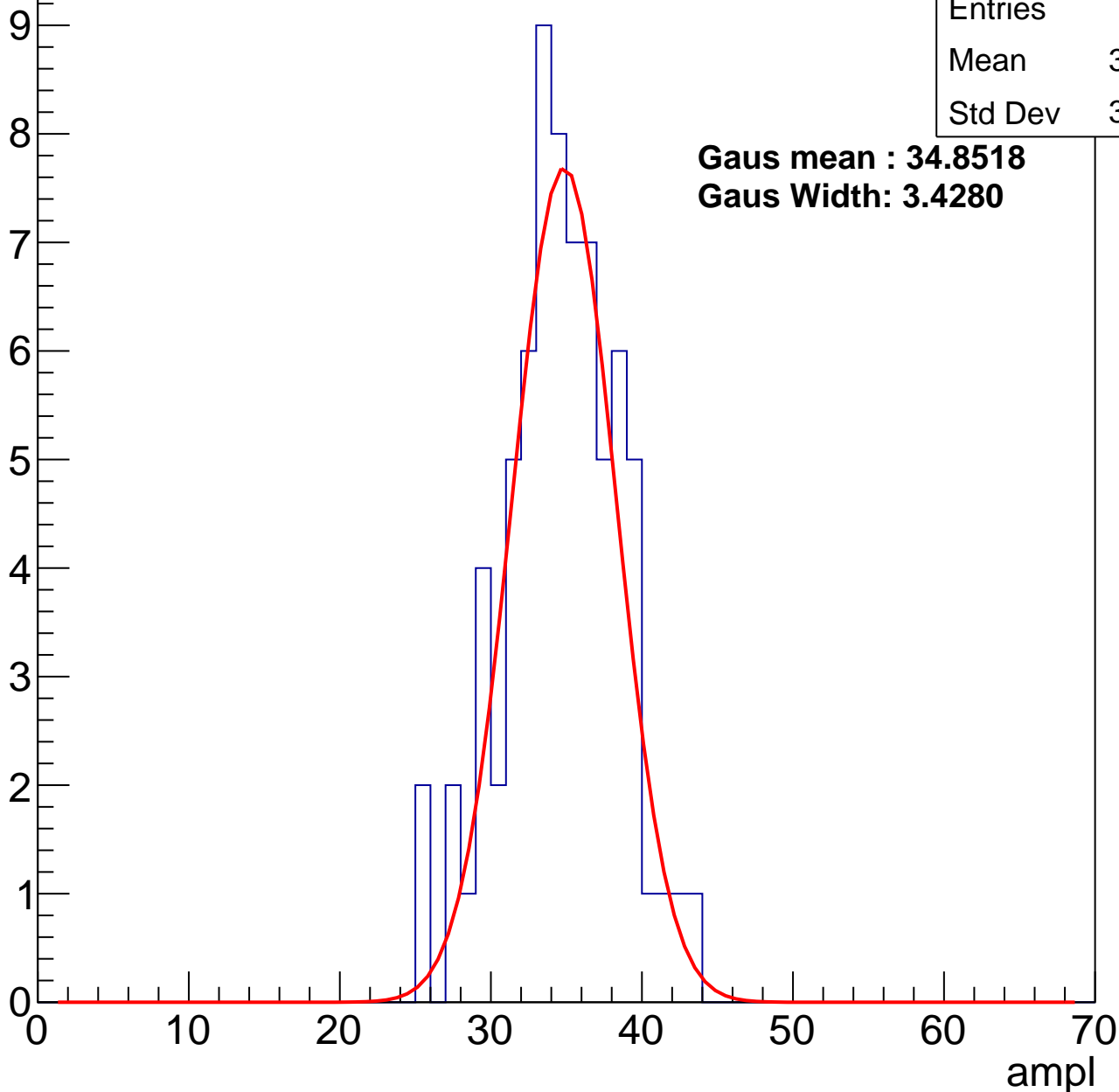
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	34.18
Std Dev	3.765

Gaus mean : 34.8518

Gaus Width: 3.4280



B1L101S, U1-ch64, adc2

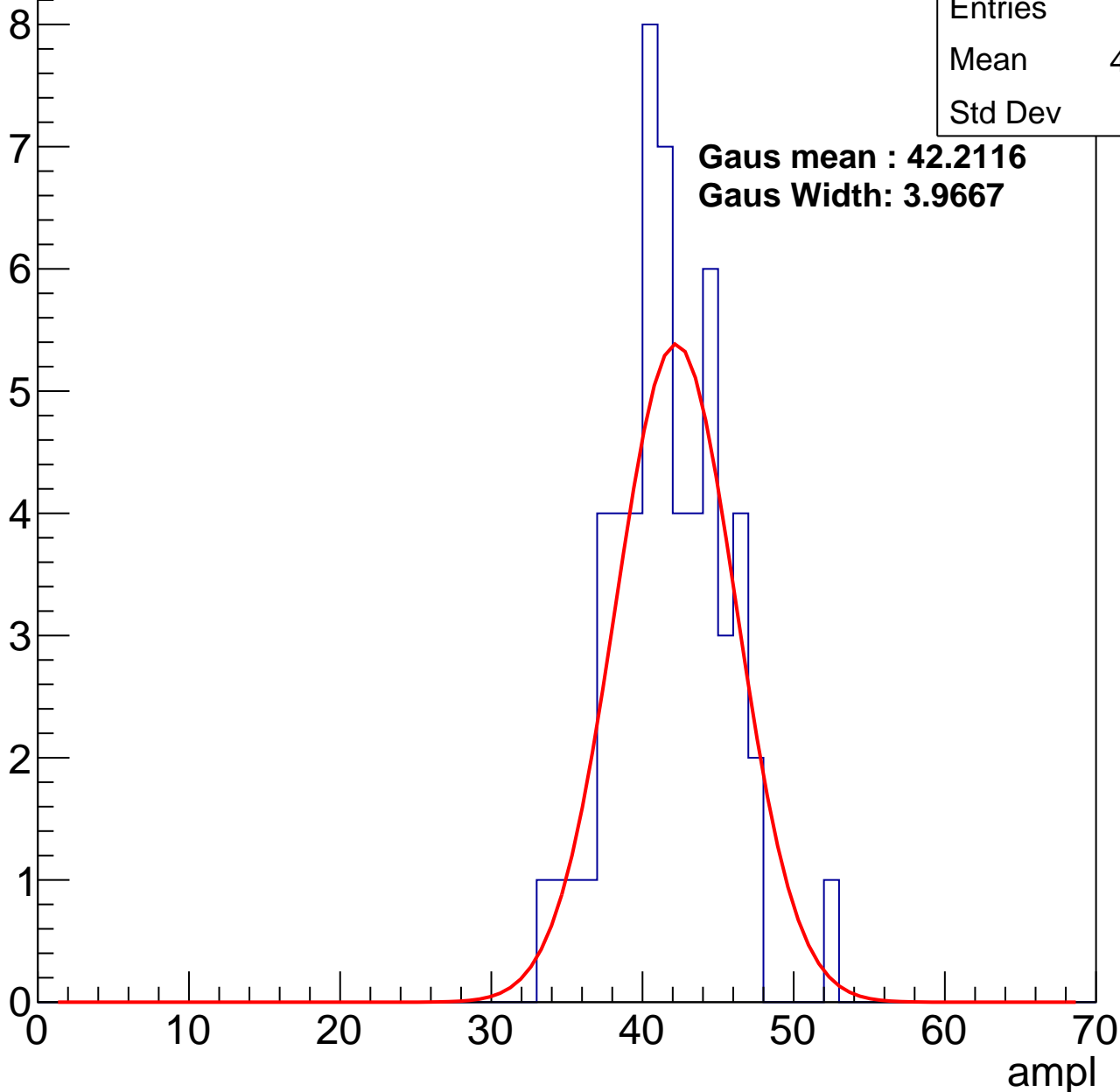
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.27
Std Dev	3.59

Gaus mean : 42.2116

Gaus Width: 3.9667

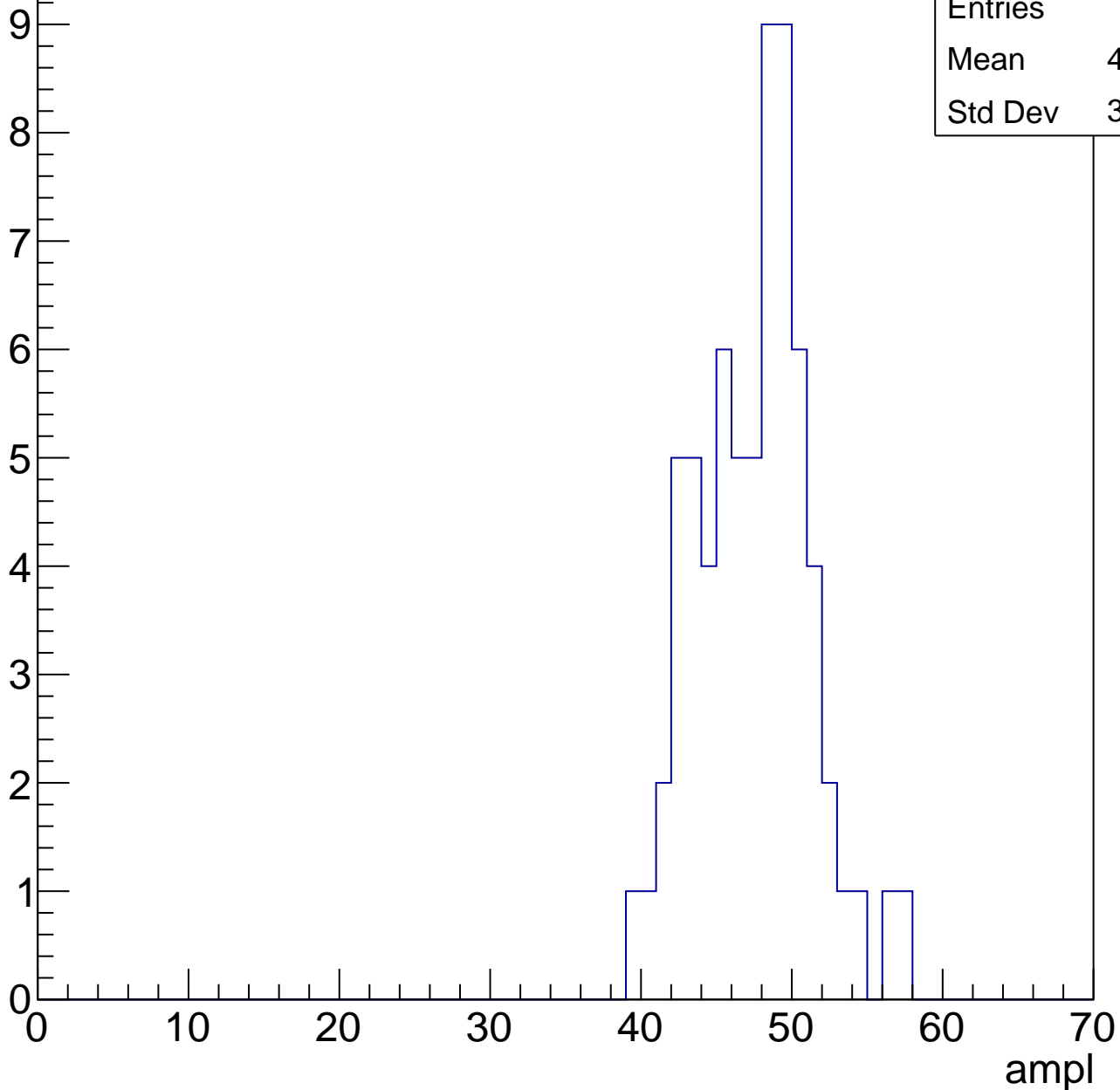


B1L101S, U1-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	47.03
Std Dev	3.706

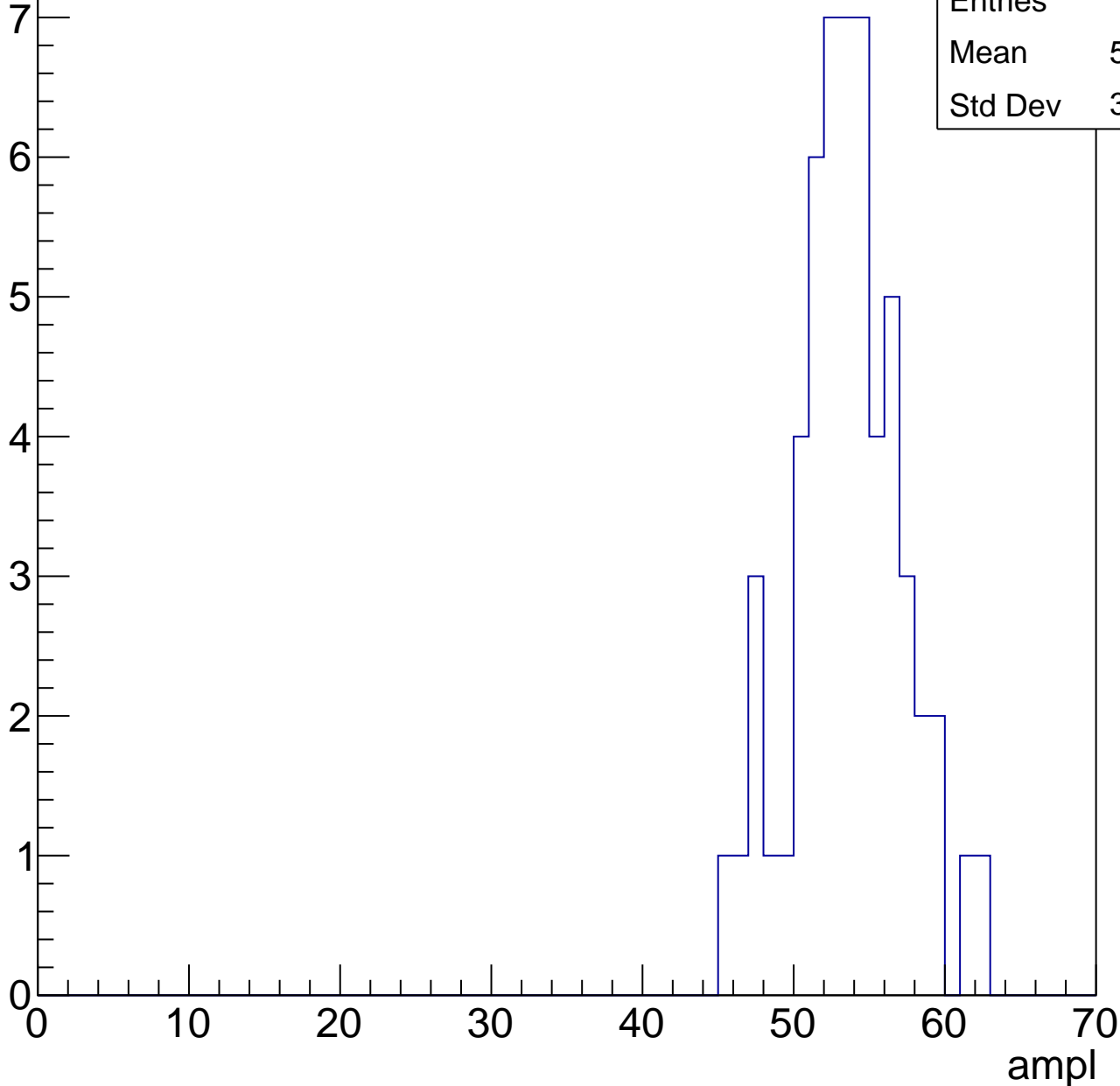


B1L101S, U1-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	53.14
Std Dev	3.563

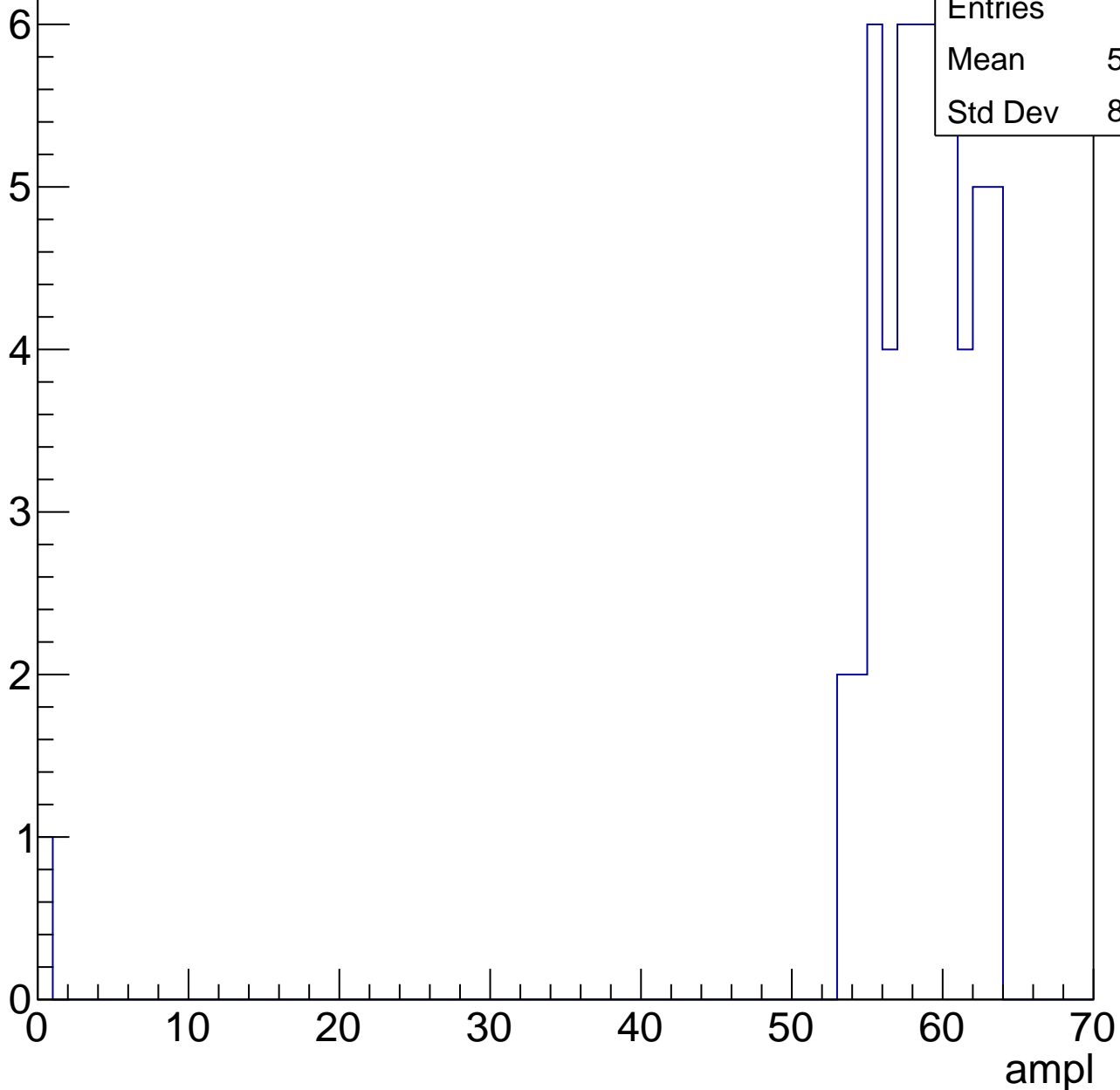


B1L101S, U1-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.38
Std Dev	8.437

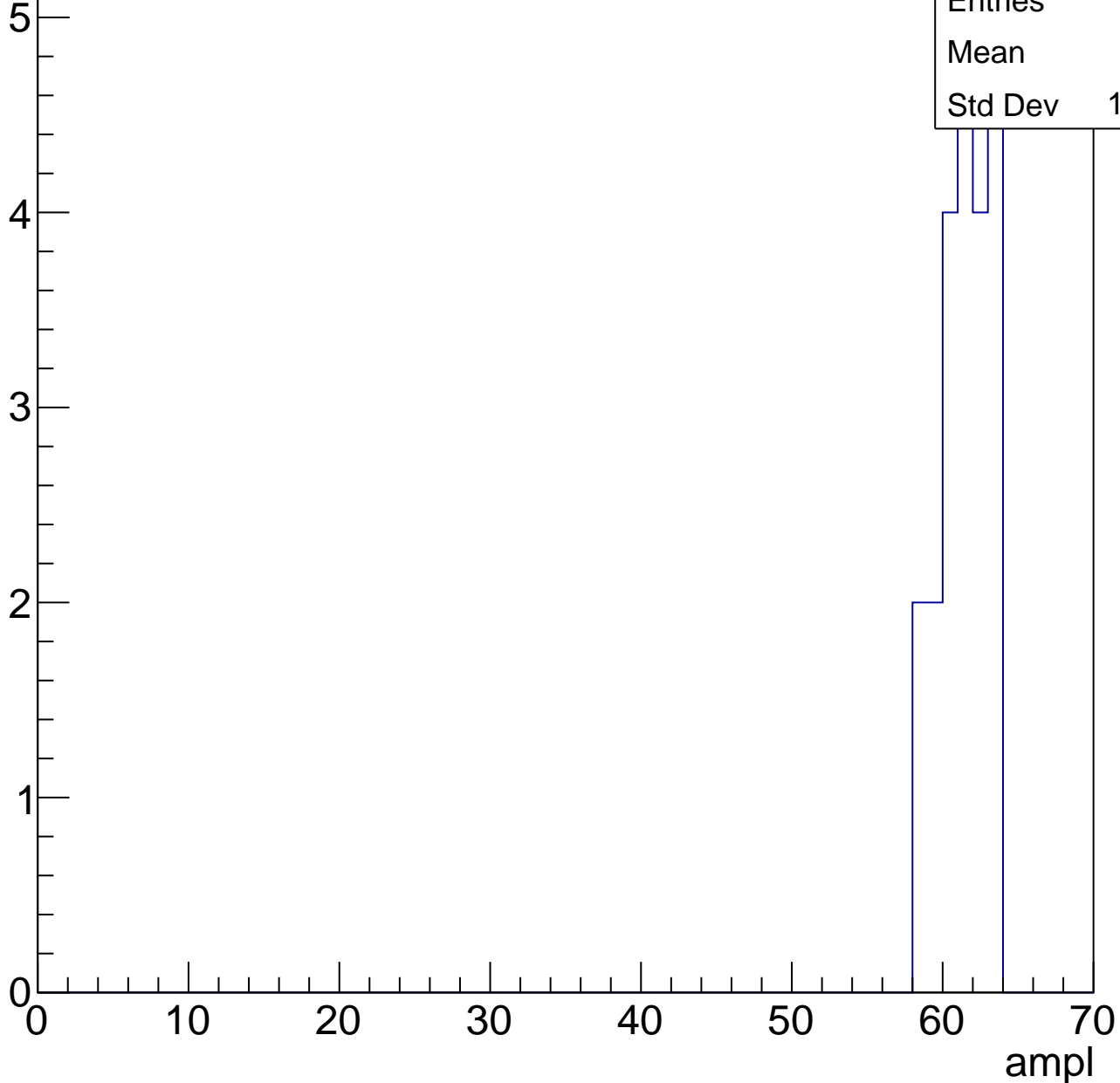


B1L101S, U1-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	61
Std Dev	1.567



B1L101S, U1-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch65, adc0

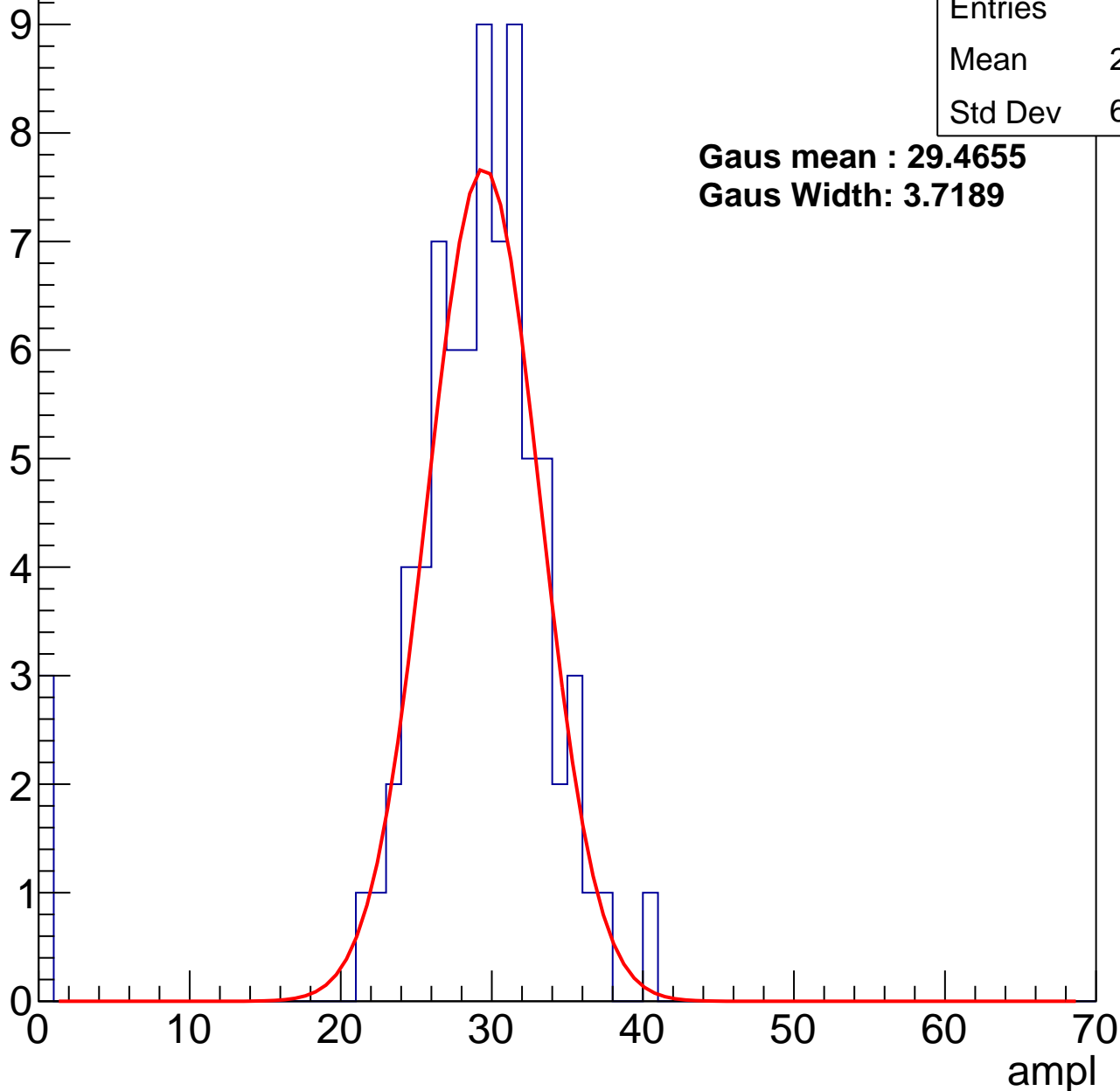
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.03
Std Dev	6.696

Gaus mean : 29.4655

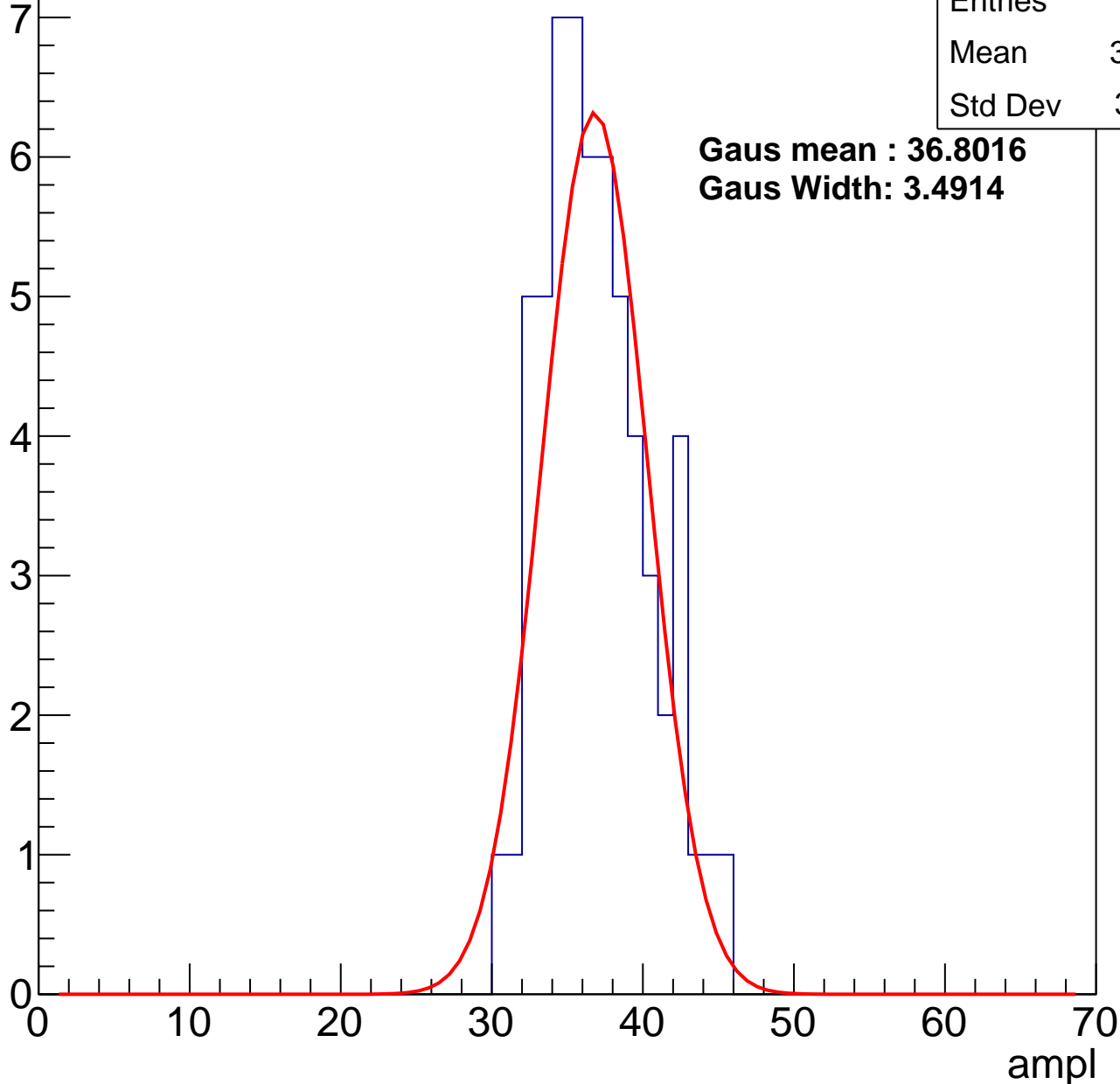
Gaus Width: 3.7189



B1L101S, U1-ch65, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch65, adc2

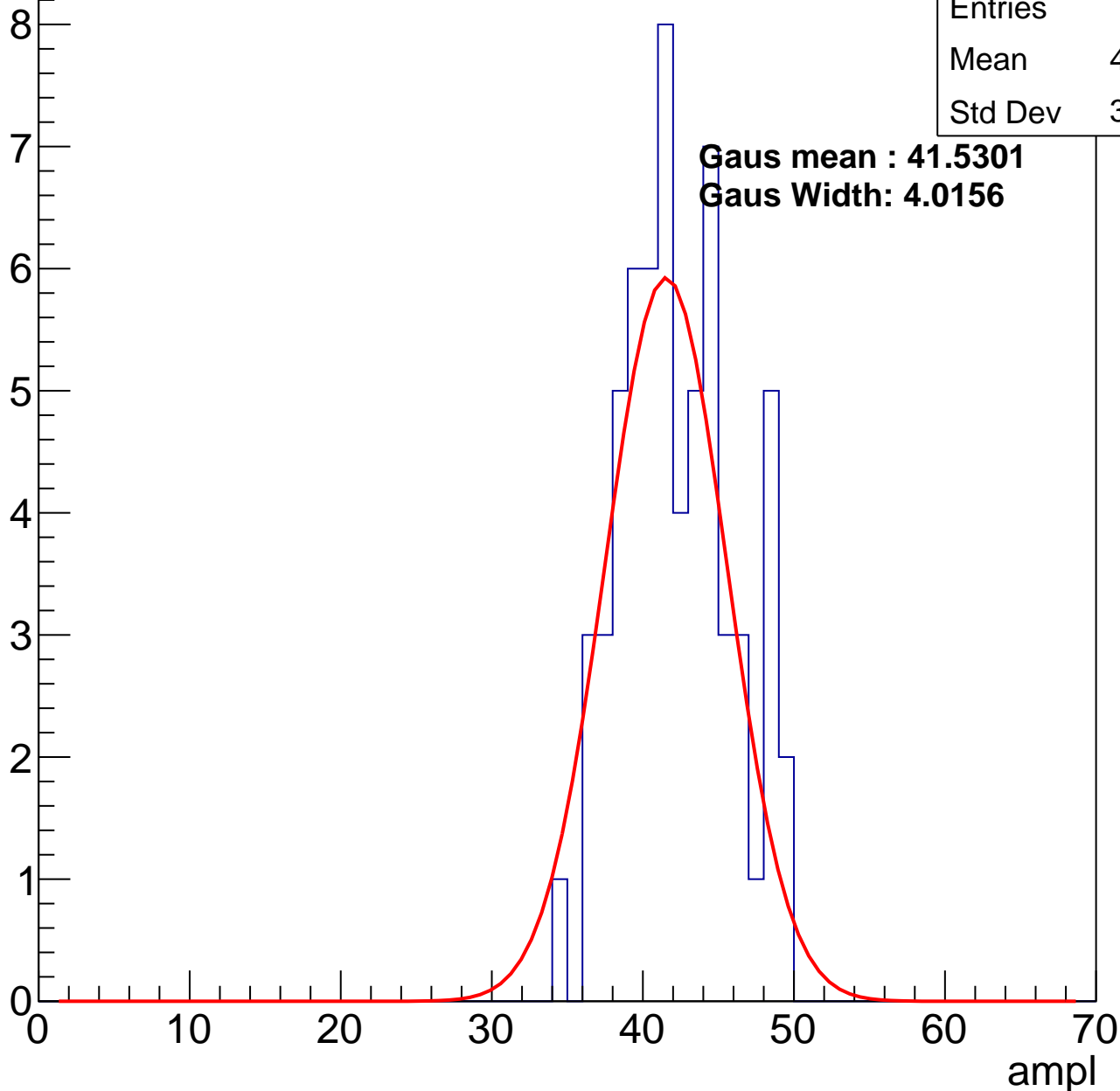
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.84
Std Dev	3.642

Gaus mean : 41.5301

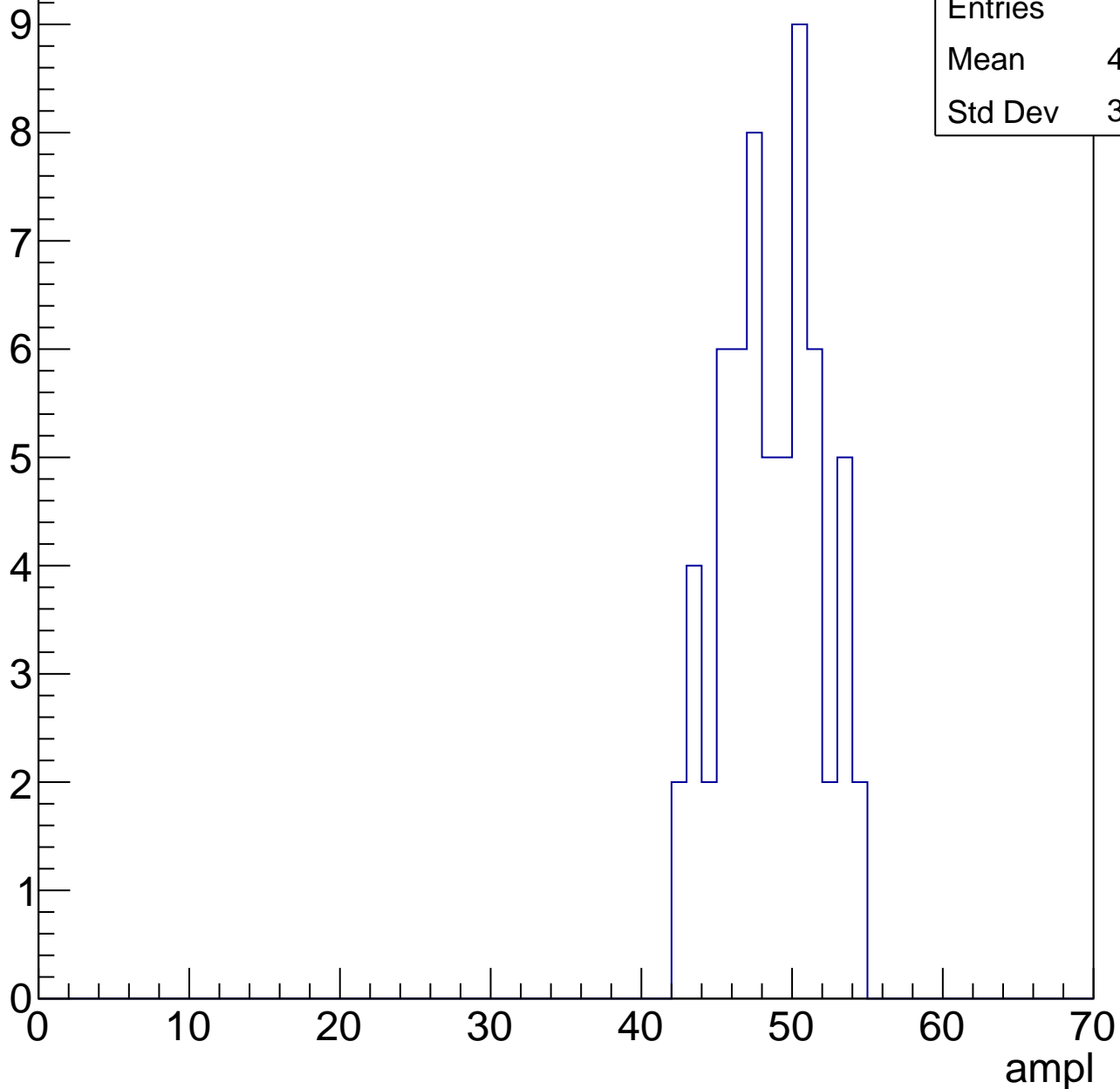
Gaus Width: 4.0156



B1L101S, U1-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

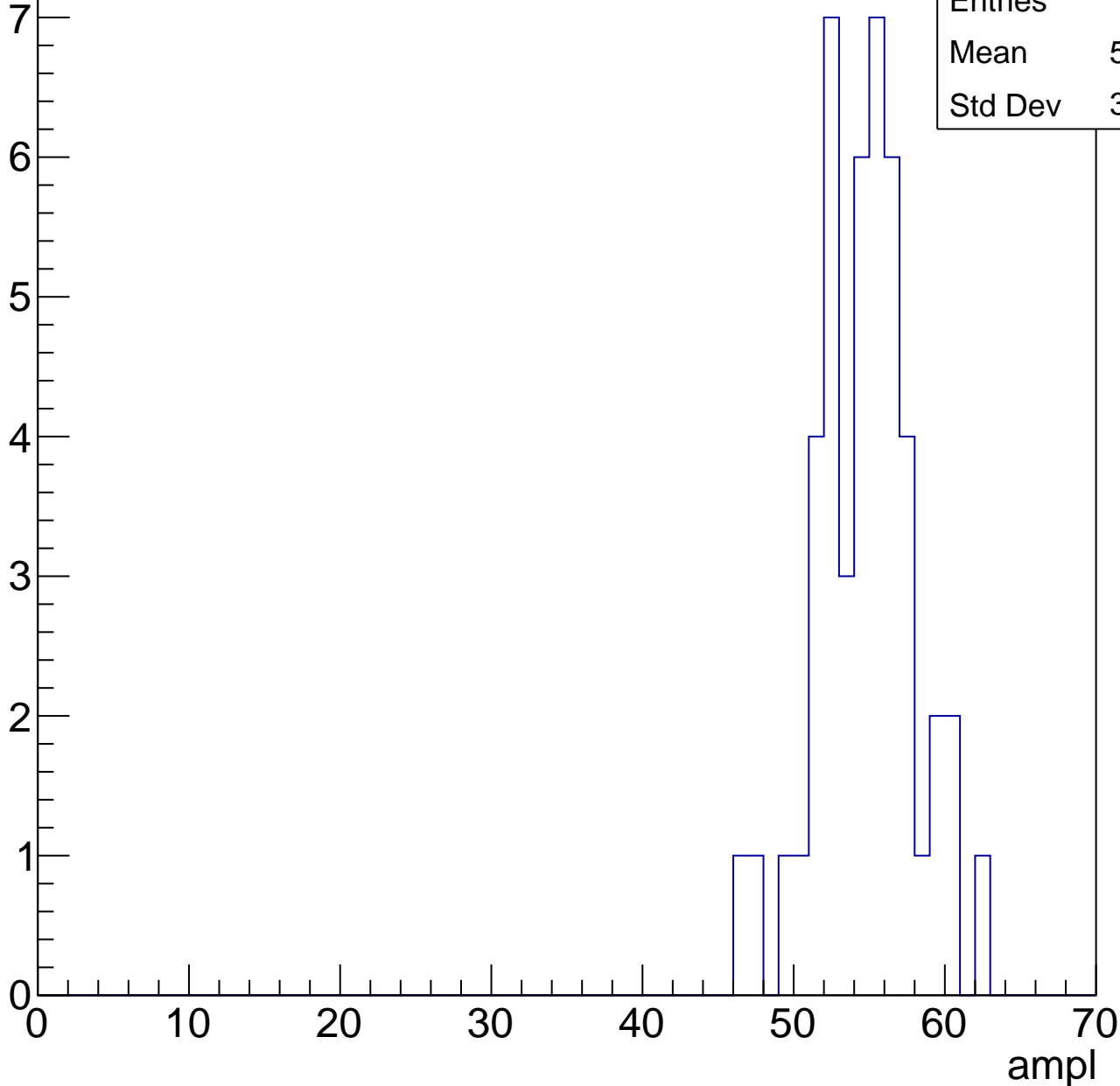


B1L101S, U1-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	54.26
Std Dev	3.219

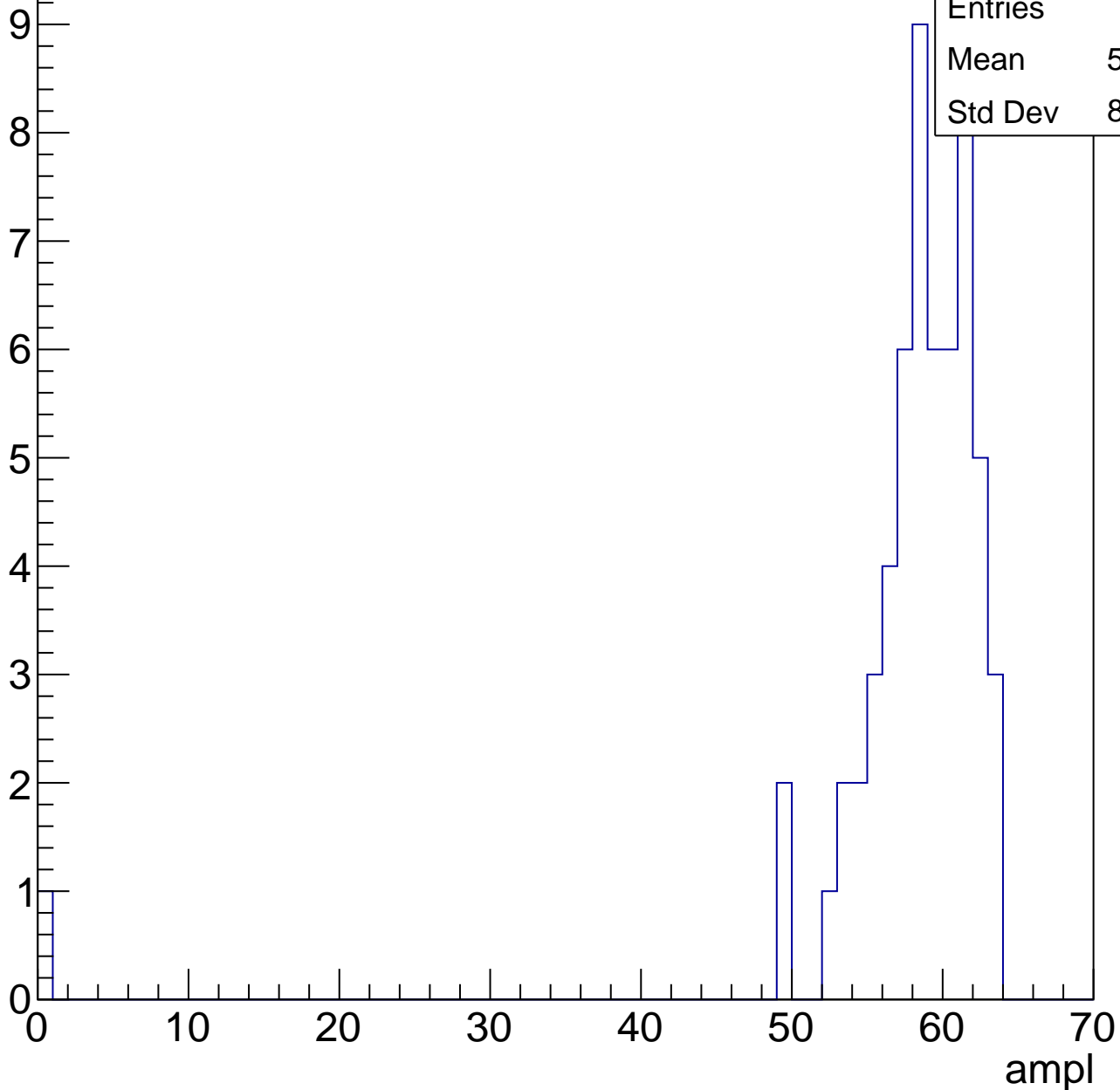


B1L101S, U1-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	57.27
Std Dev	8.159

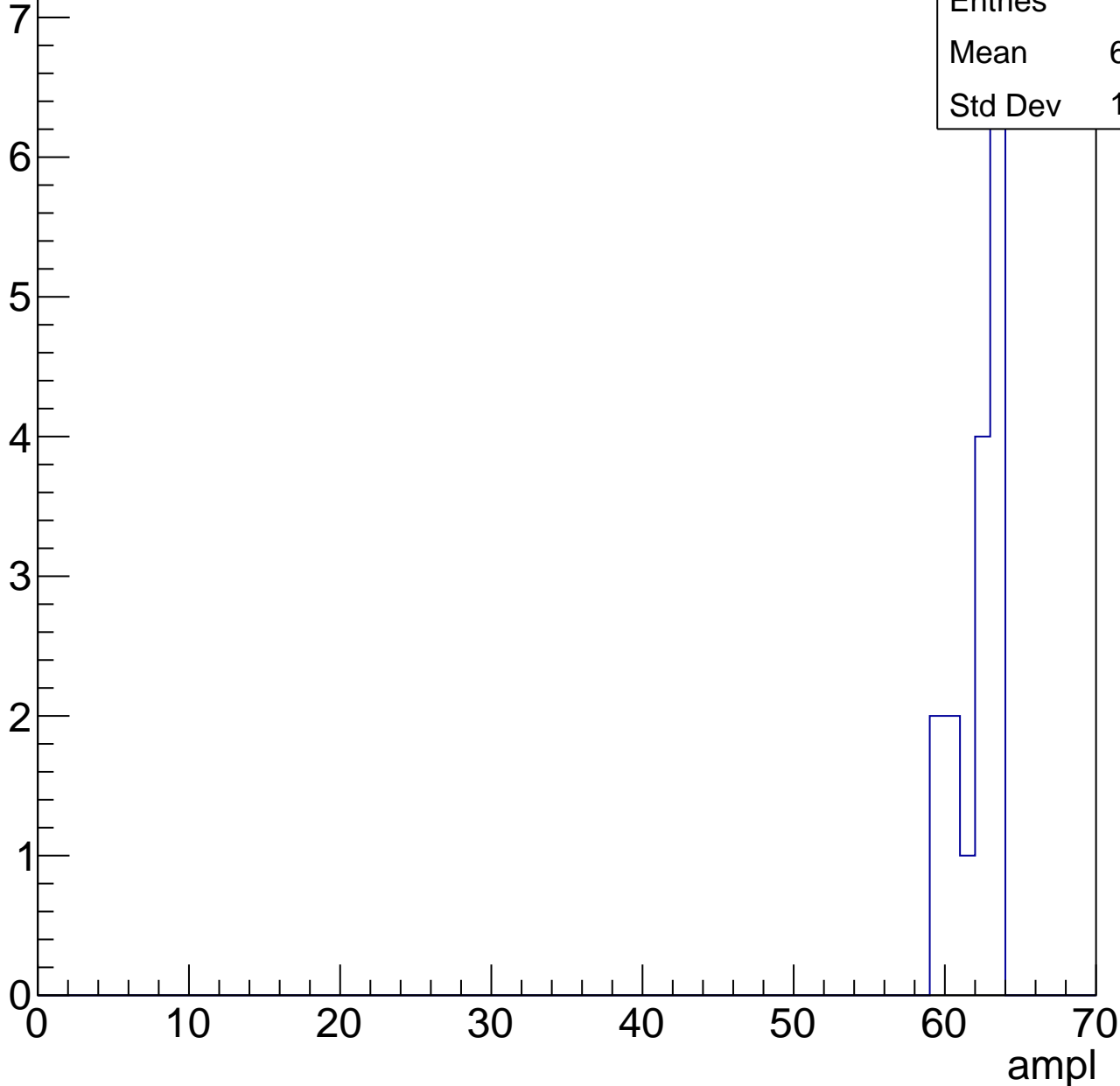


B1L101S, U1-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.75
Std Dev	1.436



B1L101S, U1-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch66, adc0

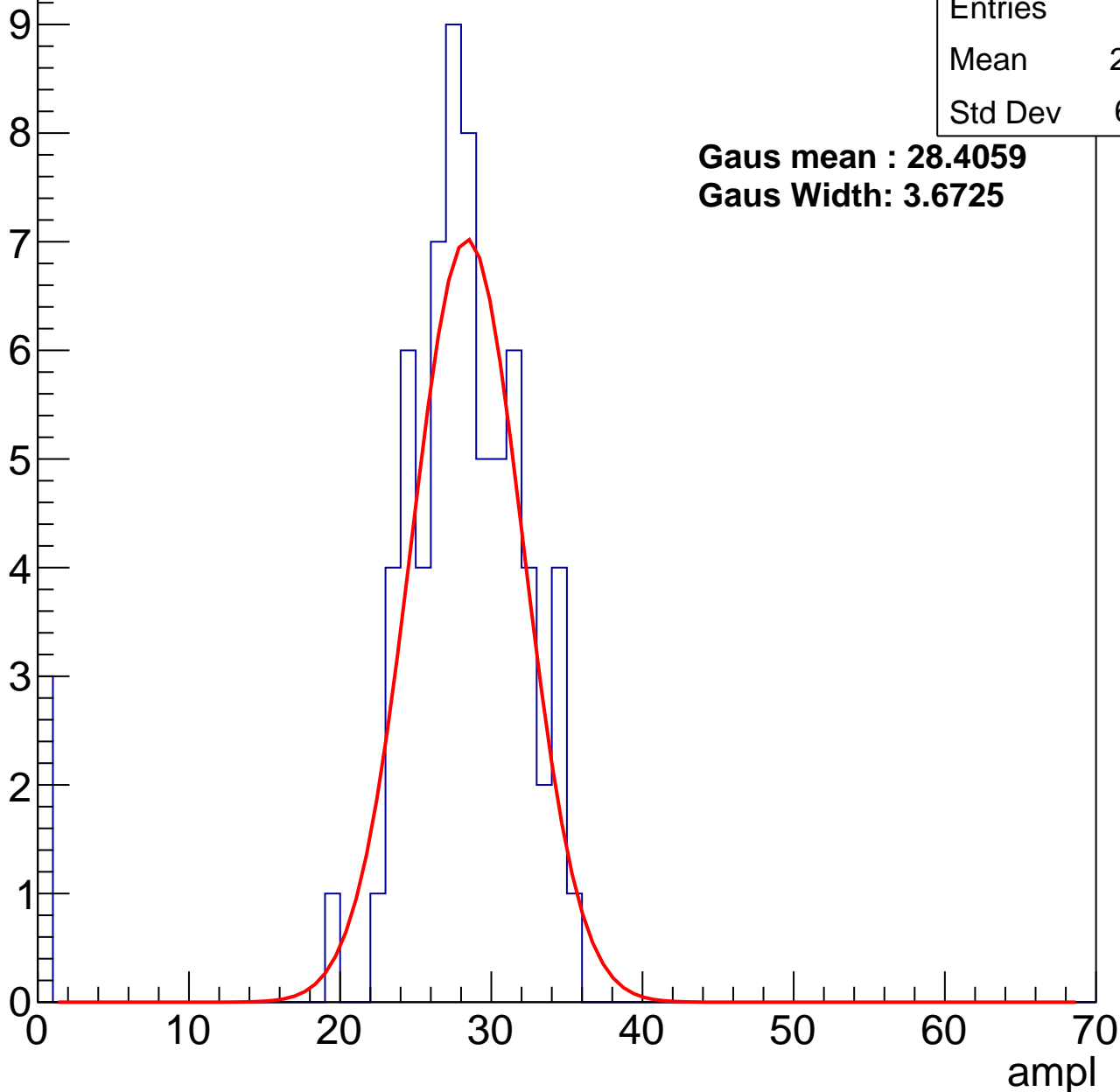
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	26.74
Std Dev	6.561

Gaus mean : 28.4059

Gaus Width: 3.6725



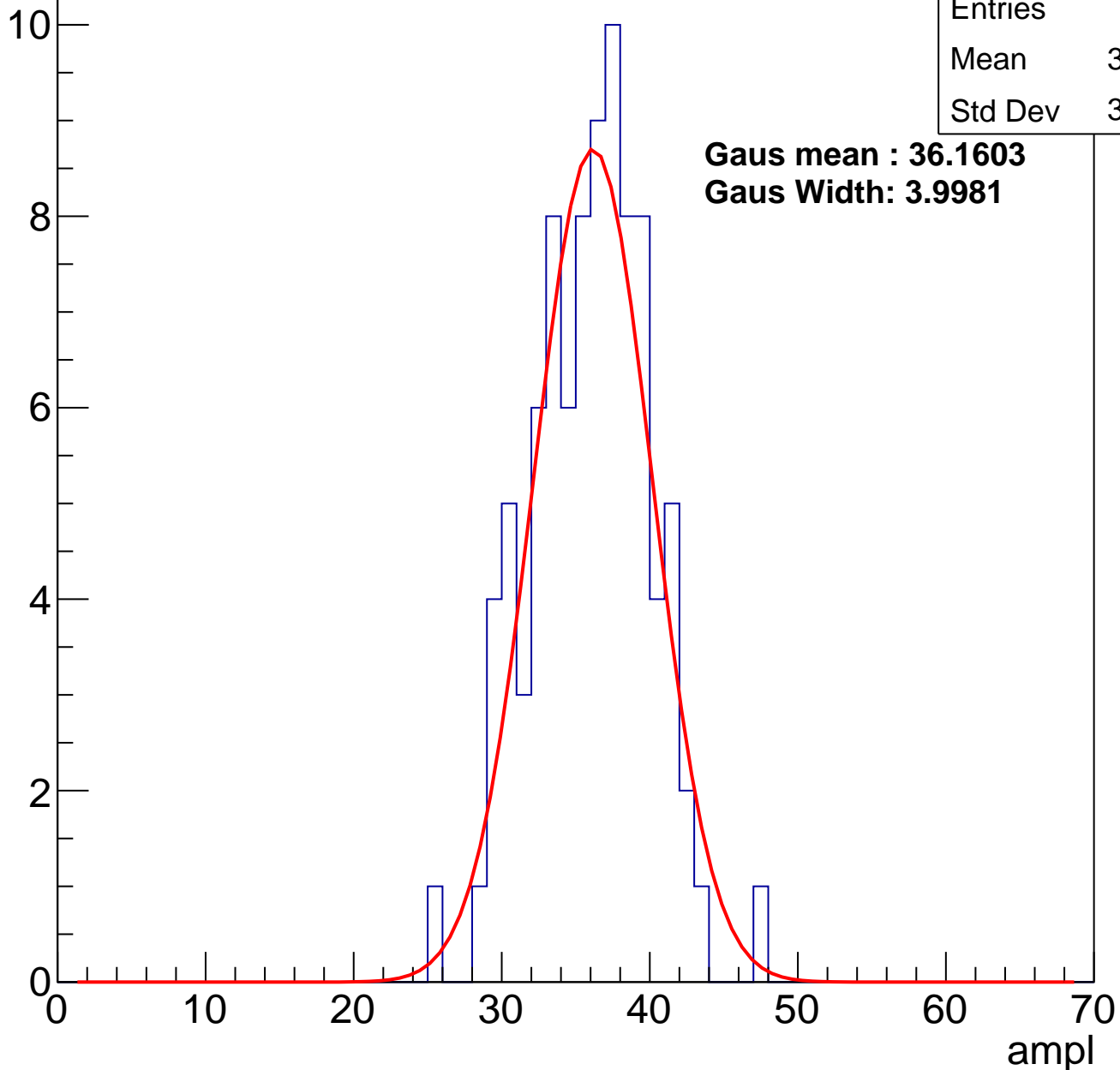
B1L101S, U1-ch66, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	35.57
Std Dev	3.893

Gaus mean : 36.1603
Gaus Width: 3.9981

Entry

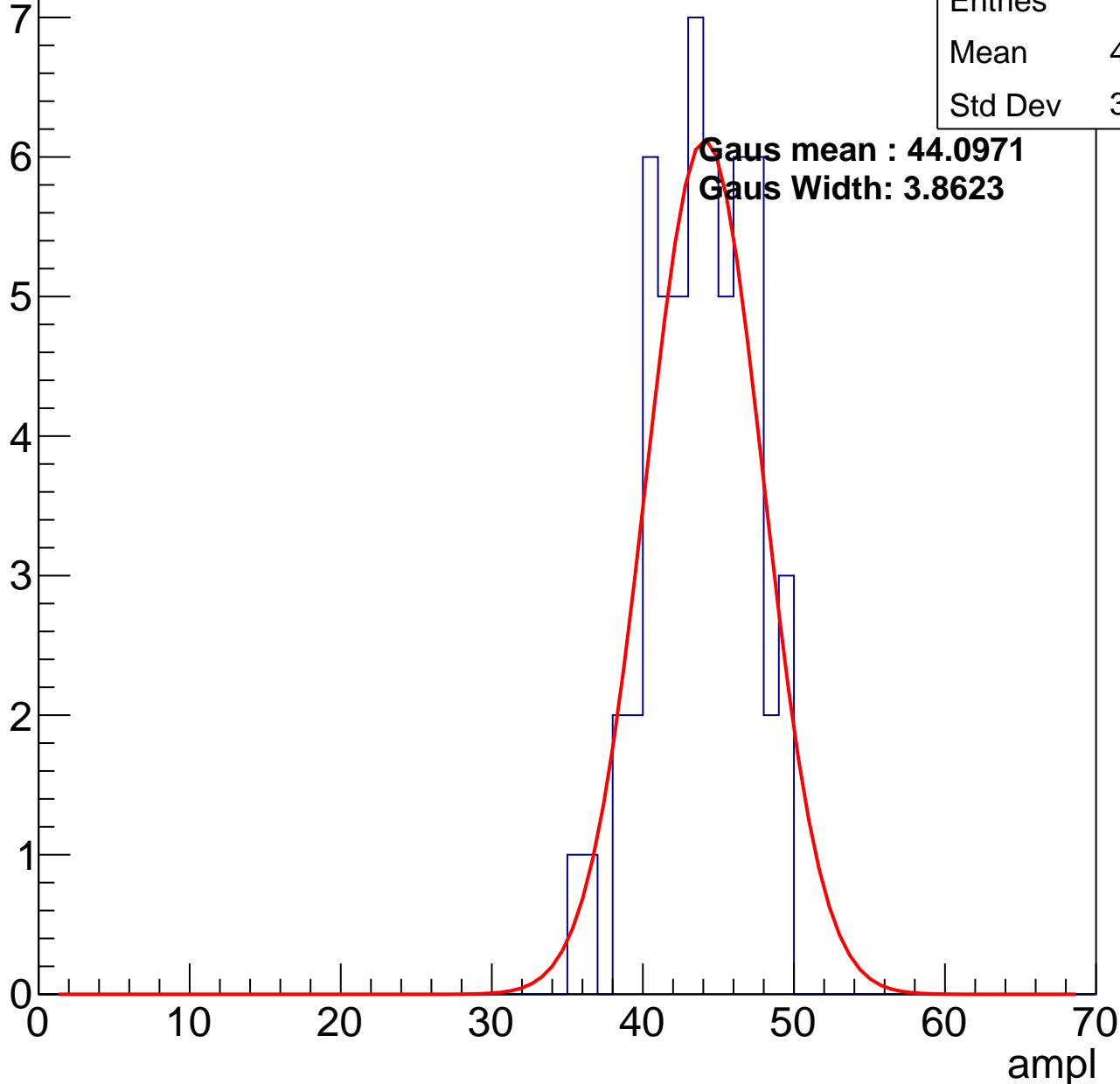


B1L101S, U1-ch66, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.35
Std Dev	3.247

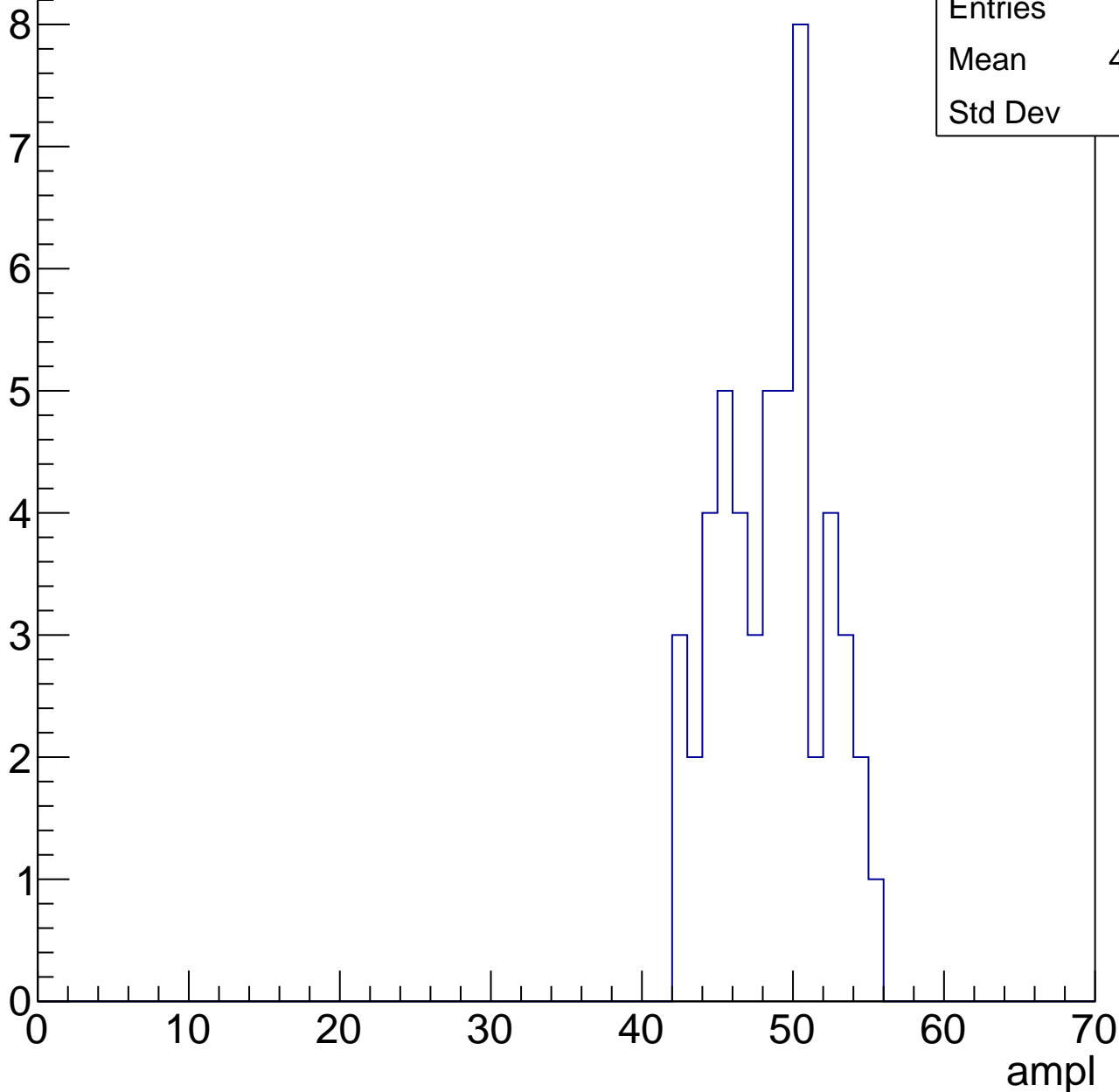


B1L101S, U1-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	48.14
Std Dev	3.43

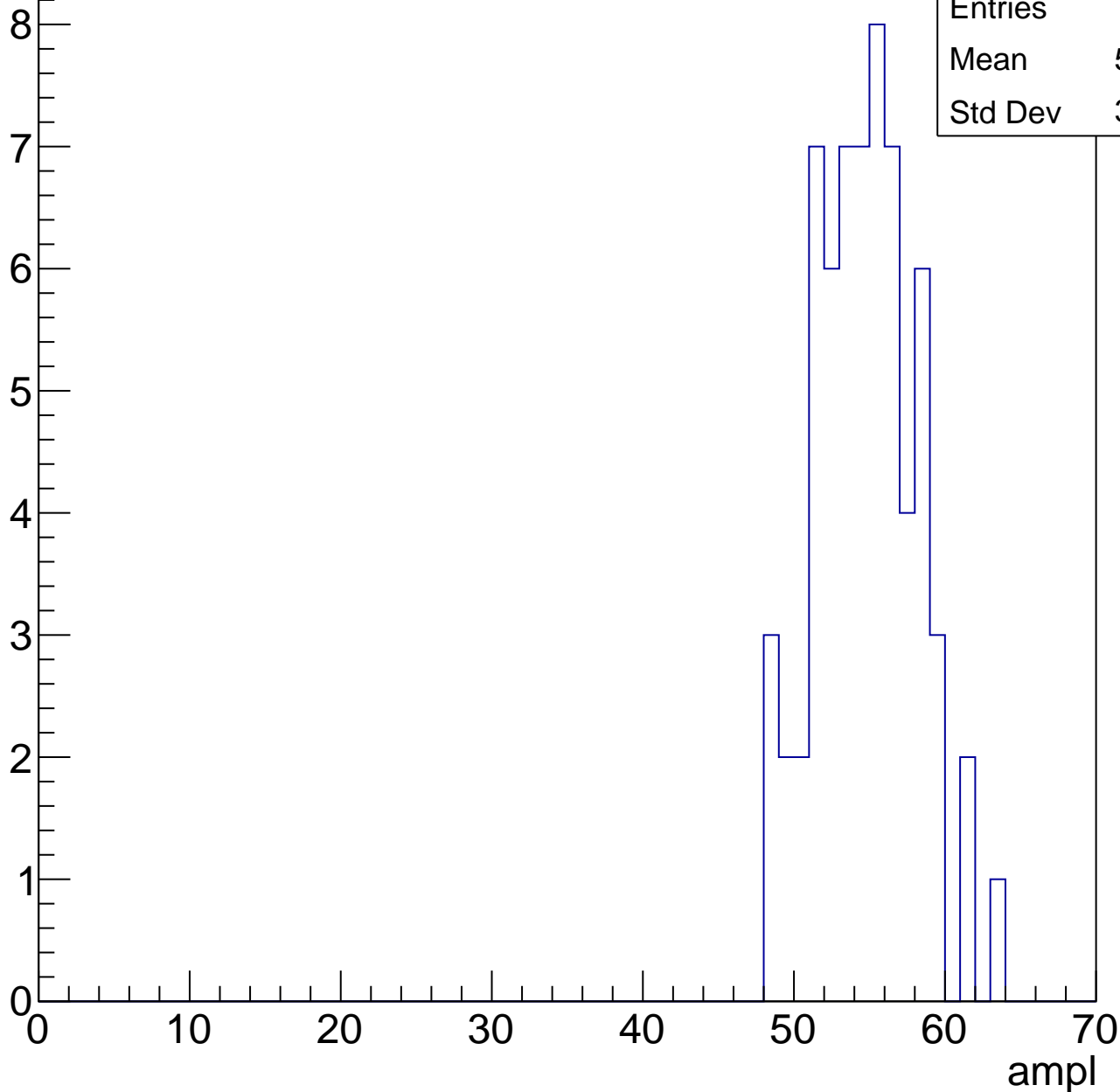


B1L101S, U1-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.31
Std Dev	3.281

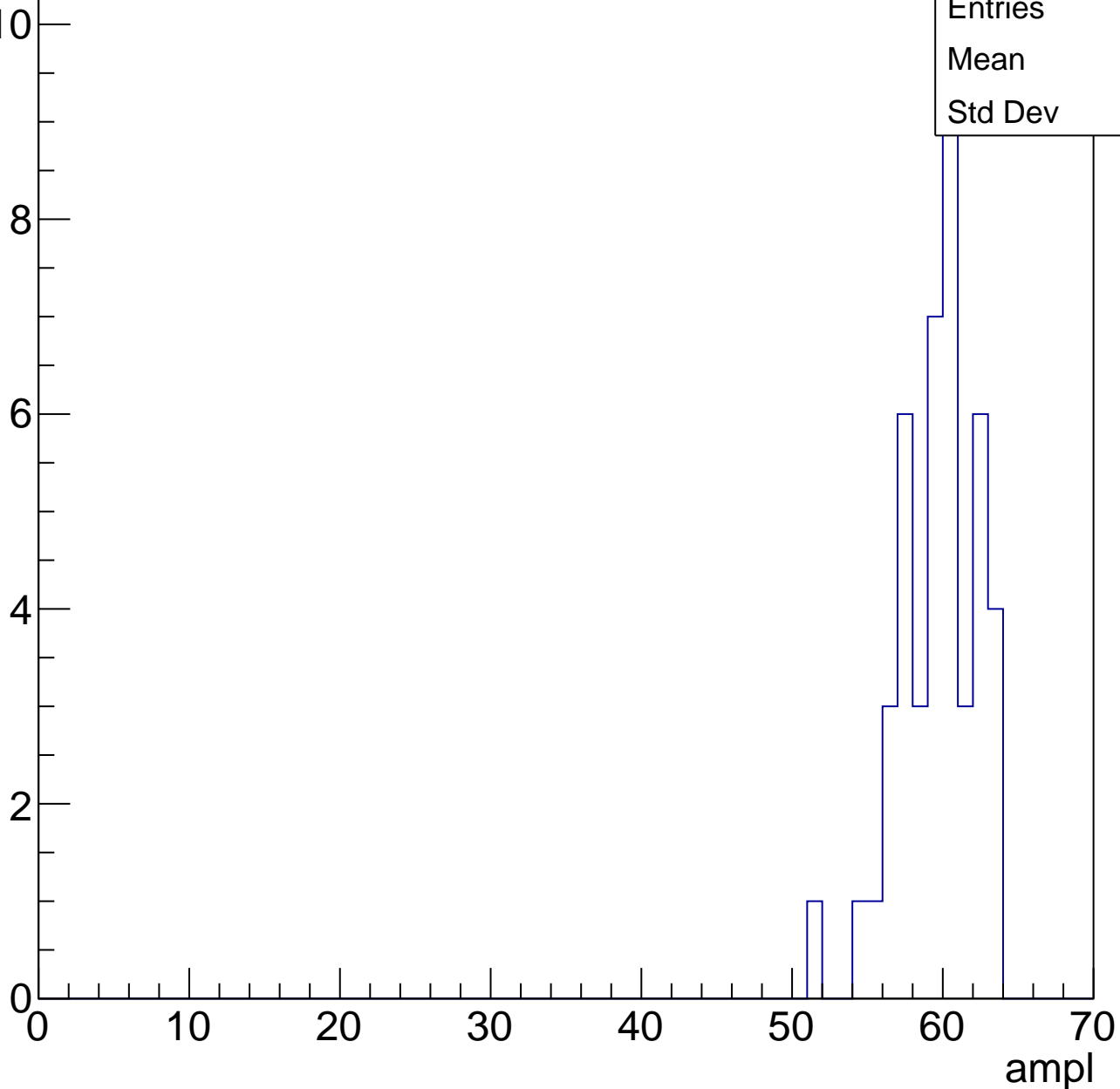


B1L101S, U1-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.2
Std Dev	2.57

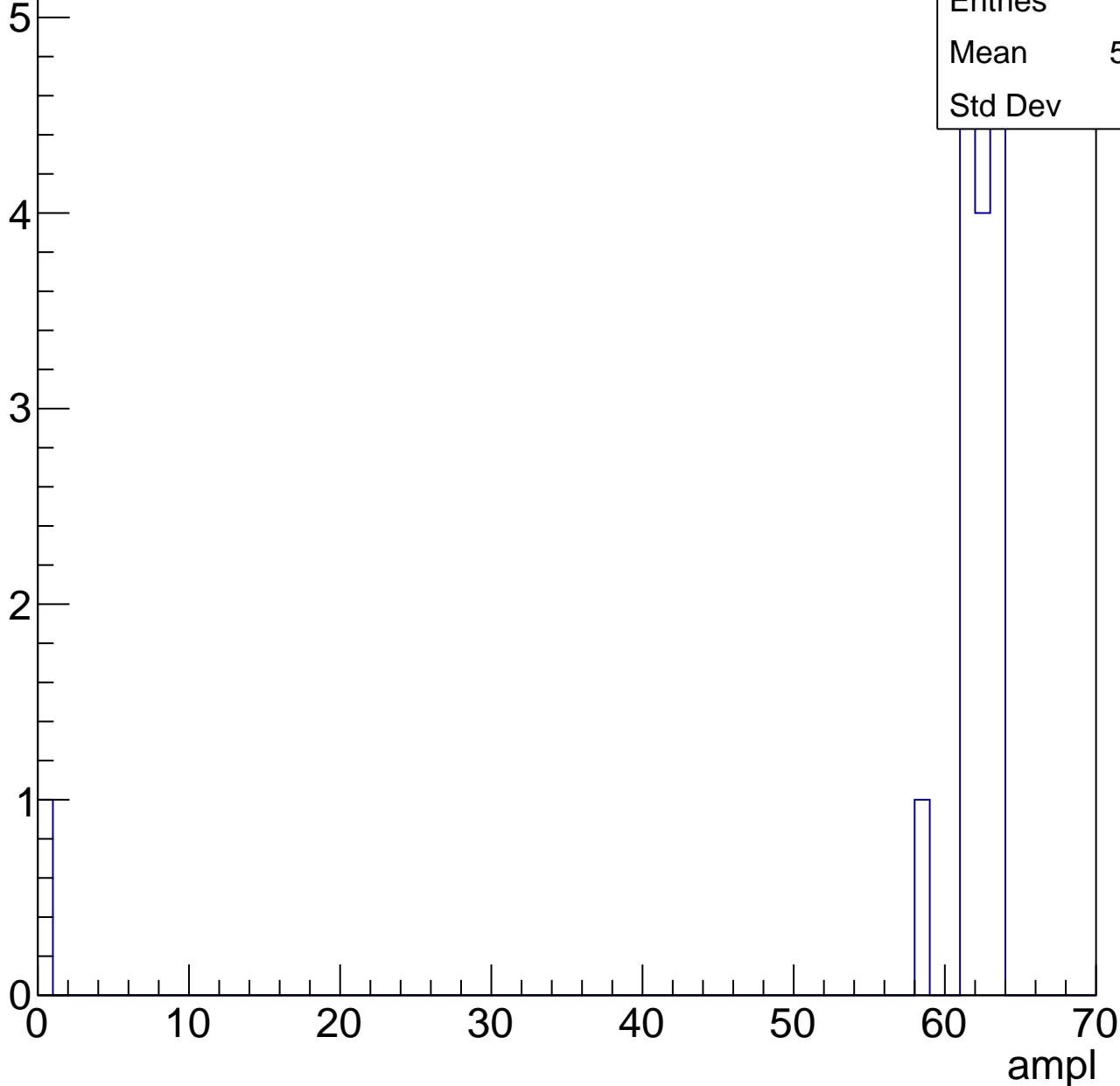


B1L101S, U1-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.88
Std Dev	15



B1L101S, U1-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch67, adc0

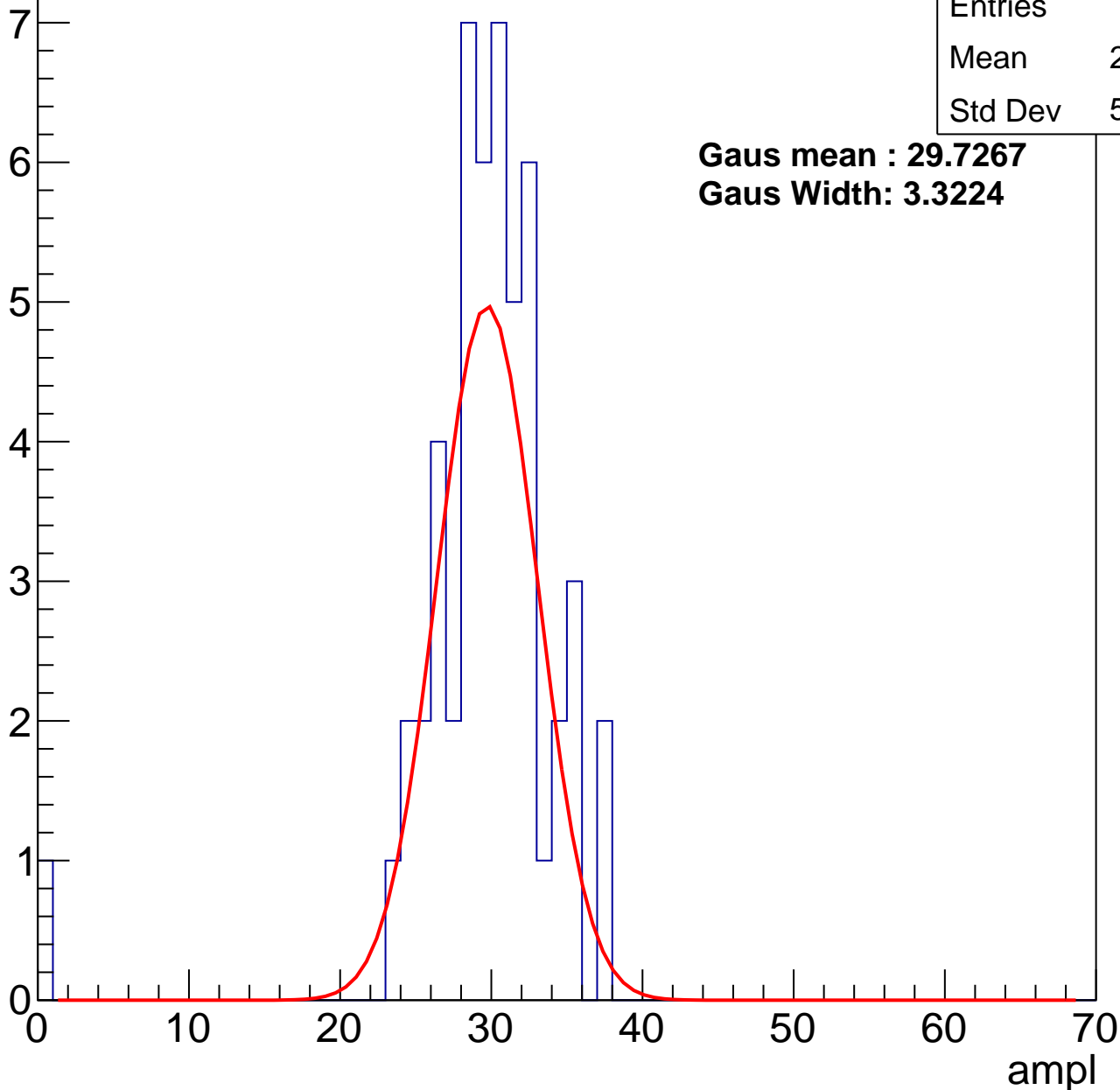
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	29.14
Std Dev	5.224

Gaus mean : 29.7267

Gaus Width: 3.3224



B1L101S, U1-ch67, adc1

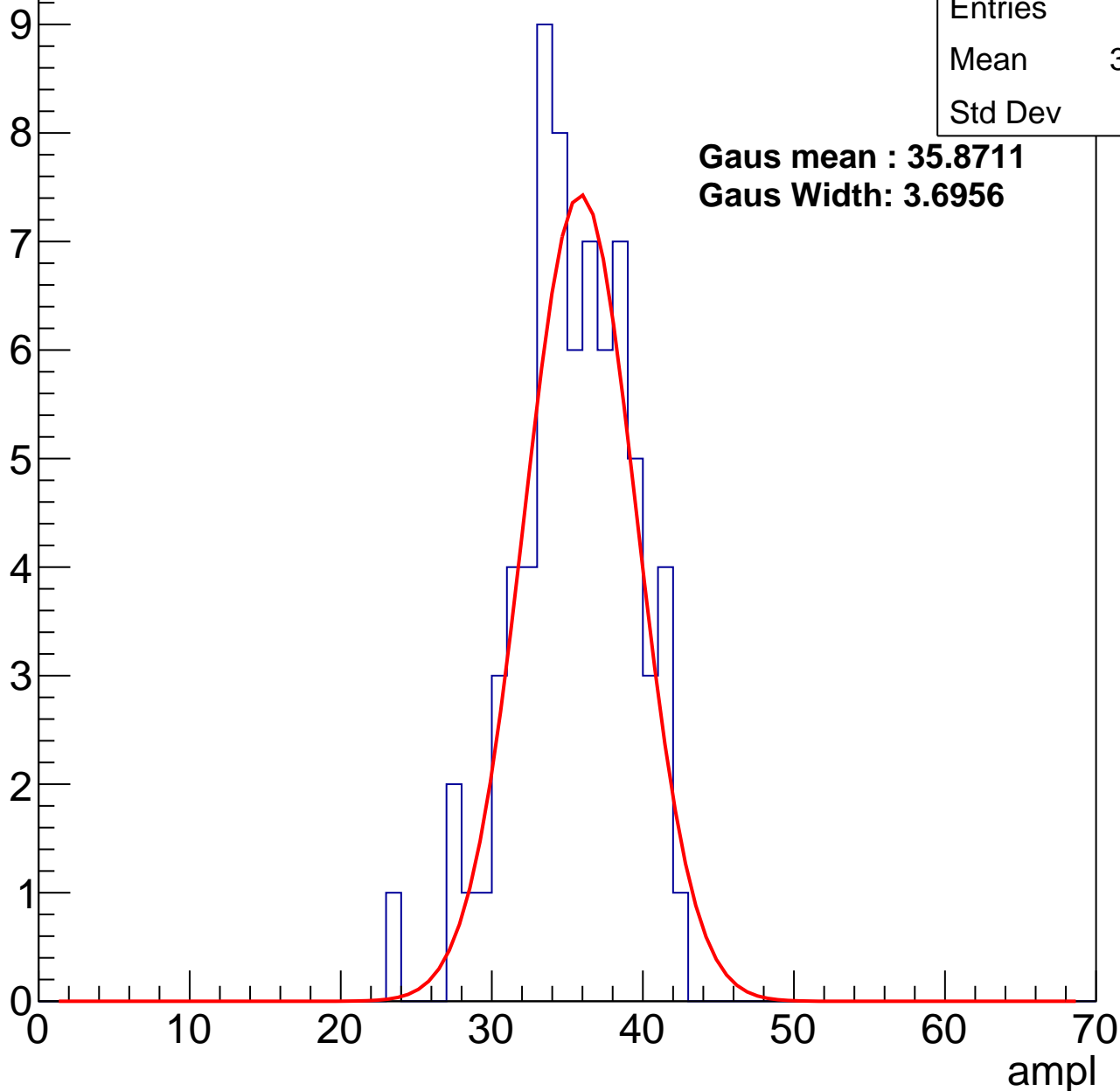
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	34.94
Std Dev	3.76

Gaus mean : 35.8711

Gaus Width: 3.6956



B1L101S, U1-ch67, adc2

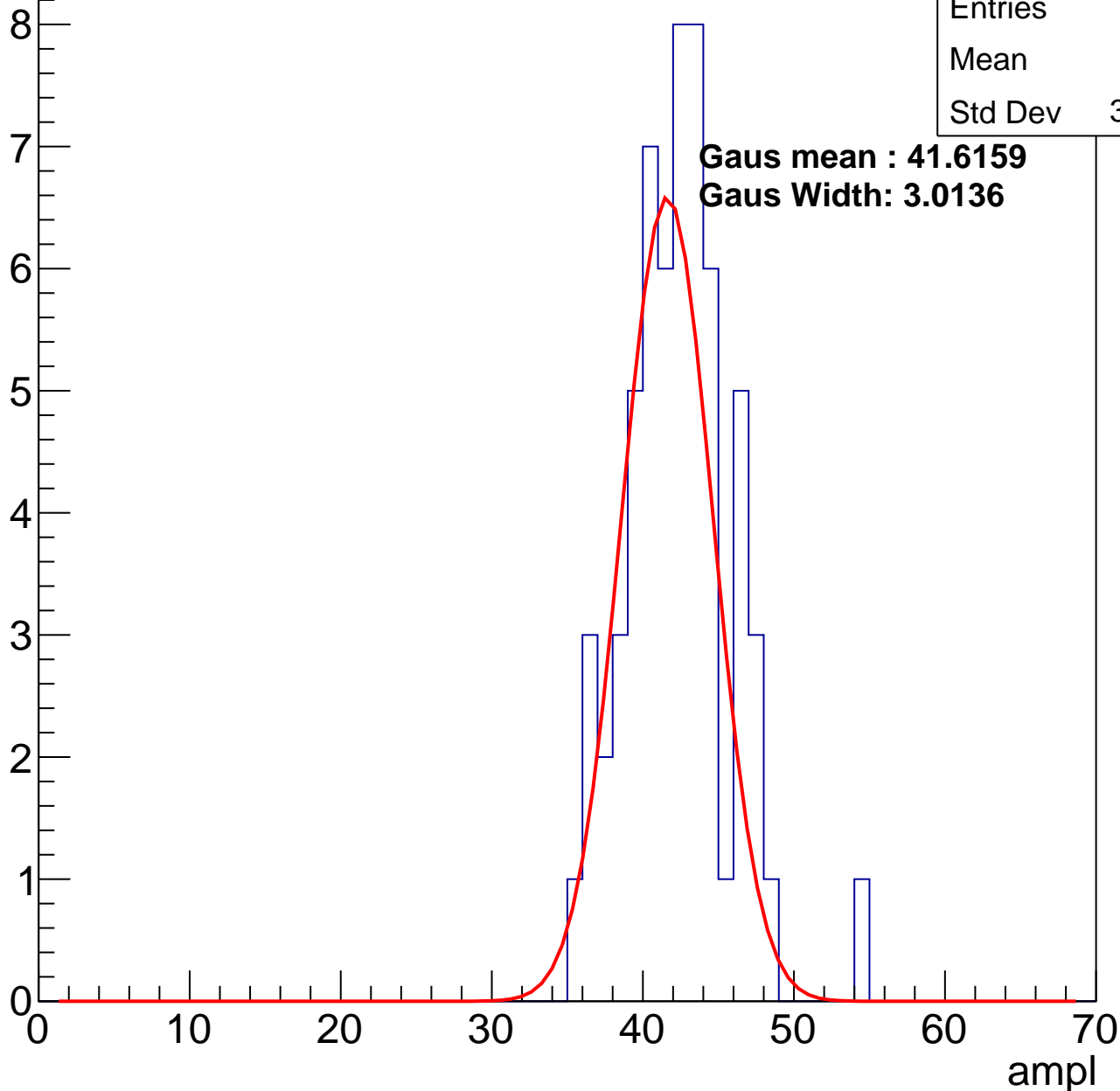
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.9
Std Dev	3.439

Gaus mean : 41.6159

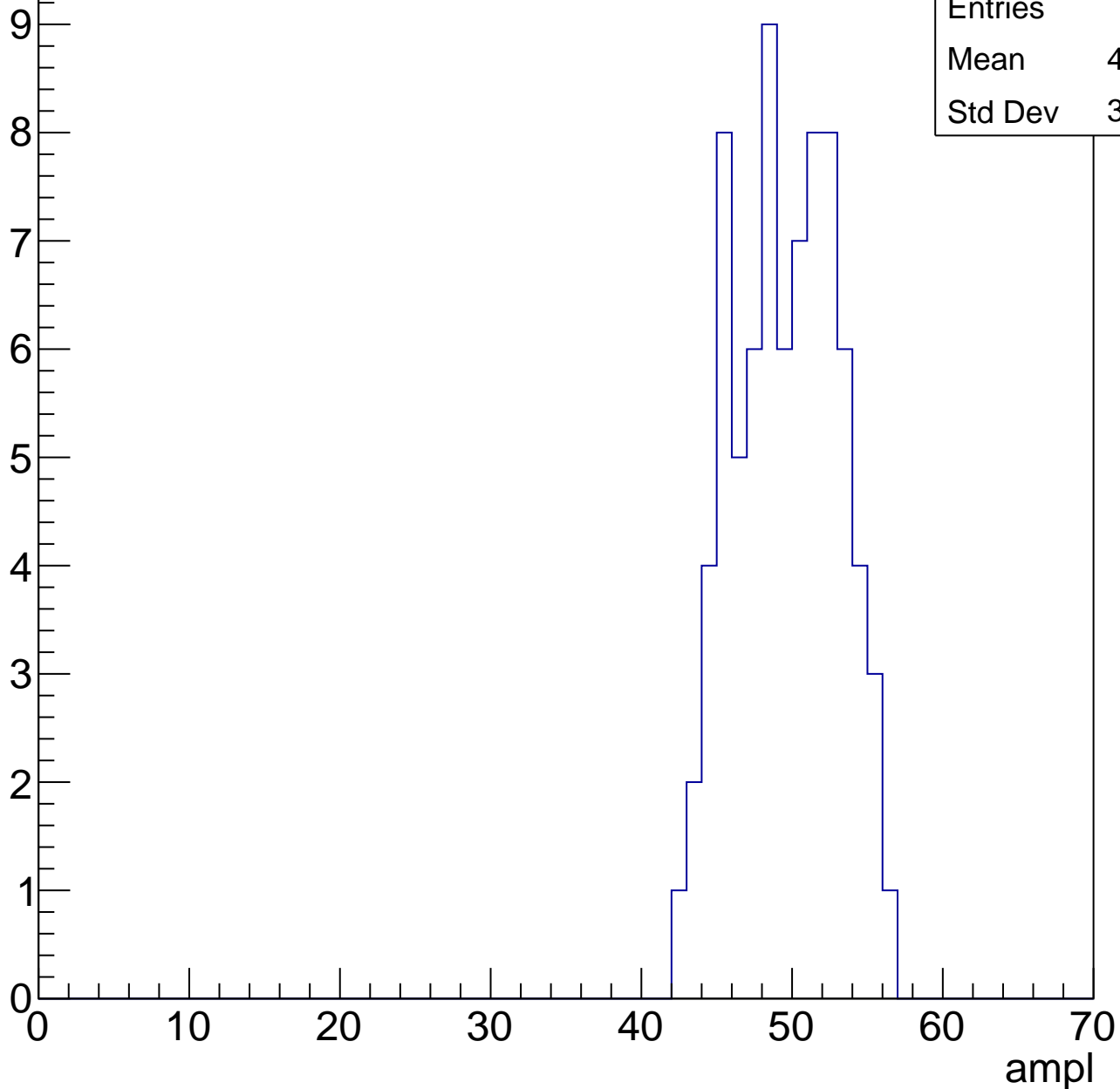
Gaus Width: 3.0136



B1L101S, U1-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



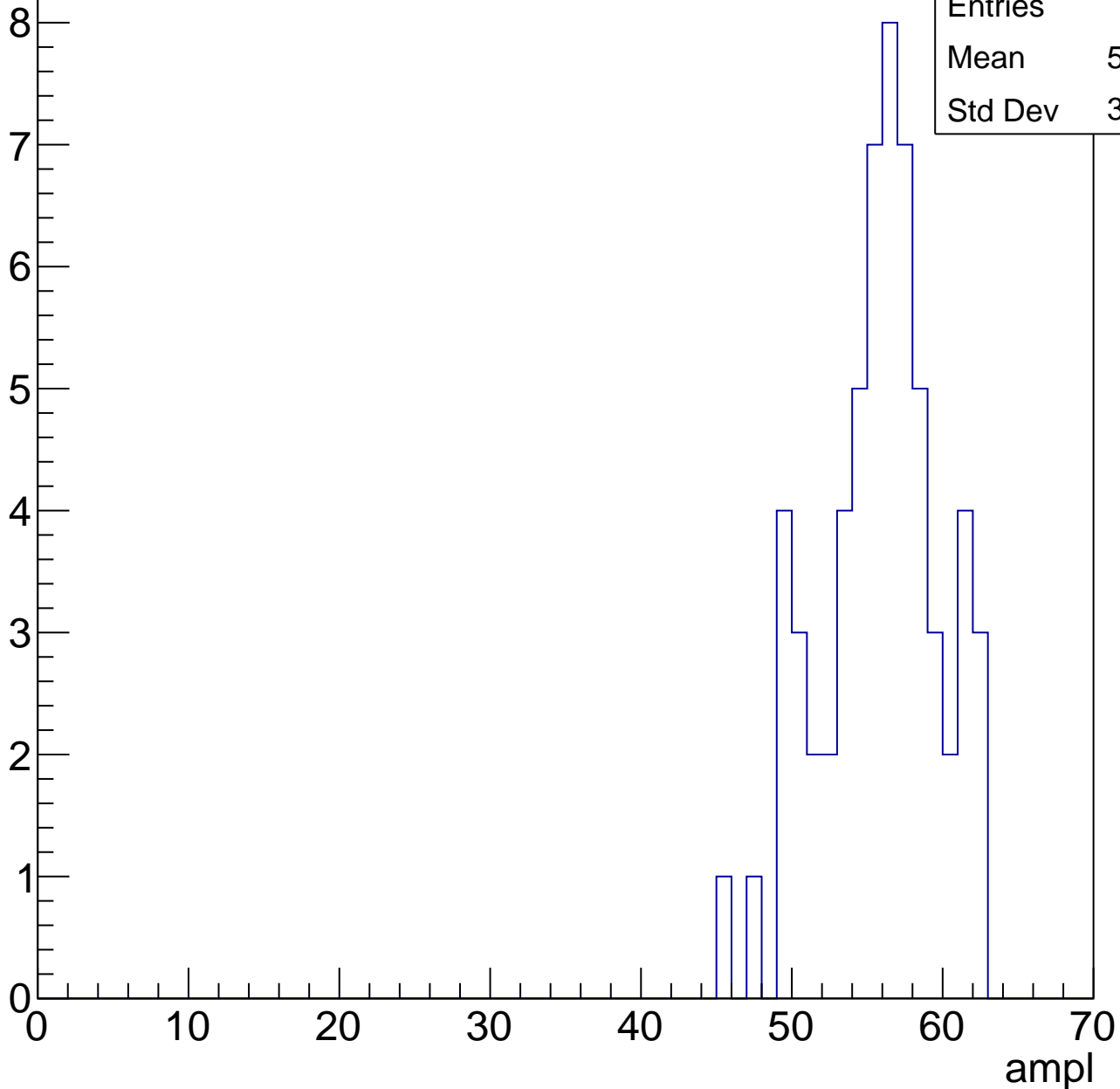
Entries	78
Mean	49.12
Std Dev	3.378

B1L101S, U1-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.33
Std Dev	3.878



B1L101S, U1-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

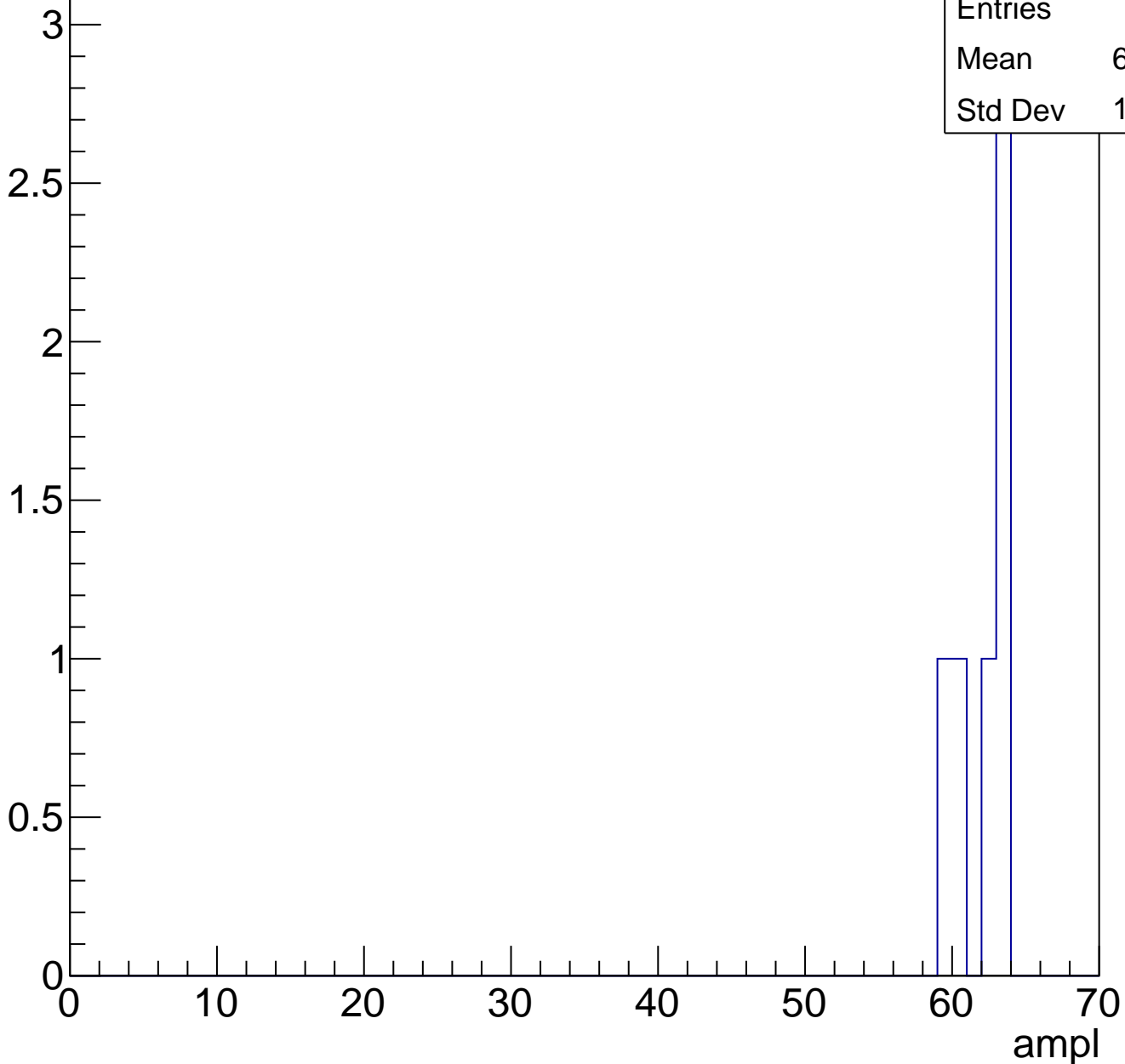
Entries	42
Mean	58.81
Std Dev	9.389

ampl

B1L101S, U1-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch68, adc0

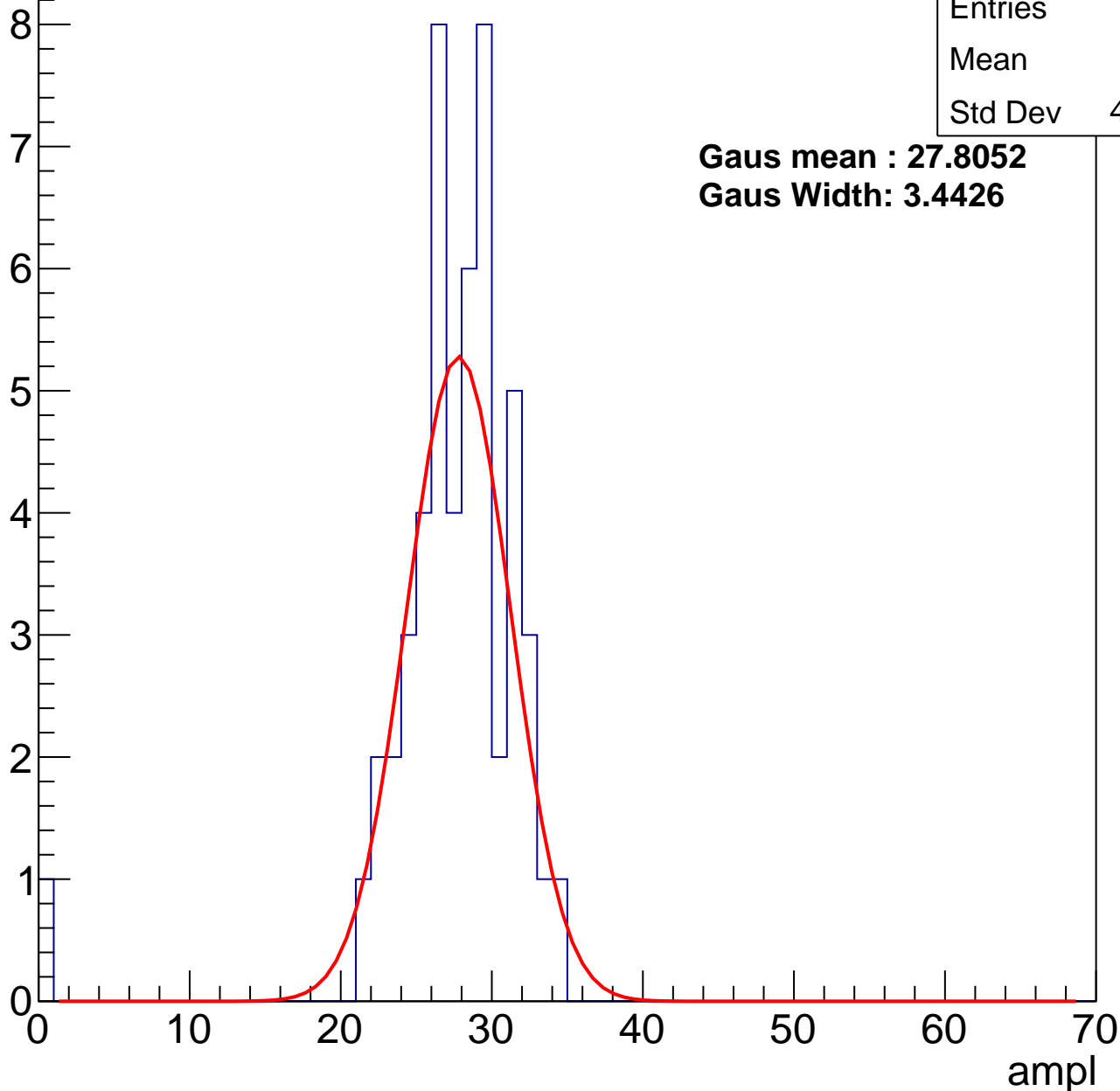
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	27
Std Dev	4.839

Gaus mean : 27.8052

Gaus Width: 3.4426



B1L101S, U1-ch68, adc1

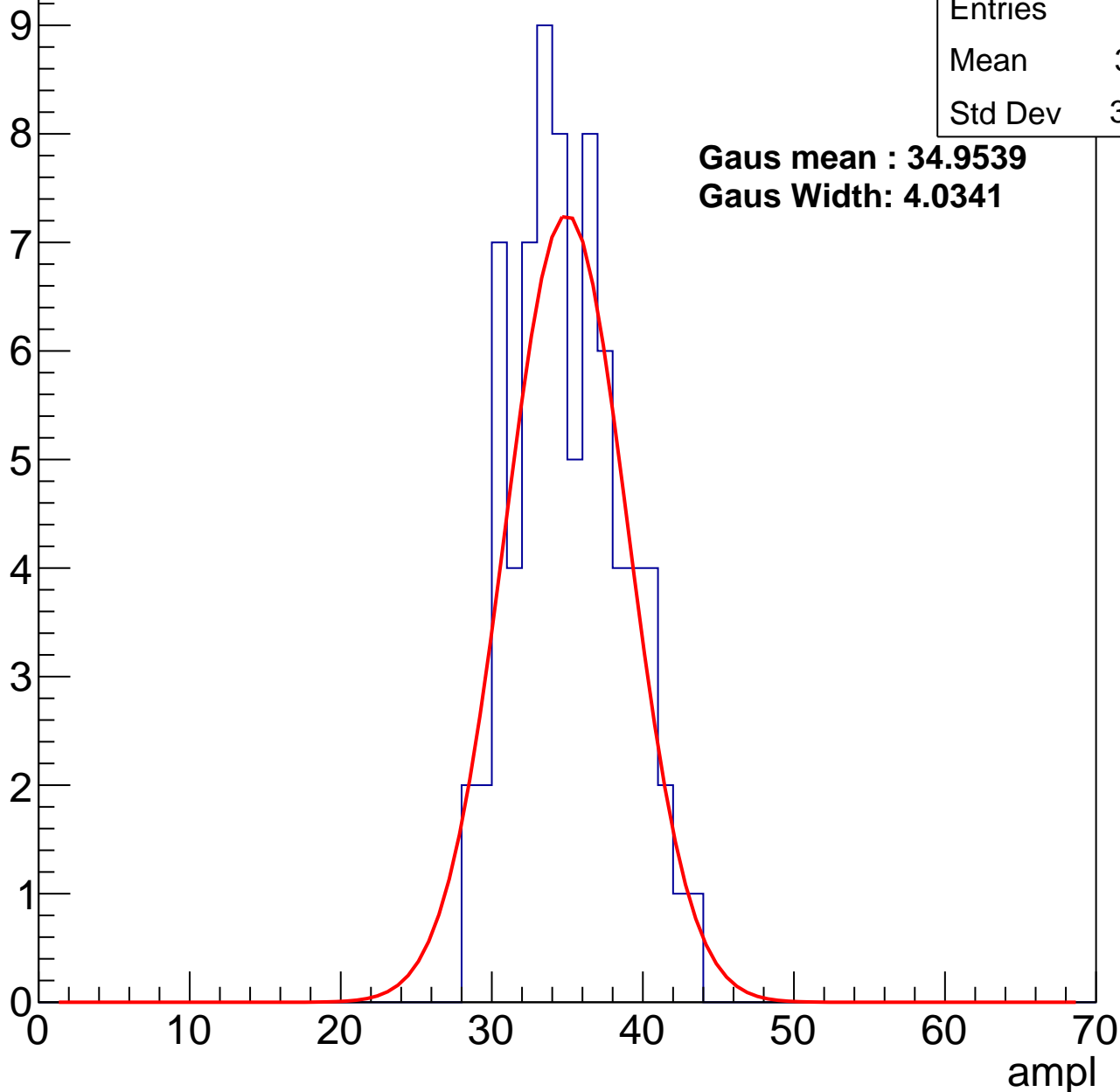
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.61
Std Dev	3.529

Gaus mean : 34.9539

Gaus Width: 4.0341



B1L101S, U1-ch68, adc2

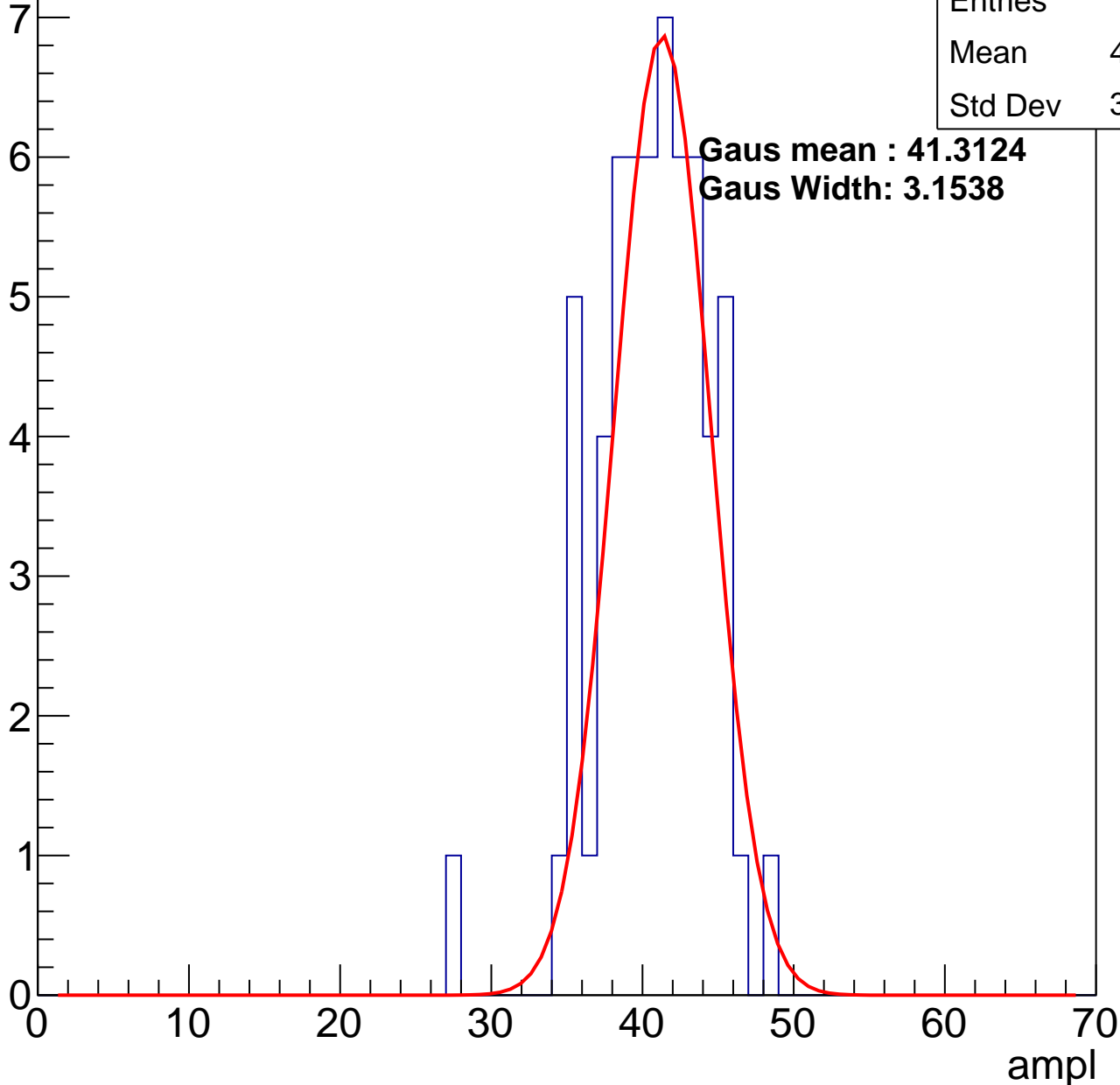
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	40.23
Std Dev	3.612

Gaus mean : 41.3124

Gaus Width: 3.1538

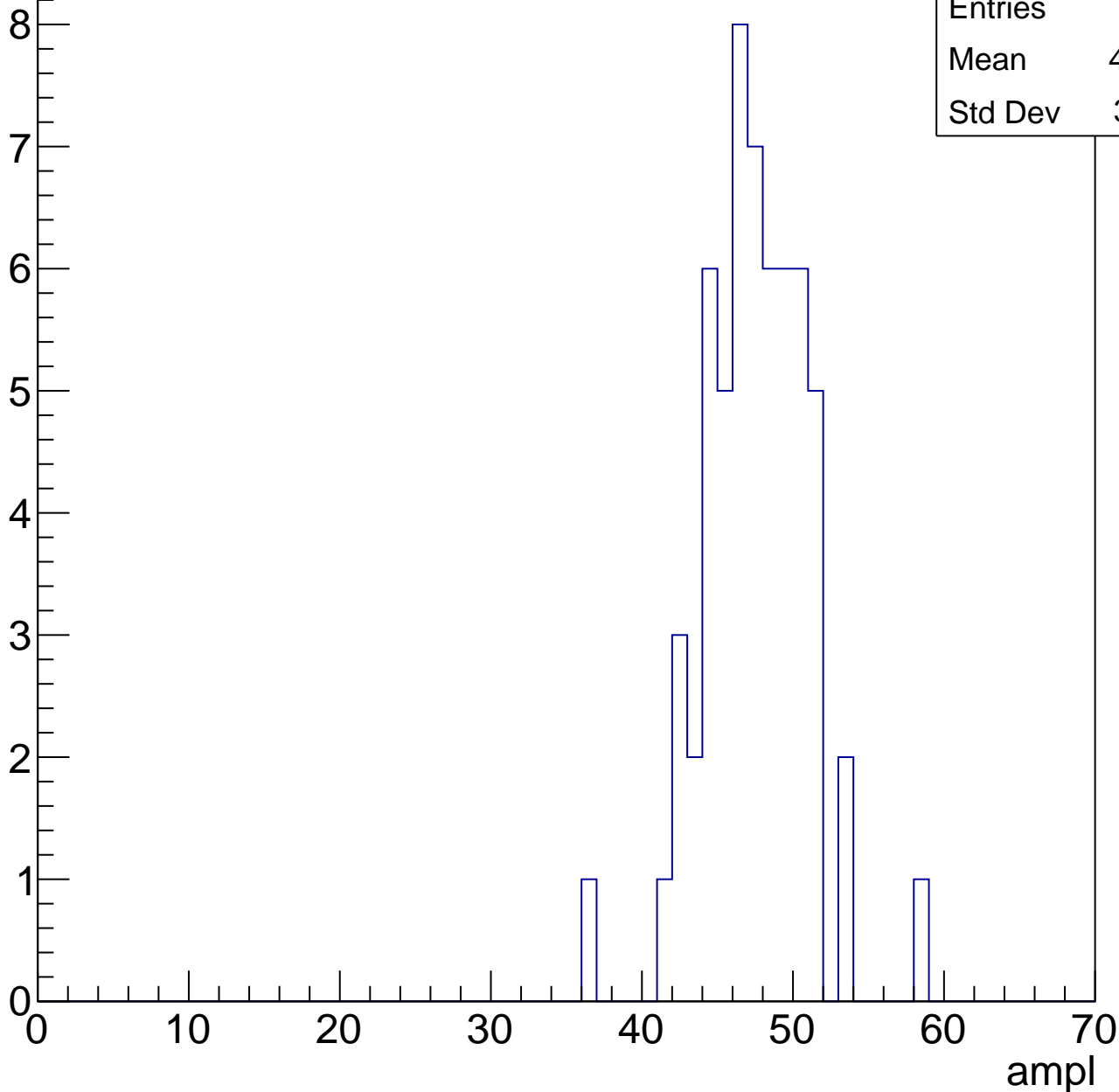


B1L101S, U1-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

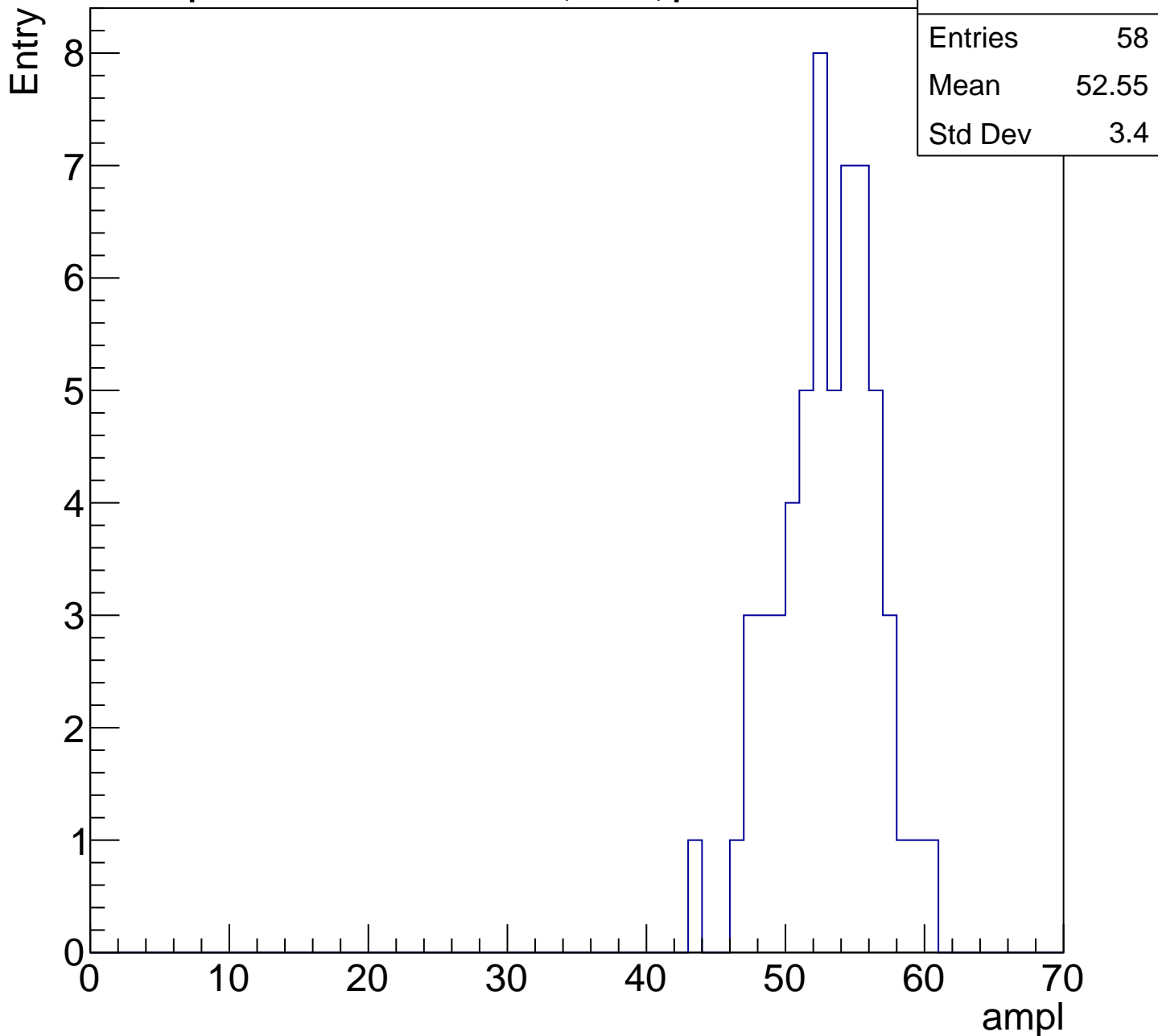
Entry

Entries	59
Mean	47.05
Std Dev	3.451



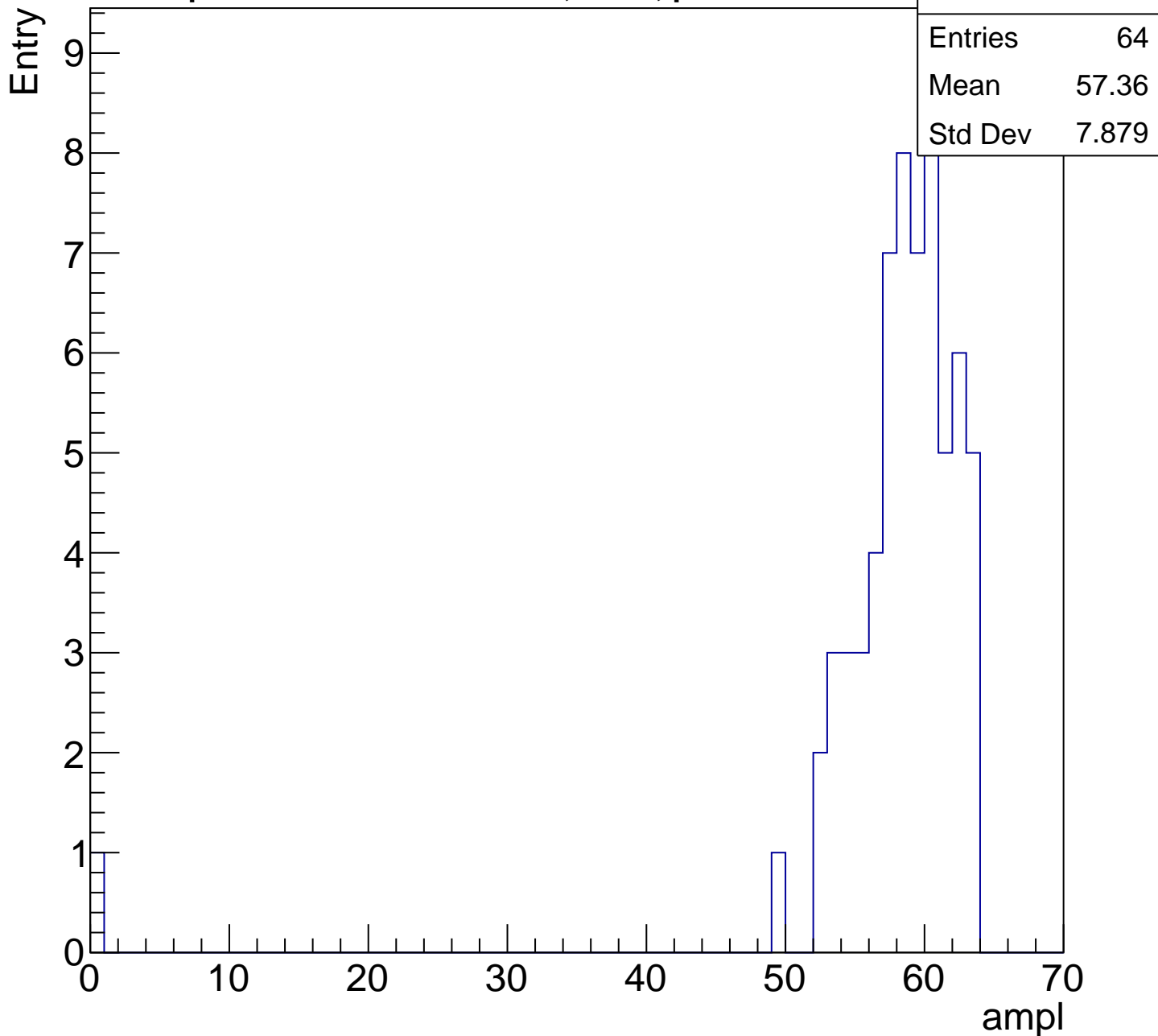
B1L101S, U1-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

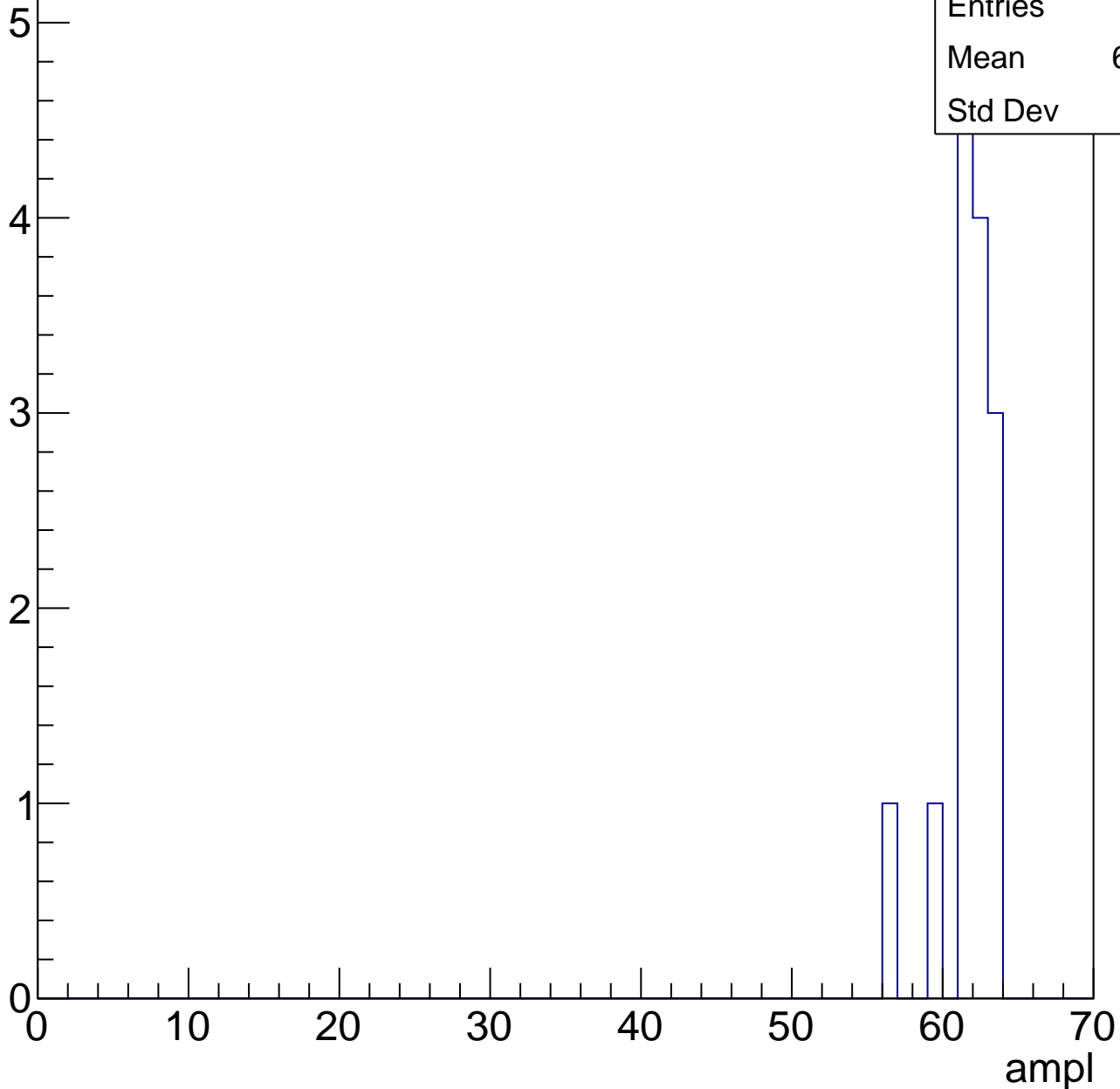


B1L101S, U1-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

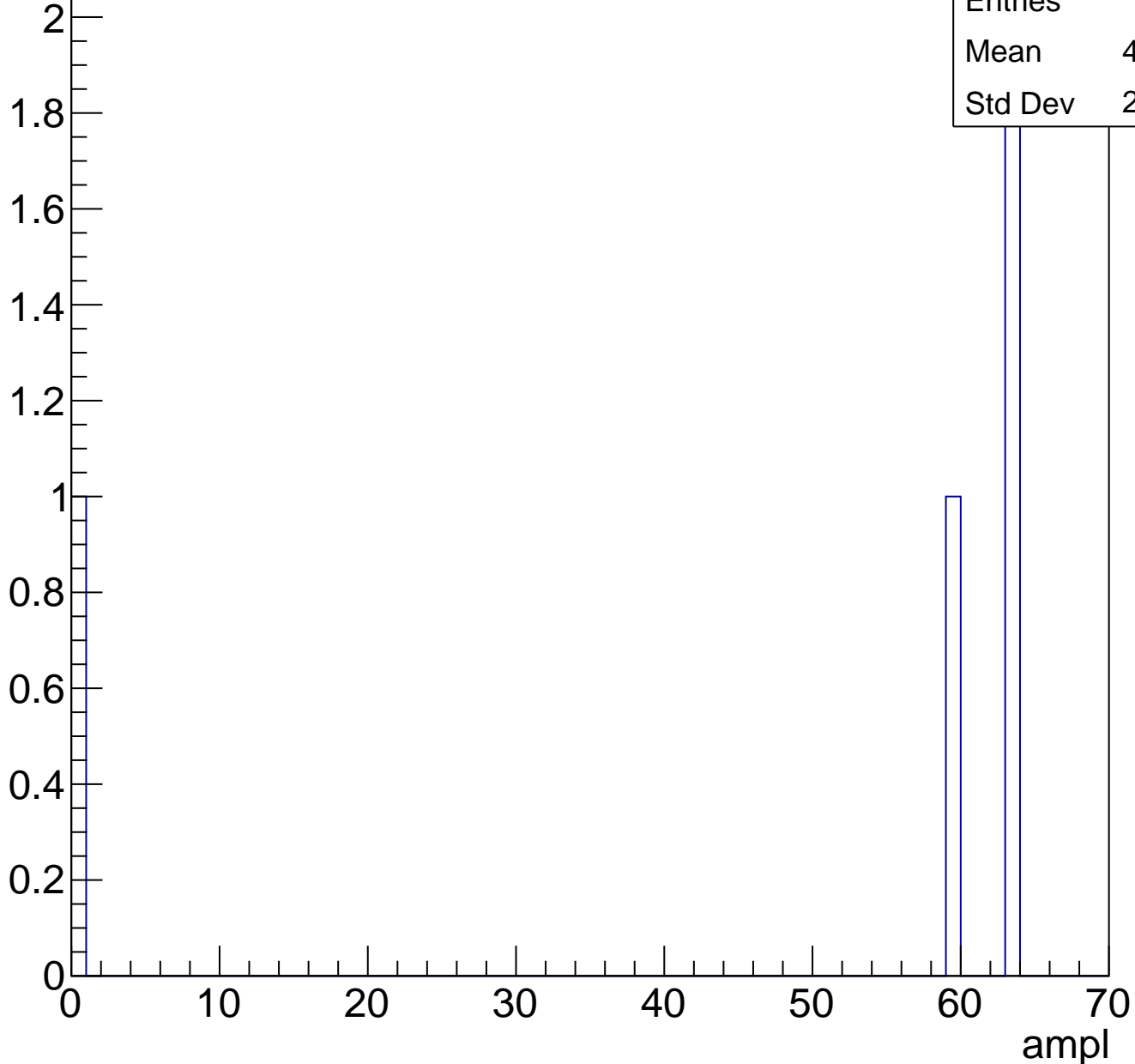
Entries	14
Mean	61.21
Std Dev	1.78



B1L101S, U1-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch69, adc0

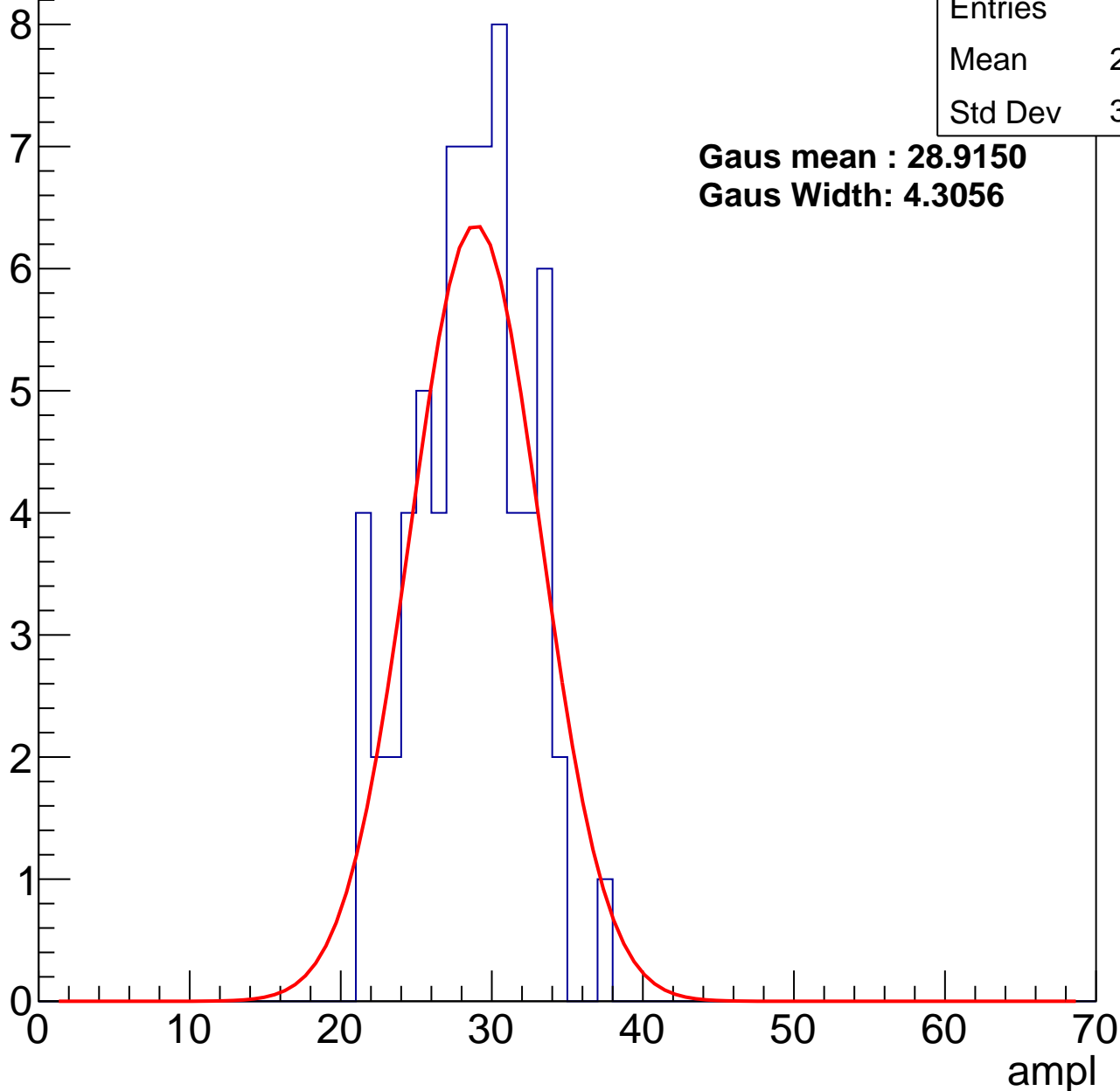
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.09
Std Dev	3.648

Gaus mean : 28.9150

Gaus Width: 4.3056



B1L101S, U1-ch69, adc1

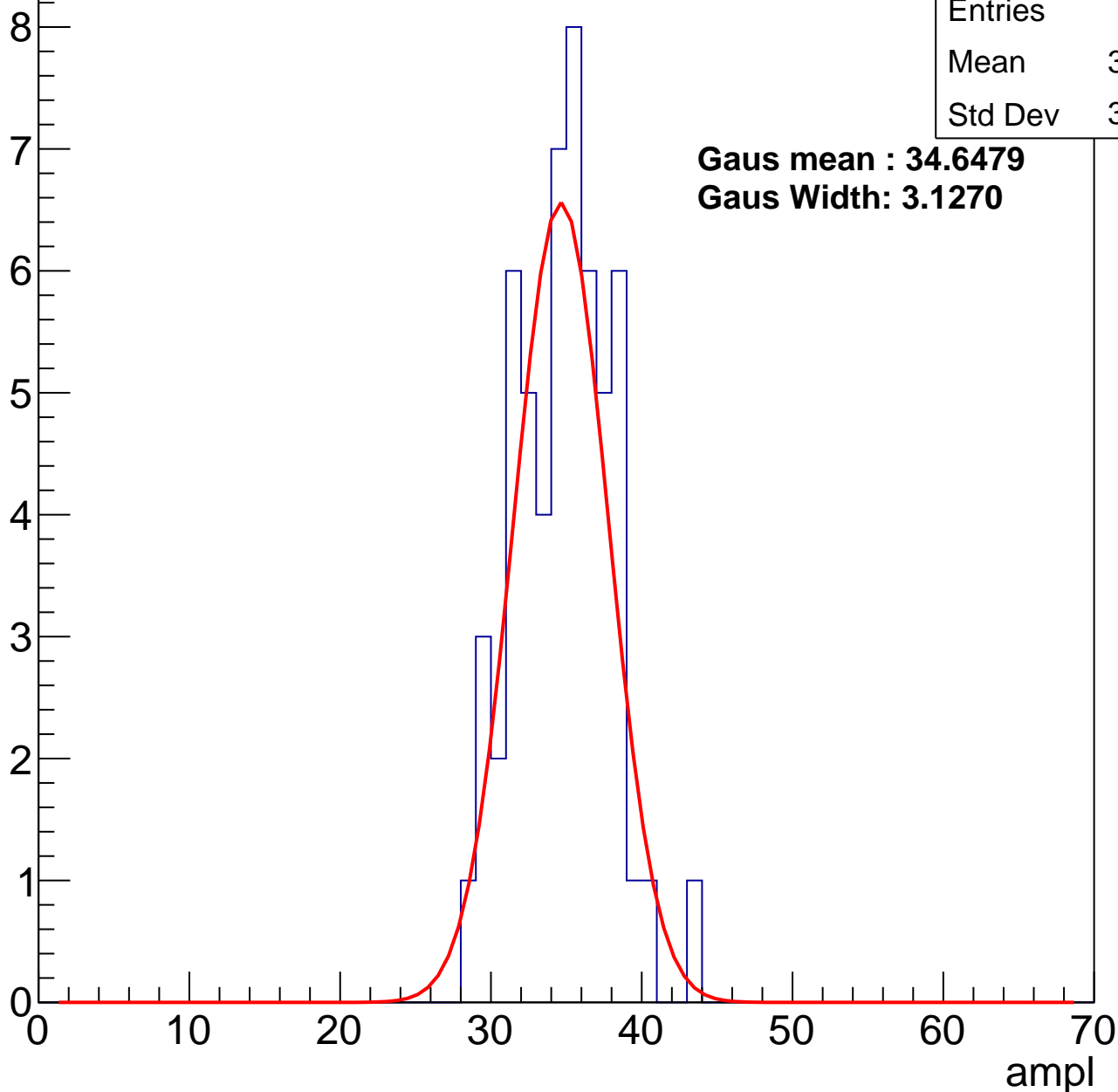
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	34.32
Std Dev	3.077

Gaus mean : 34.6479

Gaus Width: 3.1270



B1L101S, U1-ch69, adc2

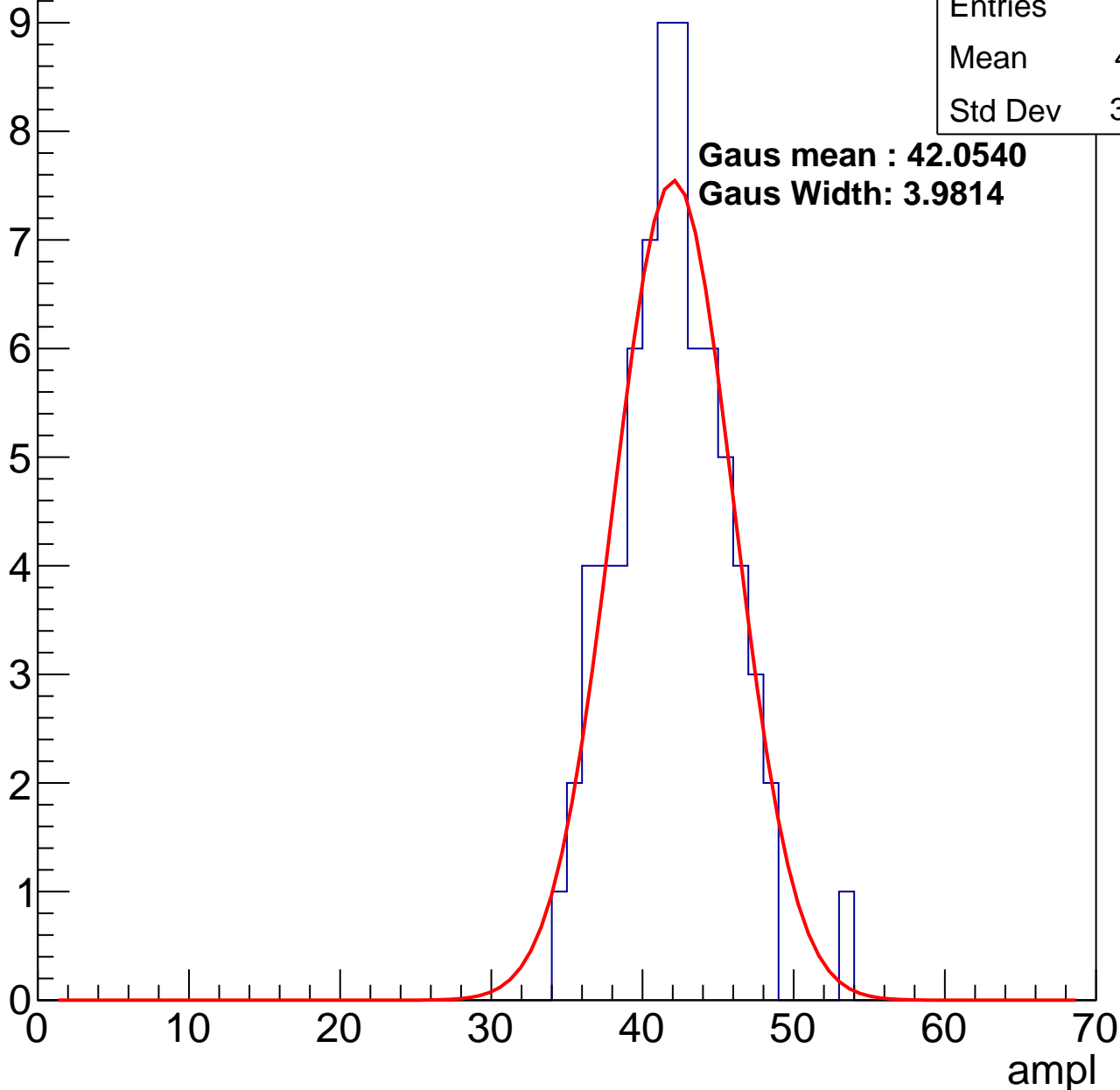
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.51
Std Dev	3.608

Gaus mean : 42.0540

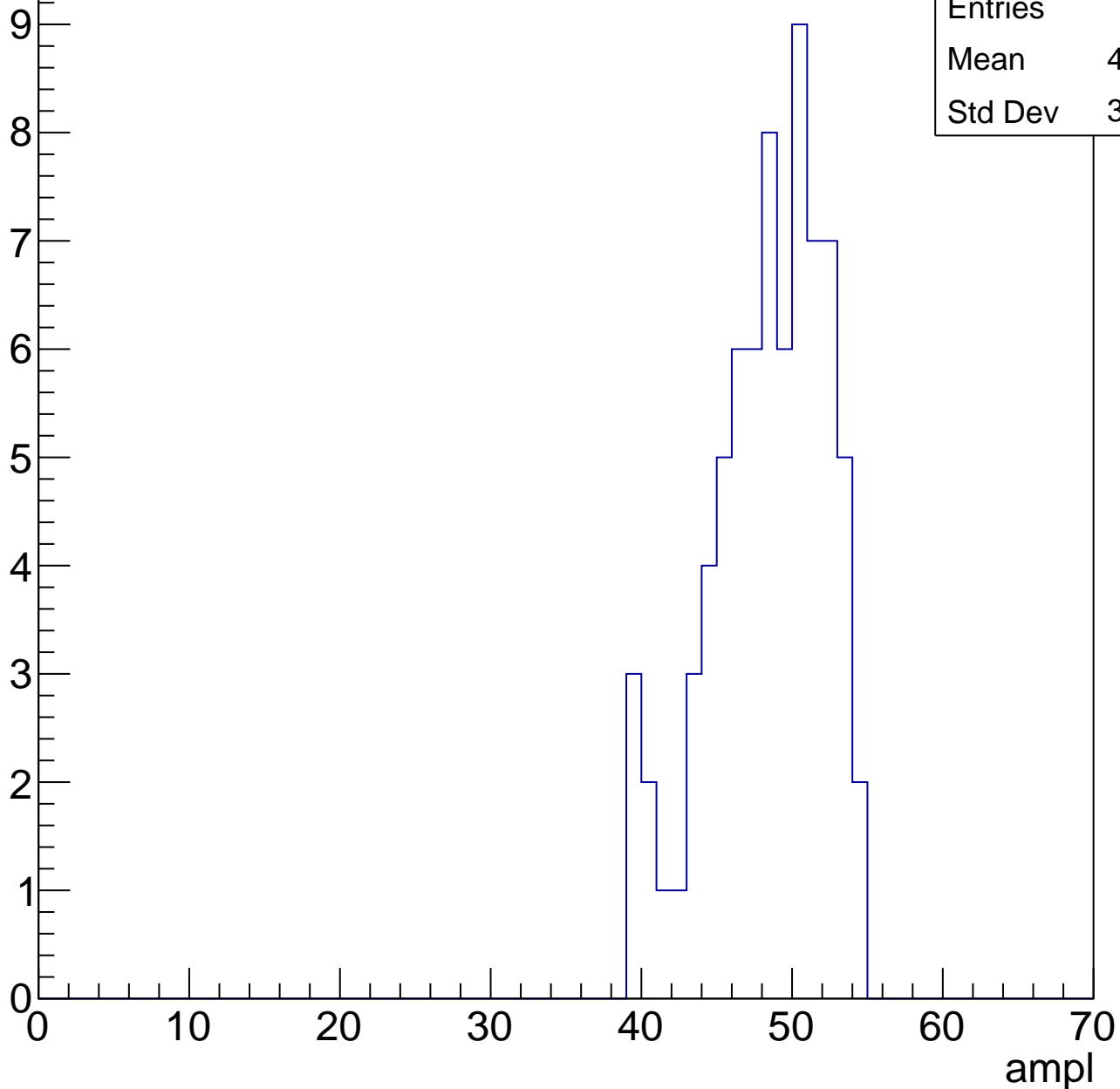
Gaus Width: 3.9814



B1L101S, U1-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

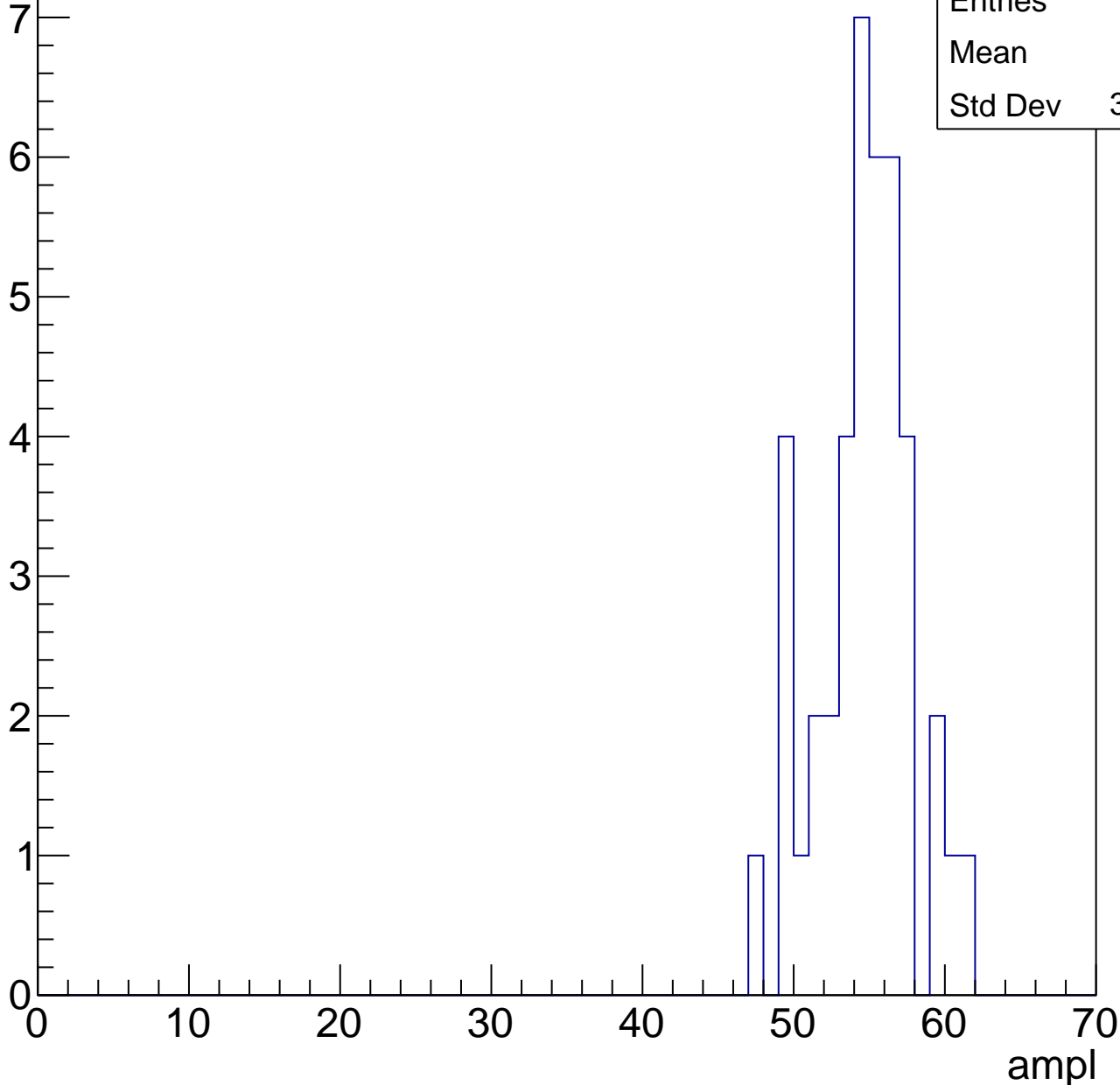


B1L101S, U1-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	54.2
Std Dev	3.102



B1L101S, U1-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	57.85
Std Dev	8.071

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

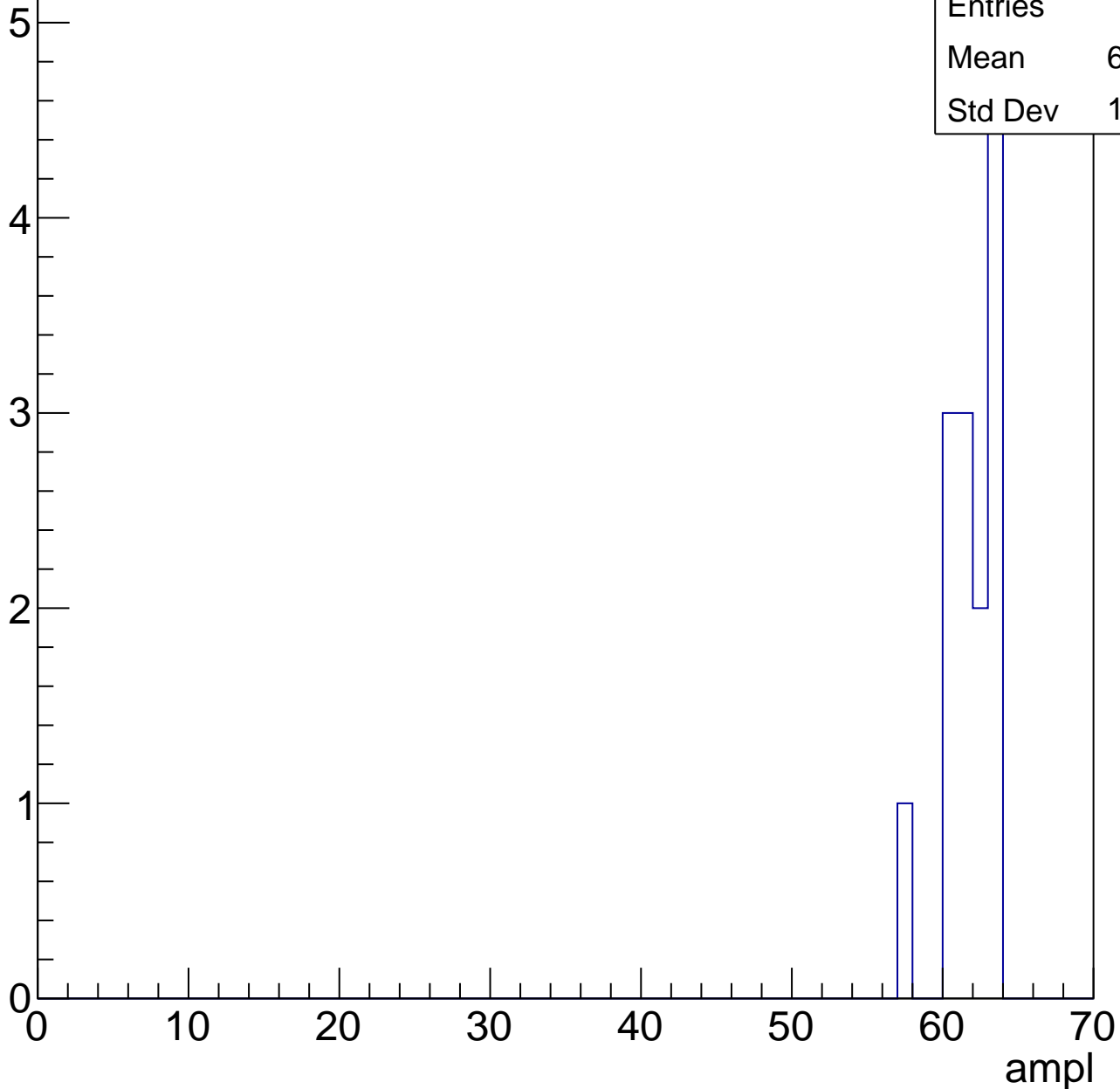
70

B1L101S, U1-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.36
Std Dev	1.674



B1L101S, U1-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U1-ch70, adc0

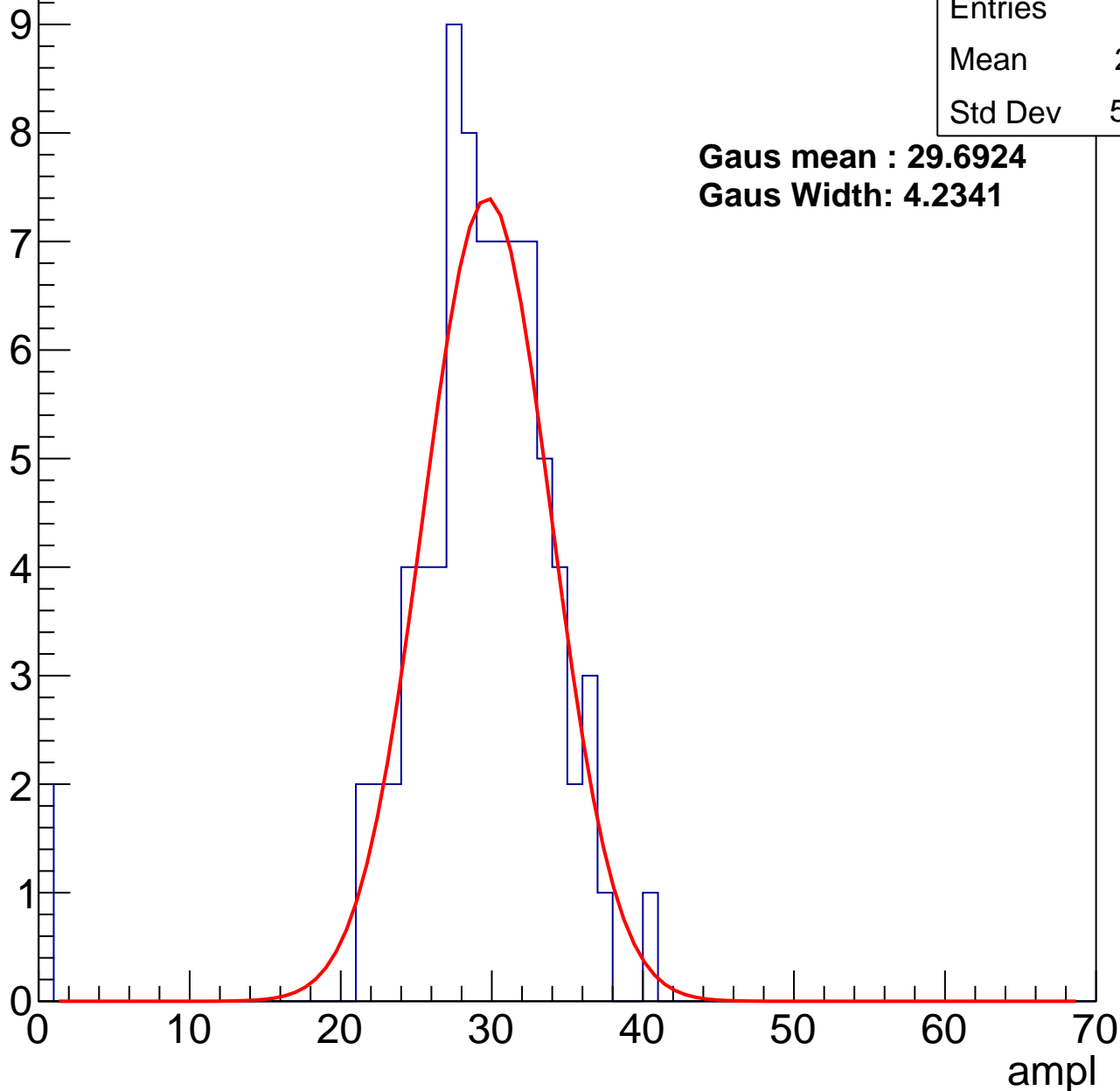
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.51
Std Dev	5.969

Gaus mean : 29.6924

Gaus Width: 4.2341



B1L101S, U1-ch70, adc1

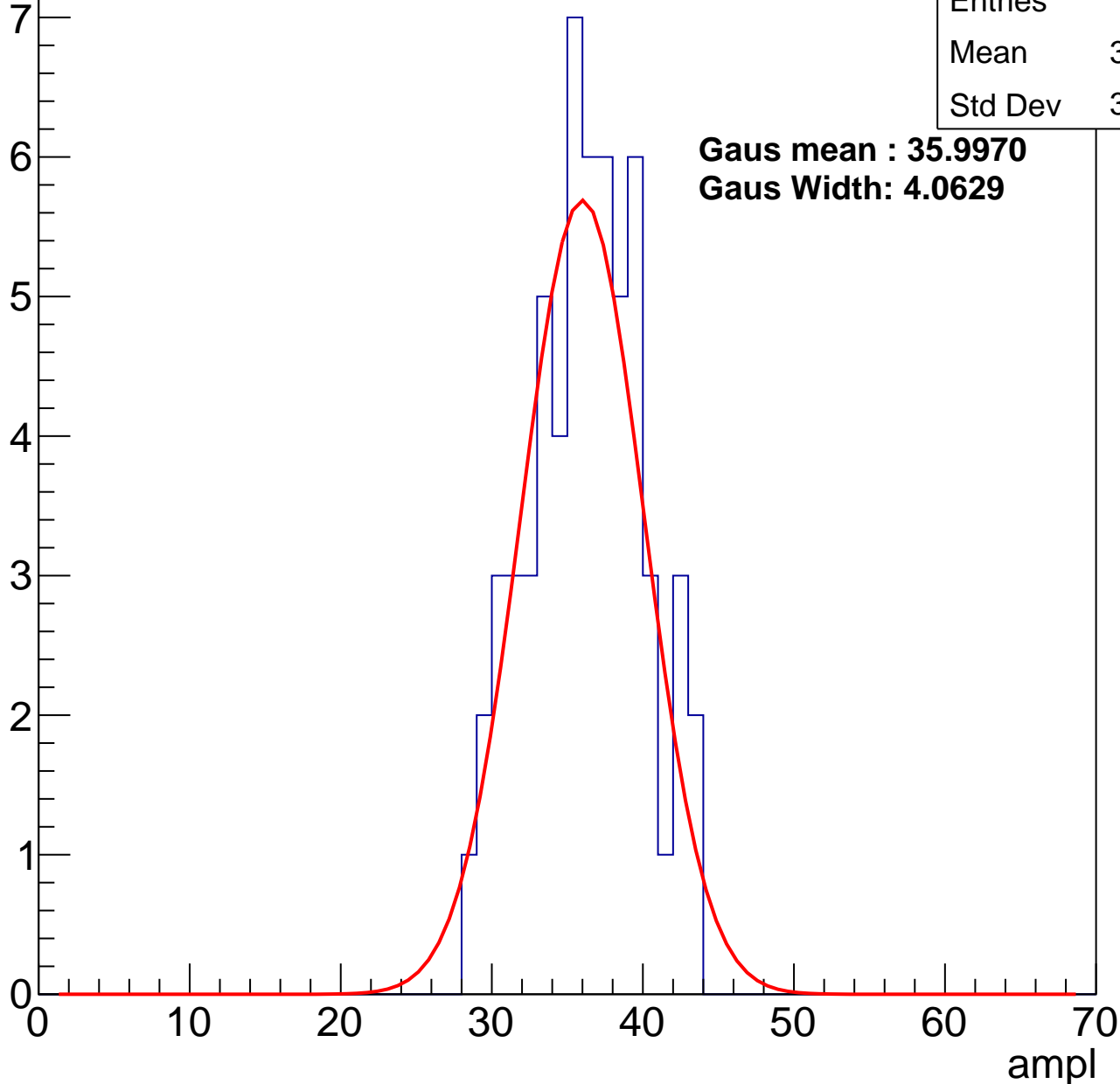
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.77
Std Dev	3.689

Gaus mean : 35.9970

Gaus Width: 4.0629



B1L101S, U1-ch70, adc2

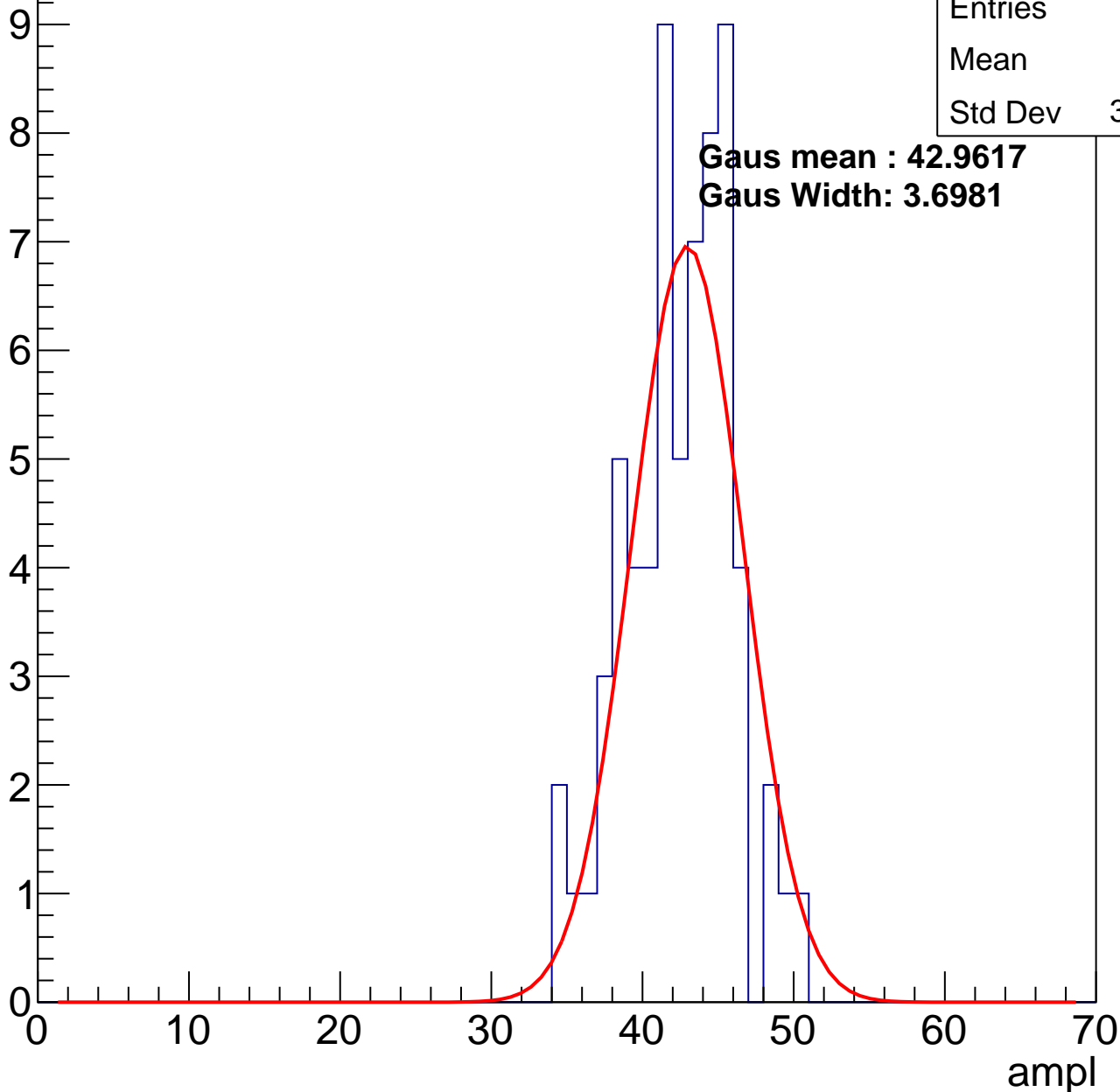
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42
Std Dev	3.477

Gaus mean : 42.9617

Gaus Width: 3.6981



B1L101S, U1-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

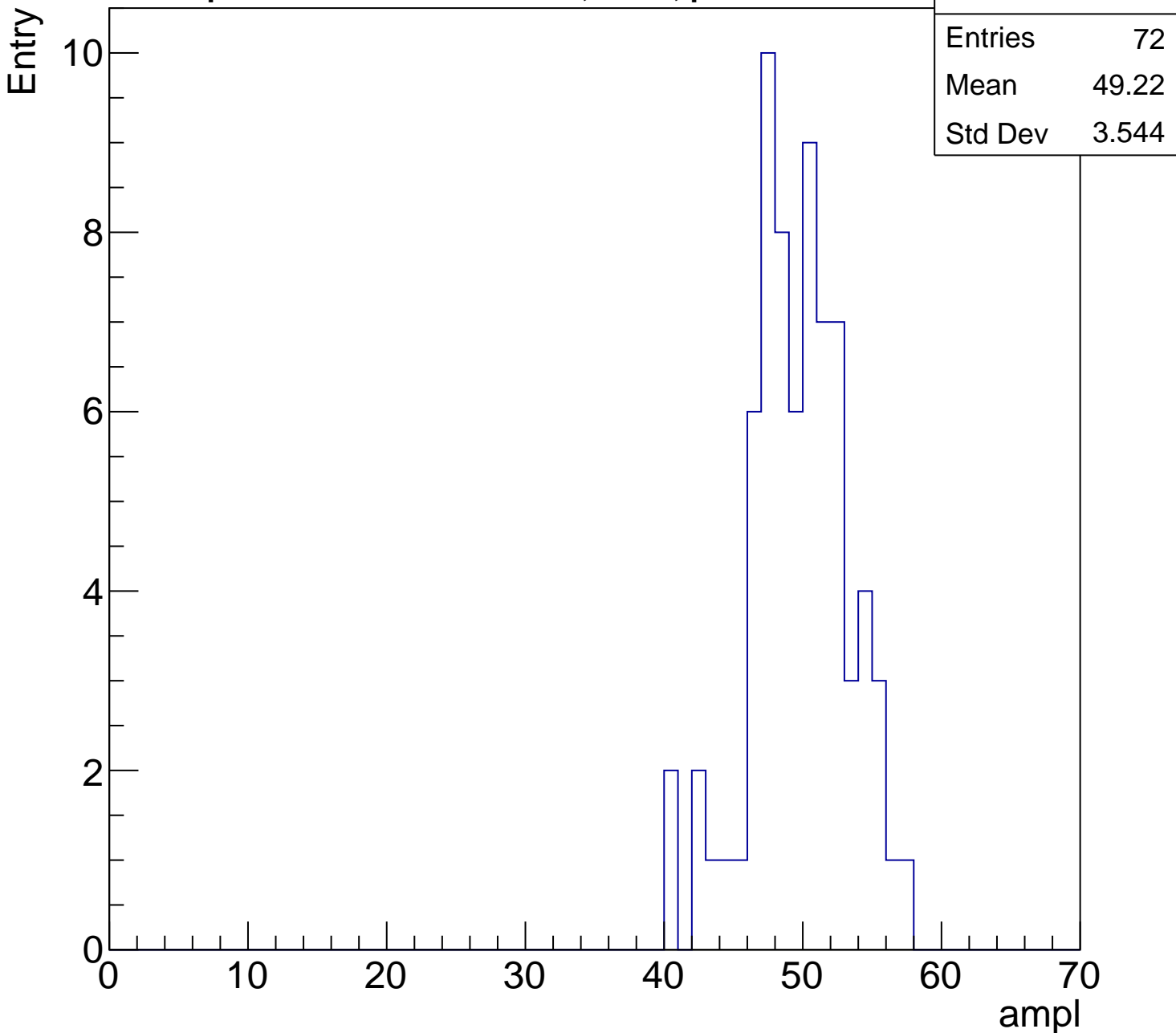
Entries	72
Mean	49.22
Std Dev	3.544

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

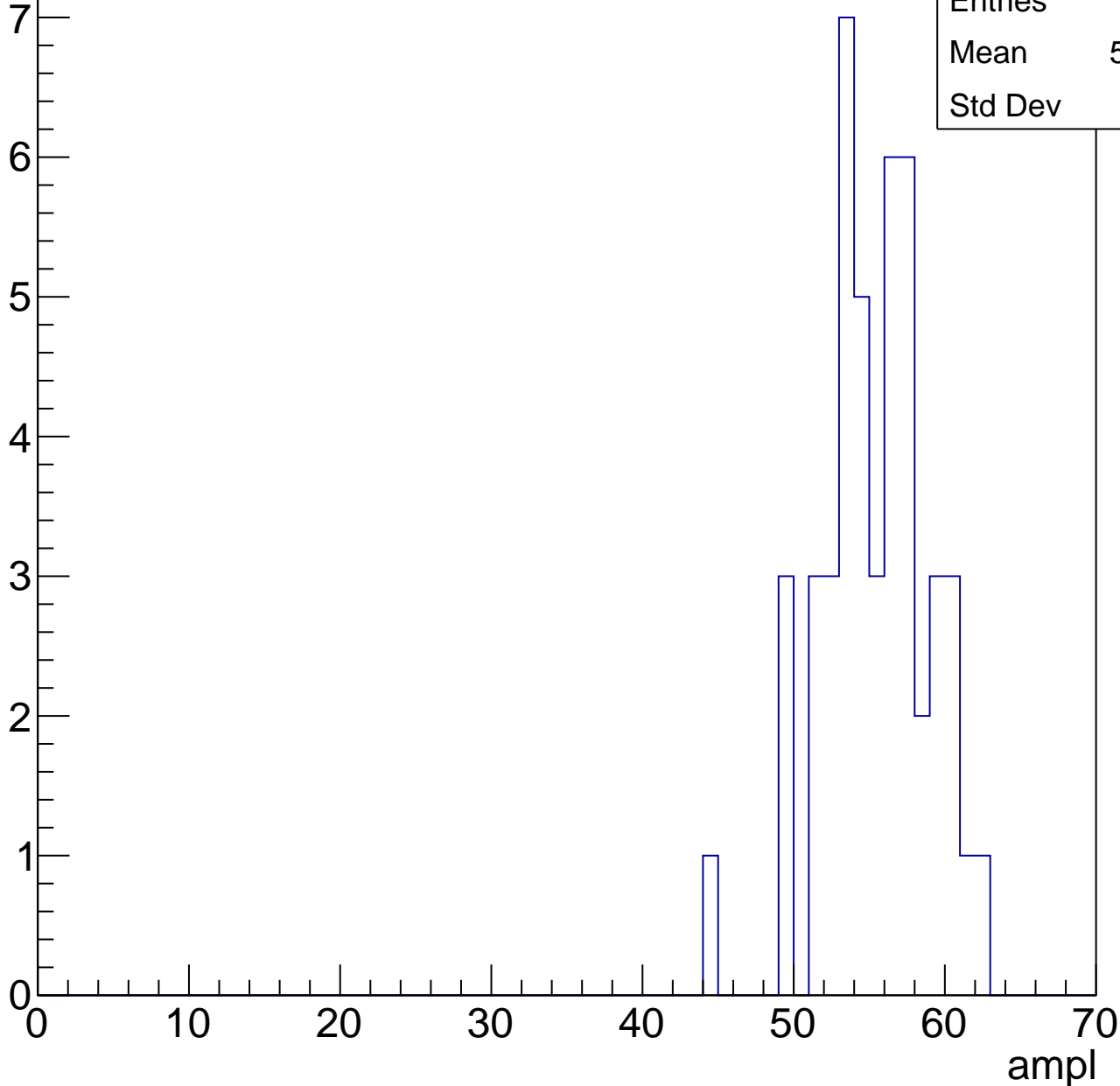


B1L101S, U1-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

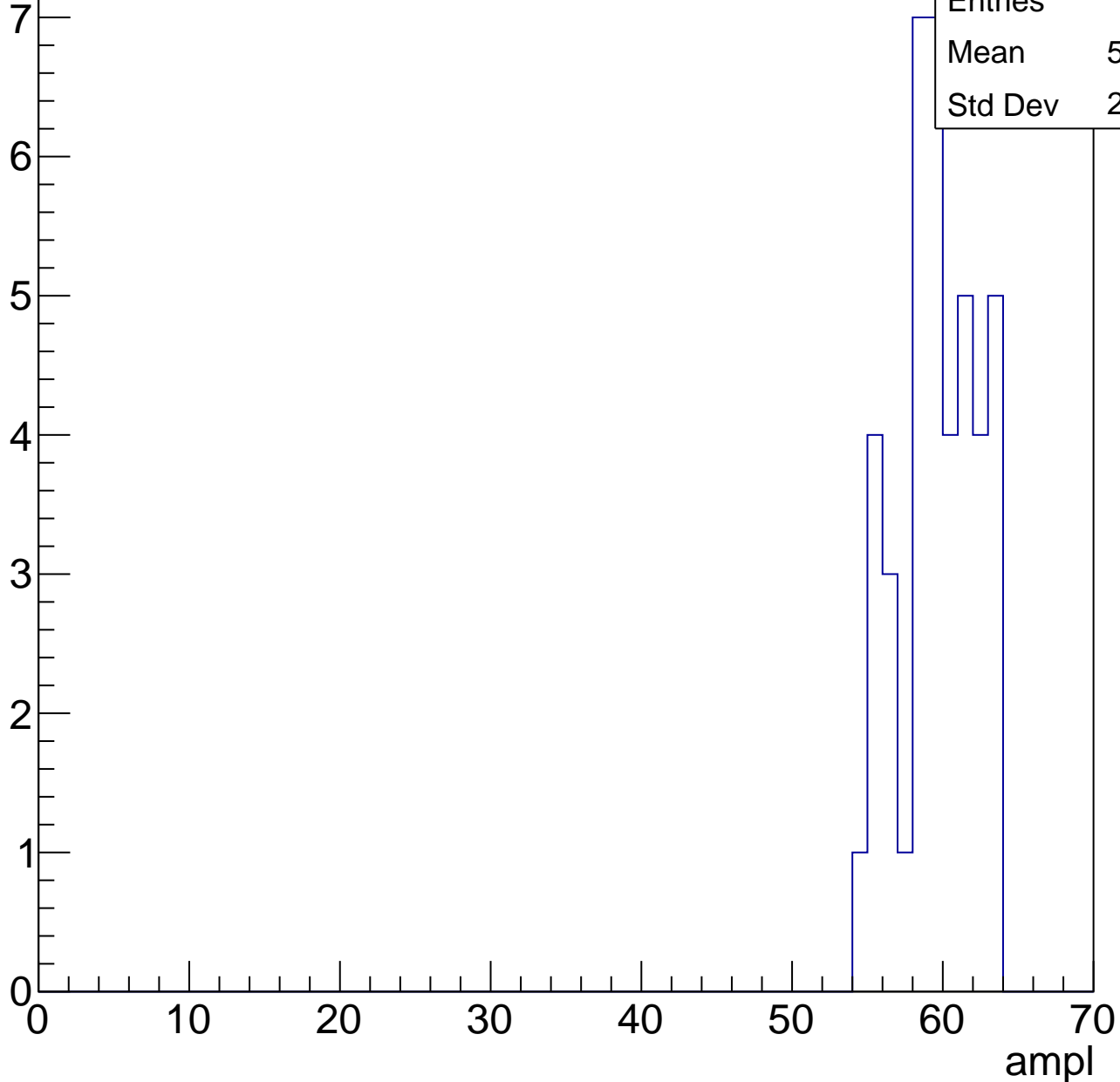
Entries	47
Mean	54.89
Std Dev	3.55



B1L101S, U1-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

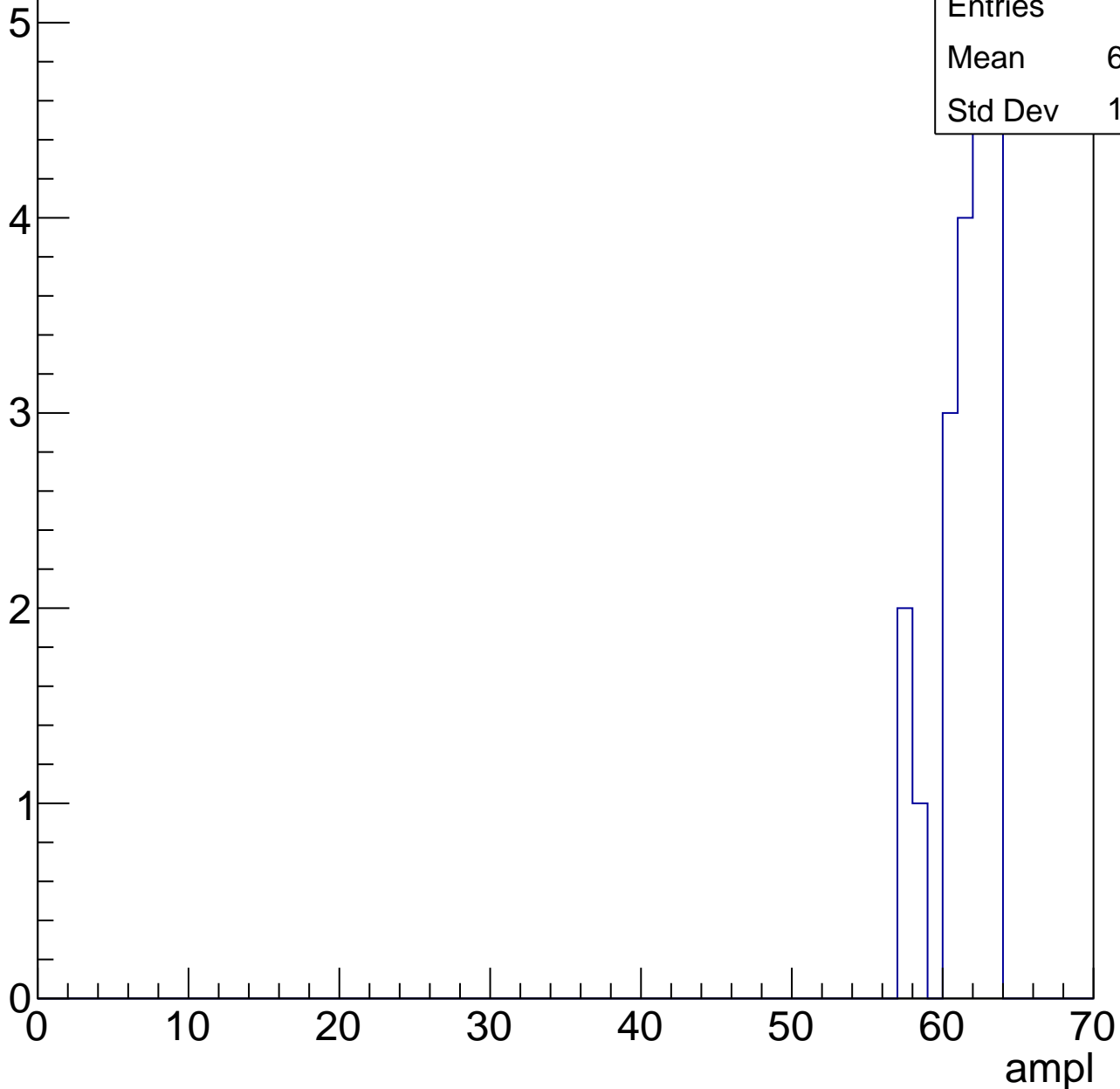


B1L101S, U1-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

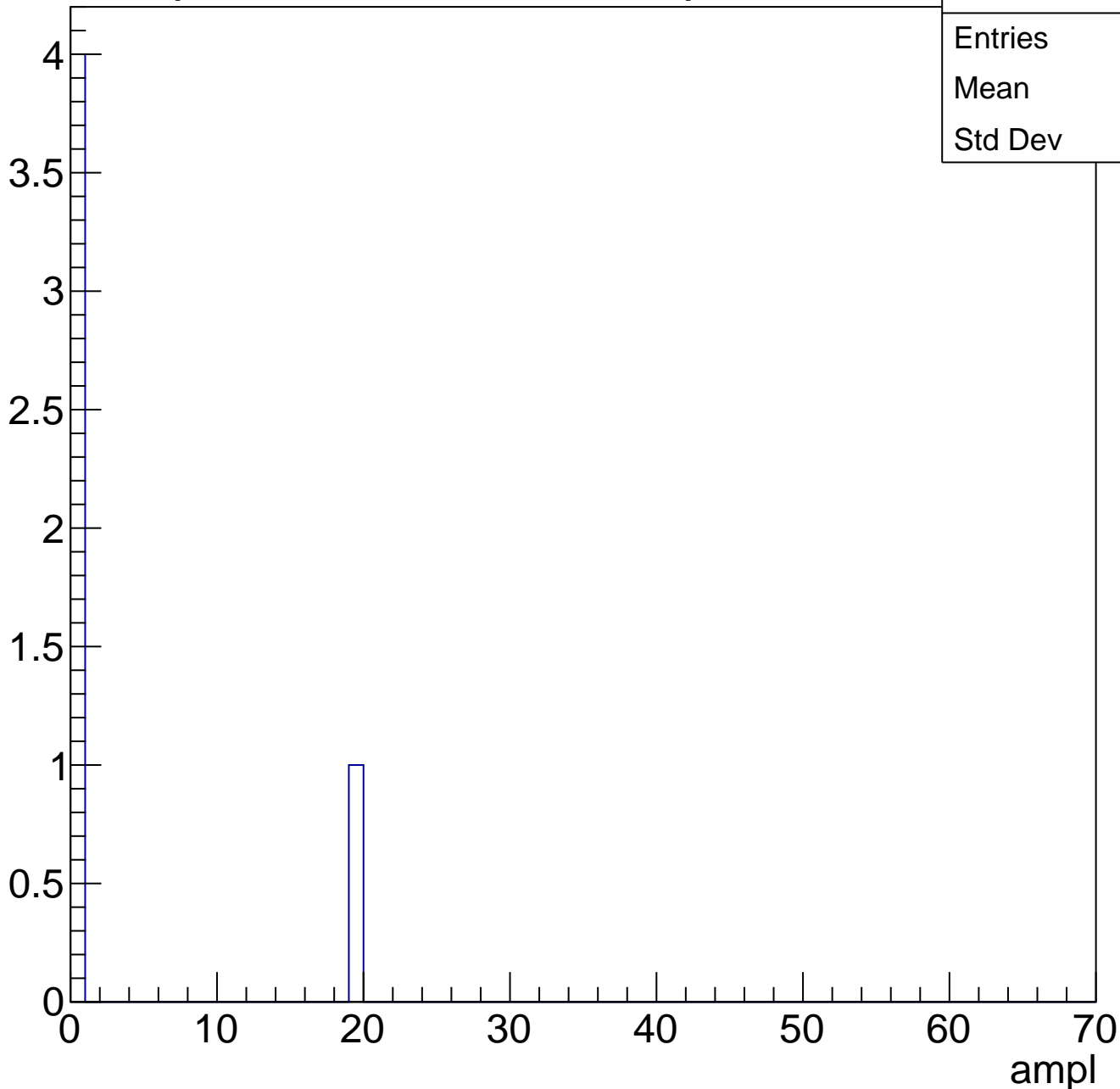
Entries	20
Mean	61.05
Std Dev	1.857



B1L101S, U1-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	3.8
Std Dev	7.6

B1L101S, U1-ch71, adc0

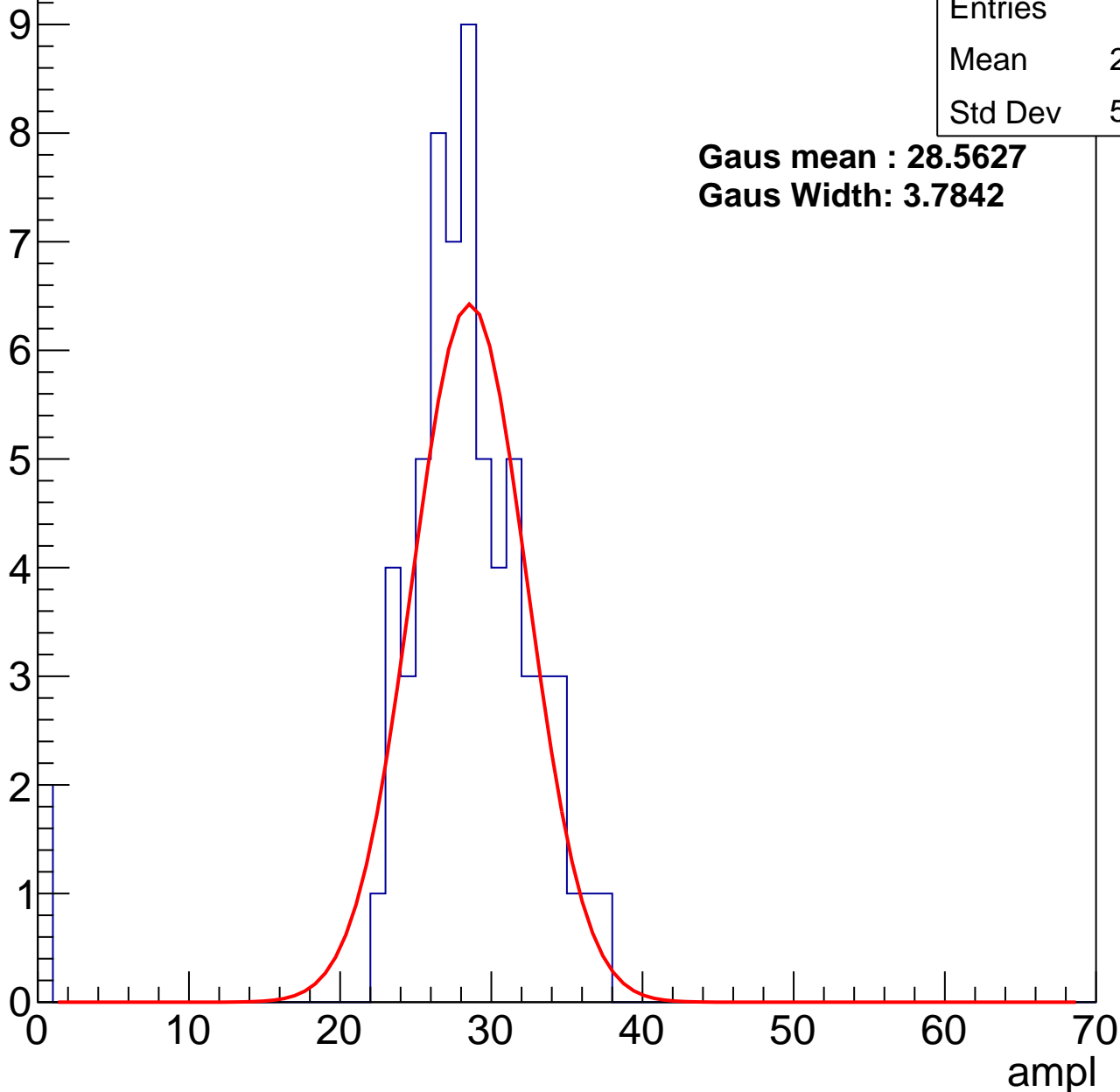
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.46
Std Dev	5.959

Gaus mean : 28.5627

Gaus Width: 3.7842



B1L101S, U1-ch71, adc1

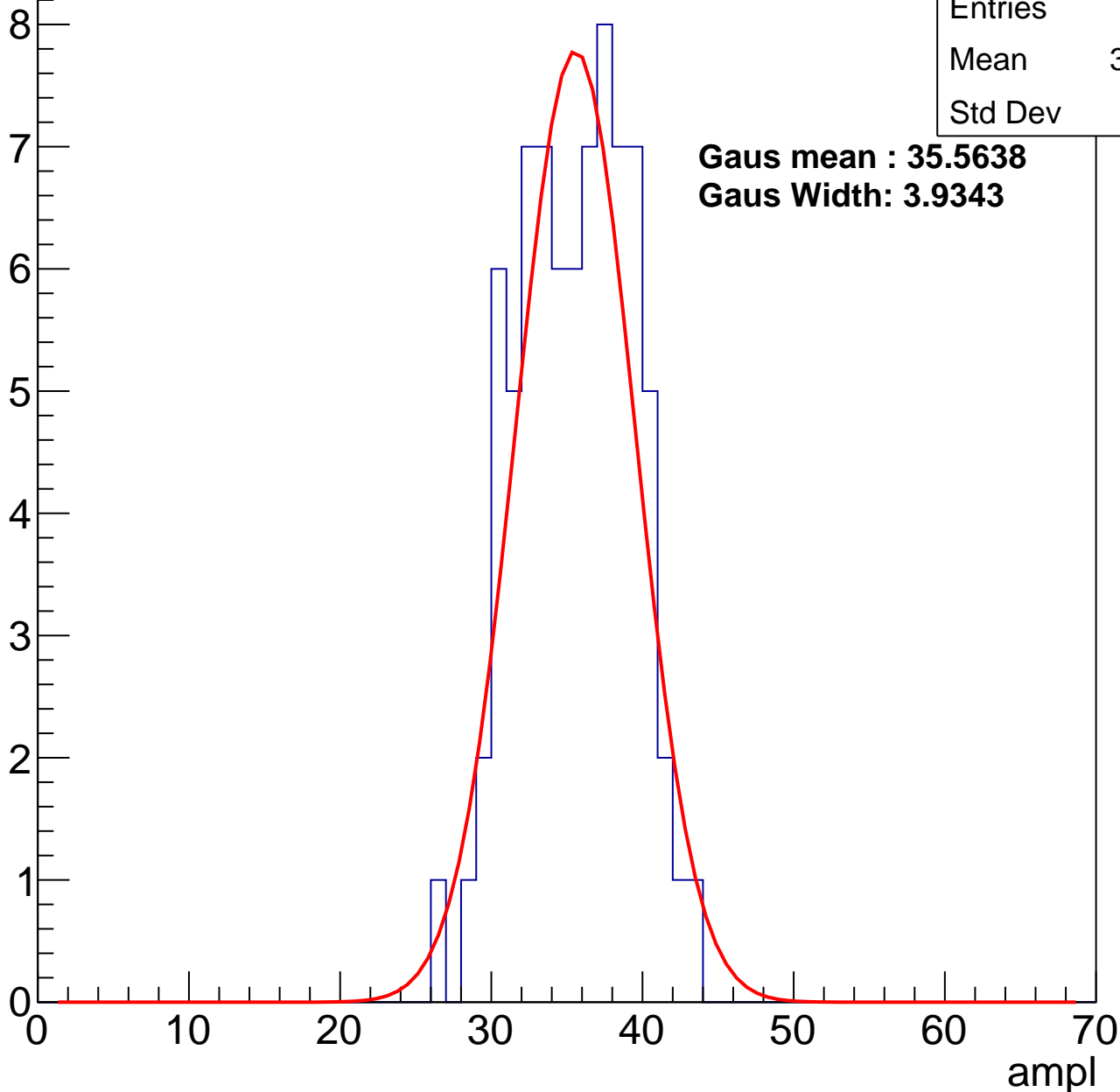
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	35.06
Std Dev	3.65

Gaus mean : 35.5638

Gaus Width: 3.9343



B1L101S, U1-ch71, adc2

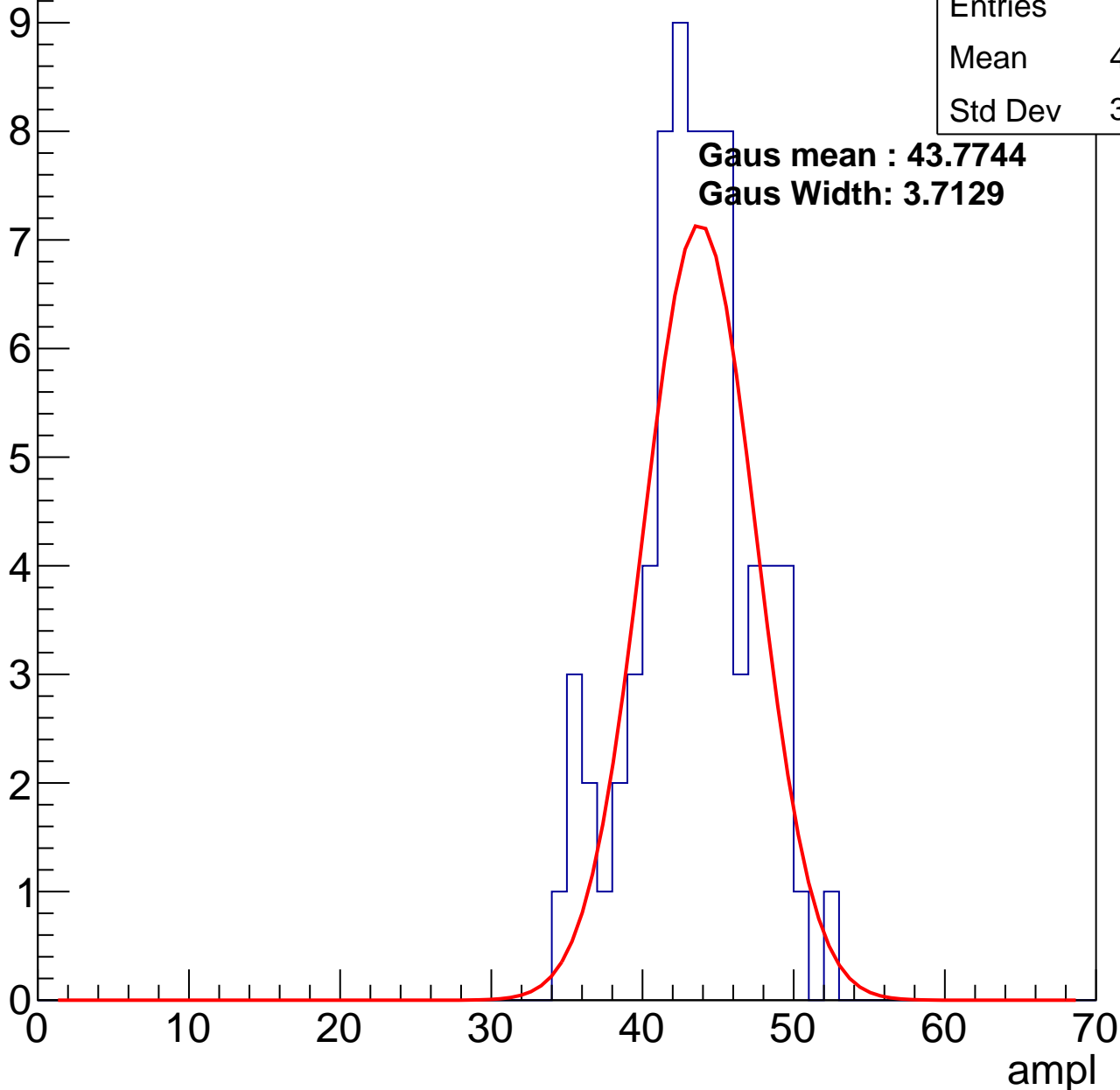
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.96
Std Dev	3.832

Gaus mean : 43.7744

Gaus Width: 3.7129

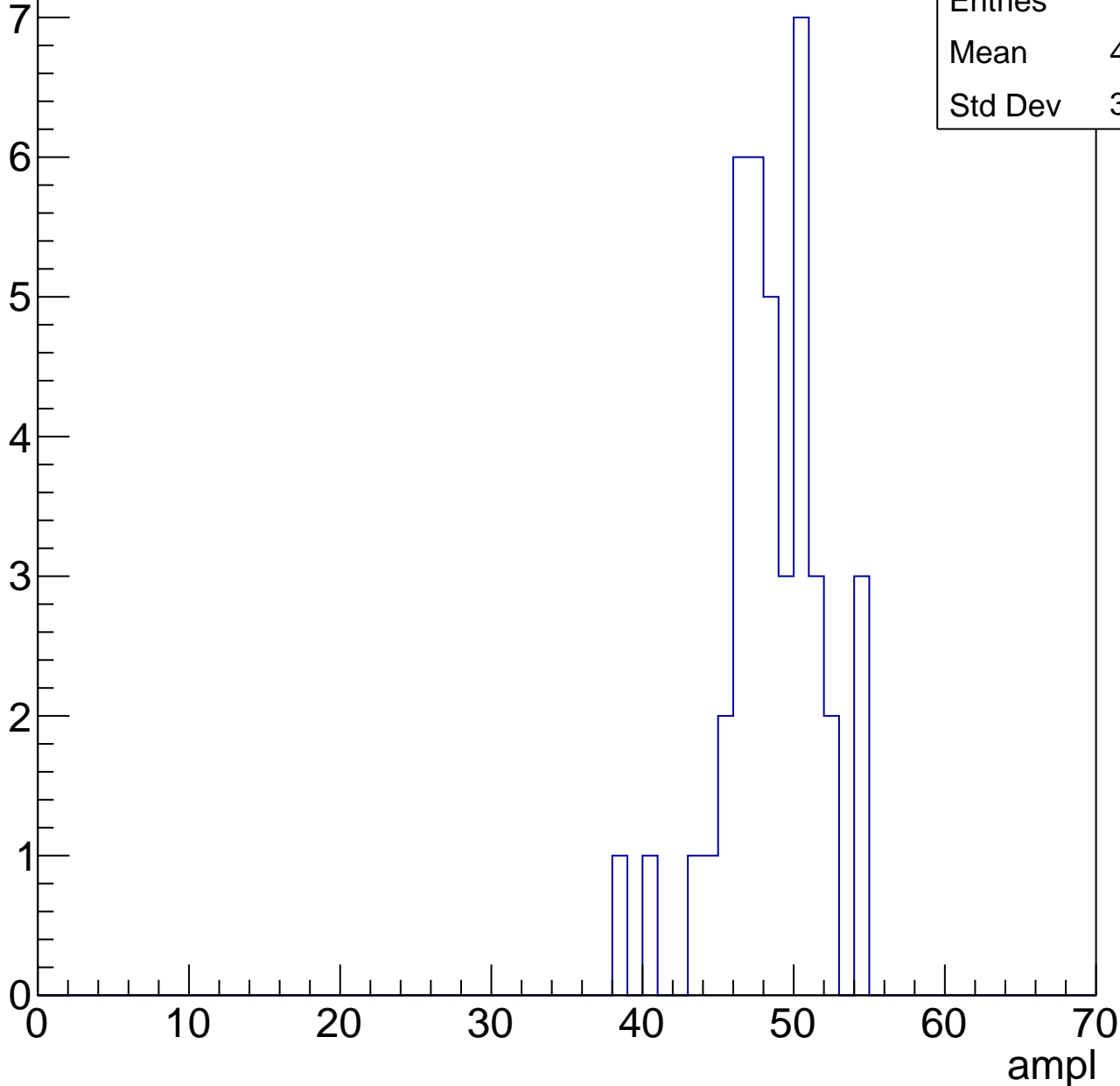


B1L101S, U1-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

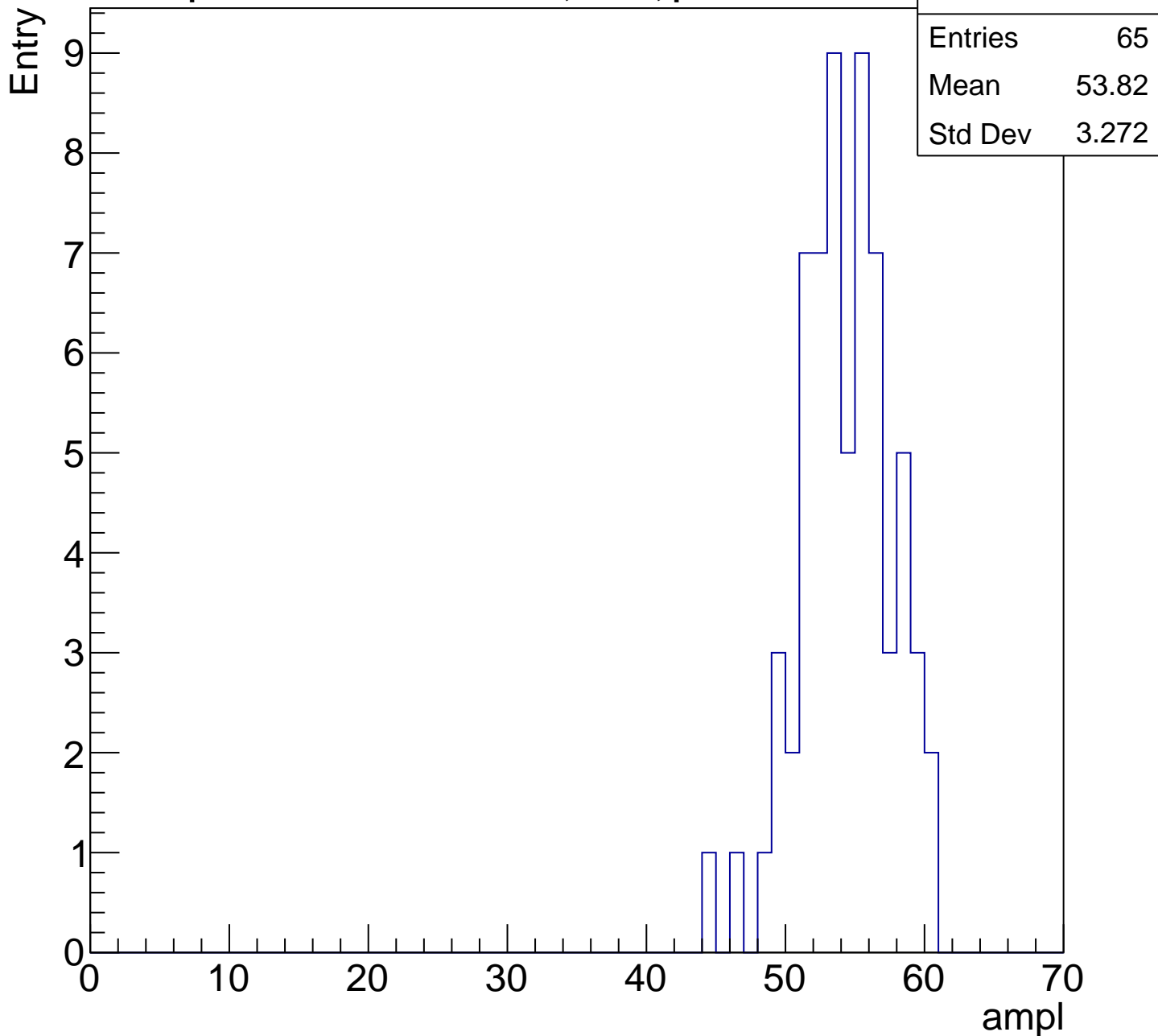
Entry

Entries	41
Mean	48.02
Std Dev	3.317



B1L101S, U1-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

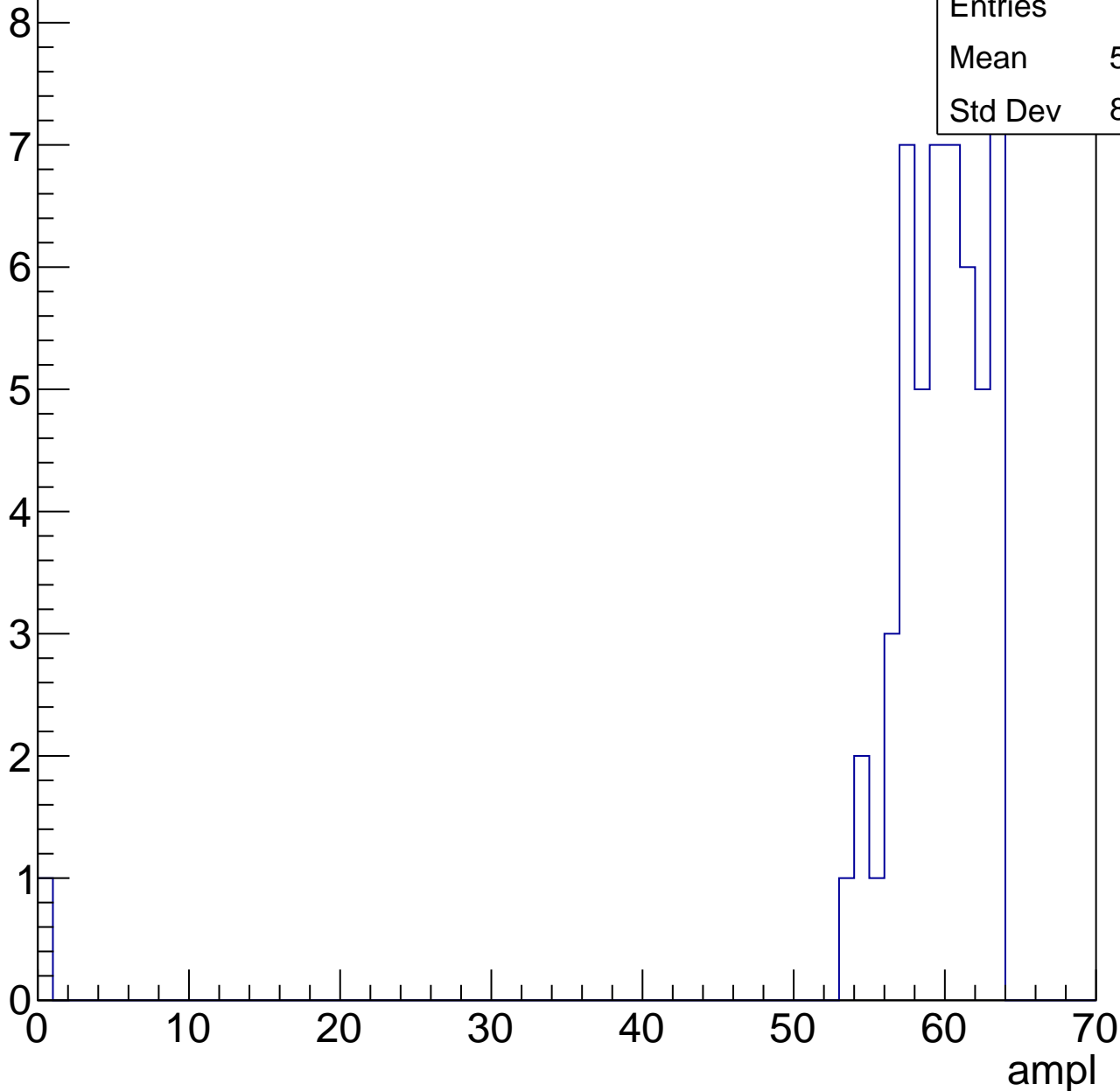


B1L101S, U1-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.23
Std Dev	8.484

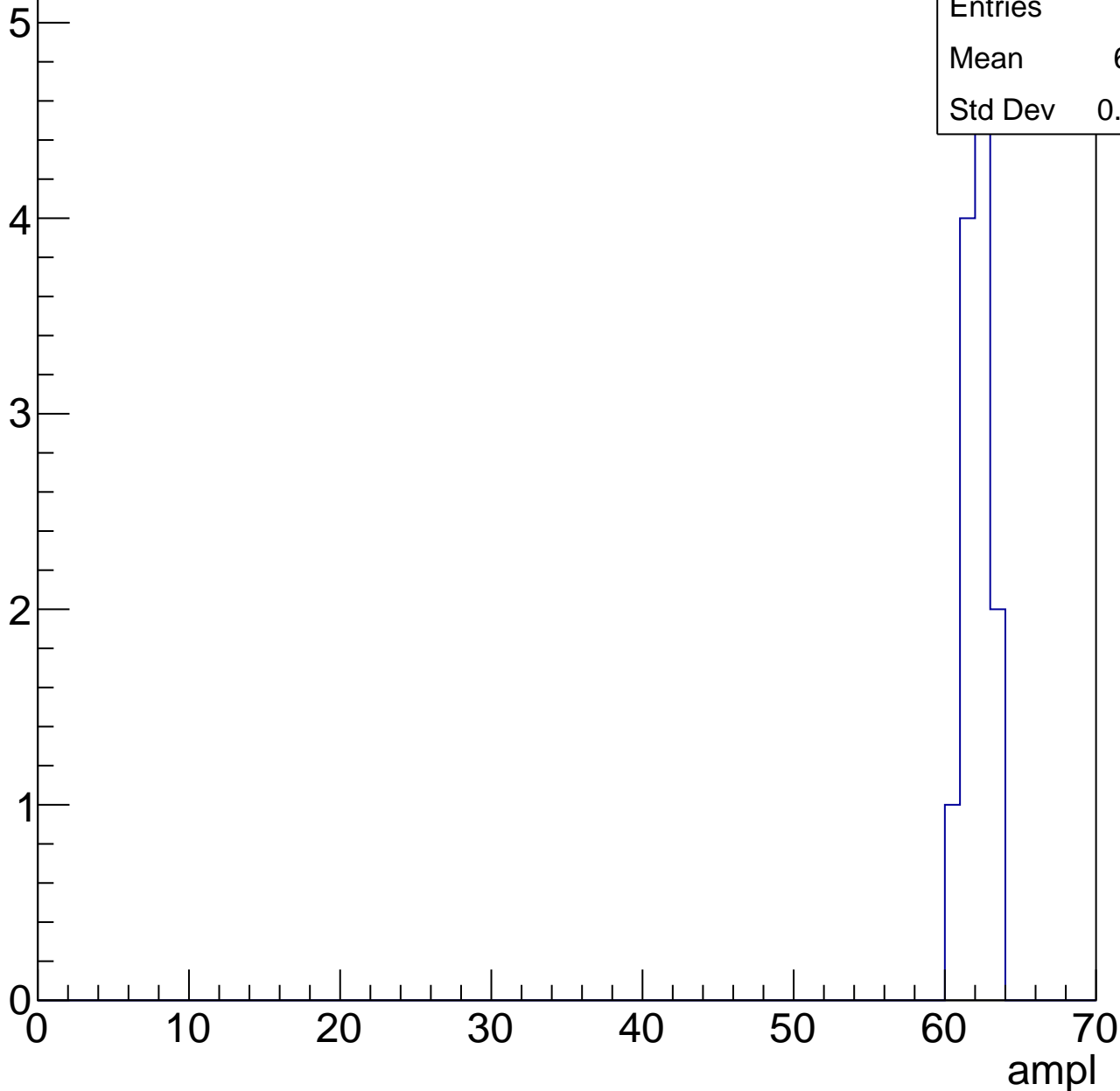


B1L101S, U1-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	61.67
Std Dev	0.8498



B1L101S, U1-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch72, adc0

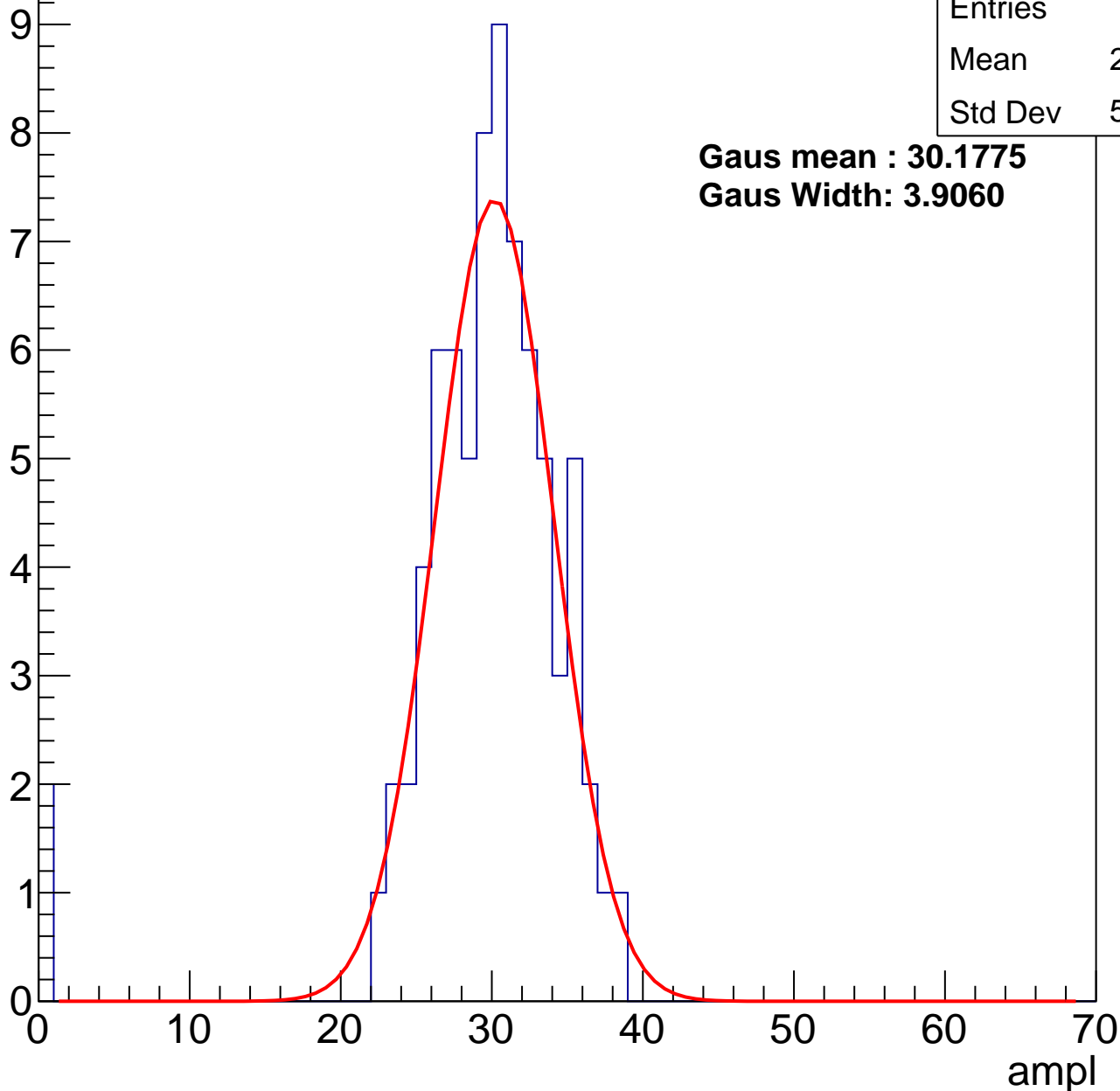
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	28.99
Std Dev	5.965

Gaus mean : 30.1775

Gaus Width: 3.9060



B1L101S, U1-ch72, adc1

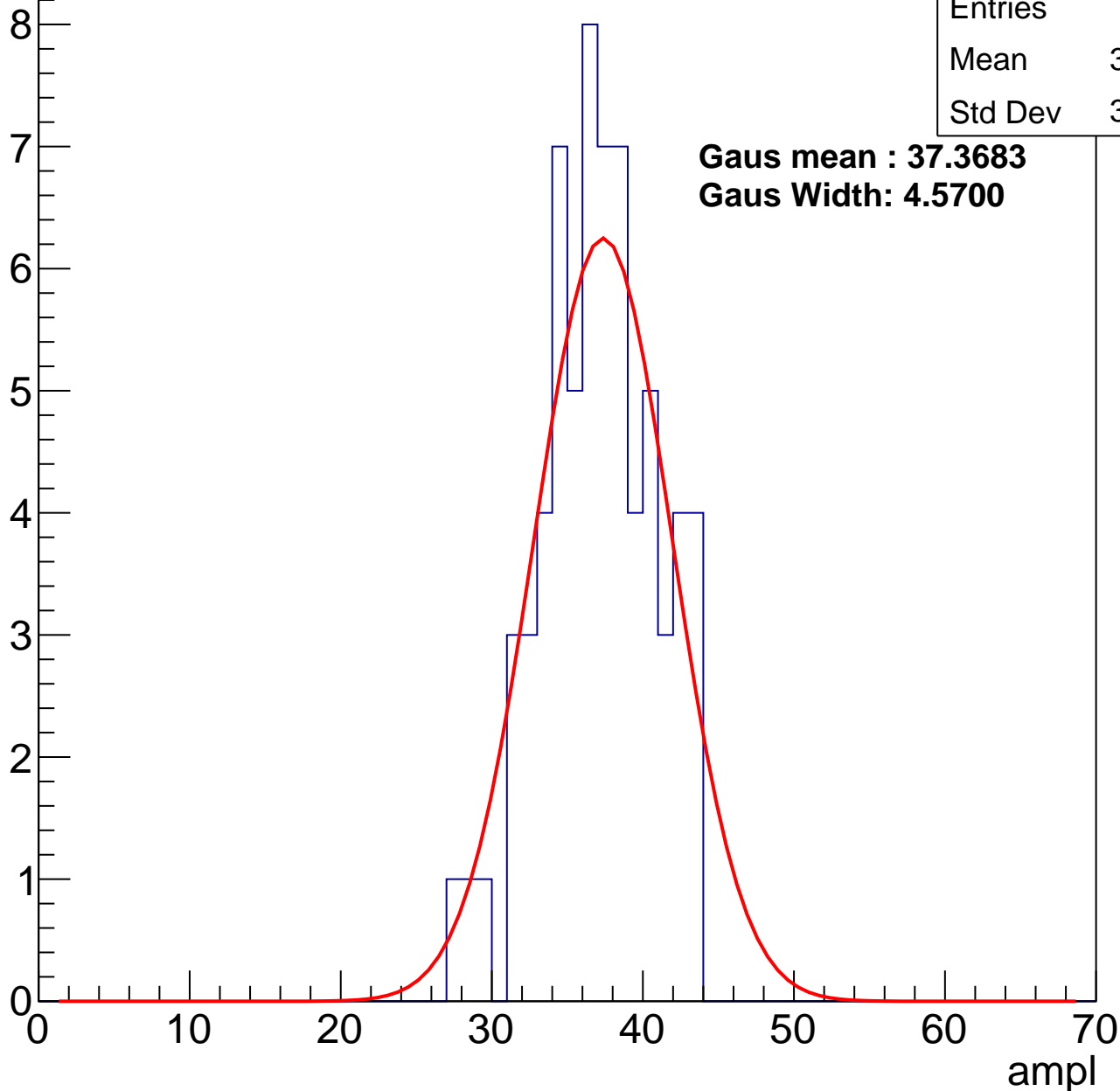
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.57
Std Dev	3.727

Gaus mean : 37.3683

Gaus Width: 4.5700



B1L101S, U1-ch72, adc2

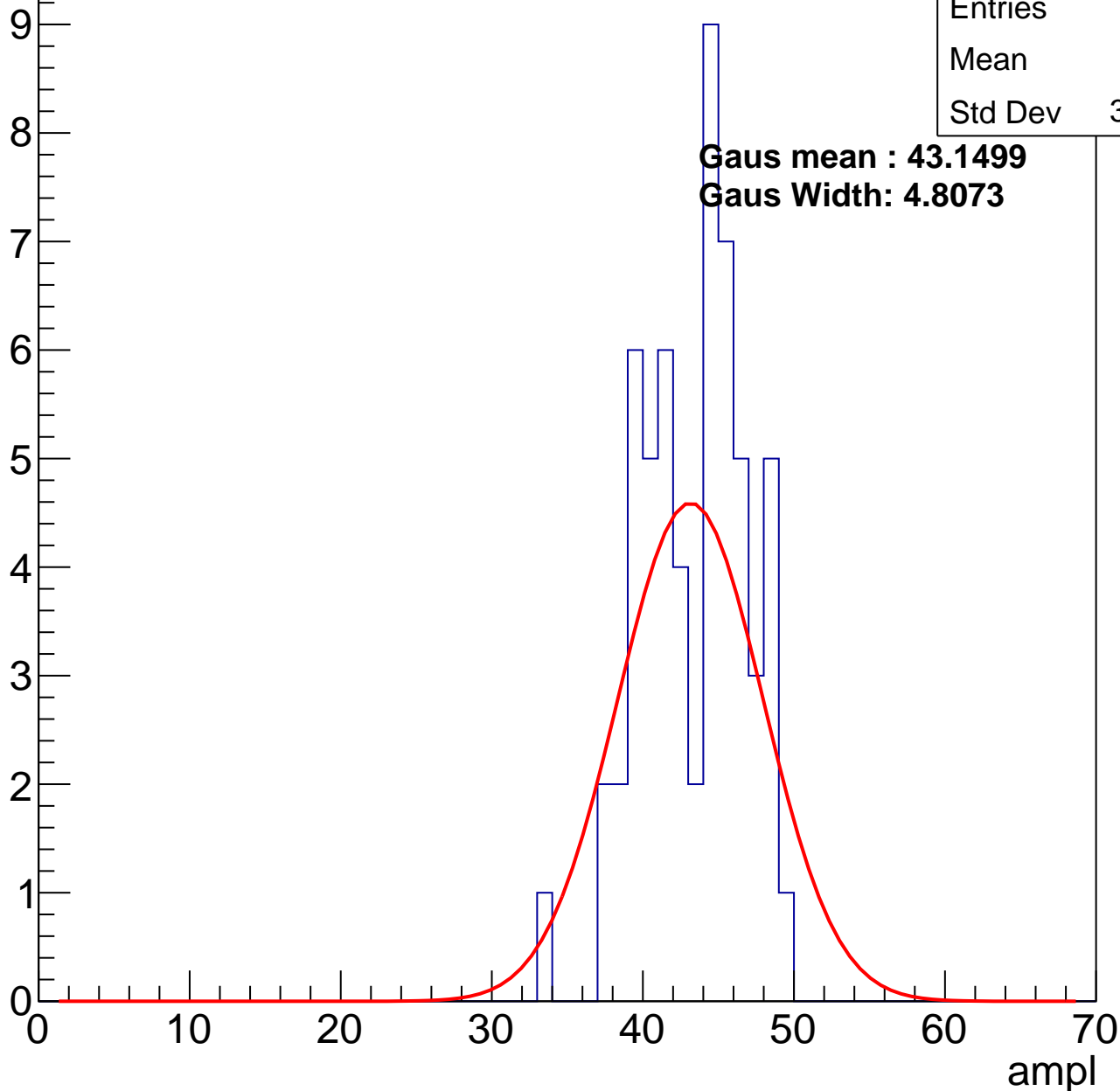
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.9
Std Dev	3.428

Gaus mean : 43.1499

Gaus Width: 4.8073

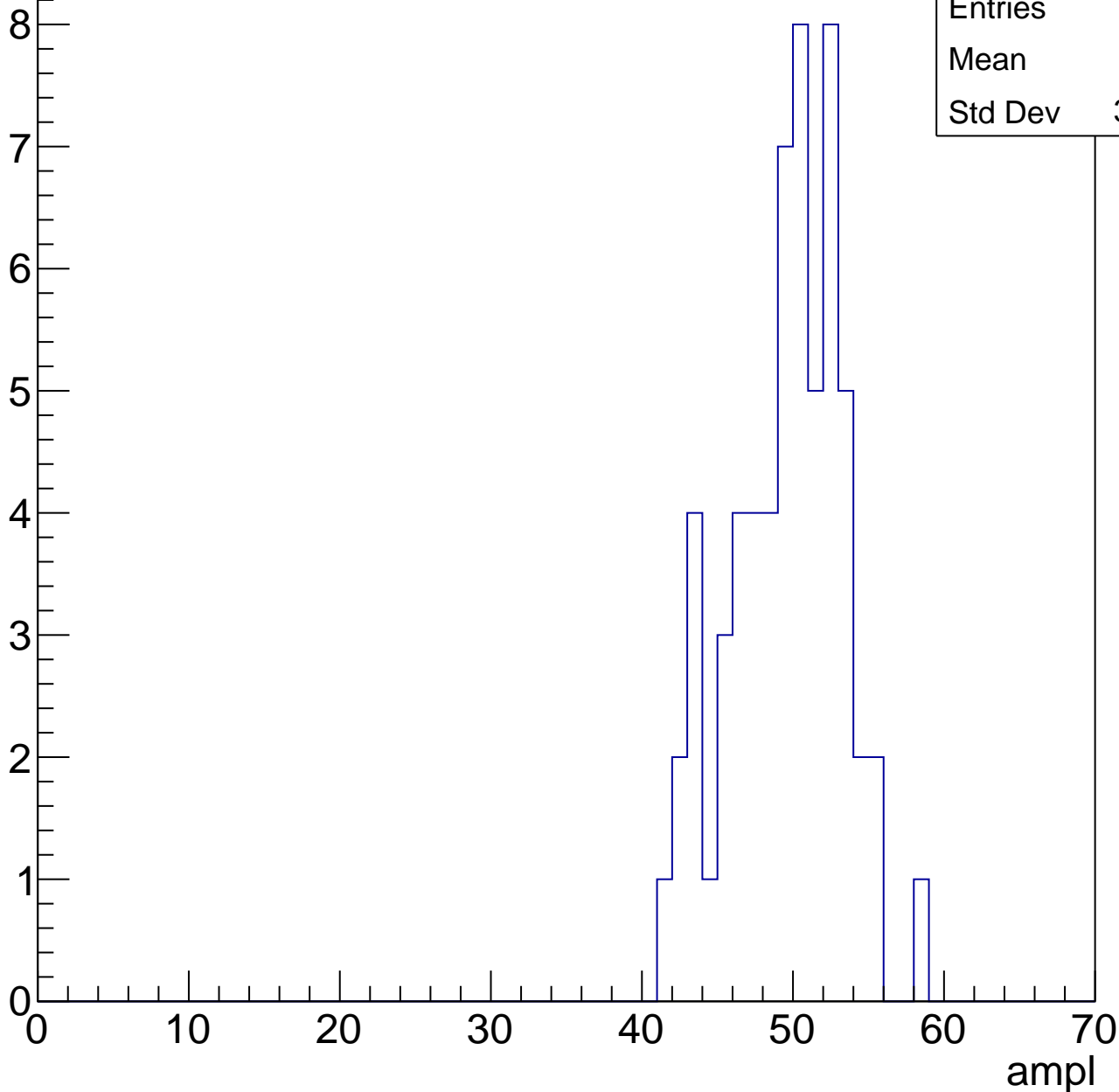


B1L101S, U1-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.1
Std Dev	3.661

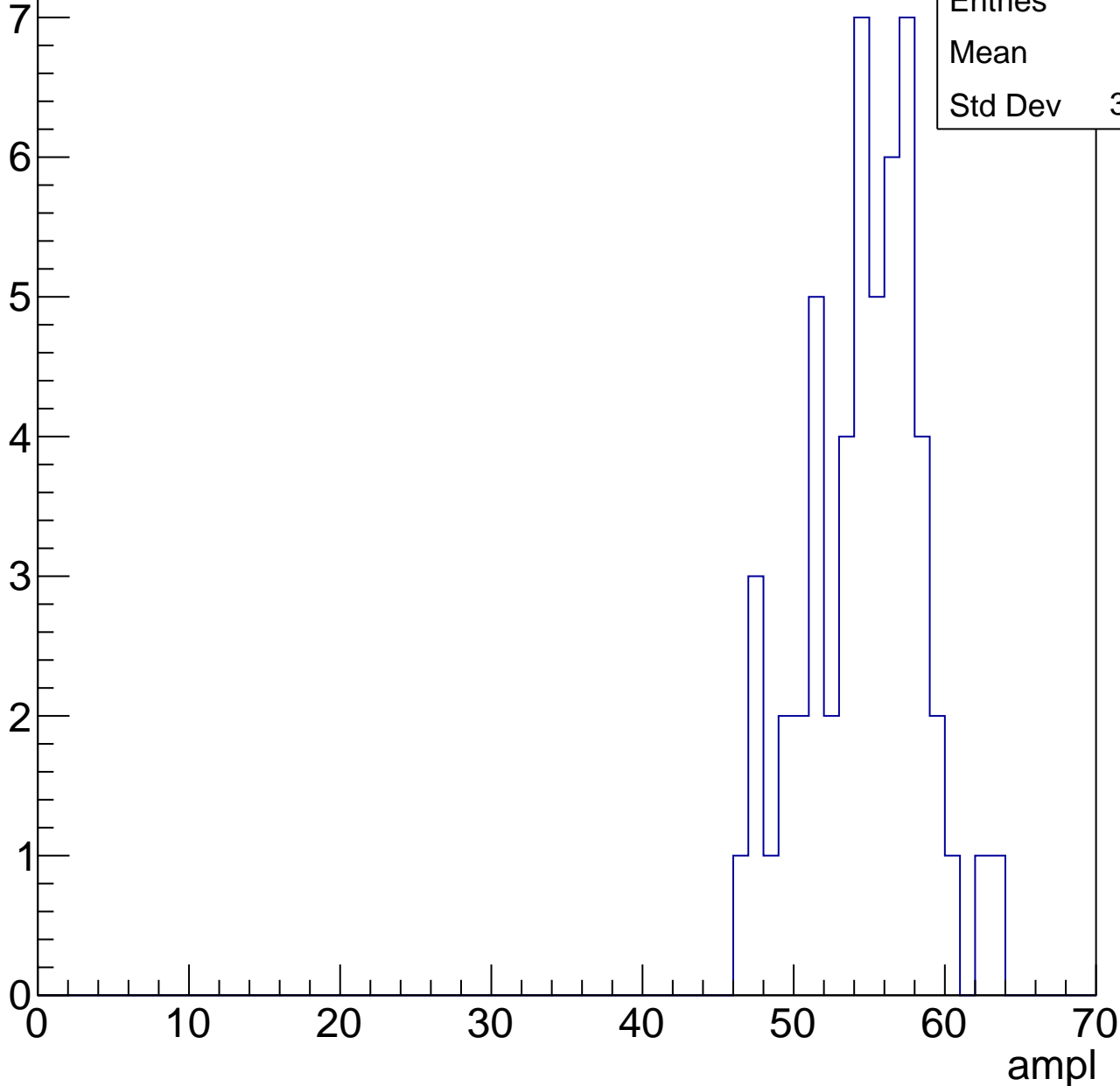


B1L101S, U1-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.2
Std Dev	3.778

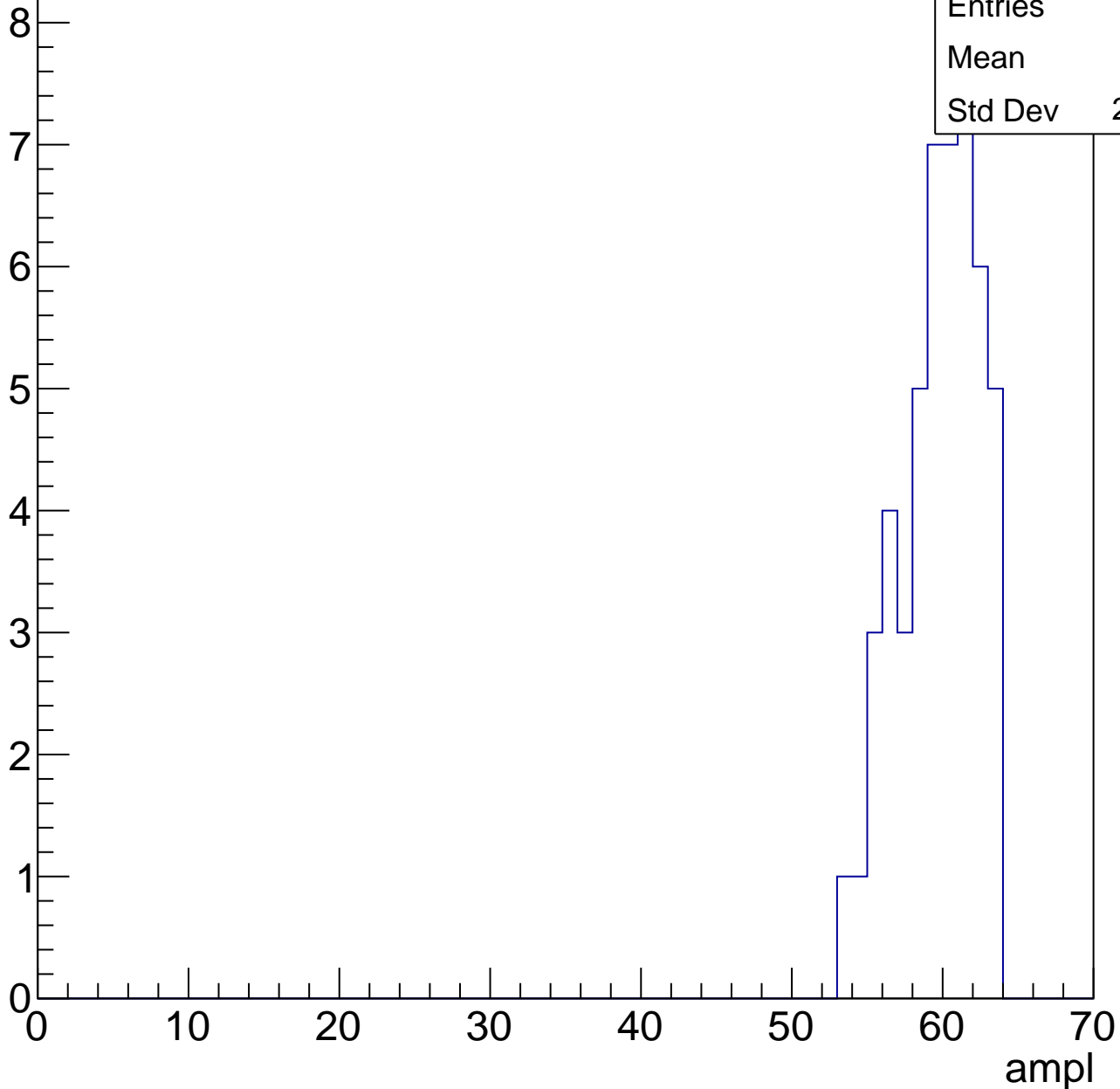


B1L101S, U1-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

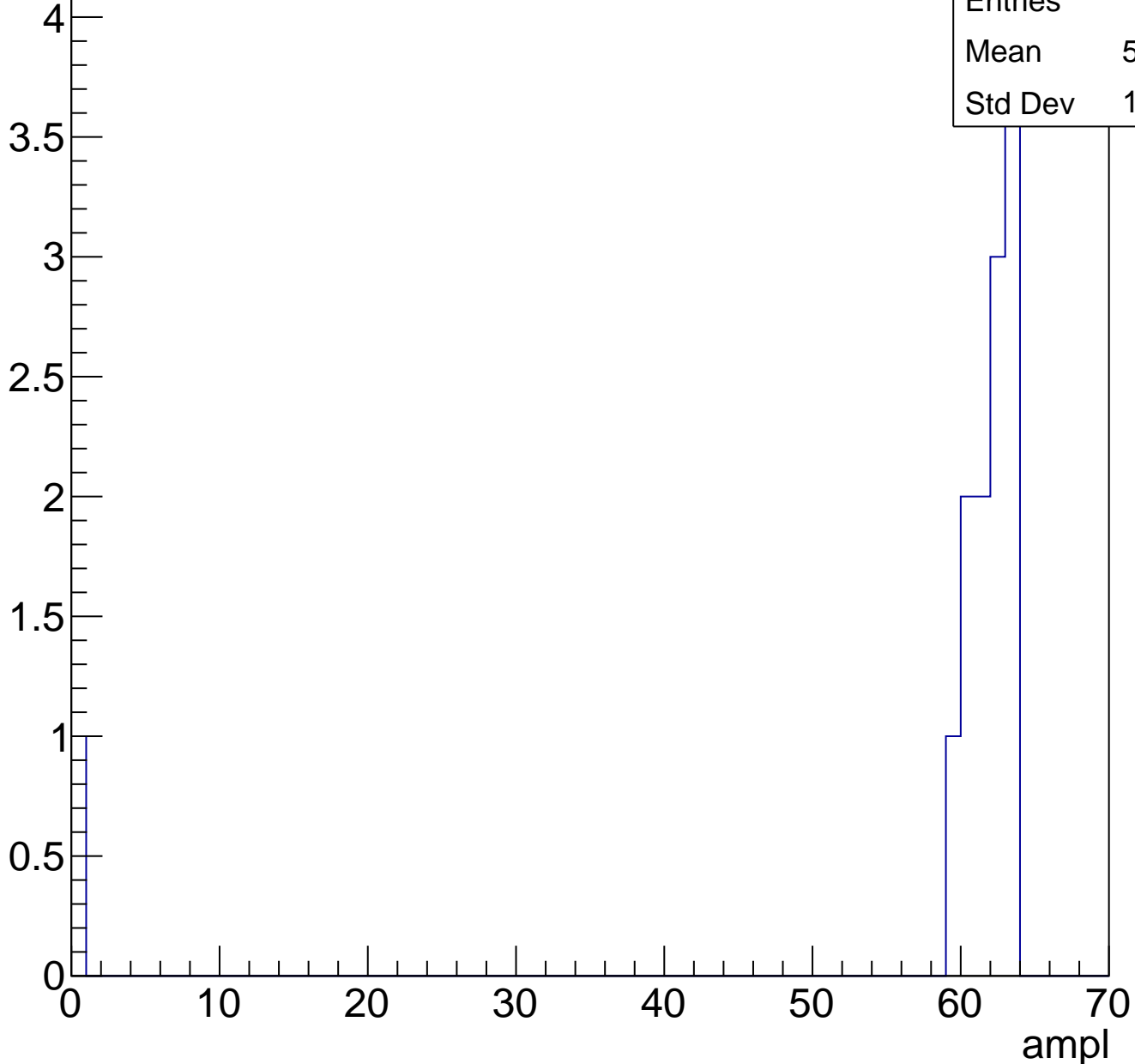
Entries	50
Mean	59.3
Std Dev	2.571



B1L101S, U1-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch73, adc0

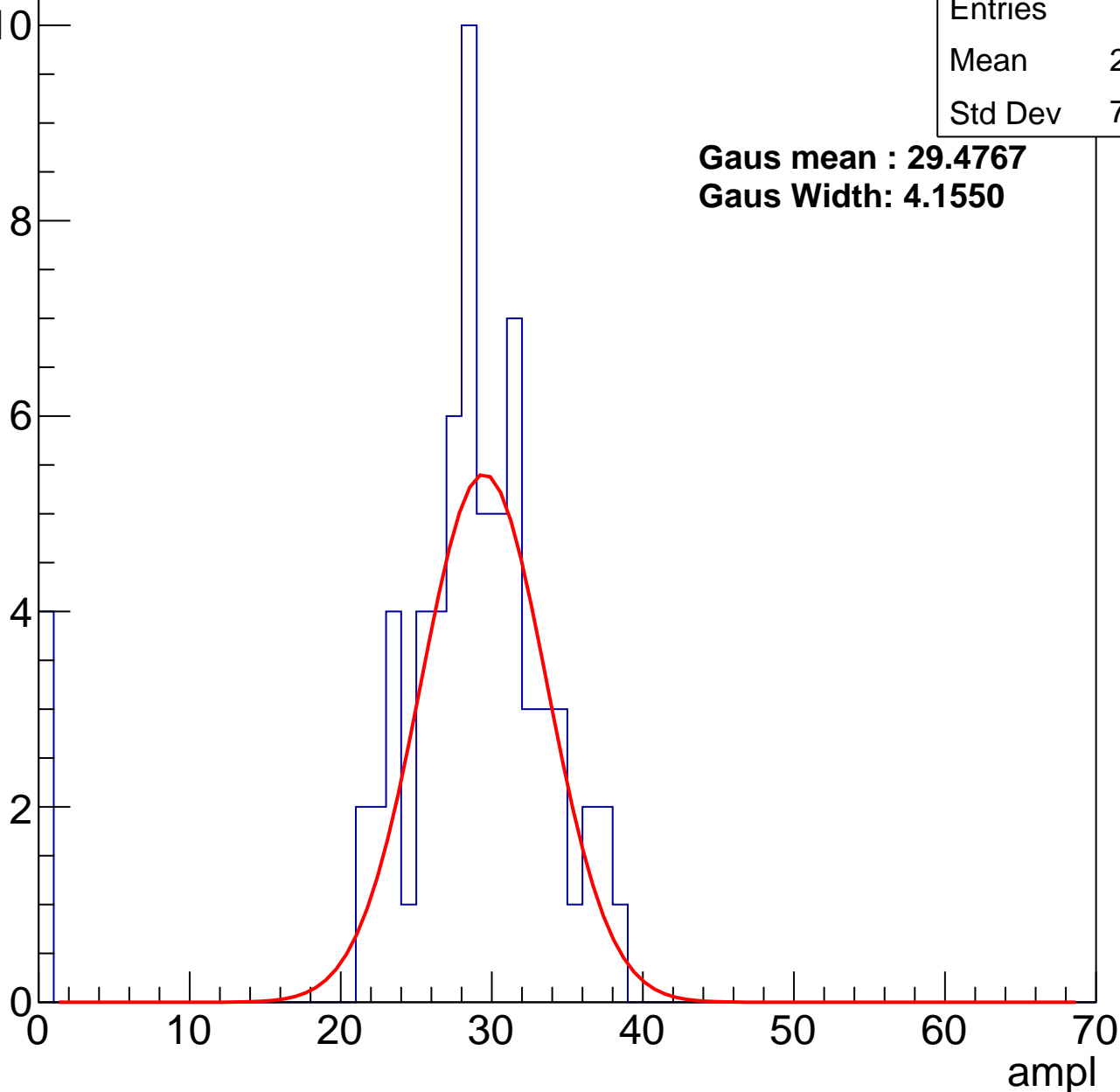
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	27.19
Std Dev	7.795

Gaus mean : 29.4767

Gaus Width: 4.1550



B1L101S, U1-ch73, adc1

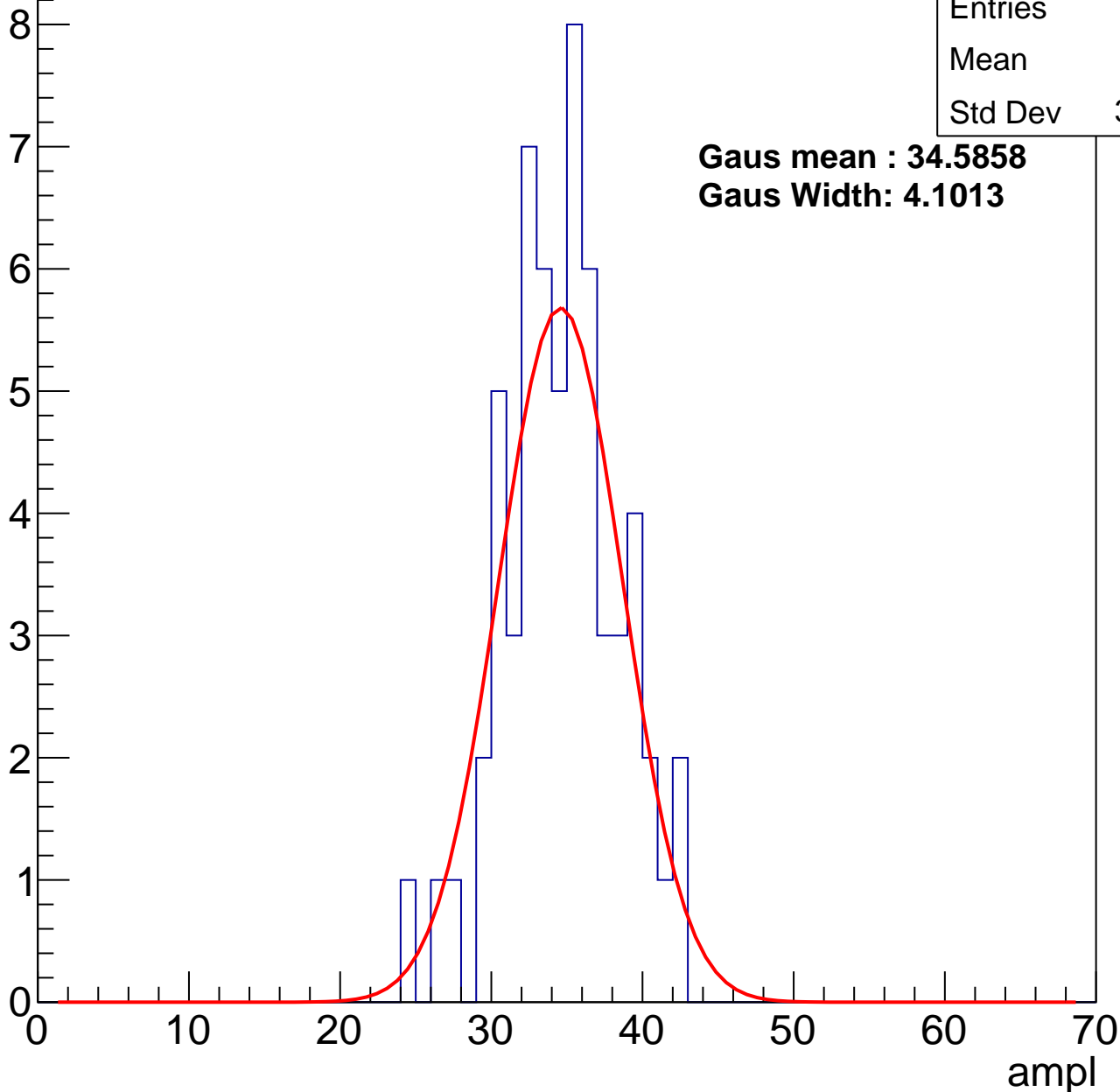
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	34.2
Std Dev	3.781

Gaus mean : 34.5858

Gaus Width: 4.1013



B1L101S, U1-ch73, adc2

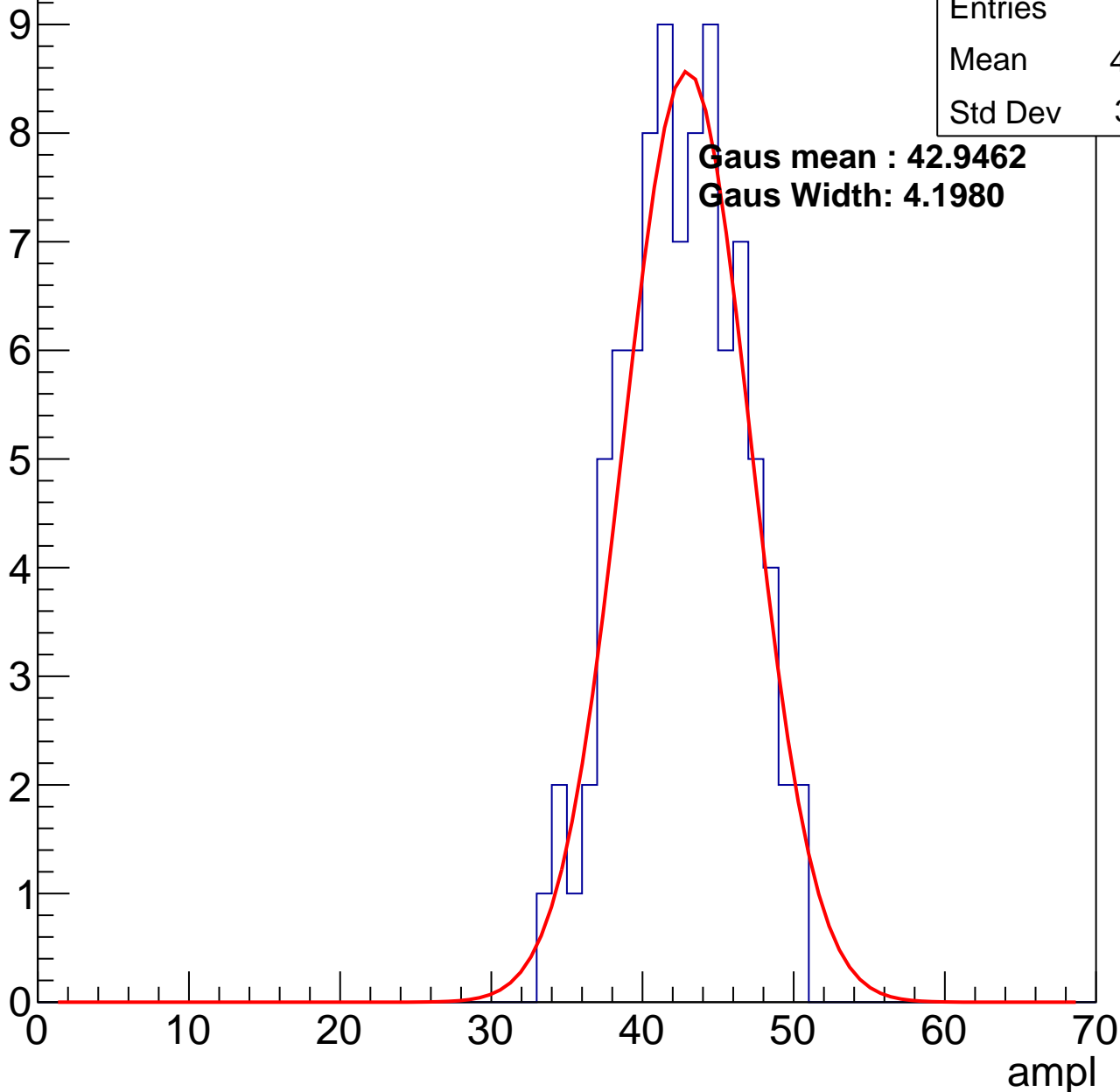
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	42.17
Std Dev	3.871

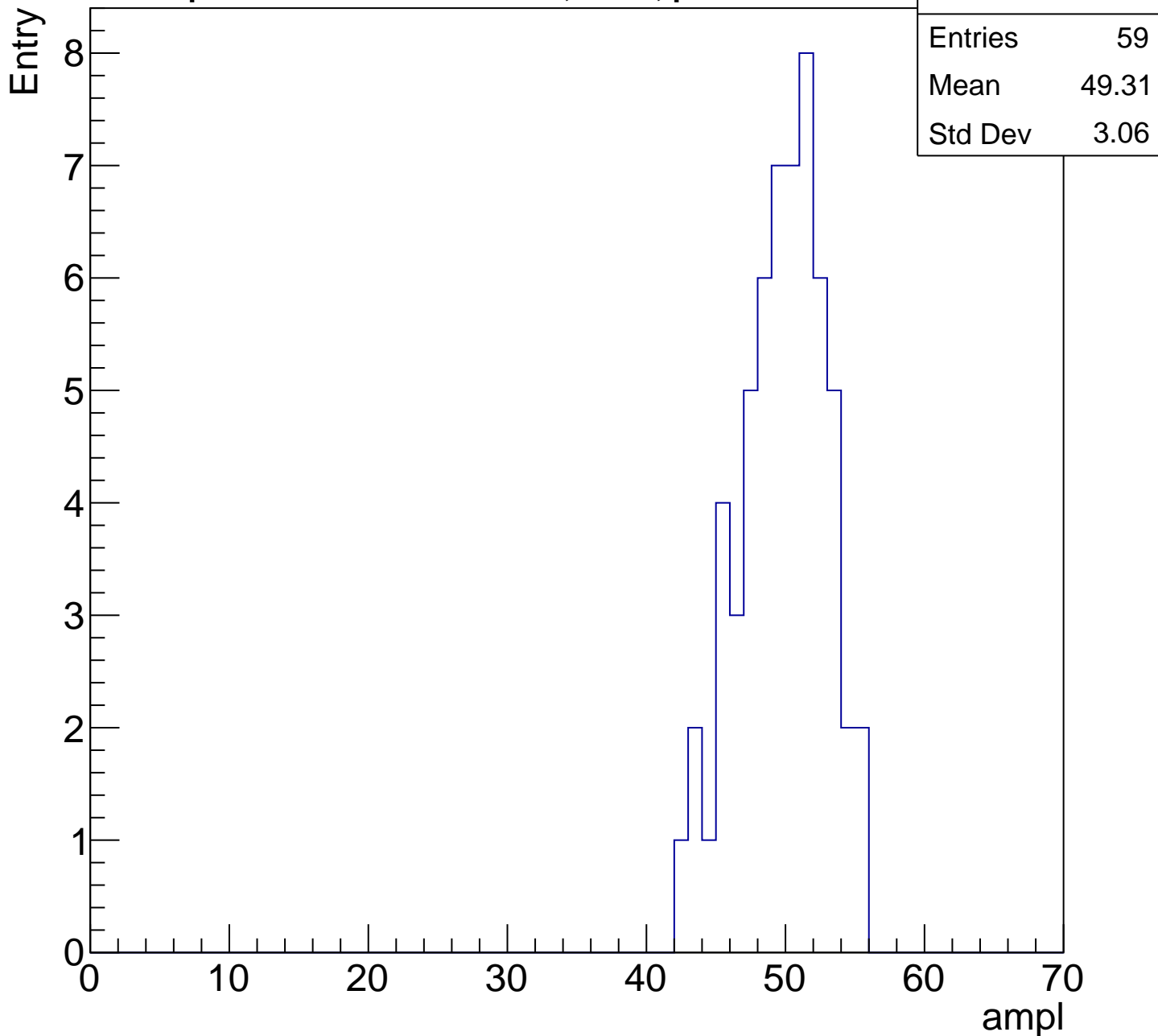
Gaus mean : 42.9462

Gaus Width: 4.1980



B1L101S, U1-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

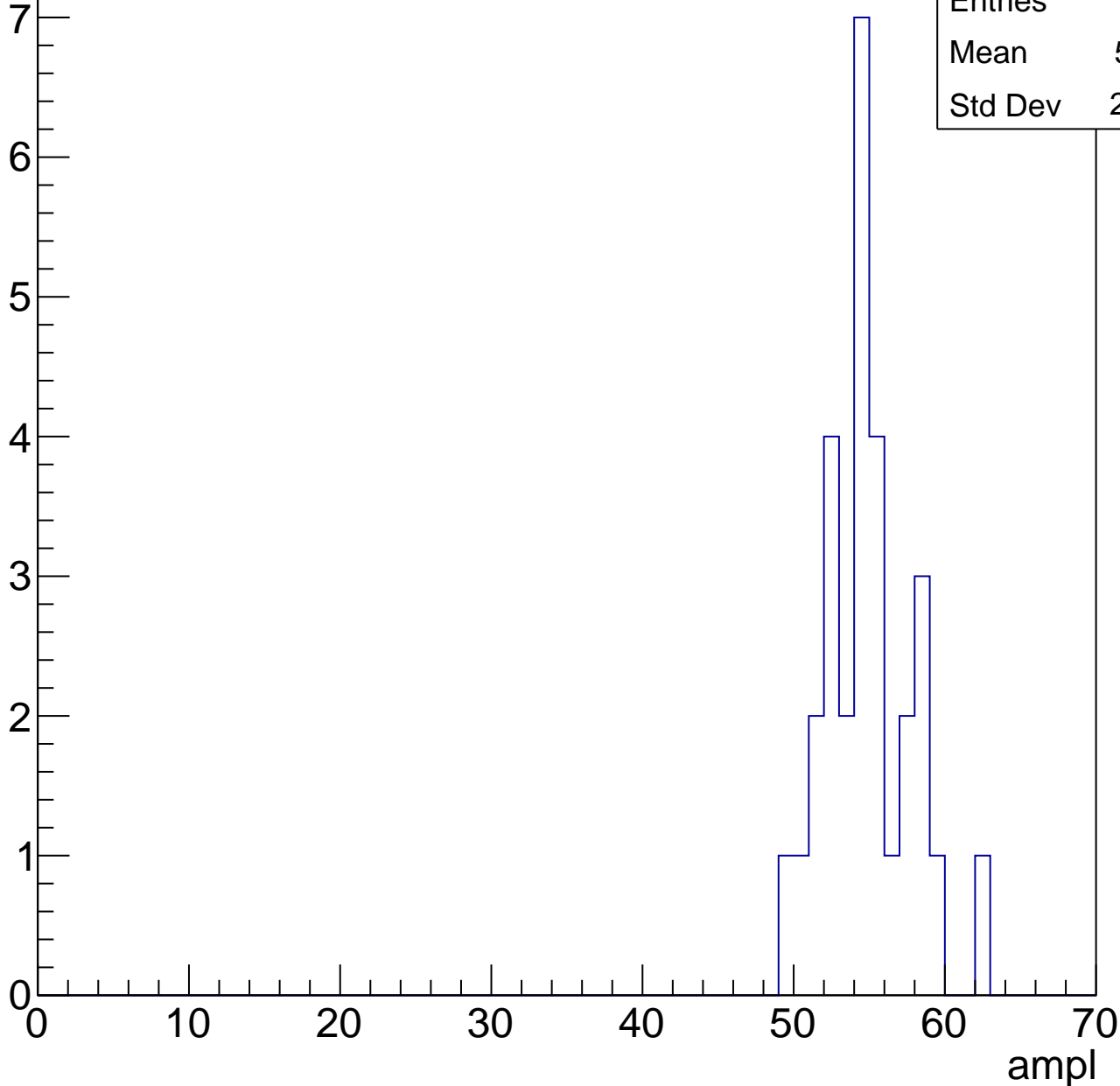


B1L101S, U1-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	54.41
Std Dev	2.847

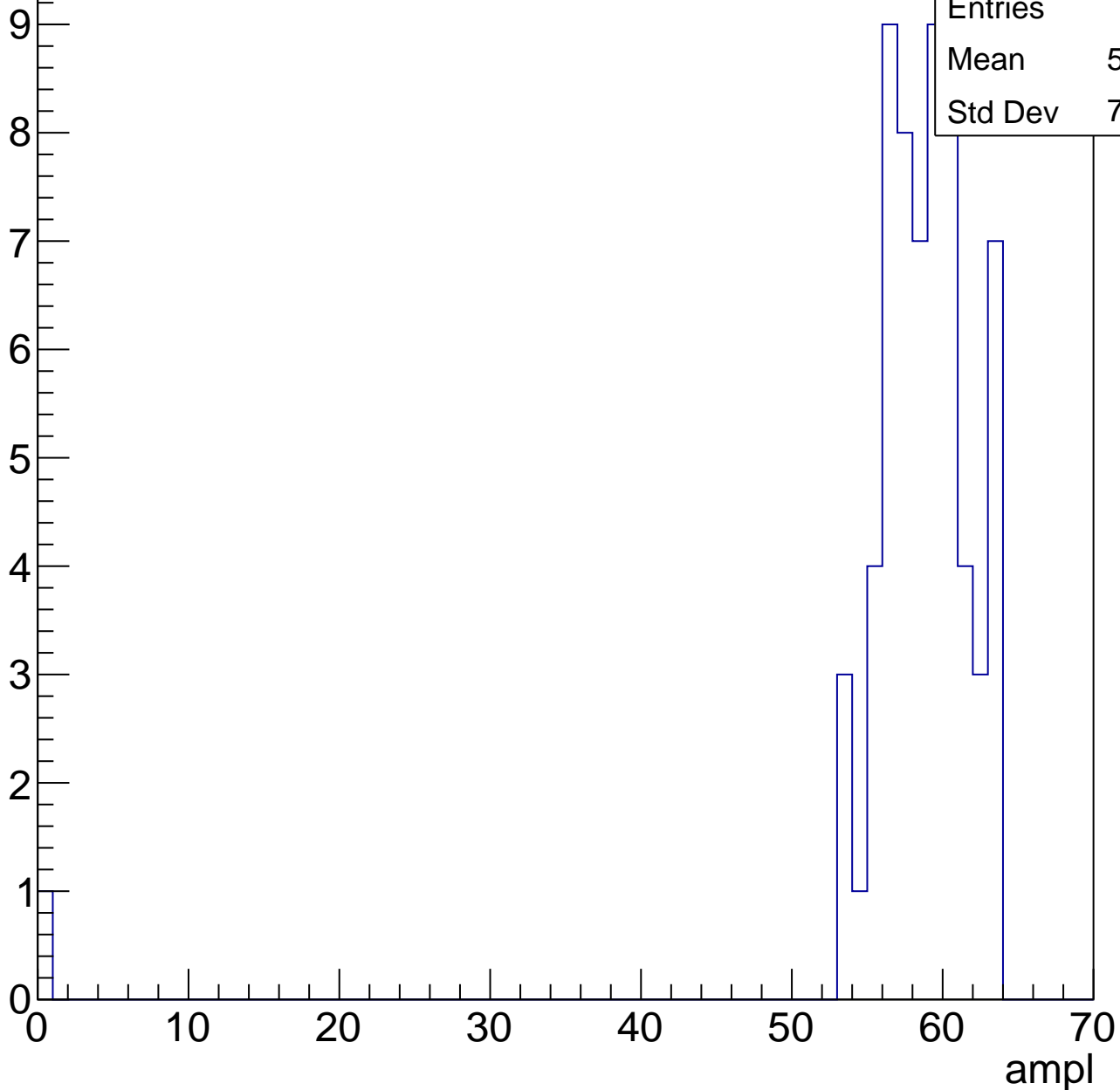


B1L101S, U1-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

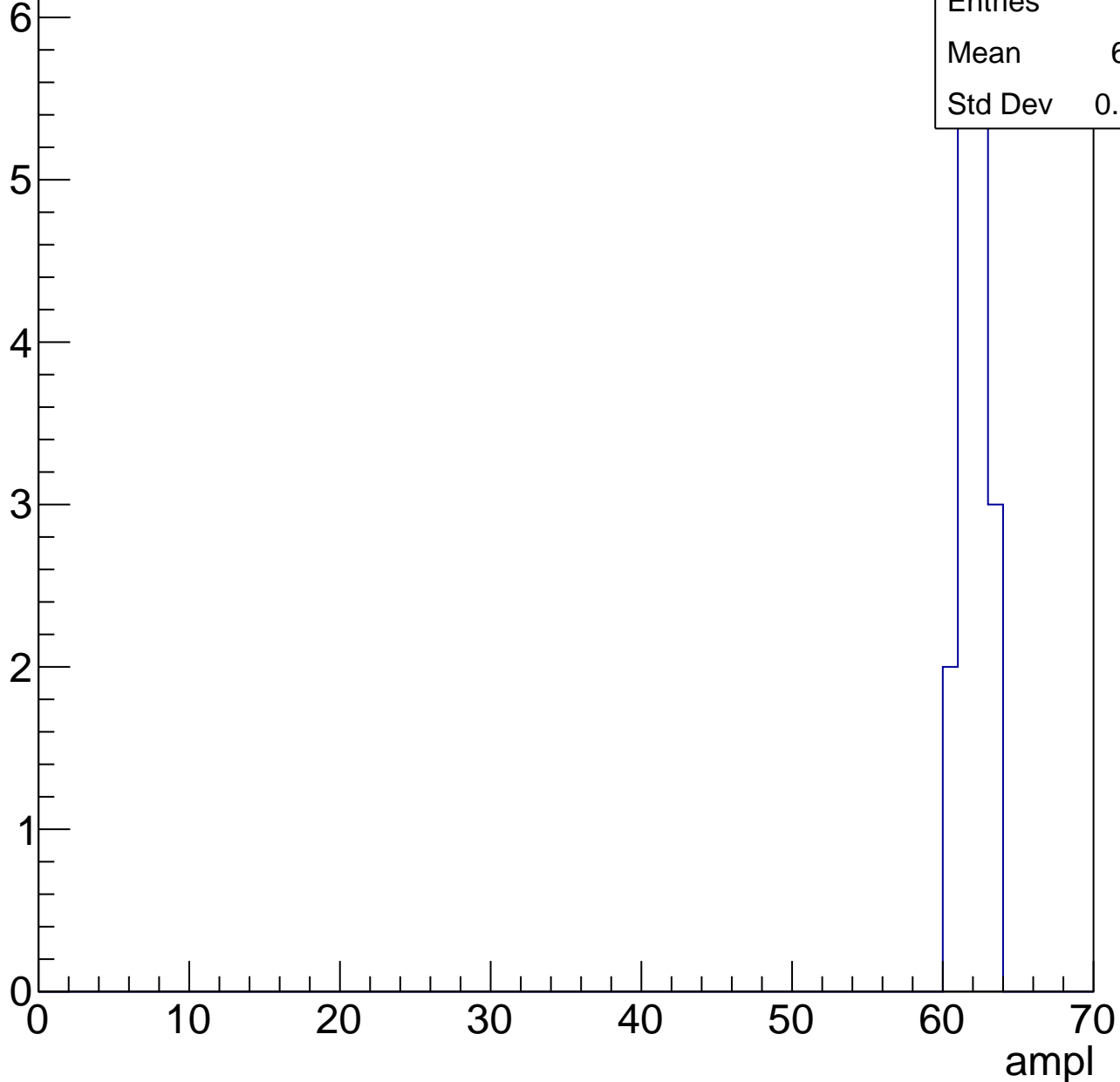
Entries	64
Mean	57.52
Std Dev	7.726



B1L101S, U1-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch74, adc0

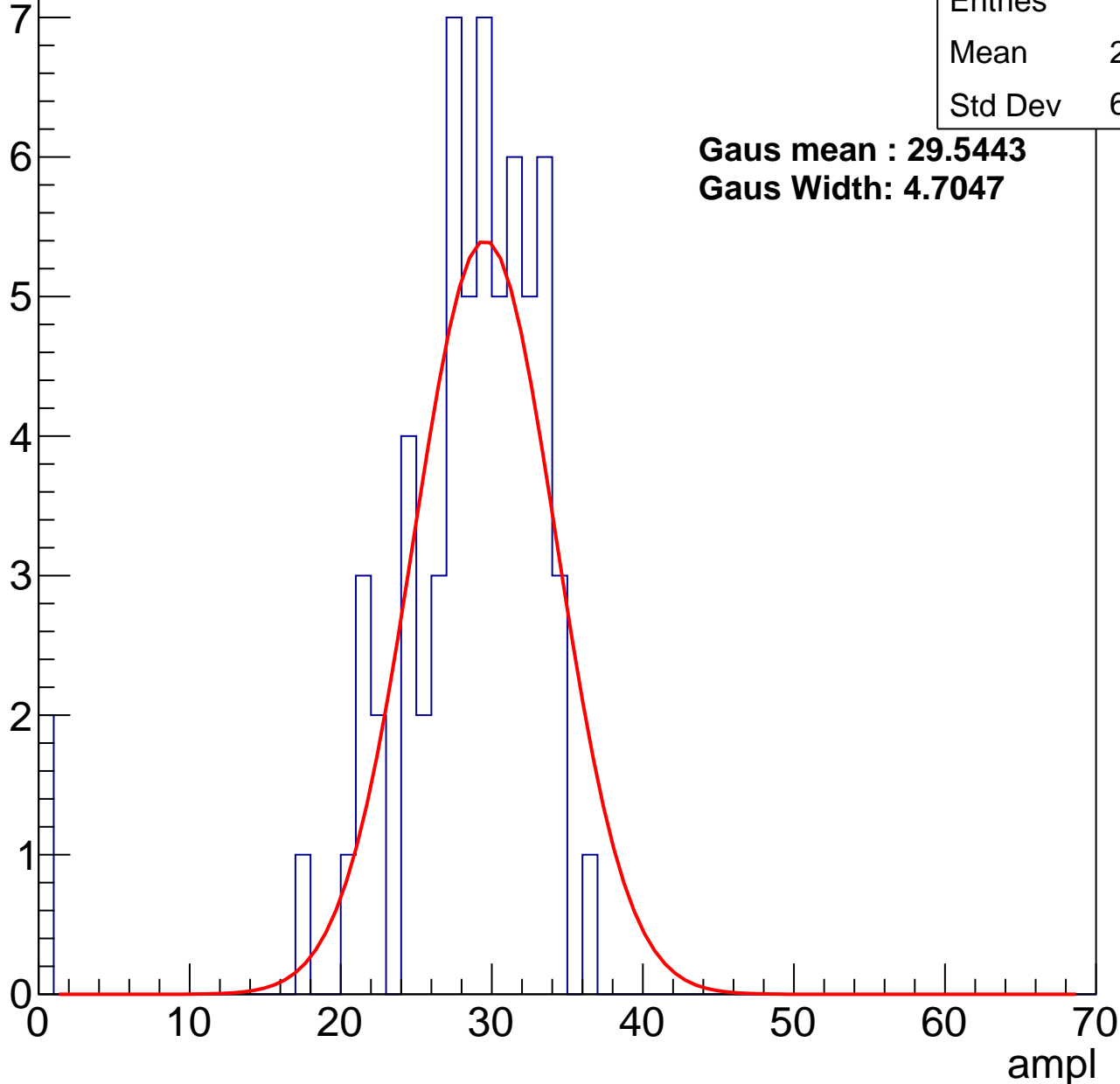
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.49
Std Dev	6.349

Gaus mean : 29.5443

Gaus Width: 4.7047



B1L101S, U1-ch74, adc1

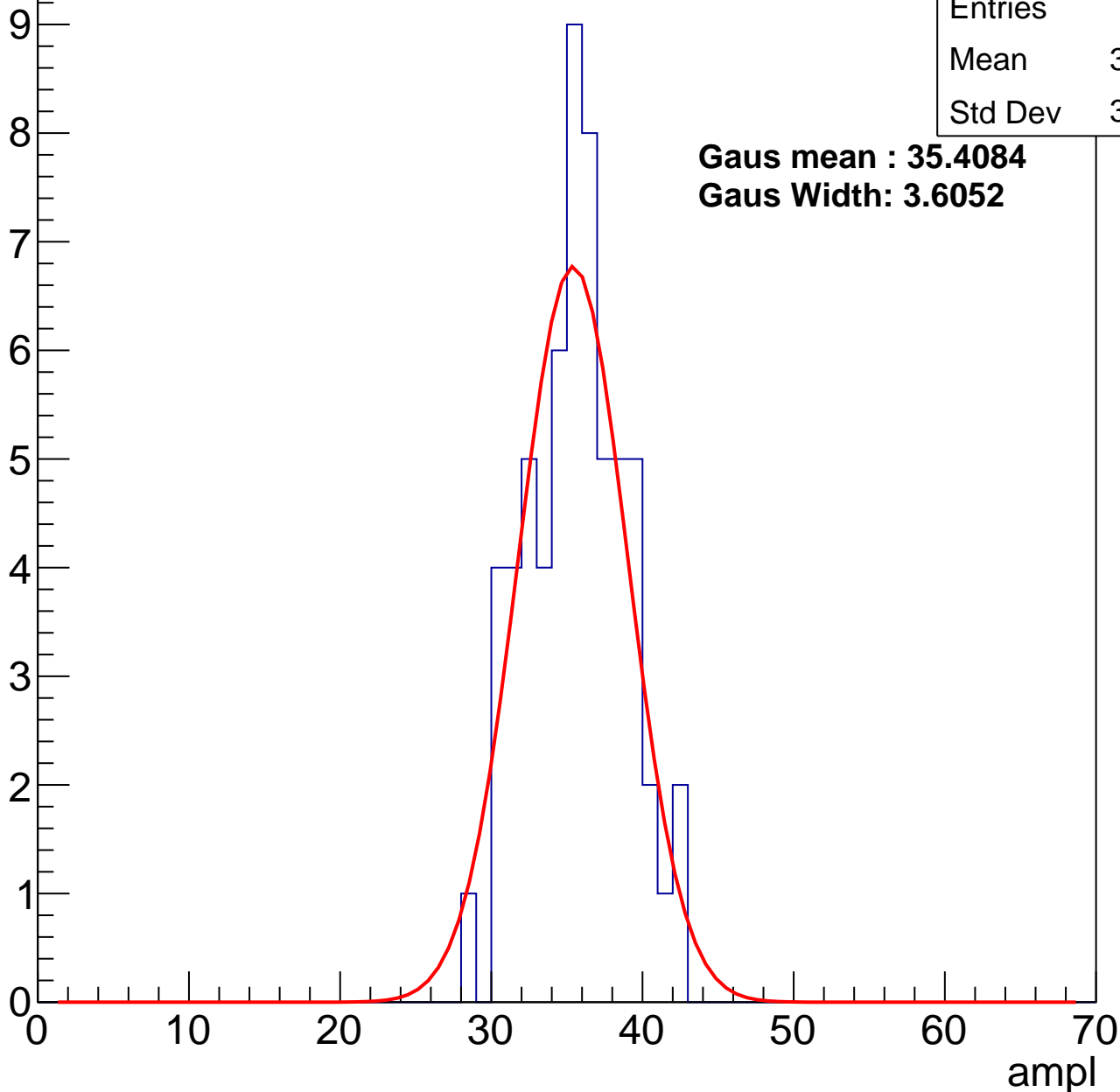
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.18
Std Dev	3.175

Gaus mean : 35.4084

Gaus Width: 3.6052



B1L101S, U1-ch74, adc2

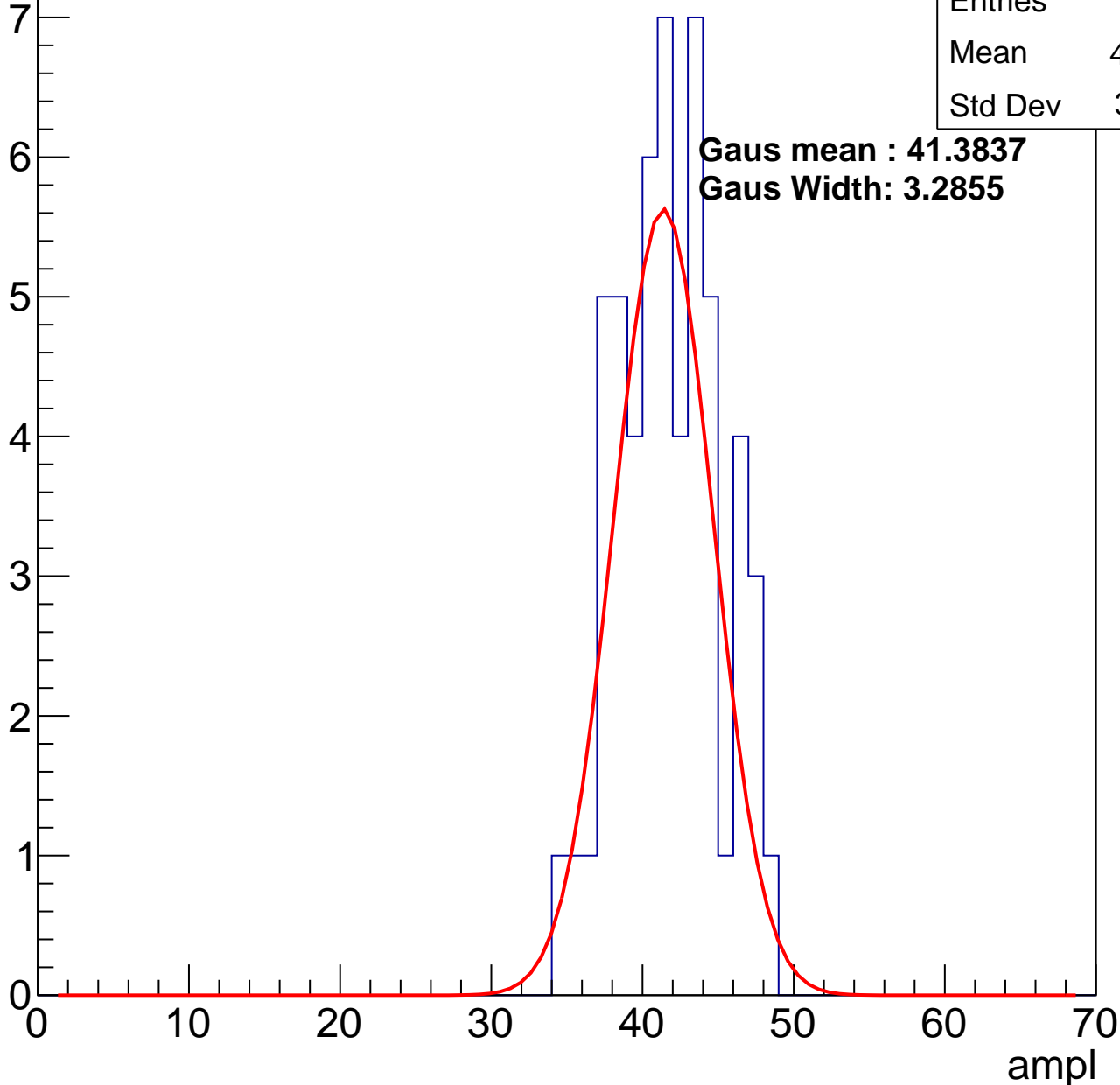
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.27
Std Dev	3.311

Gaus mean : 41.3837

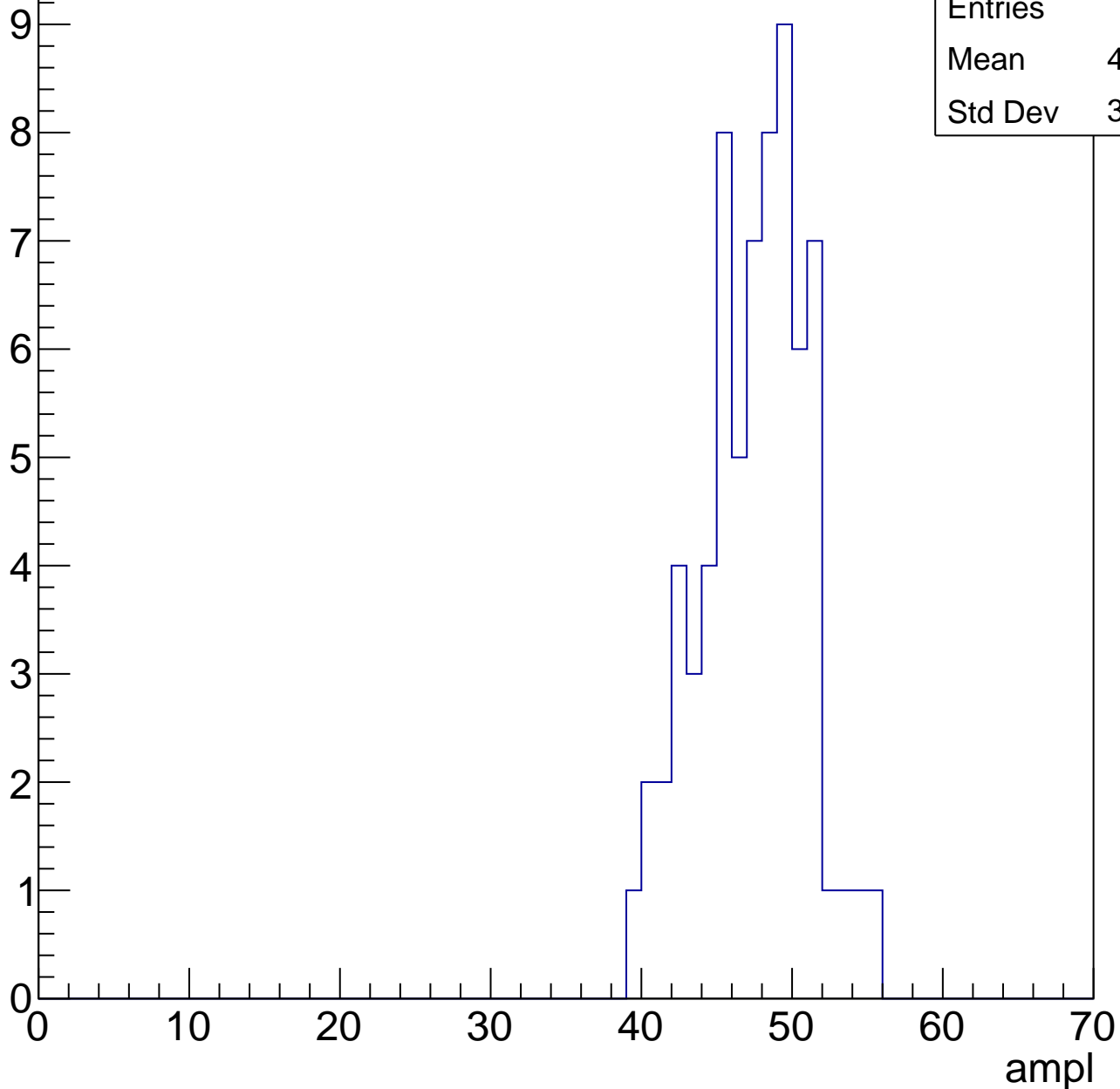
Gaus Width: 3.2855



B1L101S, U1-ch74, adc3

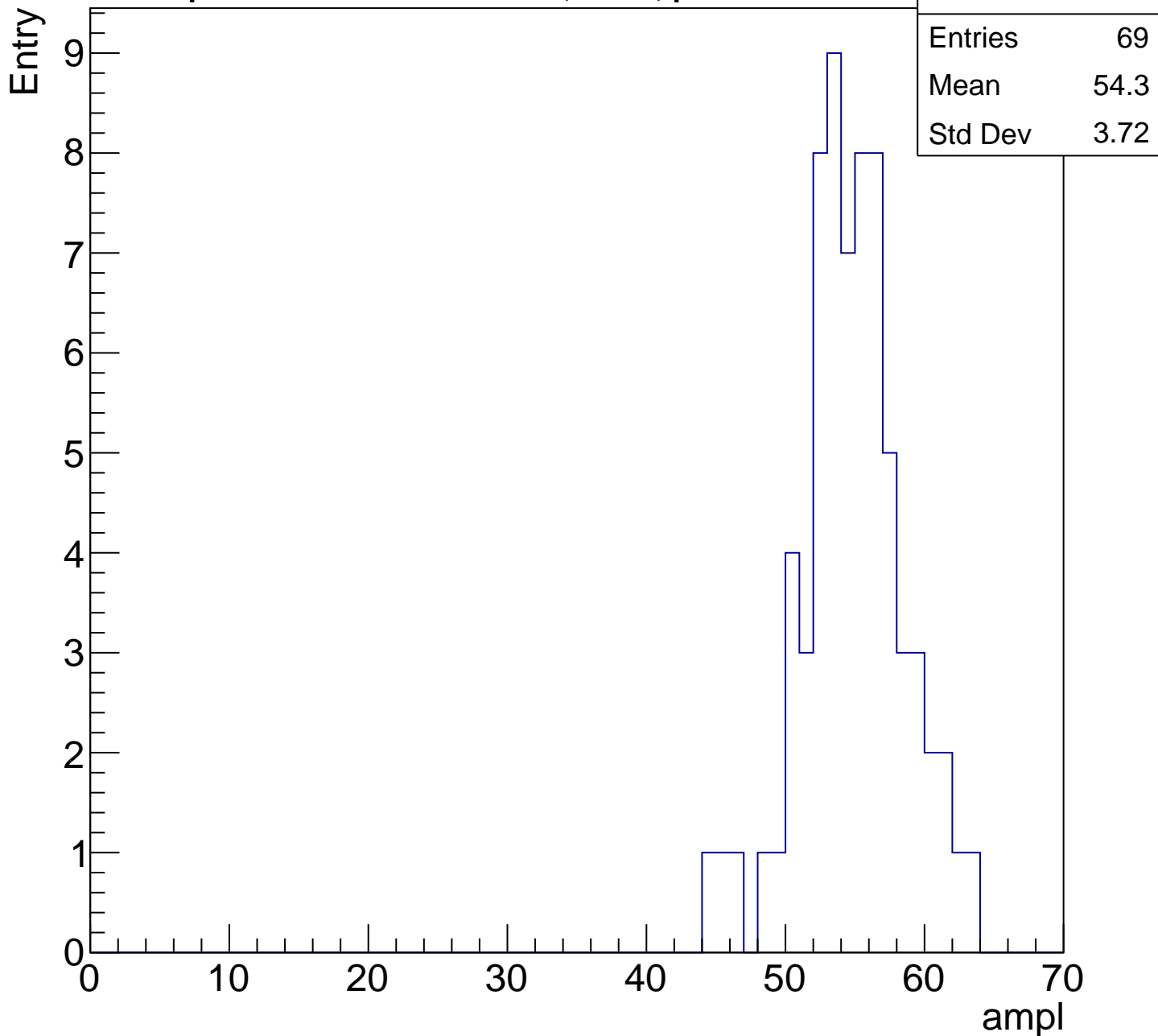
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch74, adc4

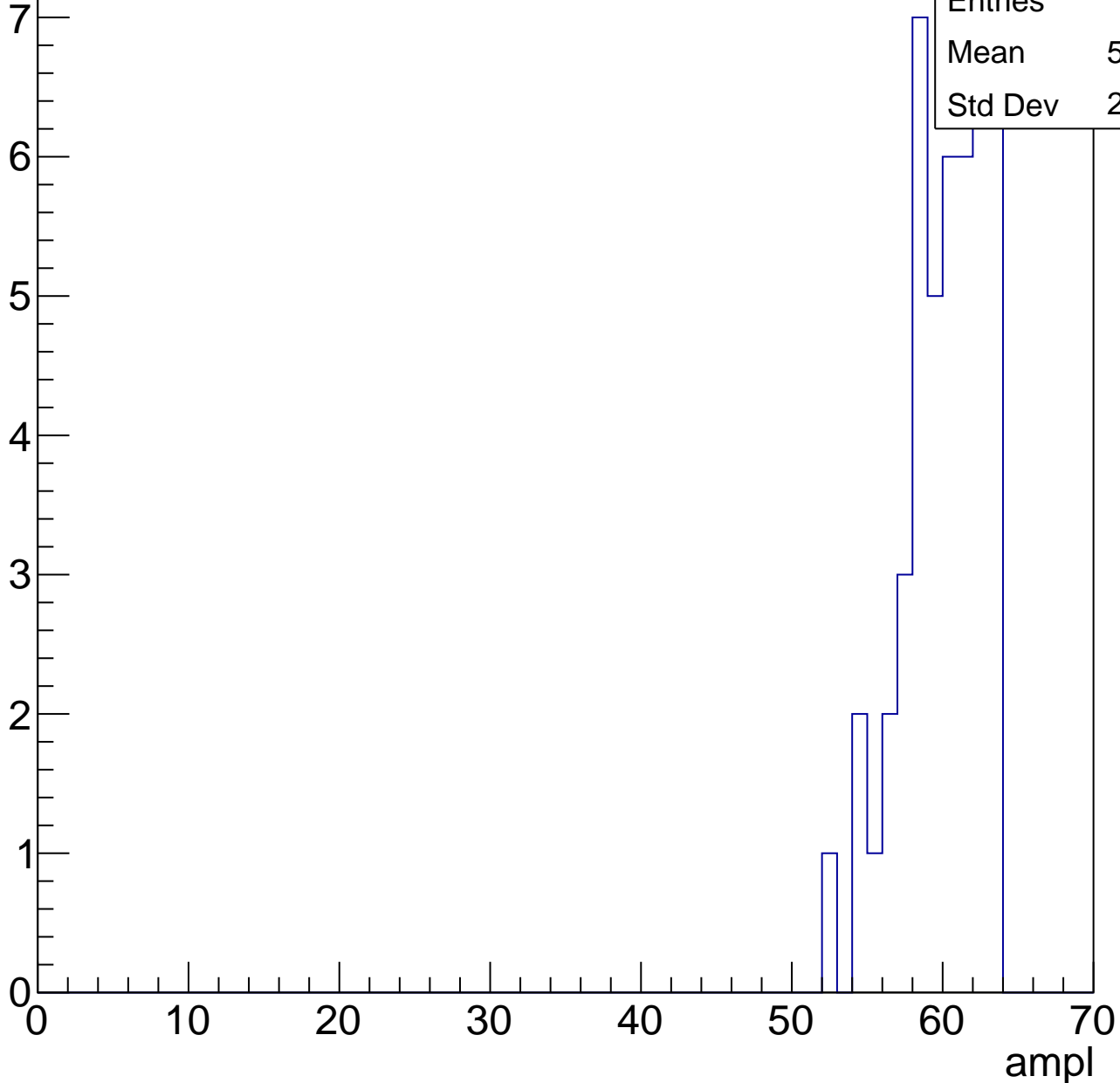
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

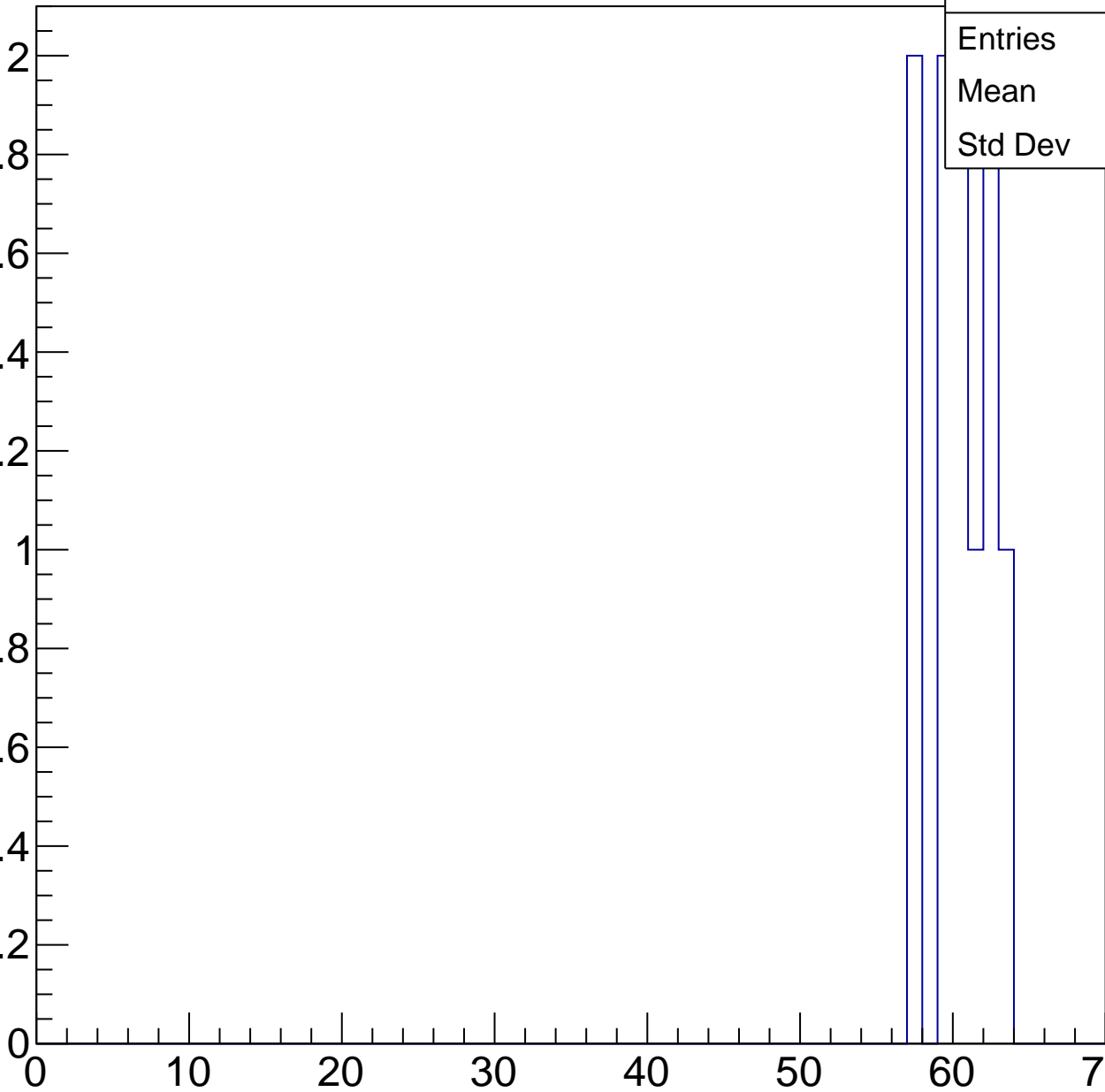
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60
Std Dev	1.949

0 10 20 30 40 50 60 70

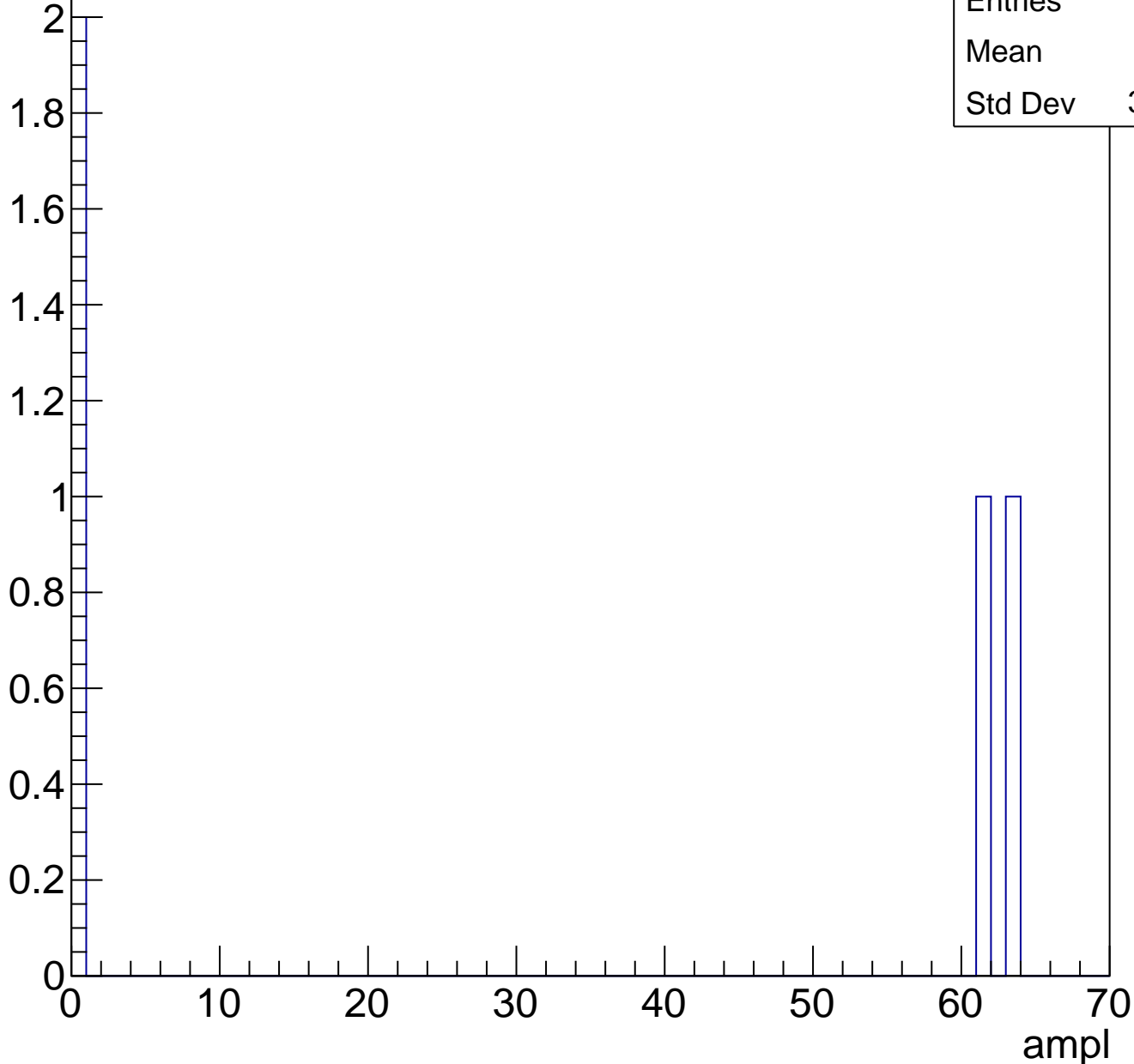
ampl



B1L101S, U1-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch75, adc0

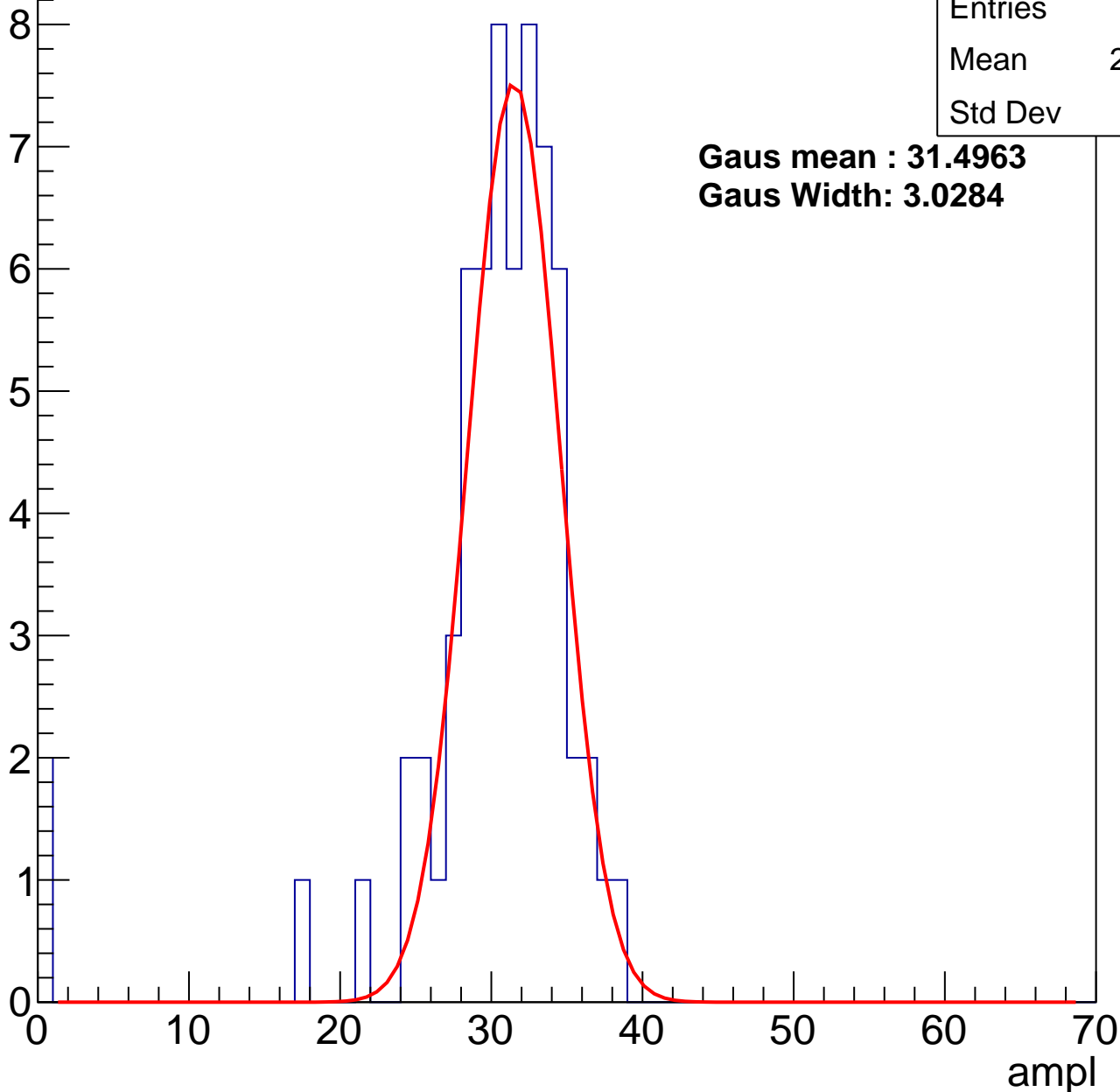
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.52
Std Dev	6.4

Gaus mean : 31.4963

Gaus Width: 3.0284



B1L101S, U1-ch75, adc1

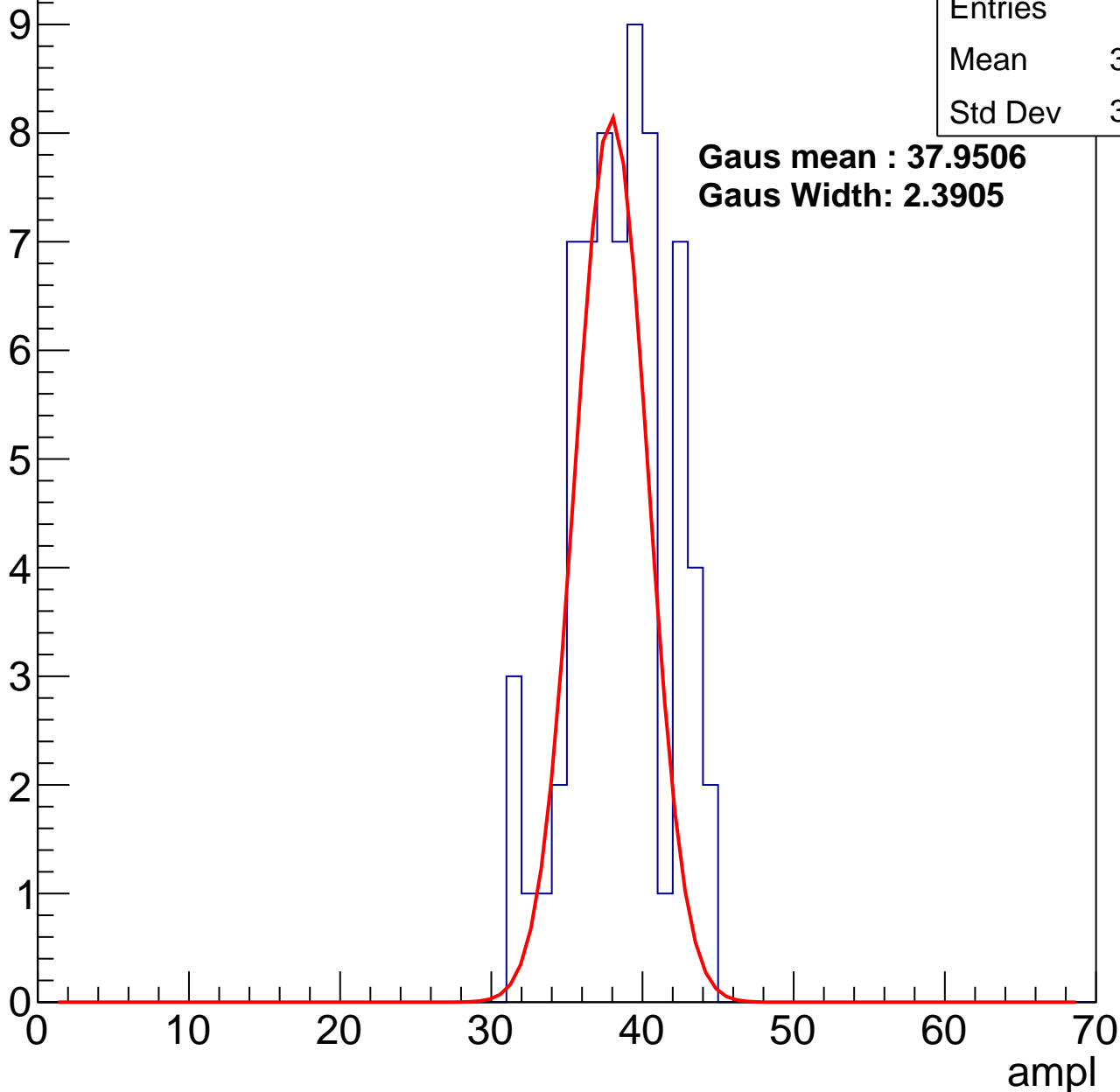
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	38.07
Std Dev	3.168

Gaus mean : 37.9506

Gaus Width: 2.3905



B1L101S, U1-ch75, adc2

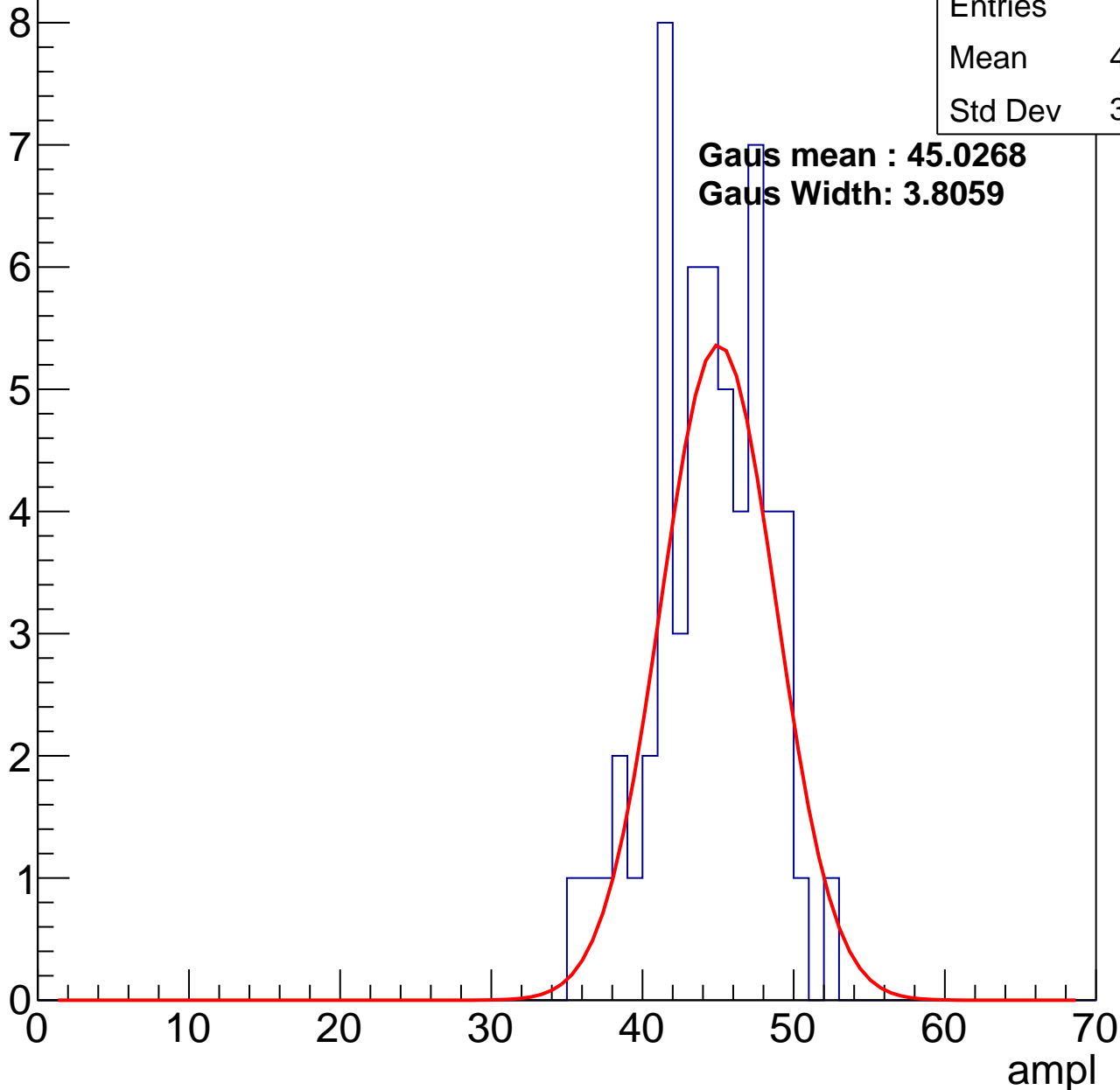
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.98
Std Dev	3.654

Gaus mean : 45.0268

Gaus Width: 3.8059

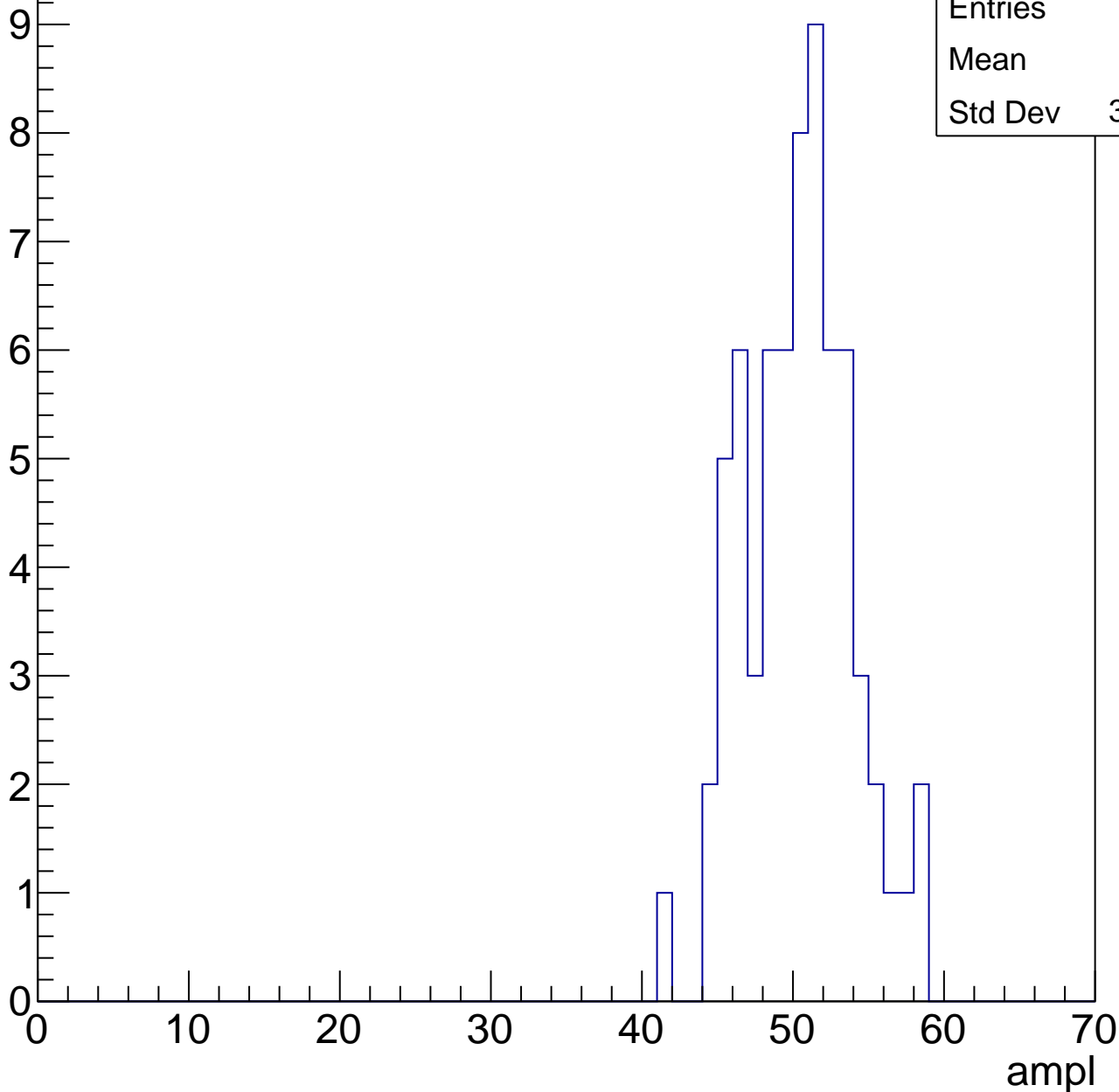


B1L101S, U1-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

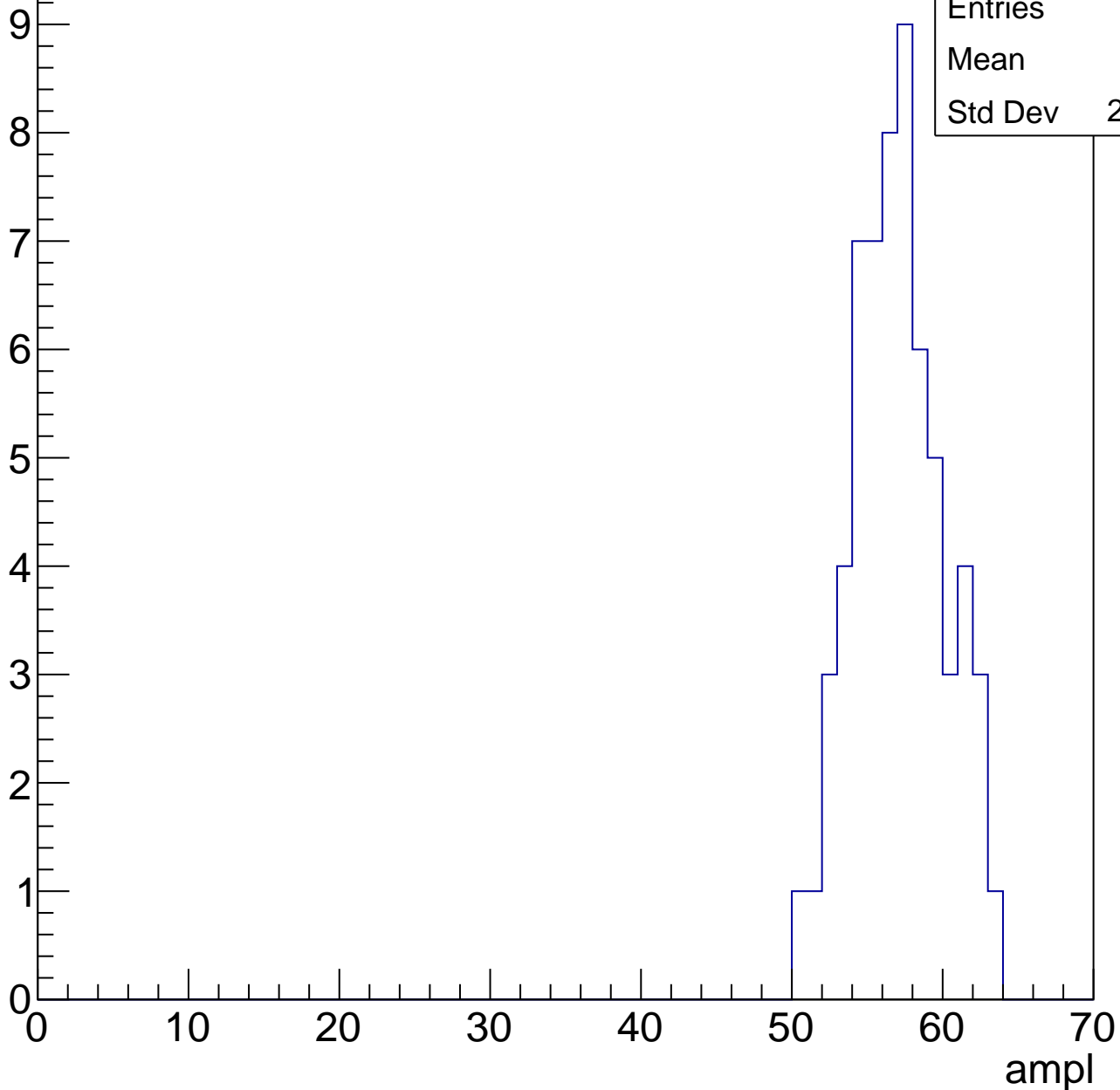
Entries	67
Mean	49.9
Std Dev	3.516



B1L101S, U1-ch75, adc4

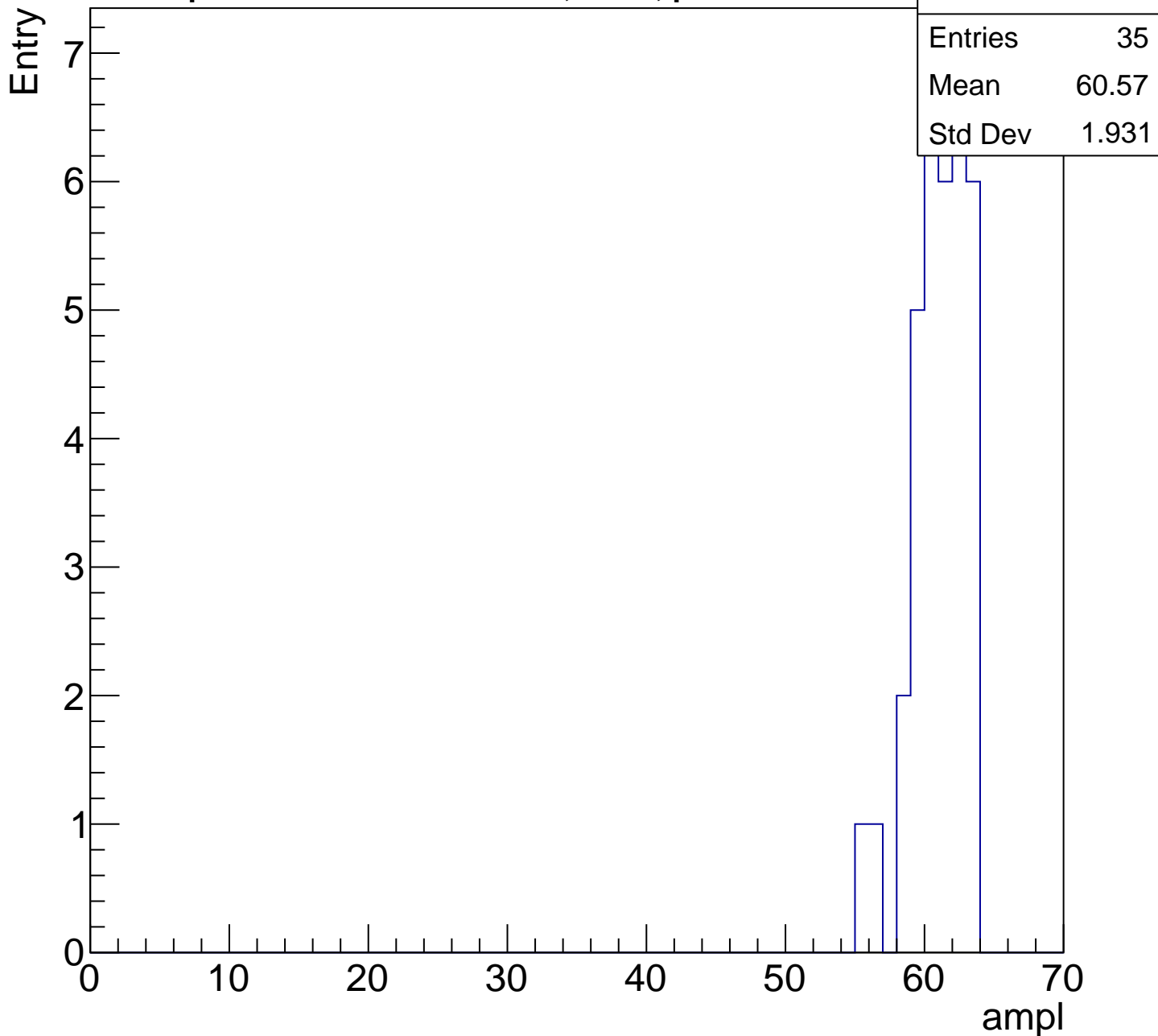
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch75, adc5

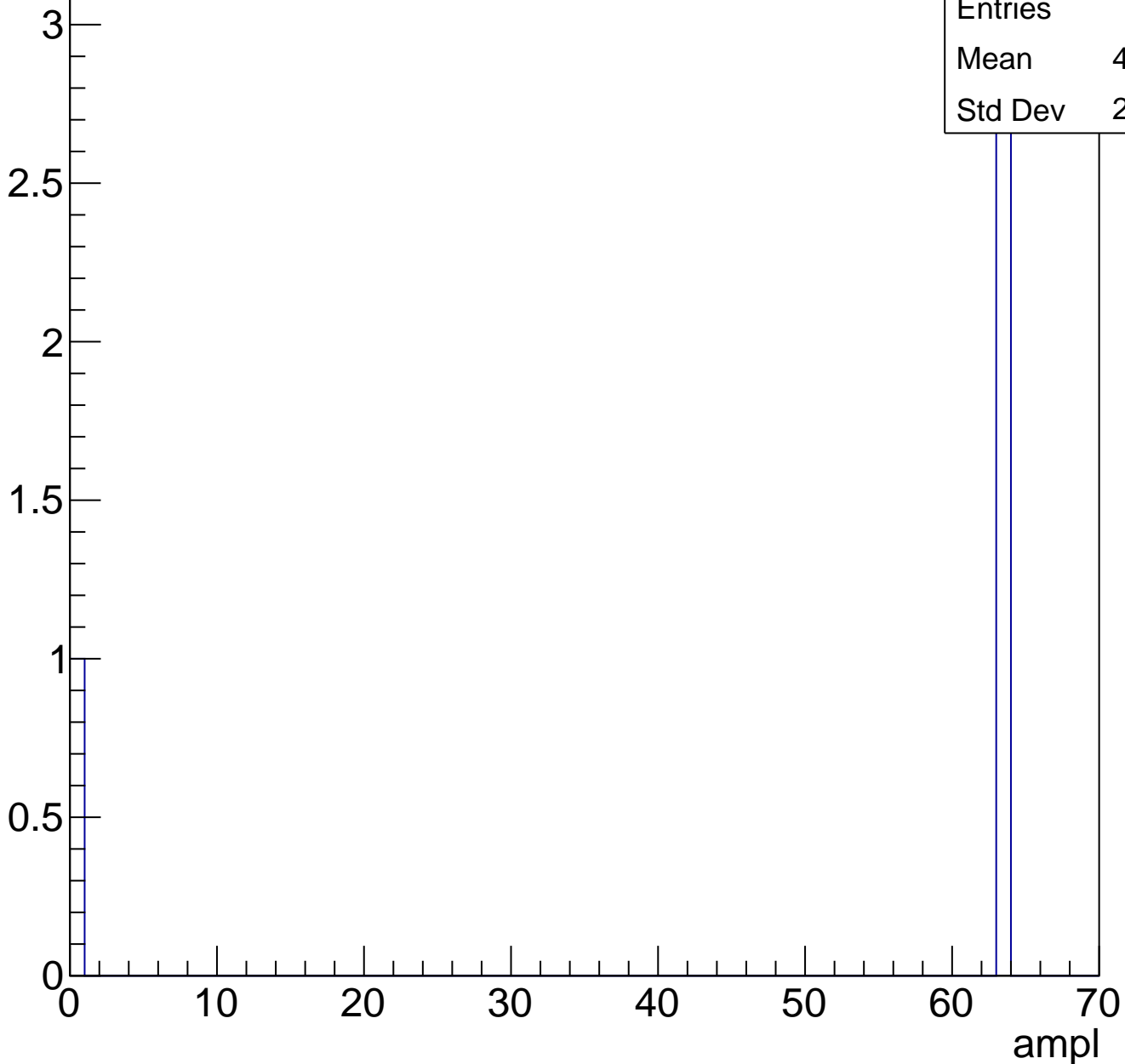
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch76, adc0

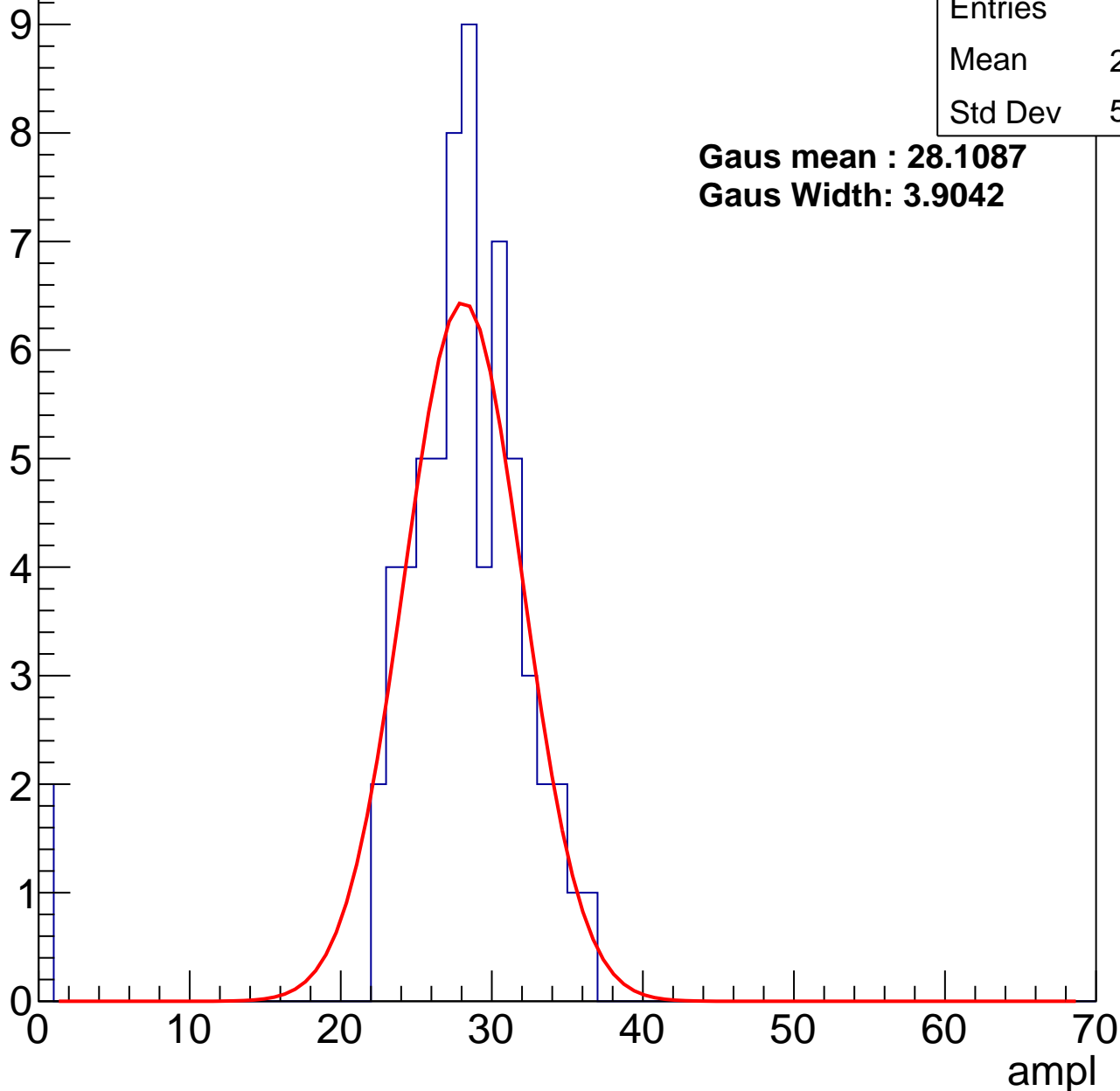
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.14
Std Dev	5.849

Gaus mean : 28.1087

Gaus Width: 3.9042



B1L101S, U1-ch76, adc1

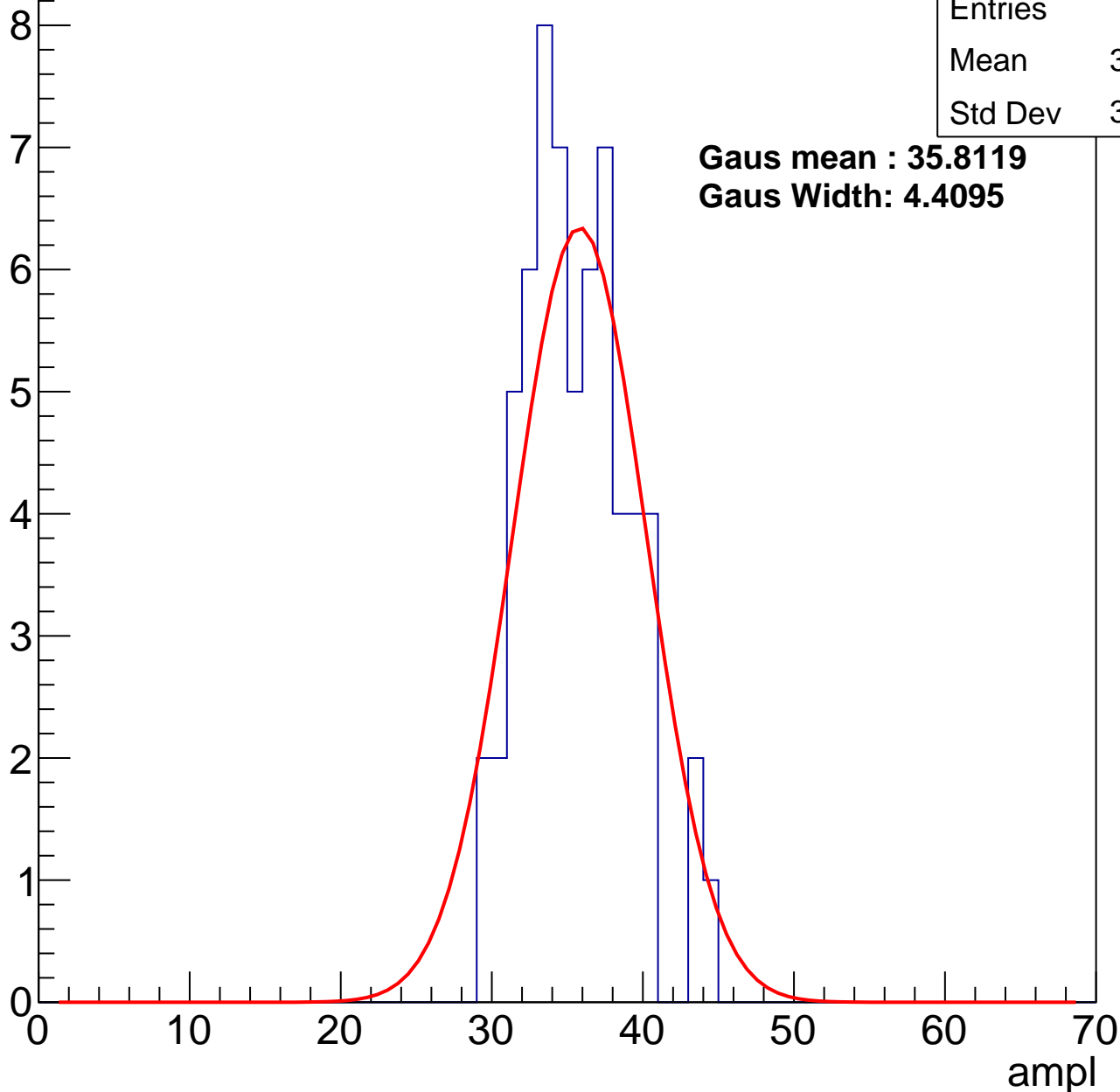
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.16
Std Dev	3.419

Gaus mean : 35.8119

Gaus Width: 4.4095



B1L101S, U1-ch76, adc2

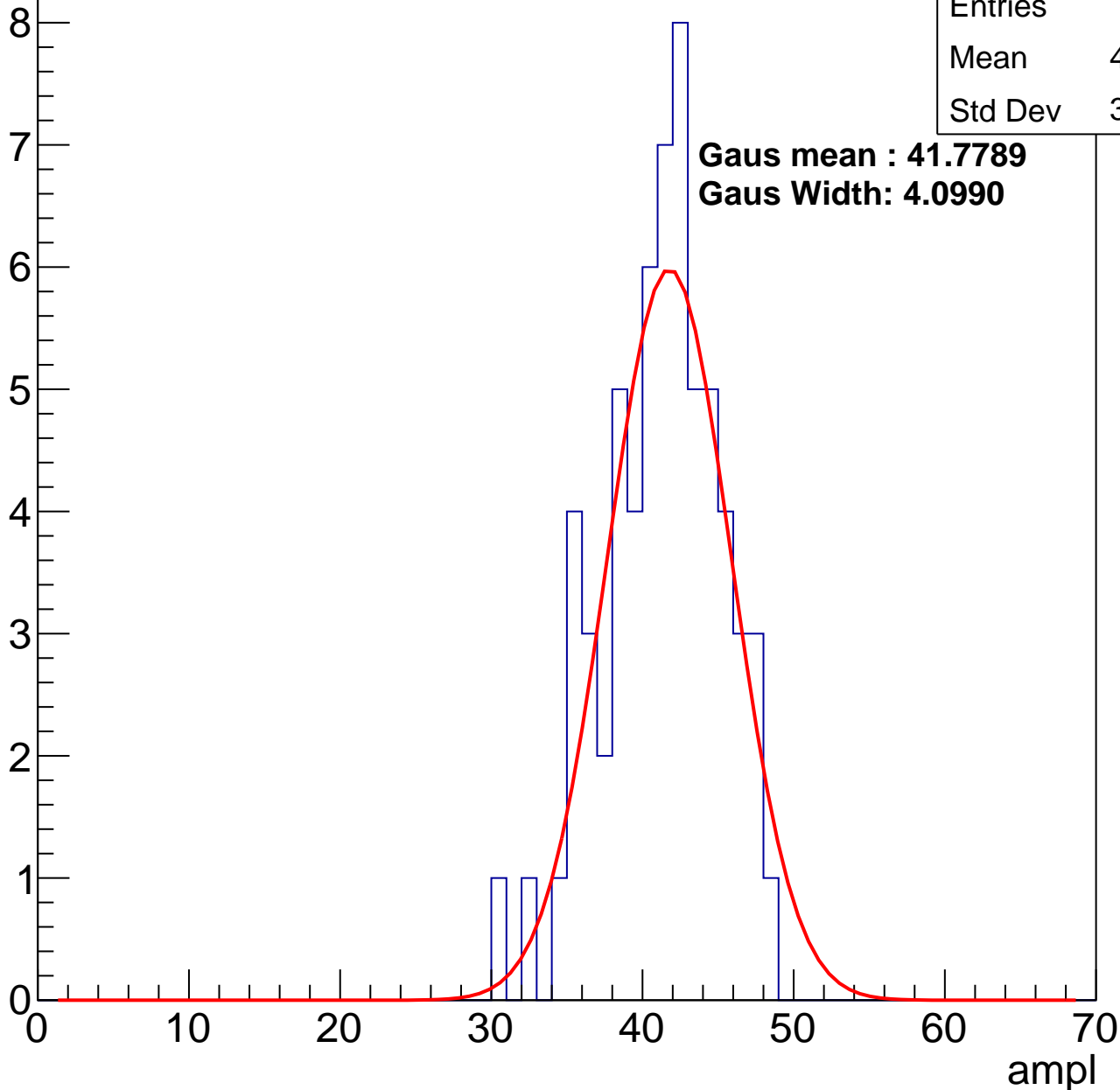
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	40.78
Std Dev	3.852

Gaus mean : 41.7789

Gaus Width: 4.0990

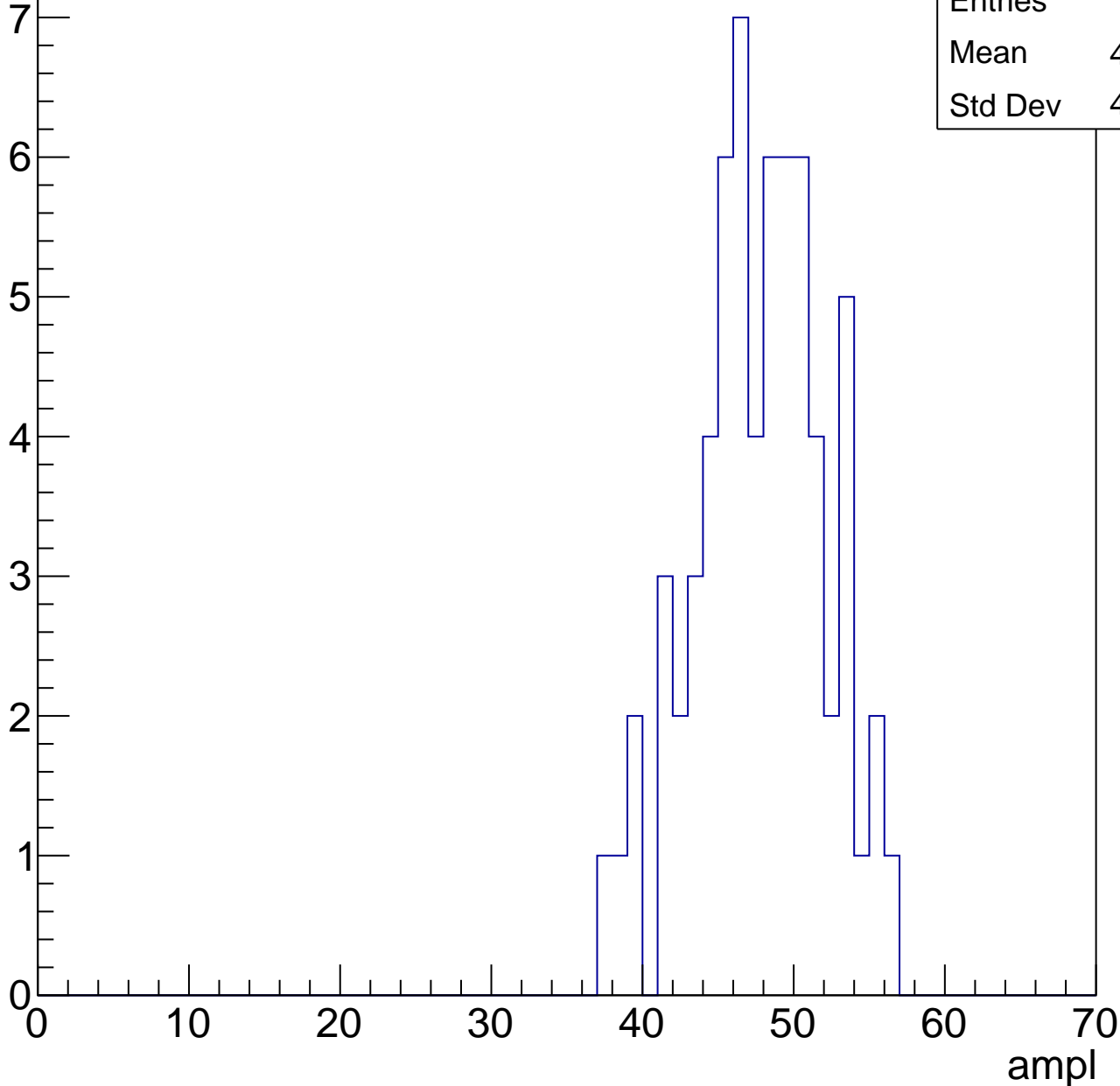


B1L101S, U1-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

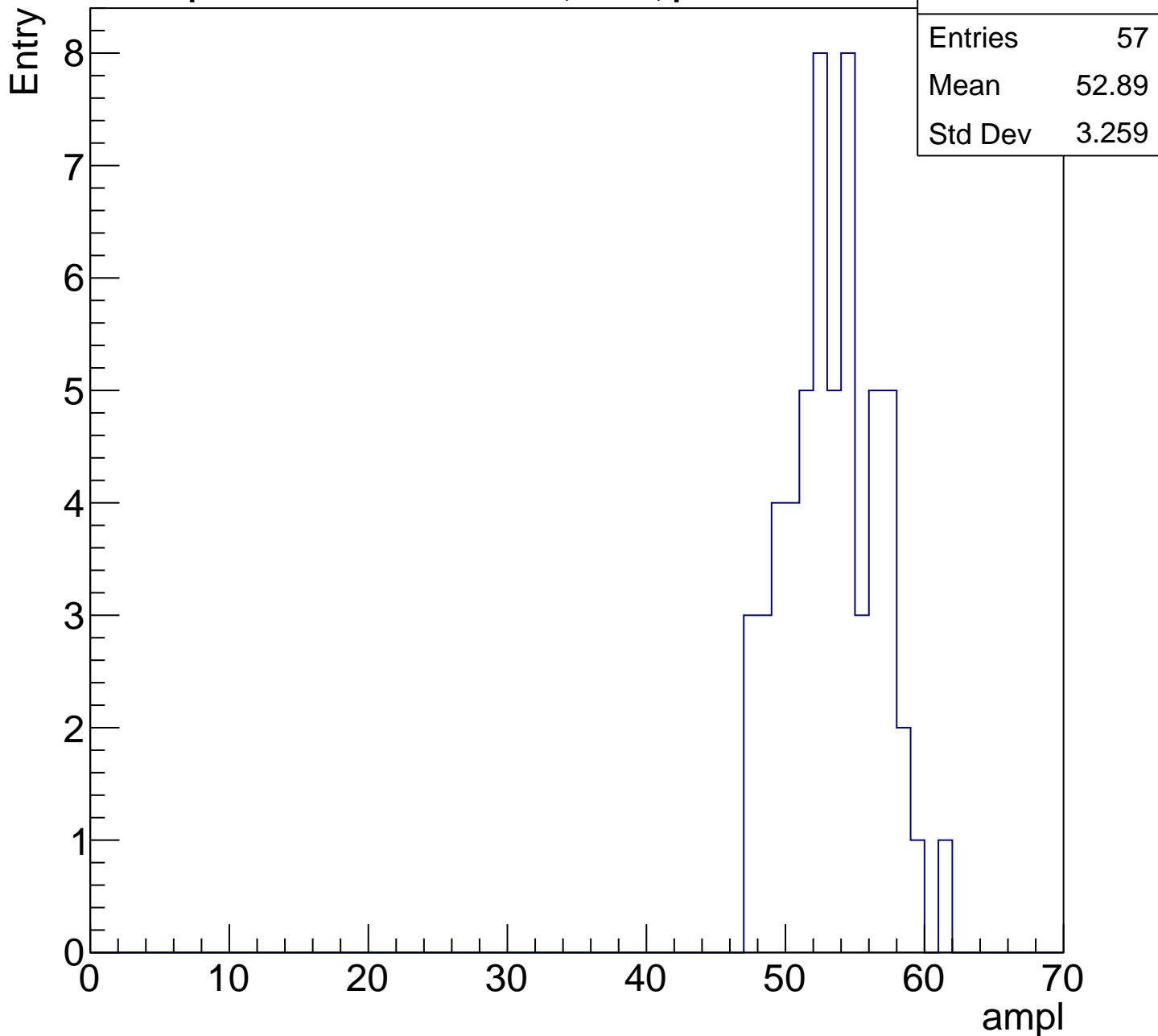
Entry

Entries	66
Mean	47.27
Std Dev	4.287



B1L101S, U1-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

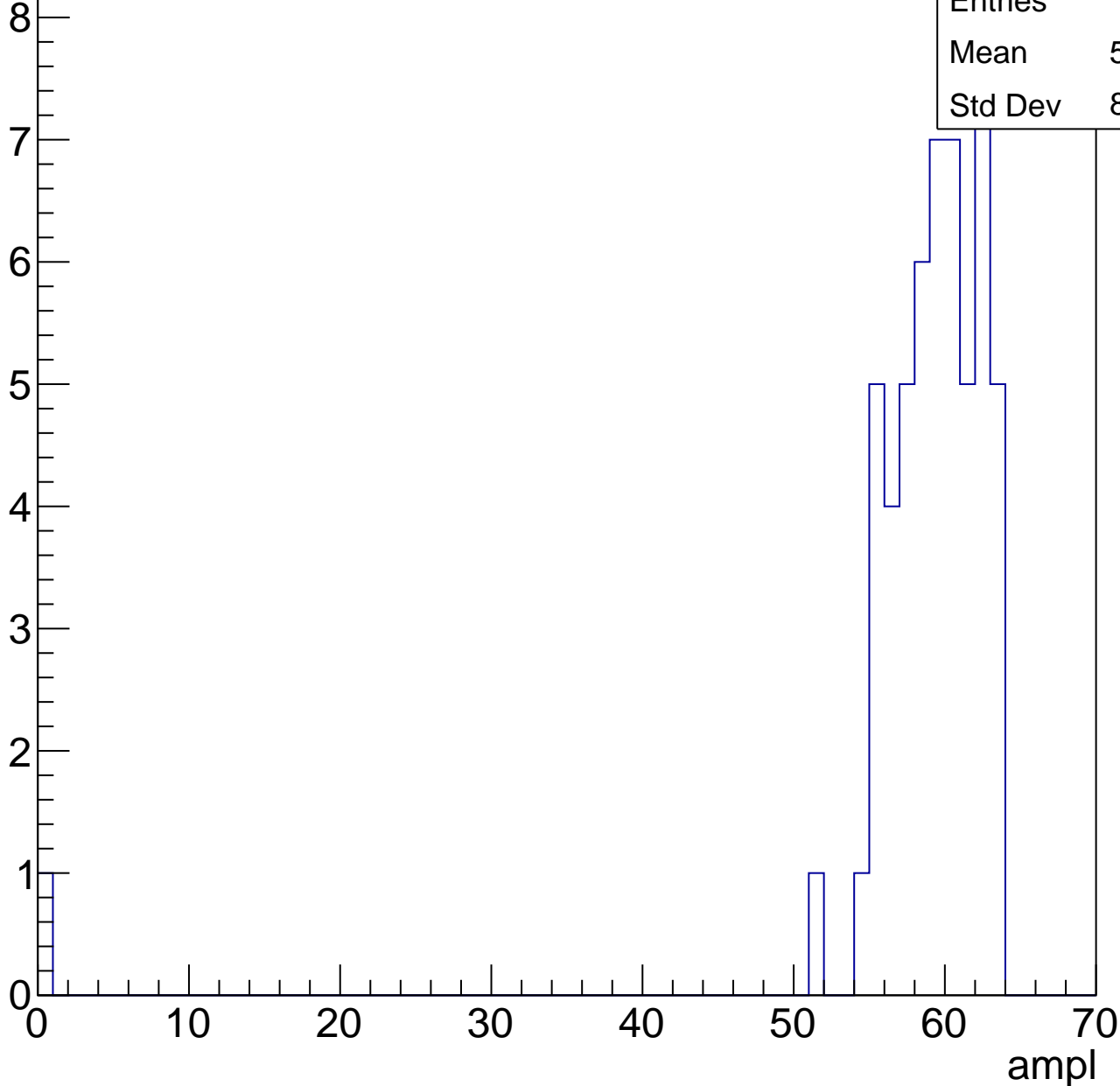


B1L101S, U1-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.93
Std Dev	8.342

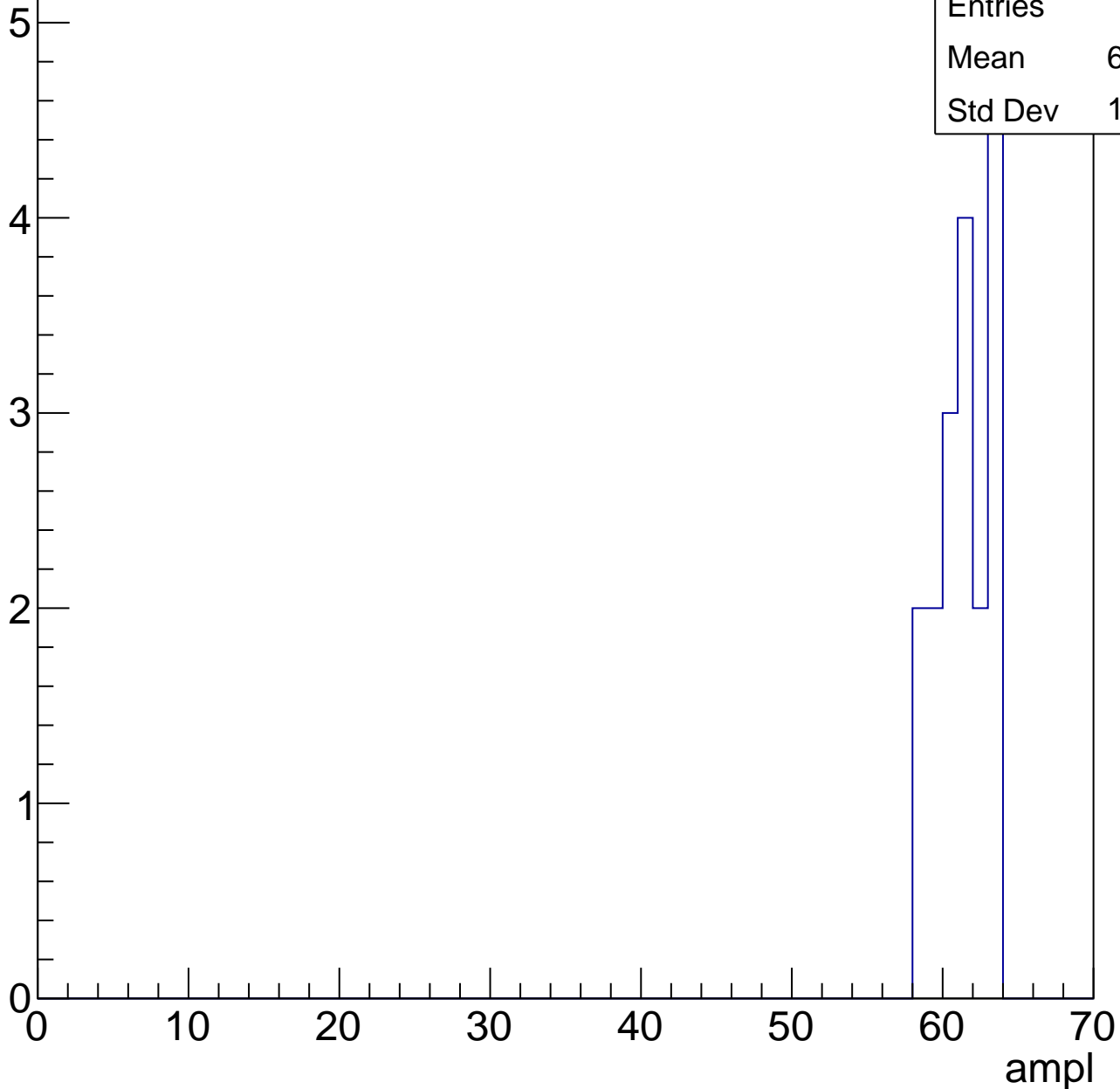


B1L101S, U1-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	60.94
Std Dev	1.682



B1L101S, U1-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch77, adc0

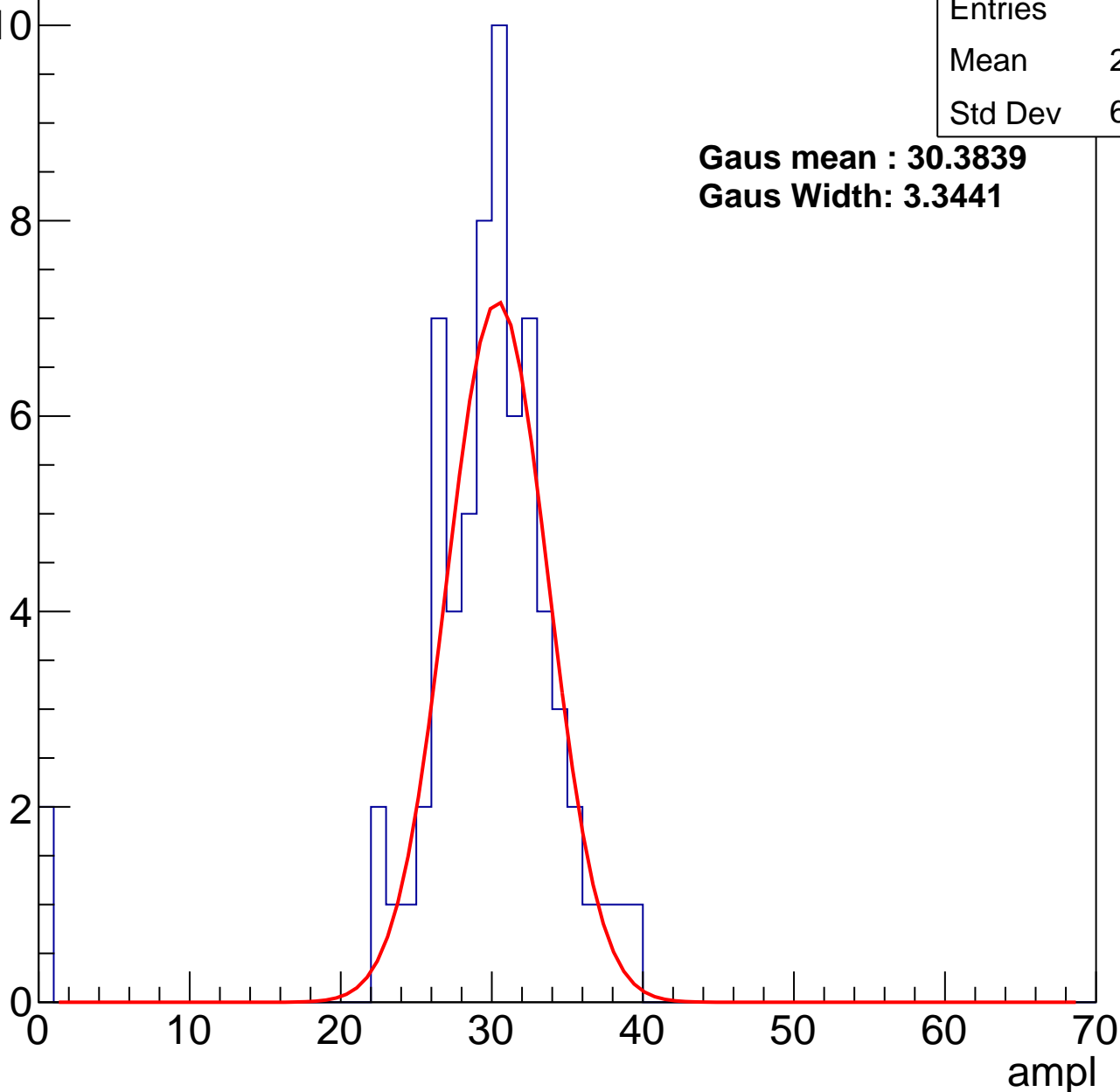
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.93
Std Dev	6.134

Gaus mean : 30.3839

Gaus Width: 3.3441



B1L101S, U1-ch77, adc1

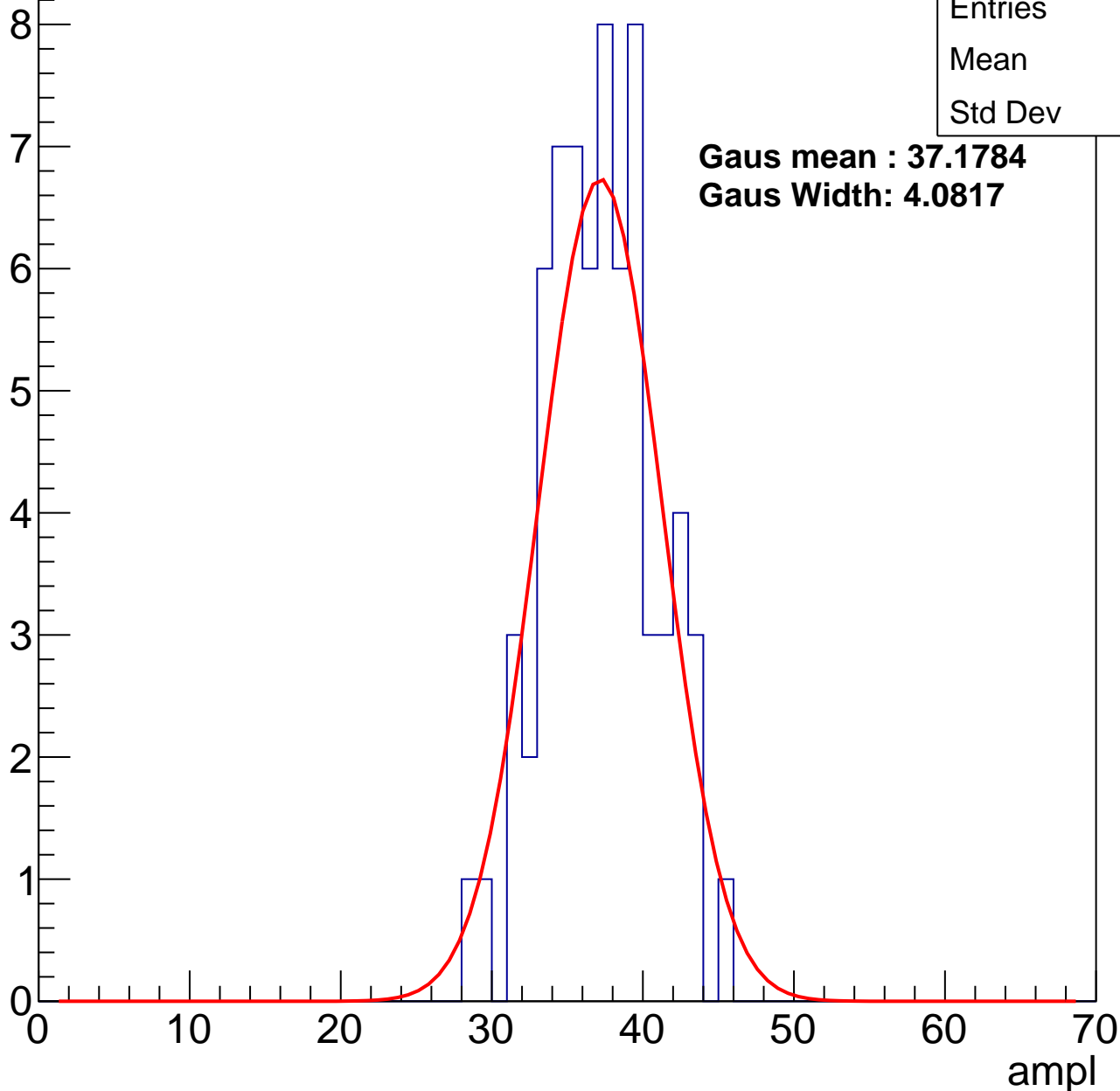
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.7
Std Dev	3.56

Gaus mean : 37.1784

Gaus Width: 4.0817



B1L101S, U1-ch77, adc2

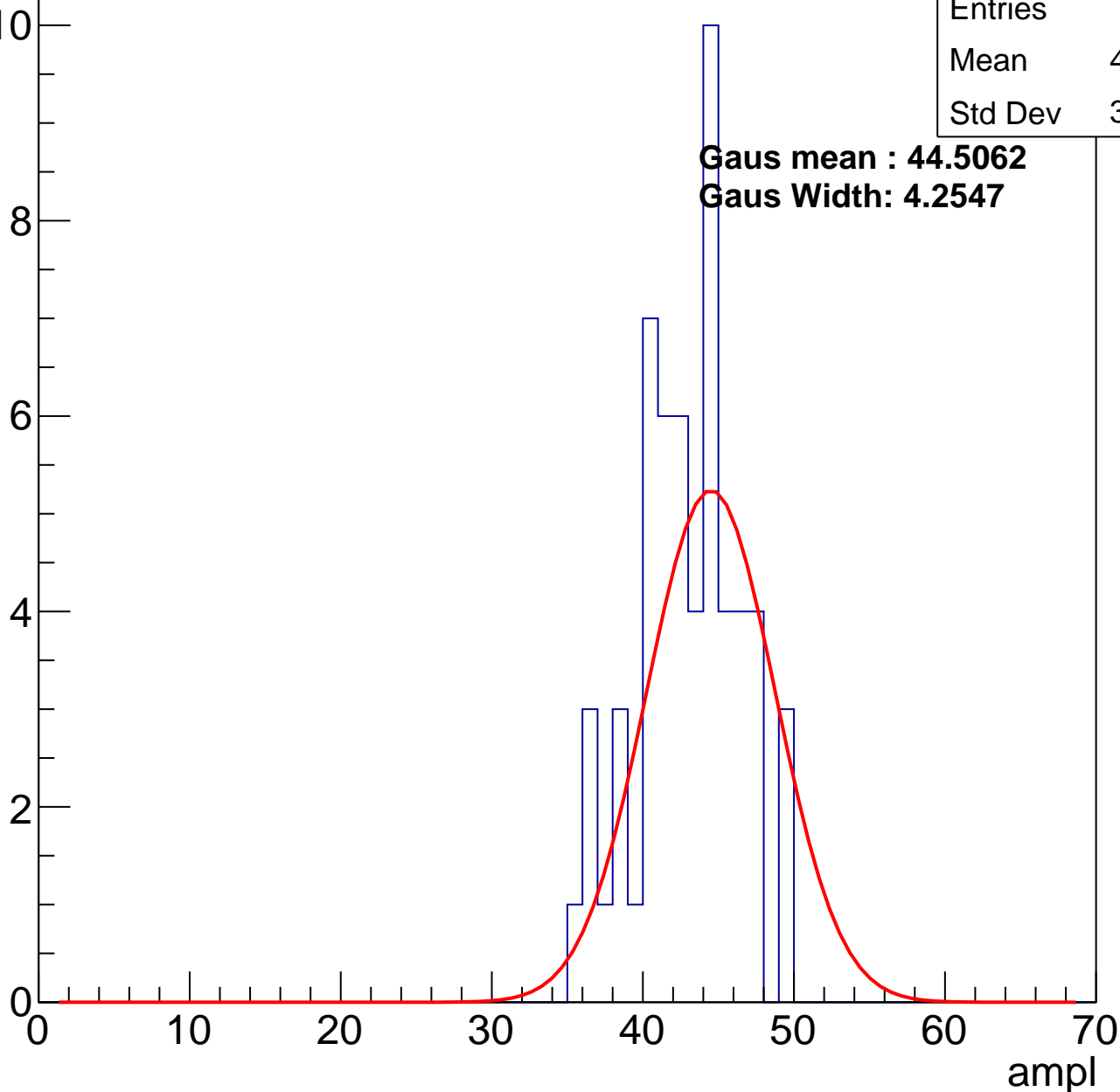
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.49
Std Dev	3.378

Gaus mean : 44.5062

Gaus Width: 4.2547

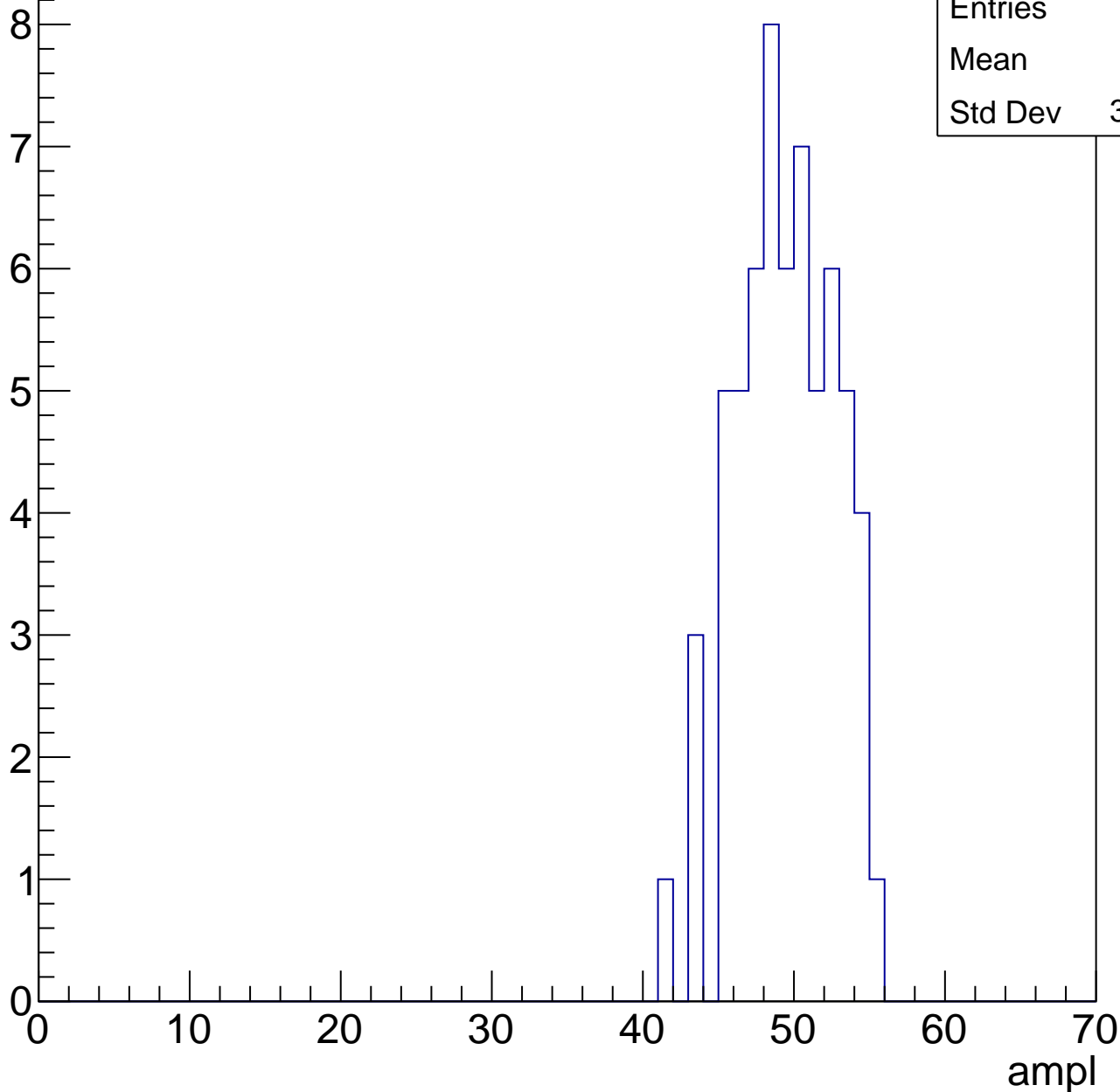


B1L101S, U1-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

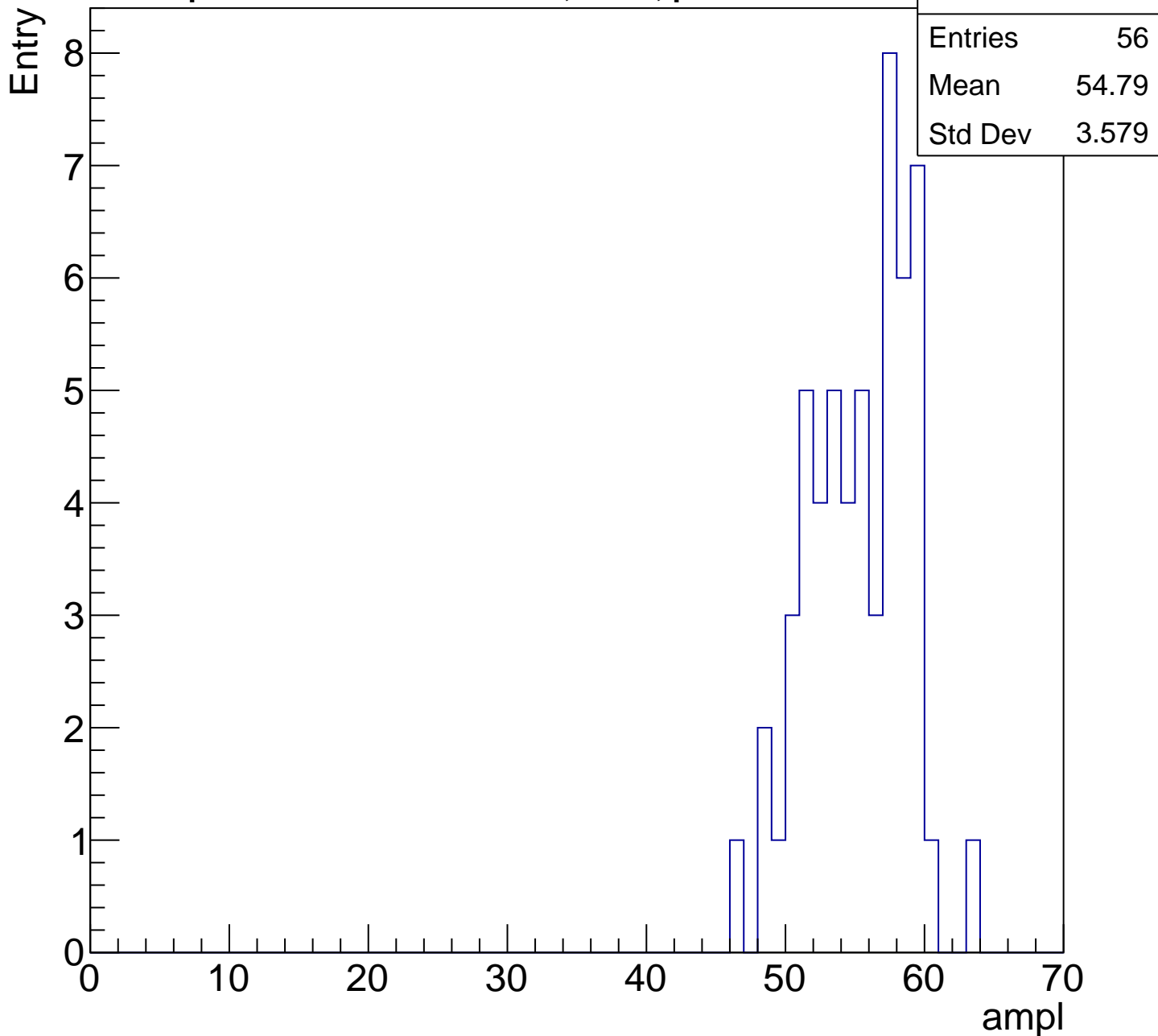
Entry

Entries	62
Mean	49
Std Dev	3.178



B1L101S, U1-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

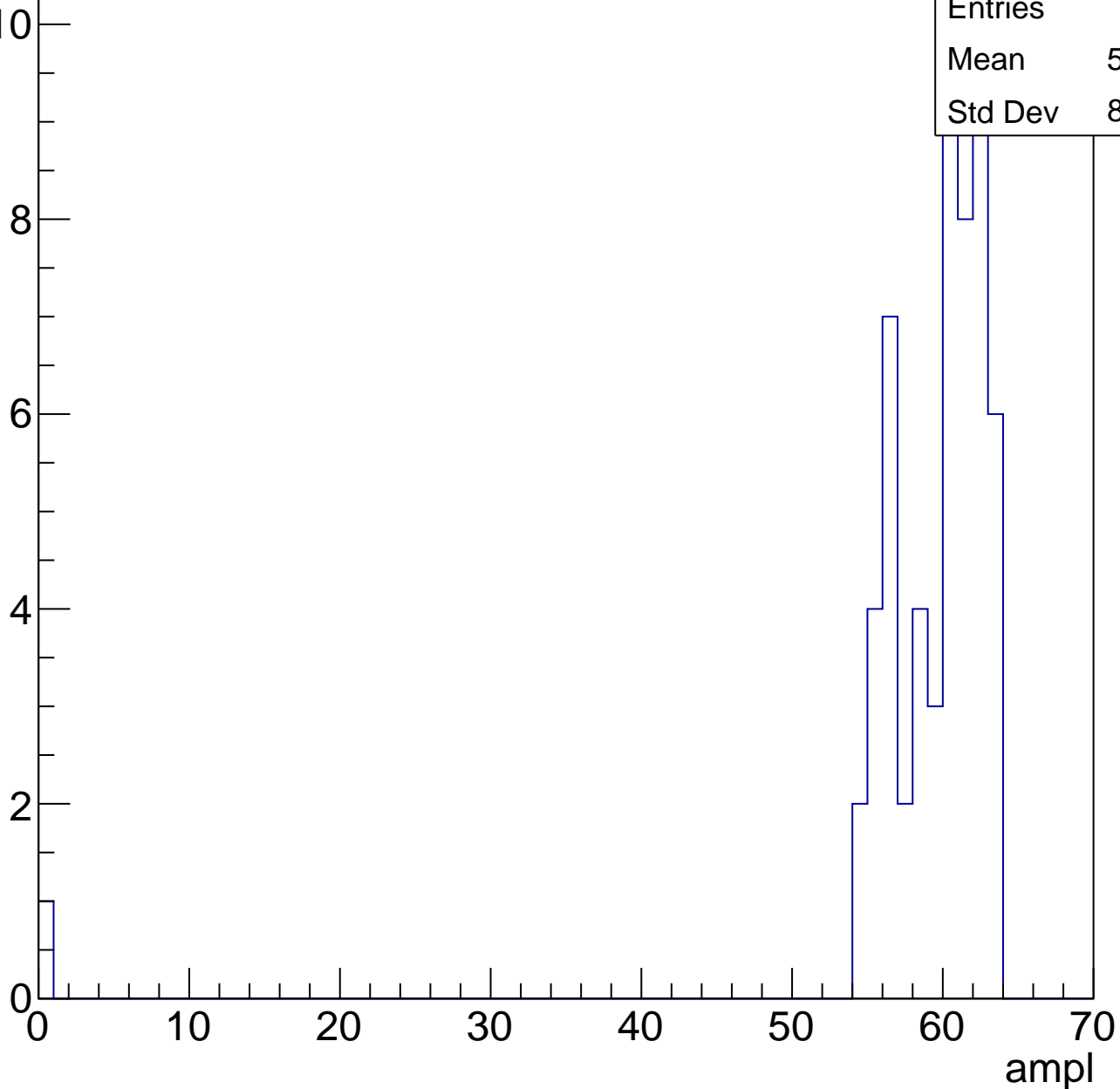


B1L101S, U1-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	58.38
Std Dev	8.319



B1L101S, U1-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L101S, U1-ch78, adc0

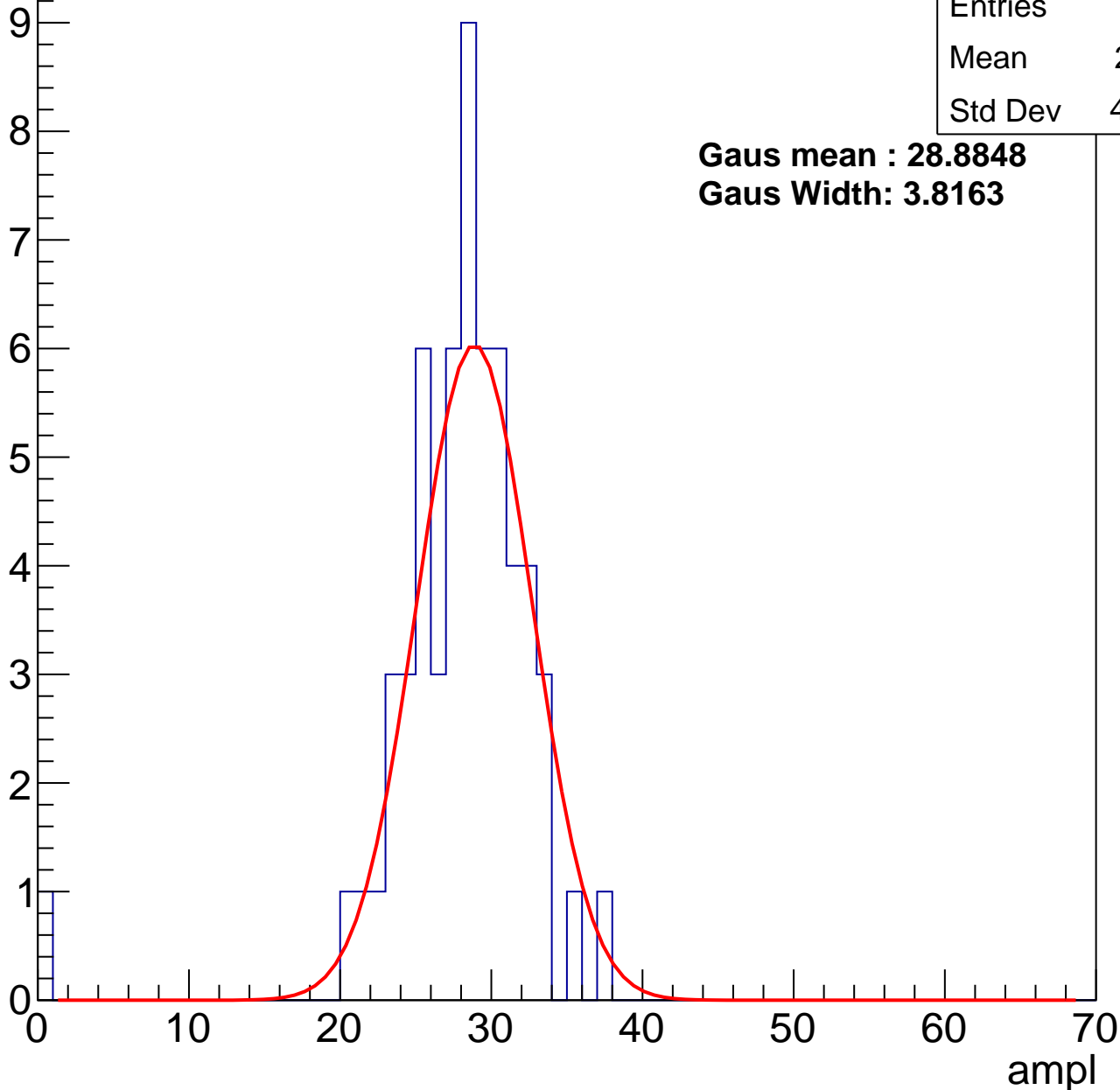
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.51
Std Dev	4.955

Gaus mean : 28.8848

Gaus Width: 3.8163



B1L101S, U1-ch78, adc1

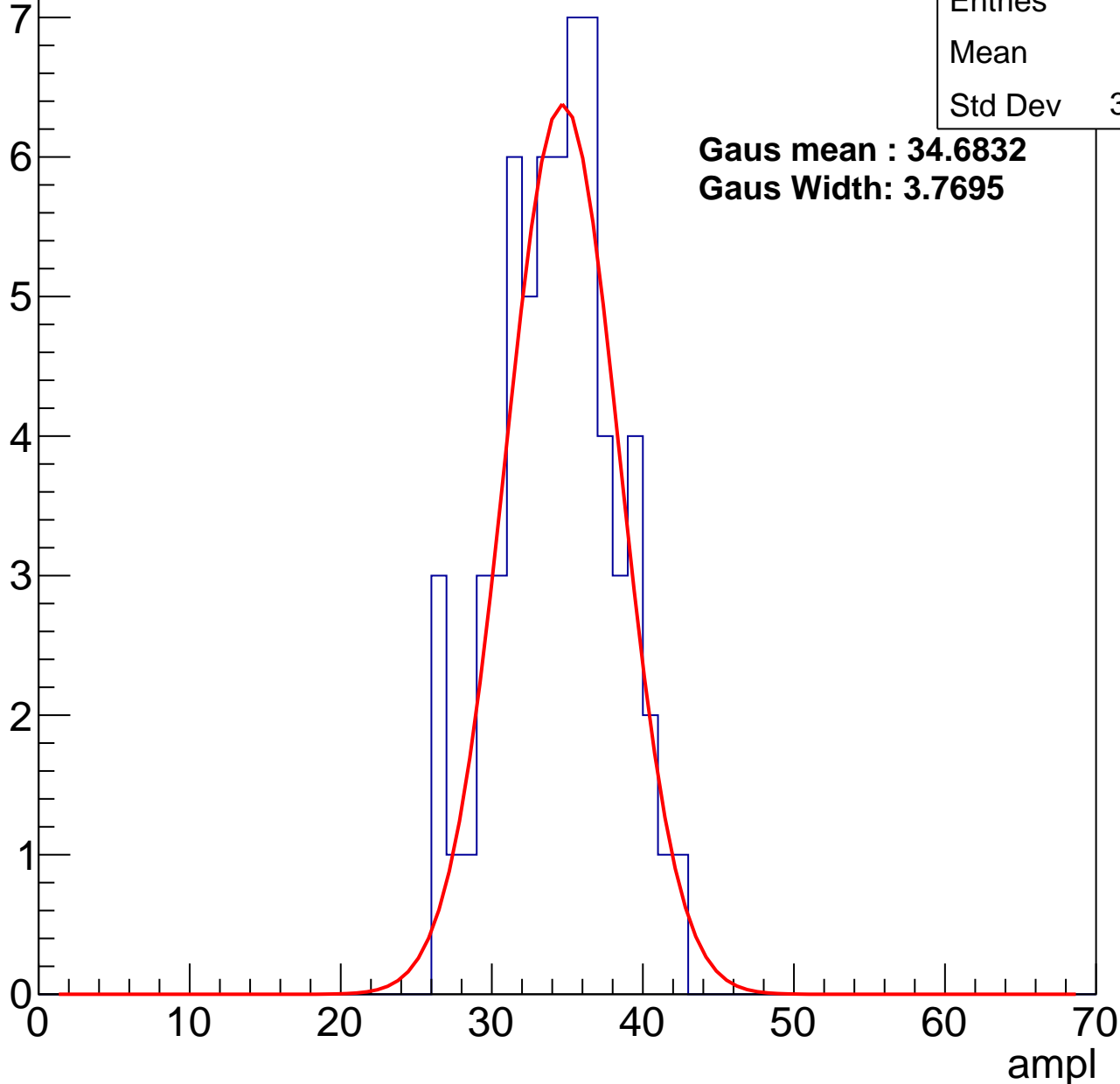
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	33.9
Std Dev	3.745

Gaus mean : 34.6832

Gaus Width: 3.7695



B1L101S, U1-ch78, adc2

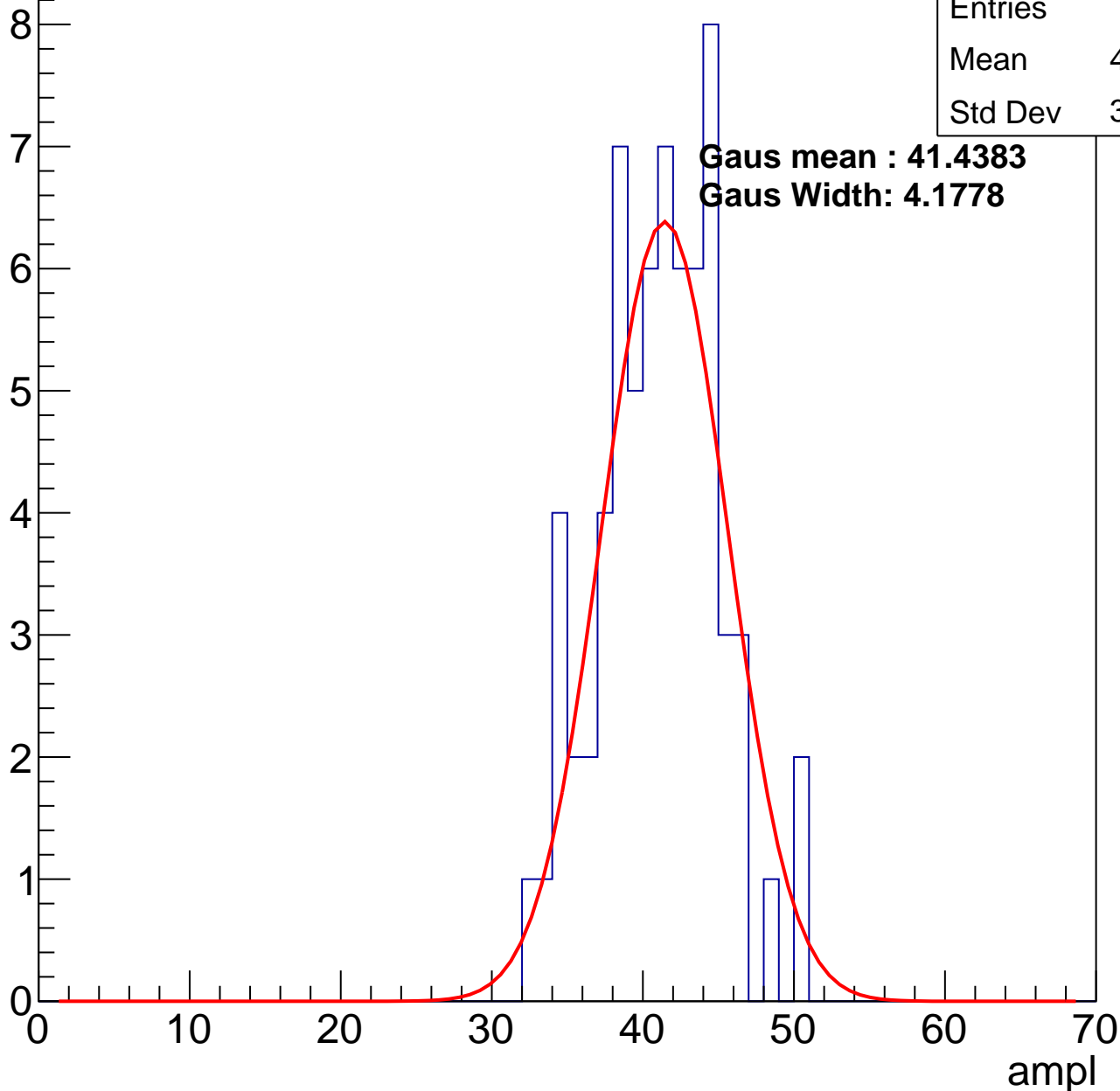
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	40.62
Std Dev	3.915

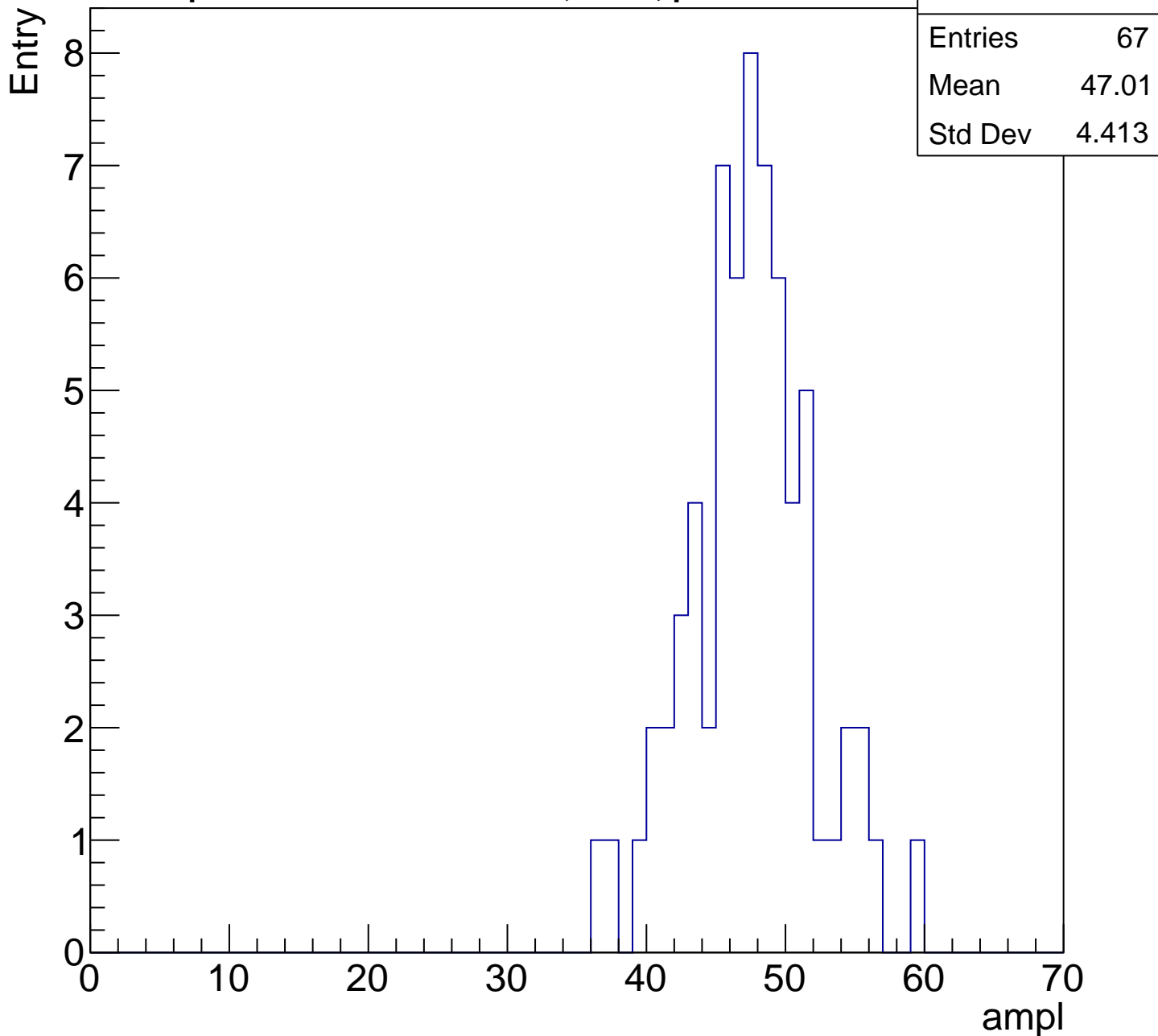
Gaus mean : 41.4383

Gaus Width: 4.1778



B1L101S, U1-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

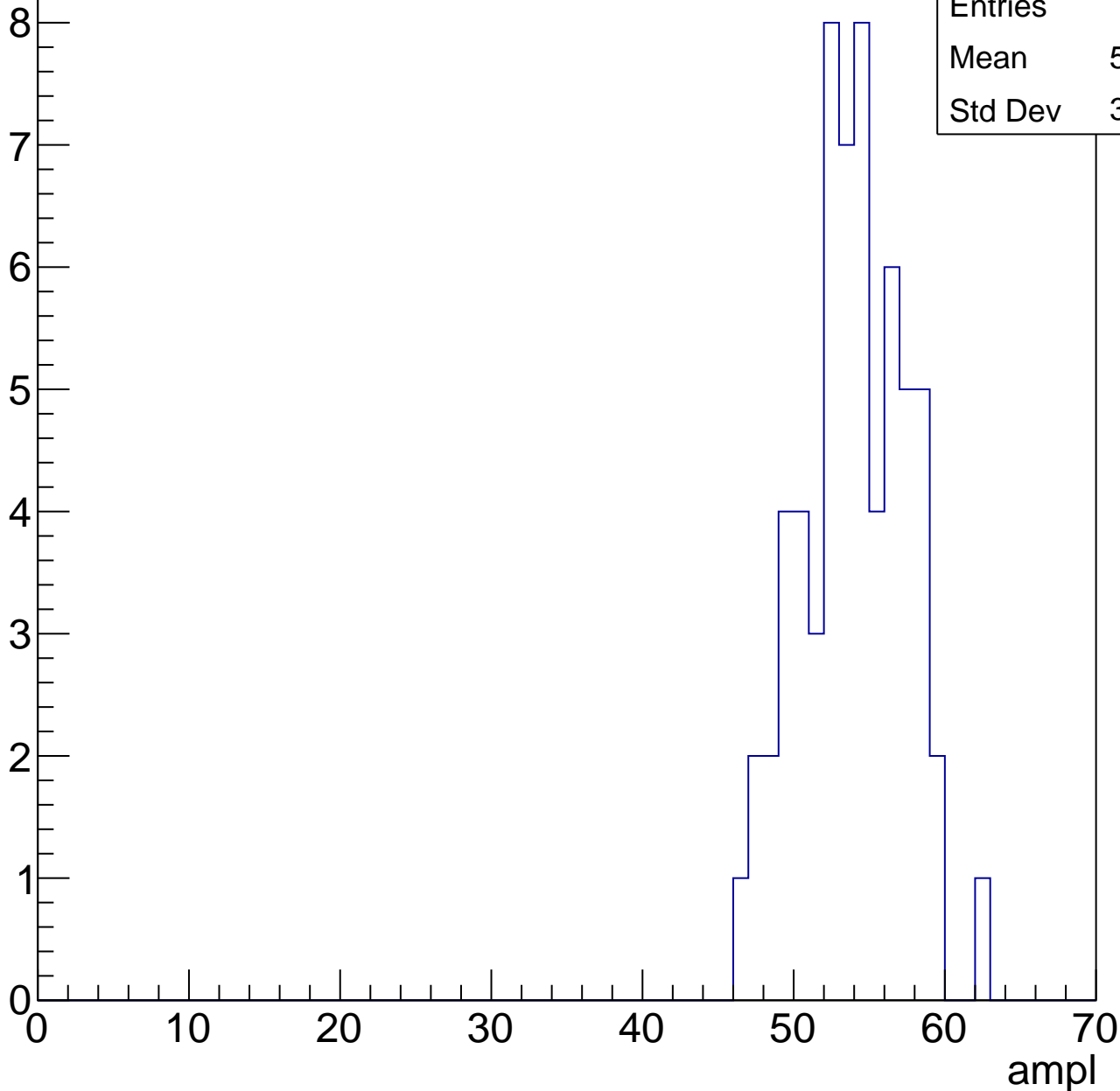


B1L101S, U1-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	53.47
Std Dev	3.392

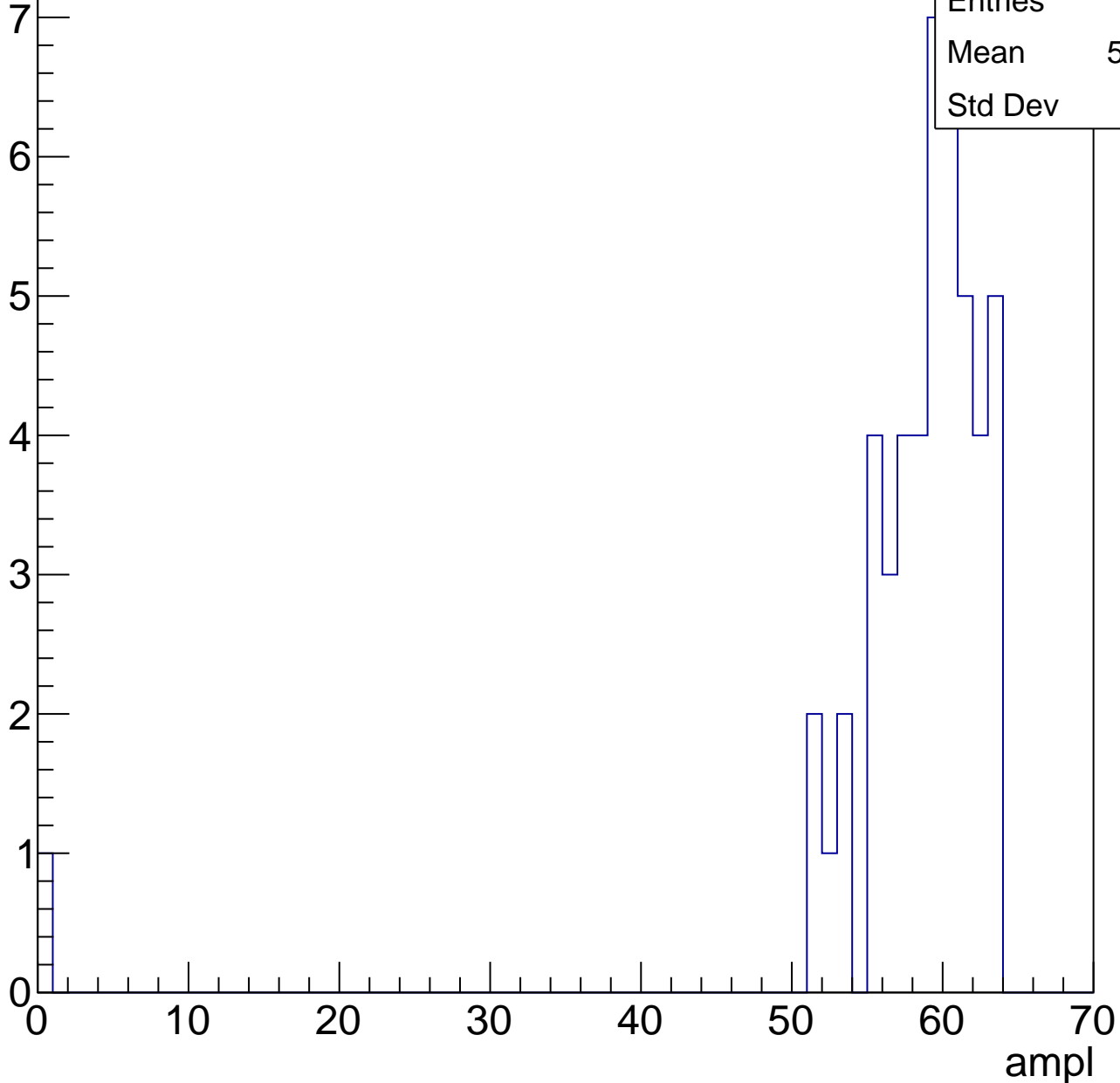


B1L101S, U1-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.33
Std Dev	8.86

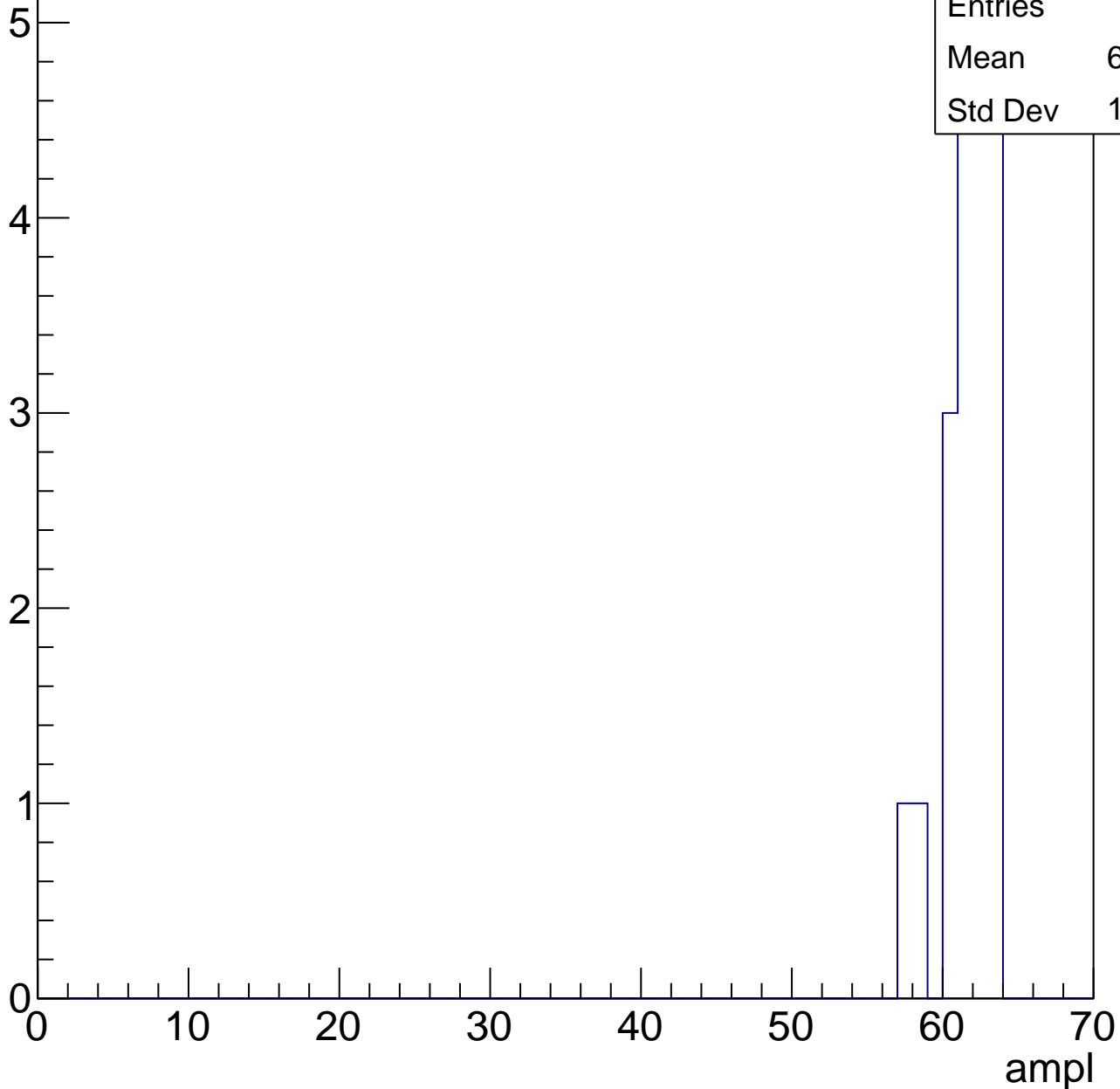


B1L101S, U1-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	61.25
Std Dev	1.609



B1L101S, U1-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch79, adc0

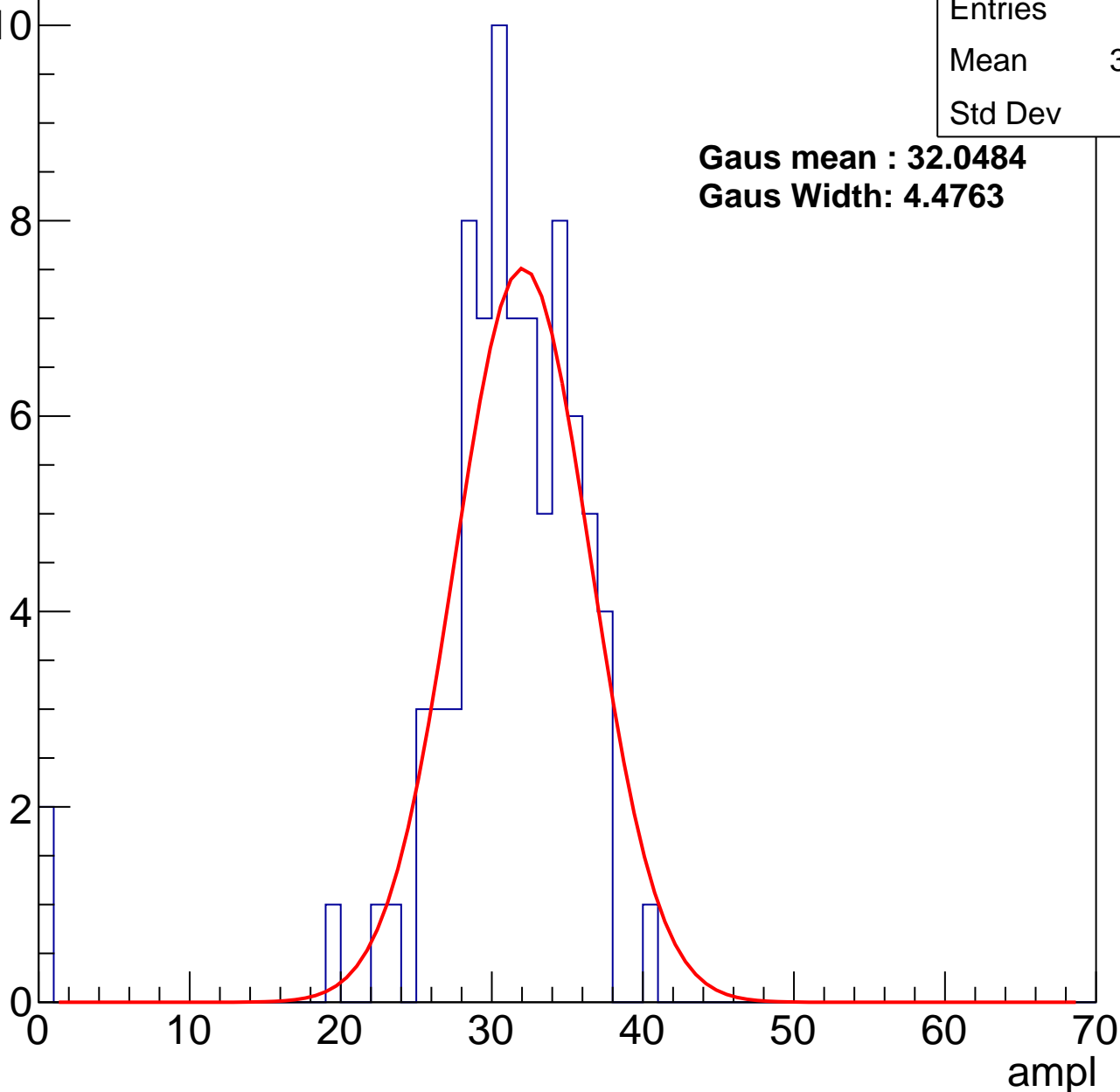
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.26
Std Dev	6.1

Gaus mean : 32.0484

Gaus Width: 4.4763



B1L101S, U1-ch79, adc1

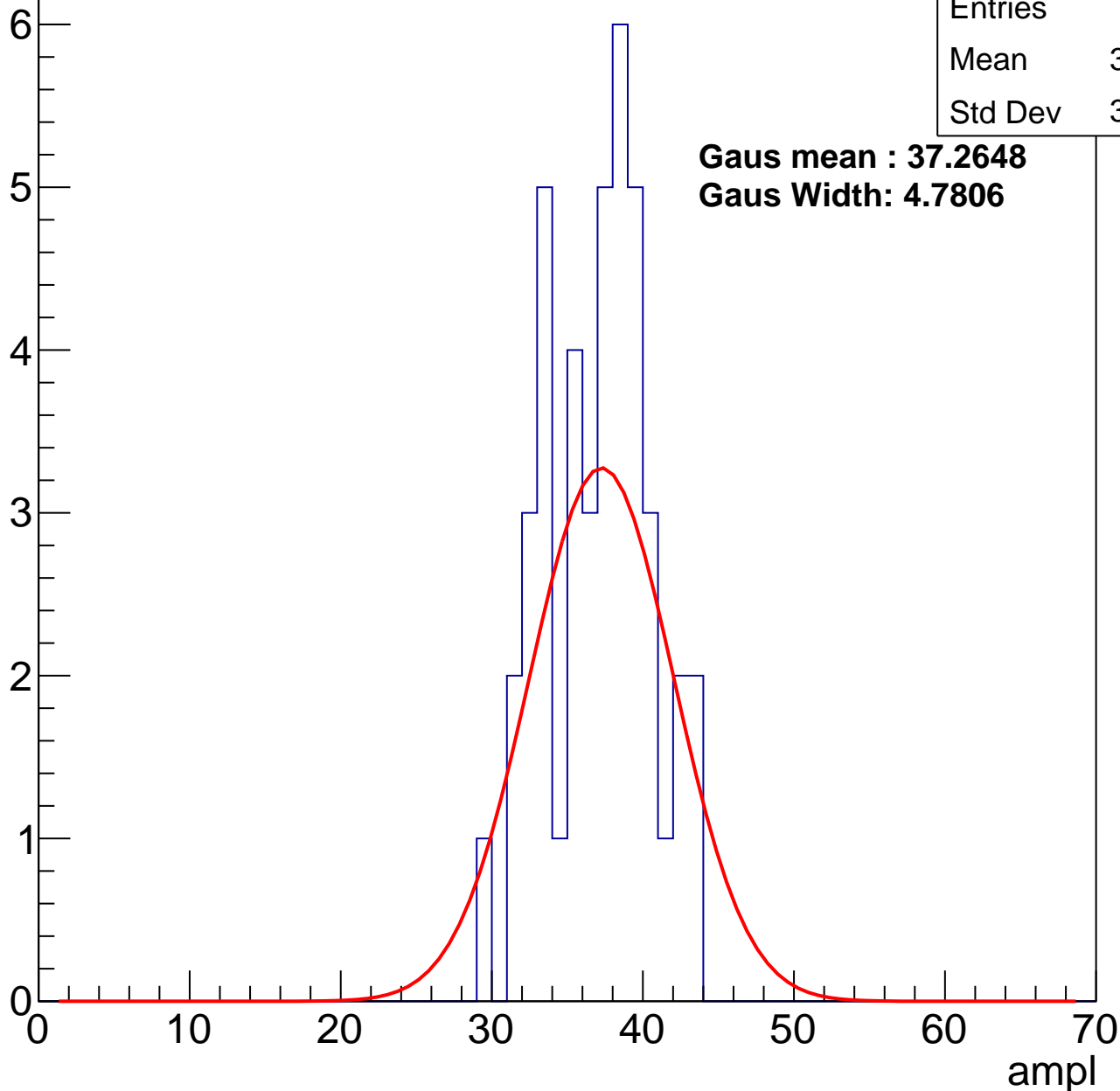
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	36.58
Std Dev	3.418

Gaus mean : 37.2648

Gaus Width: 4.7806



B1L101S, U1-ch79, adc2

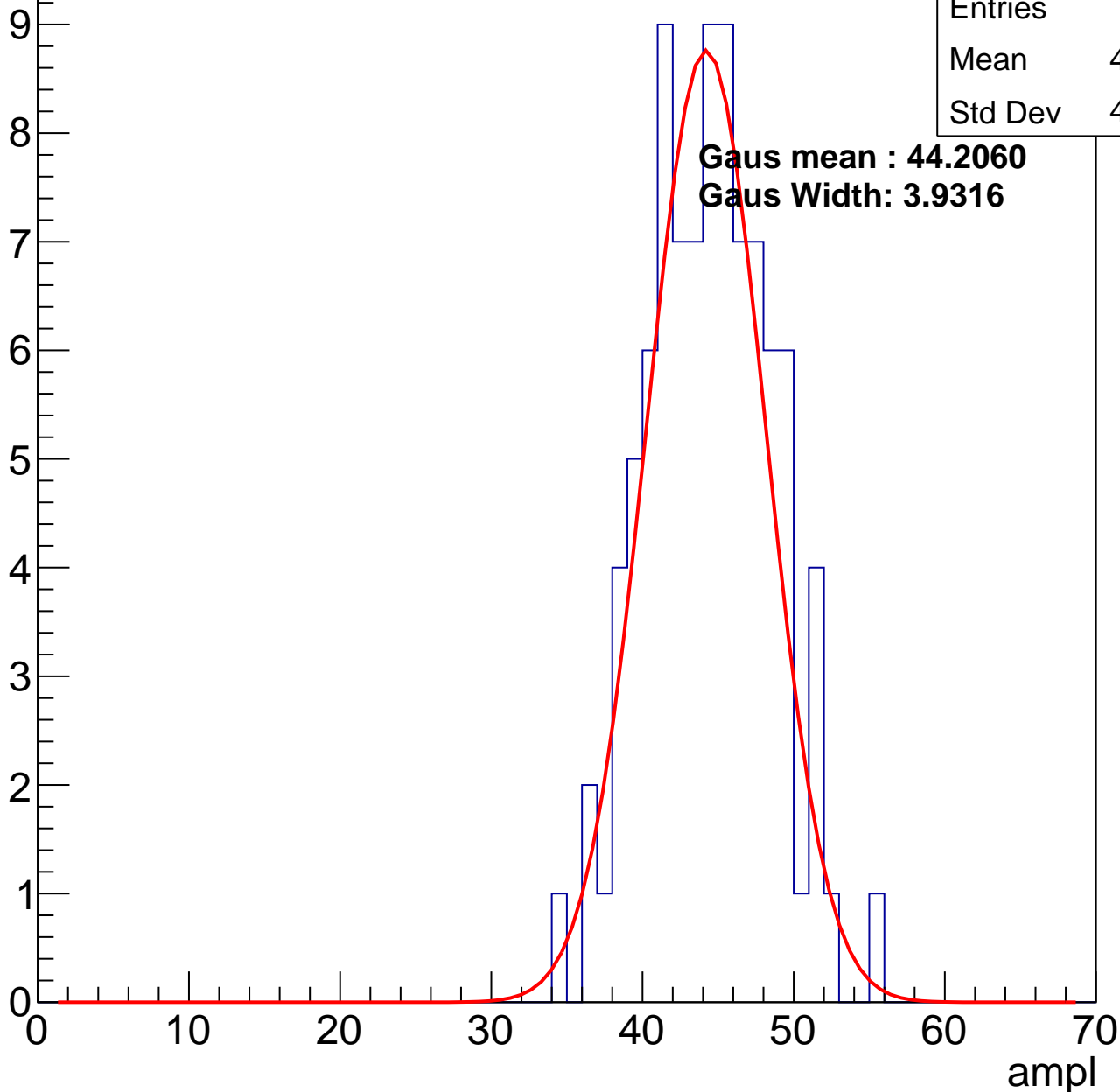
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	43.97
Std Dev	4.044

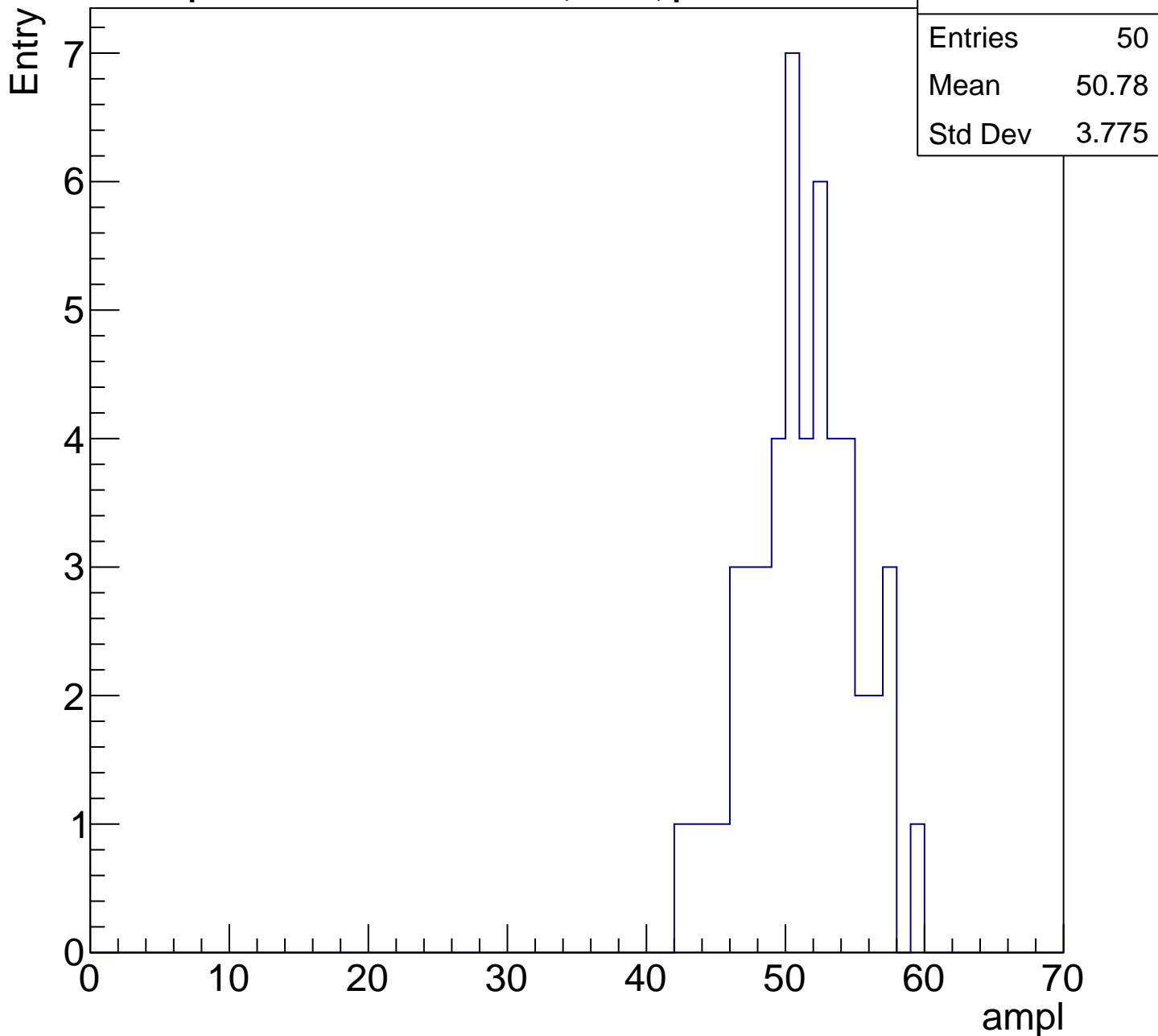
Gaus mean : 44.2060

Gaus Width: 3.9316



B1L101S, U1-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

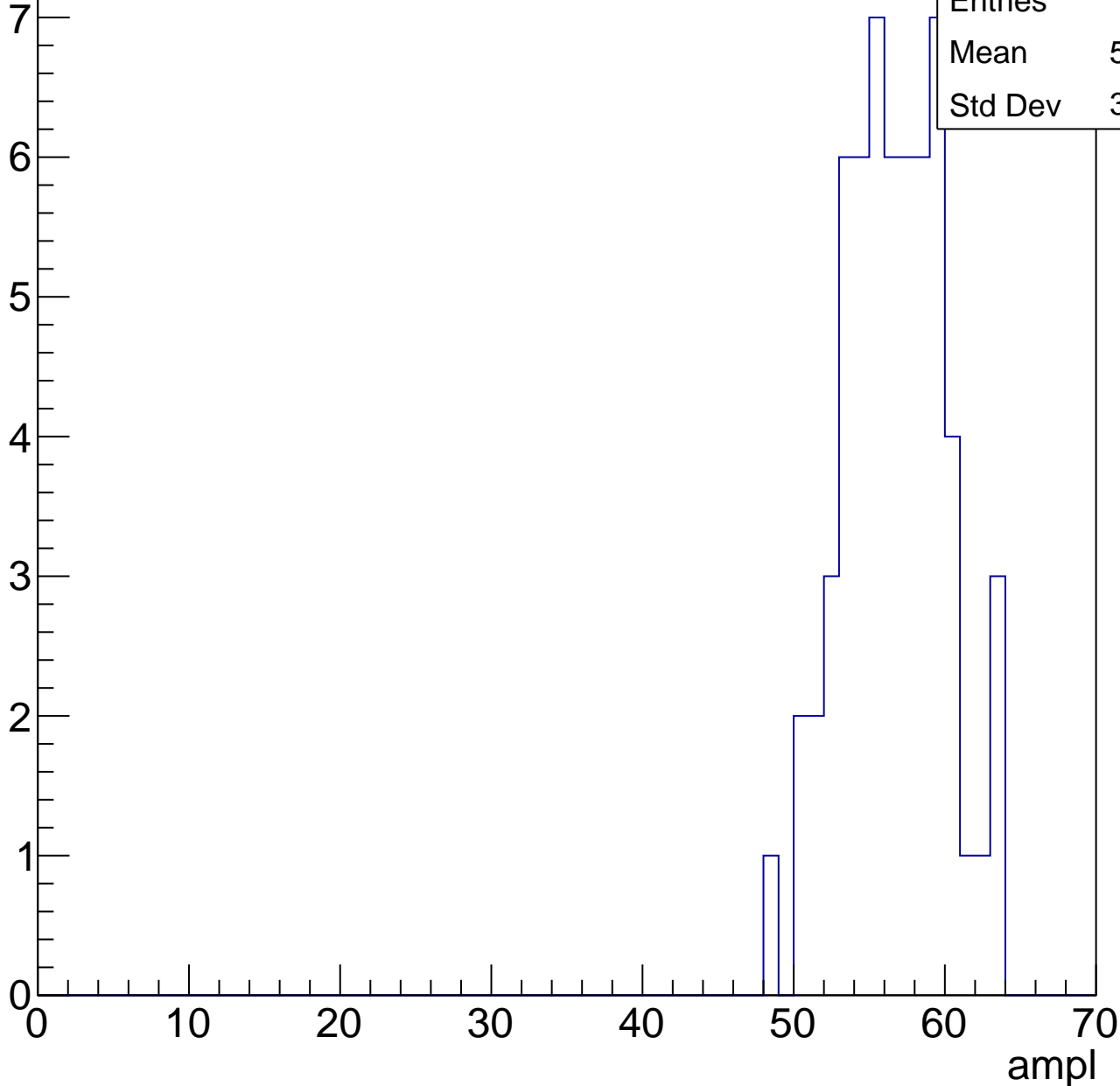


B1L101S, U1-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.13
Std Dev	3.346



B1L101S, U1-ch79, adc5

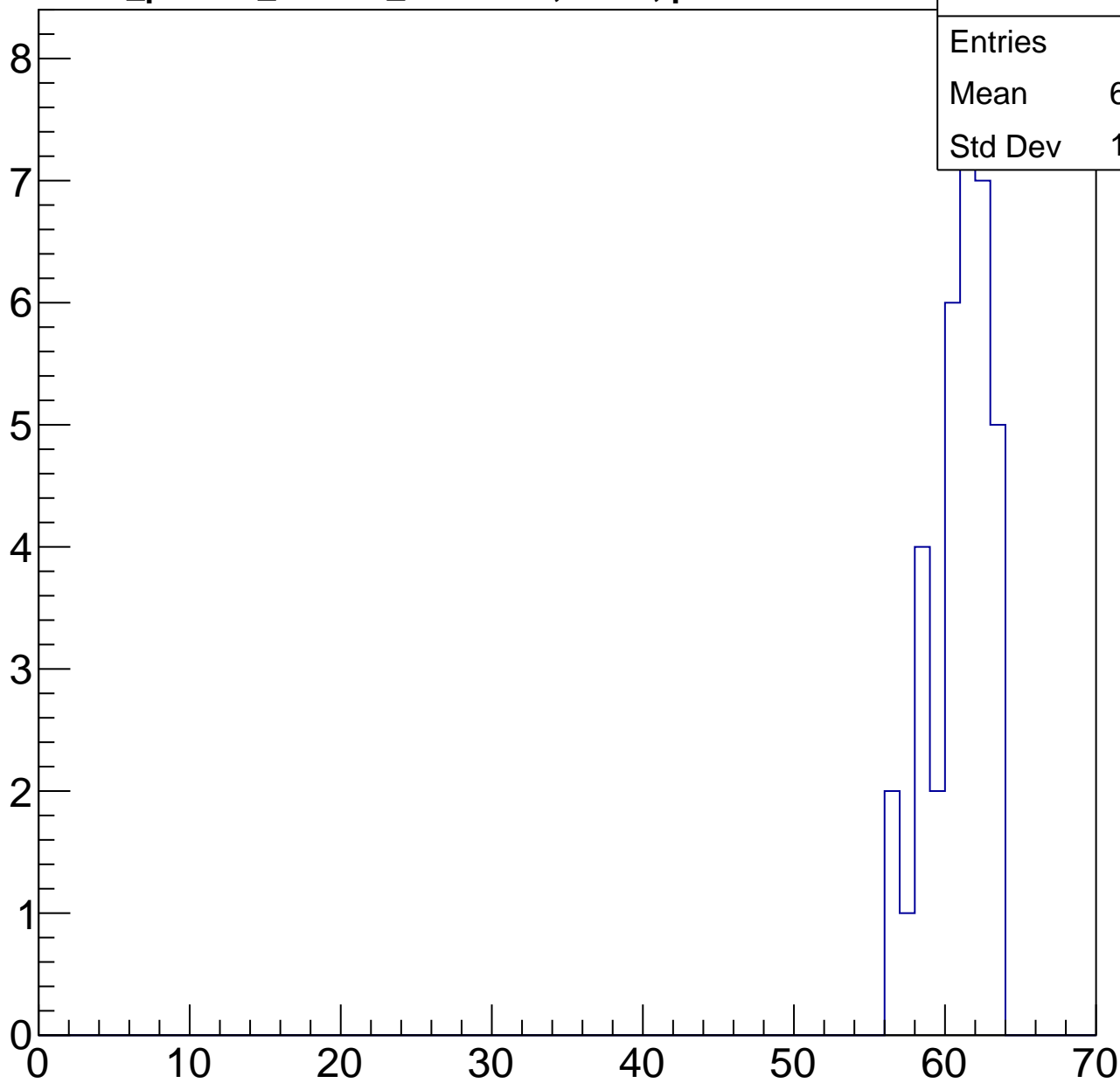
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.46
Std Dev	1.947

ampl



B1L101S, U1-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

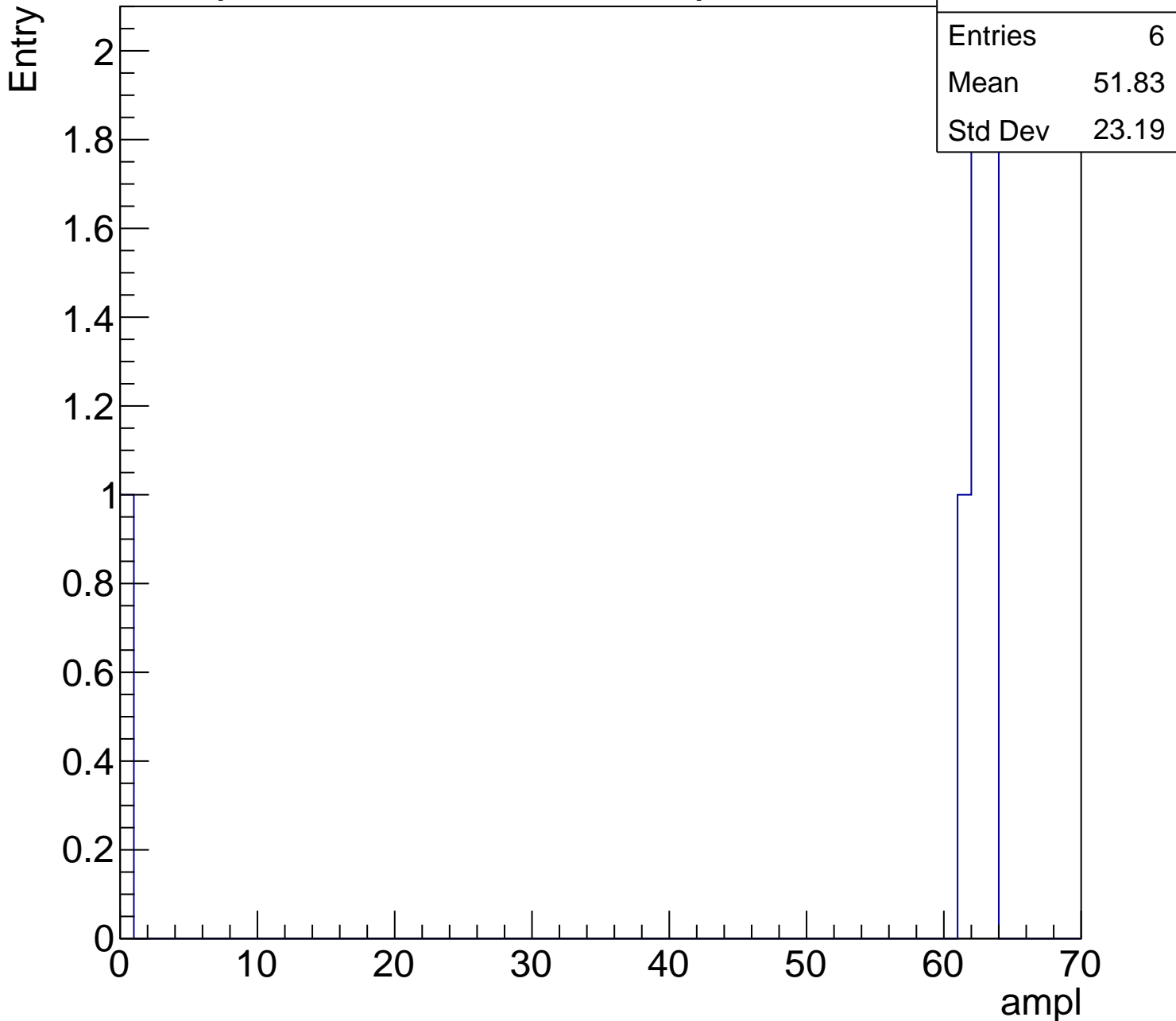
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

ampl



B1L101S, U1-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch80, adc0

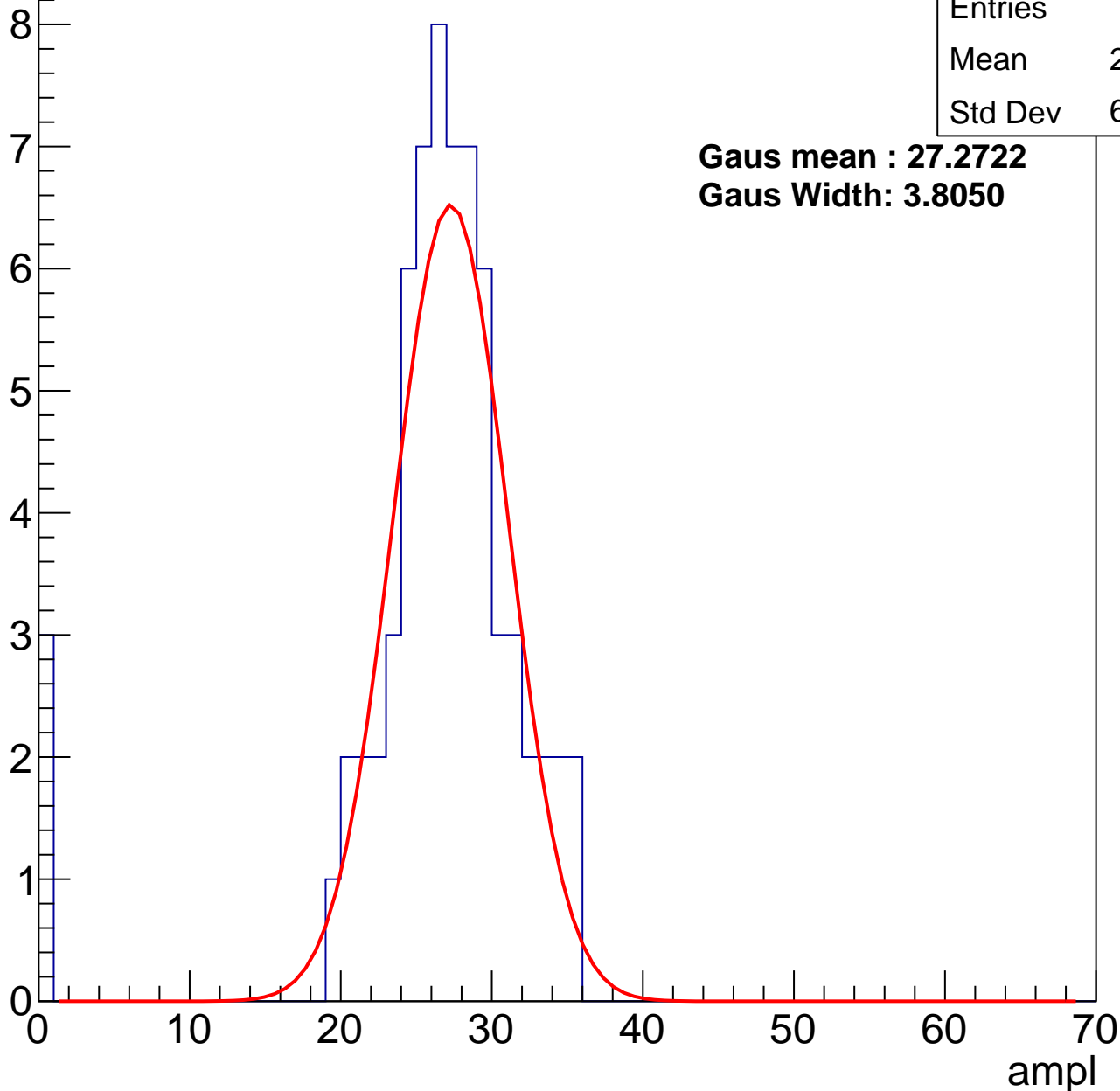
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	25.75
Std Dev	6.594

Gaus mean : 27.2722

Gaus Width: 3.8050



B1L101S, U1-ch80, adc1

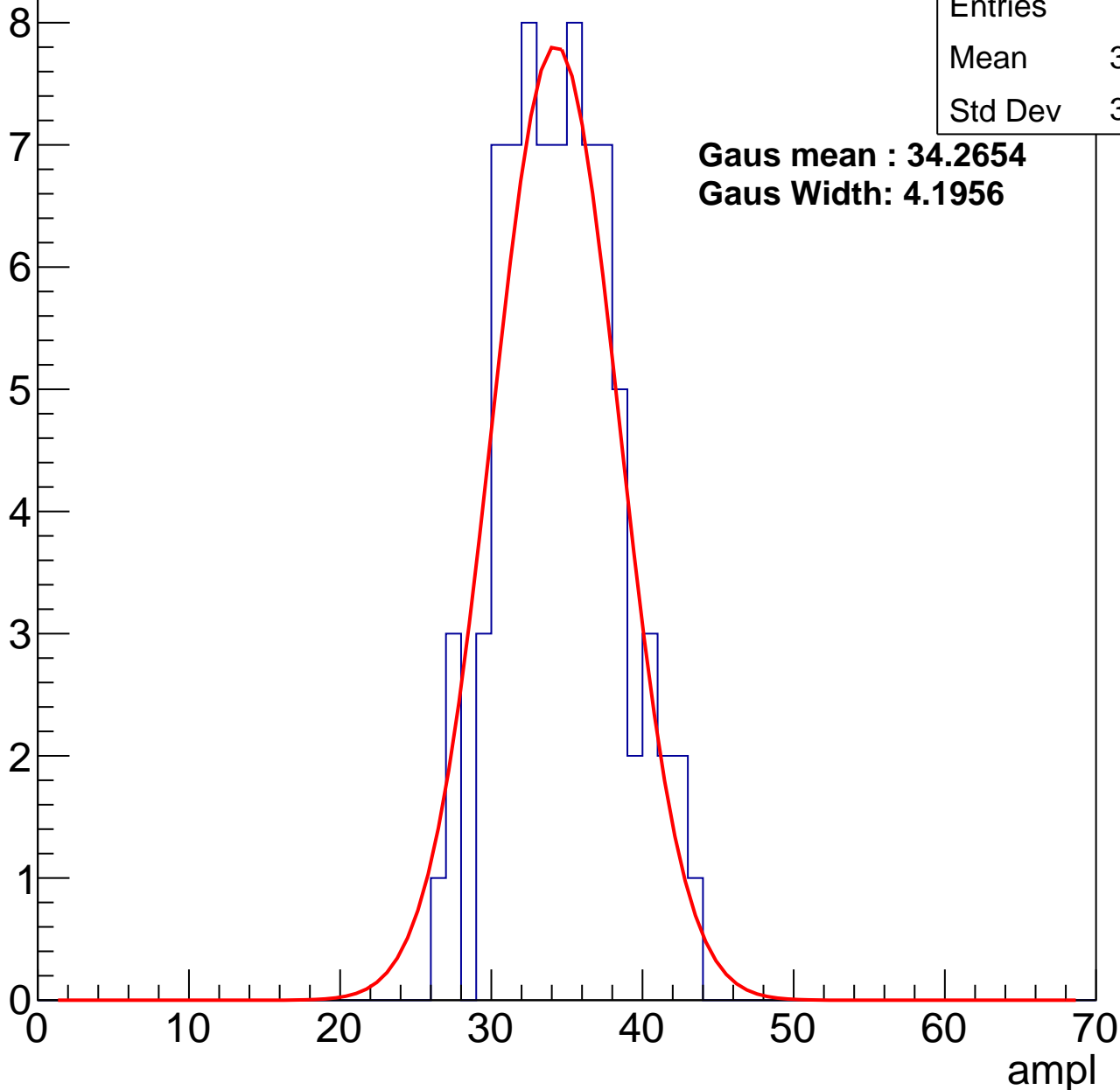
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	34.17
Std Dev	3.777

Gaus mean : 34.2654

Gaus Width: 4.1956



B1L101S, U1-ch80, adc2

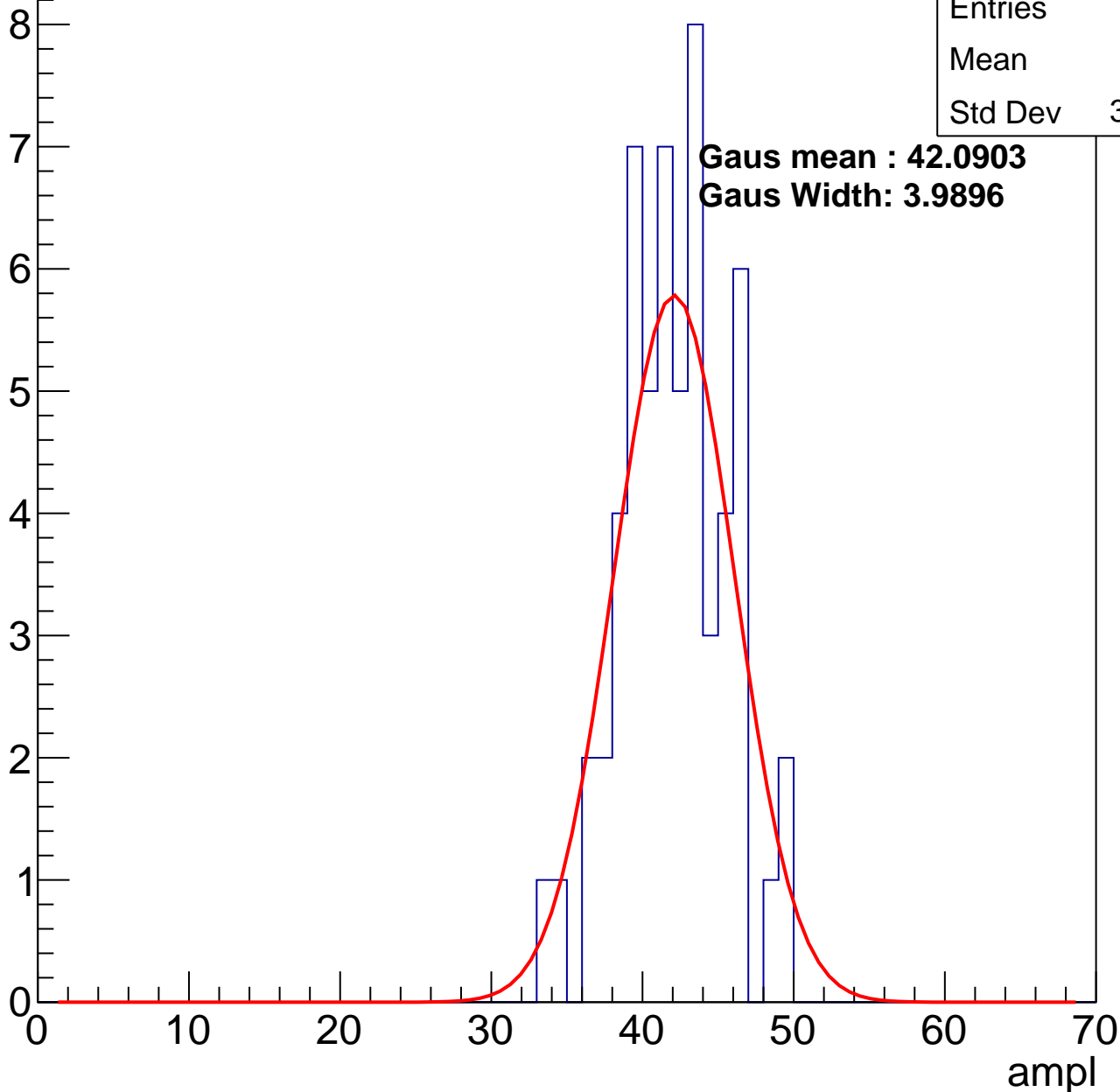
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.6
Std Dev	3.469

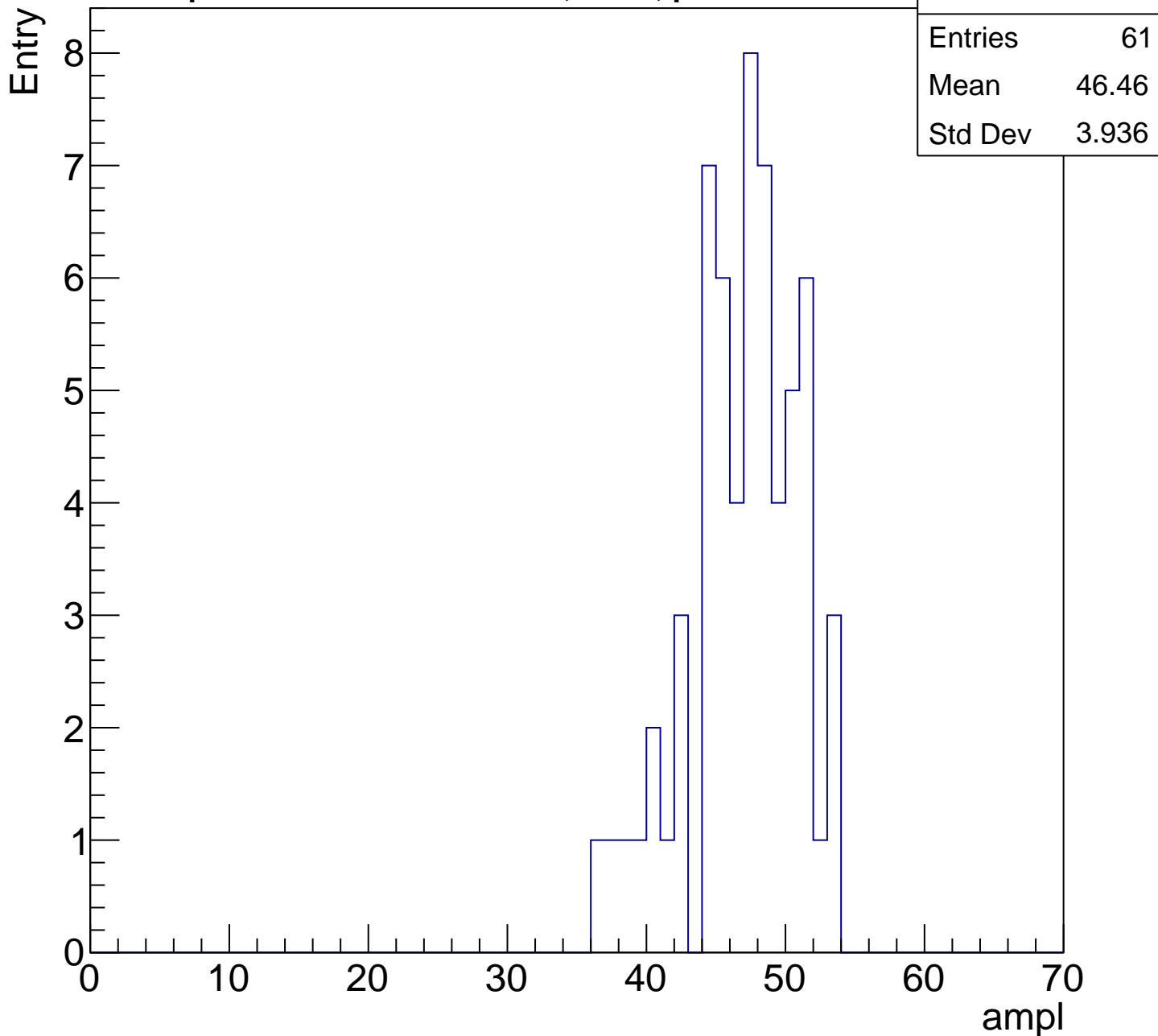
Gaus mean : 42.0903

Gaus Width: 3.9896



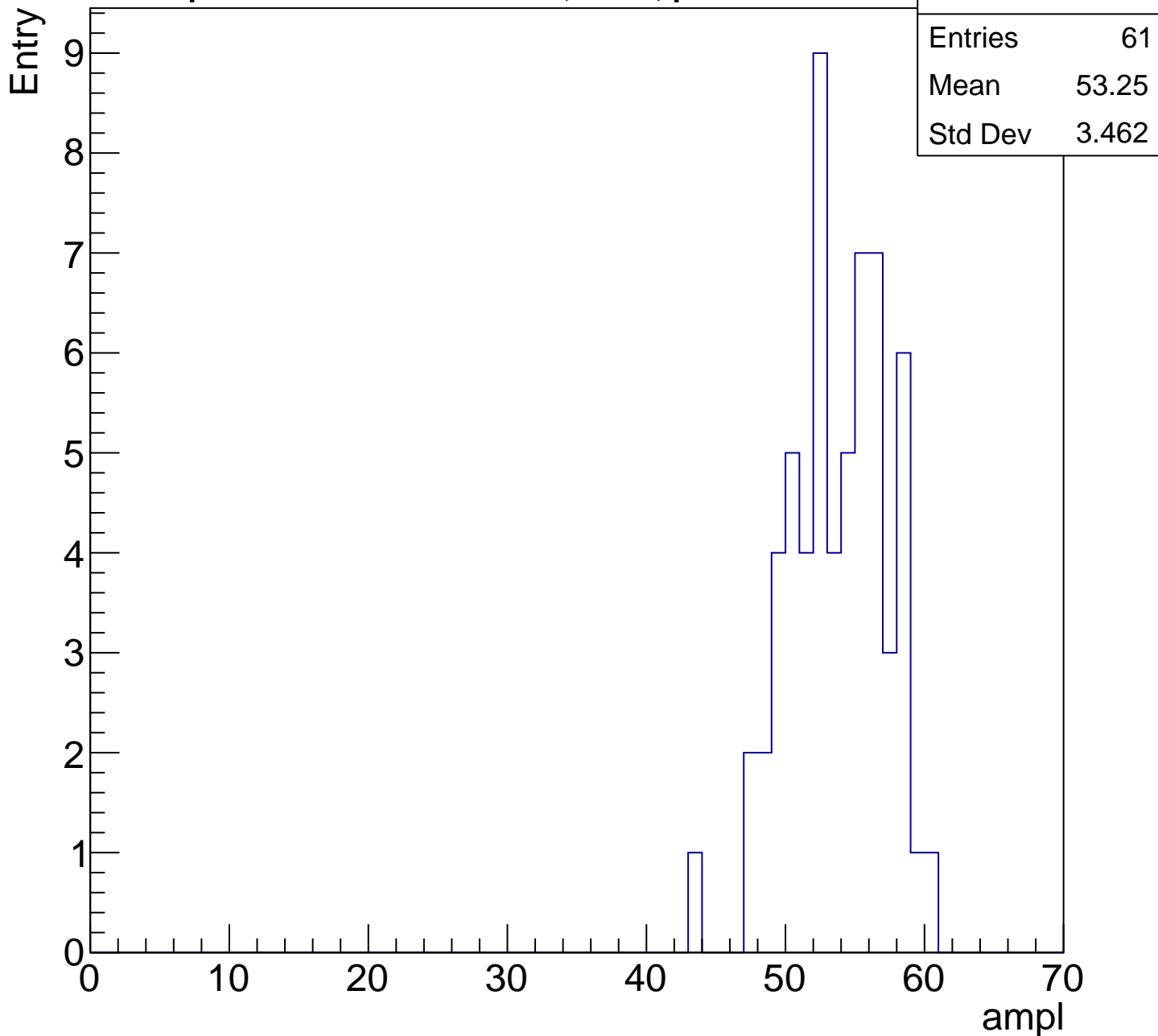
B1L101S, U1-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch80, adc4

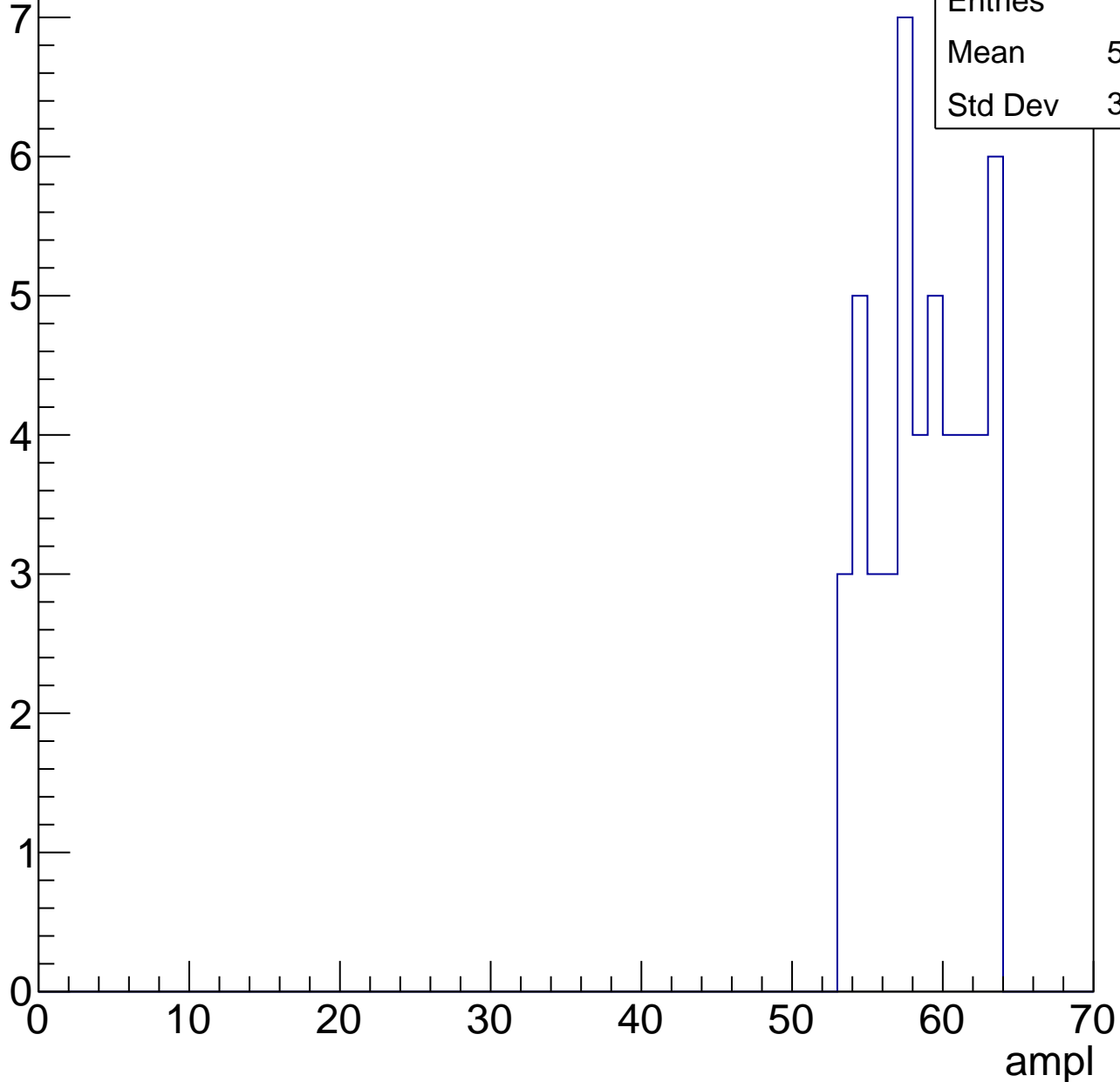
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



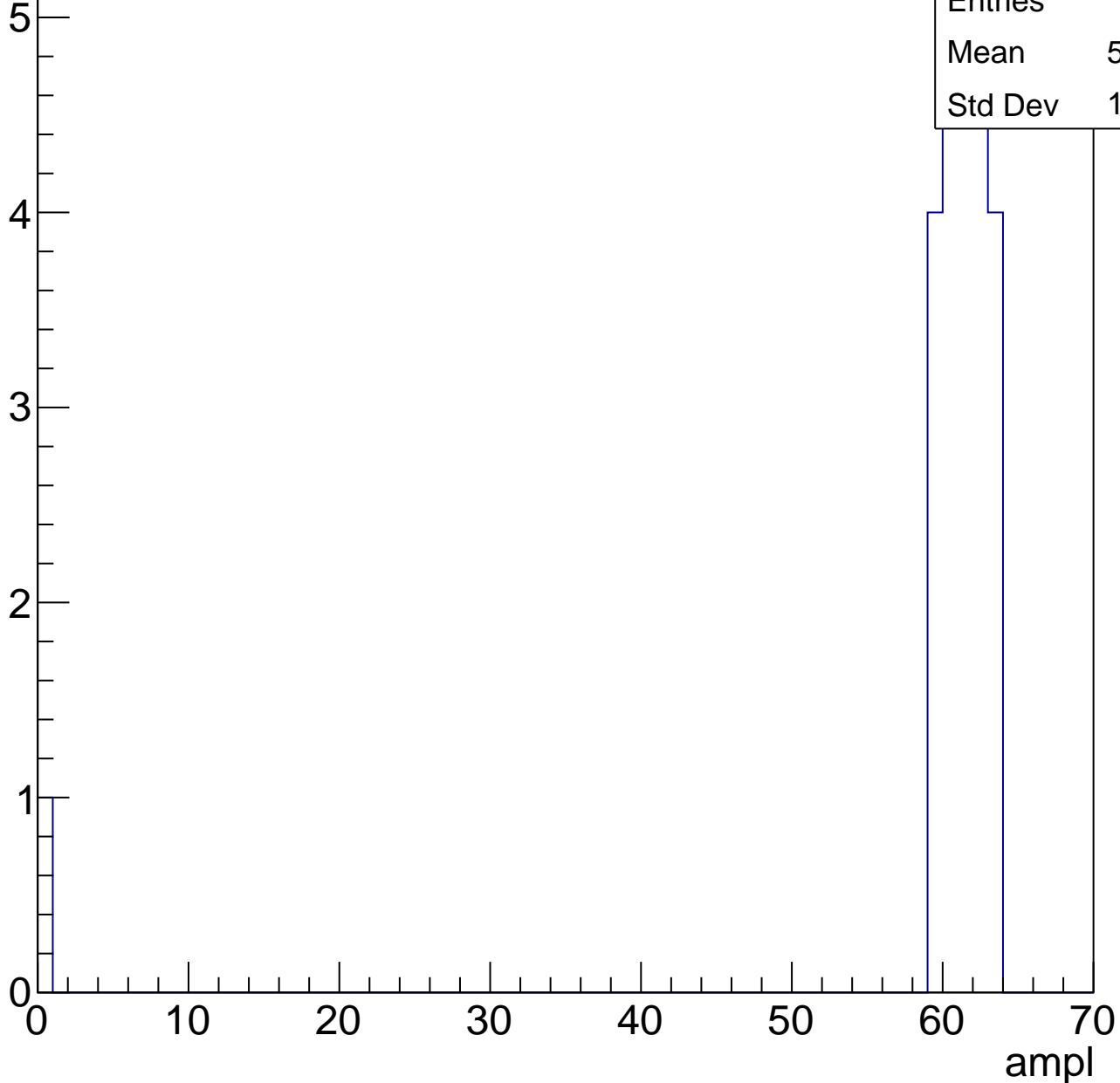
Entries	48
Mean	58.29
Std Dev	3.122

B1L101S, U1-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

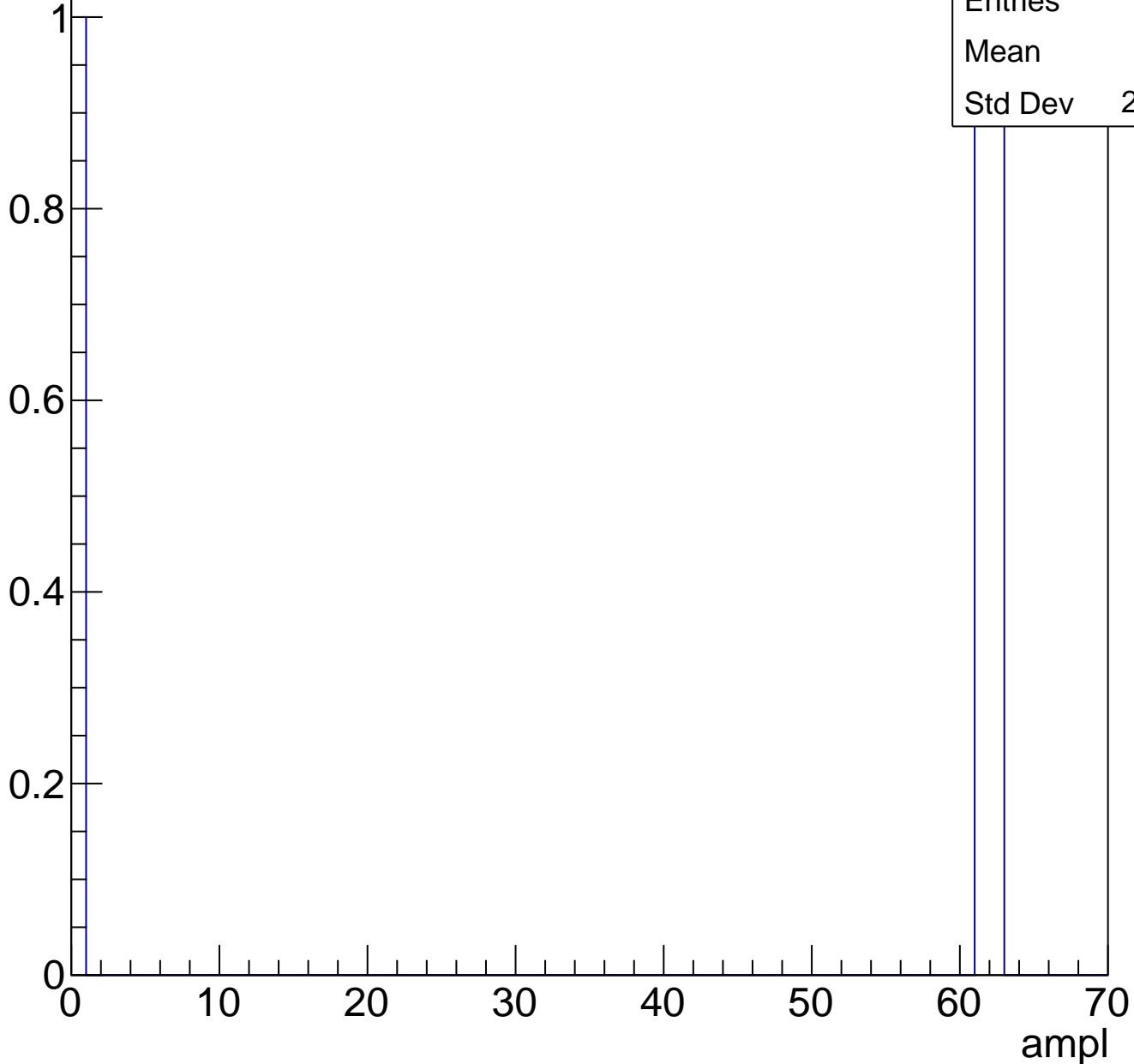
Entries	24
Mean	58.46
Std Dev	12.26



B1L101S, U1-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch81, adc0

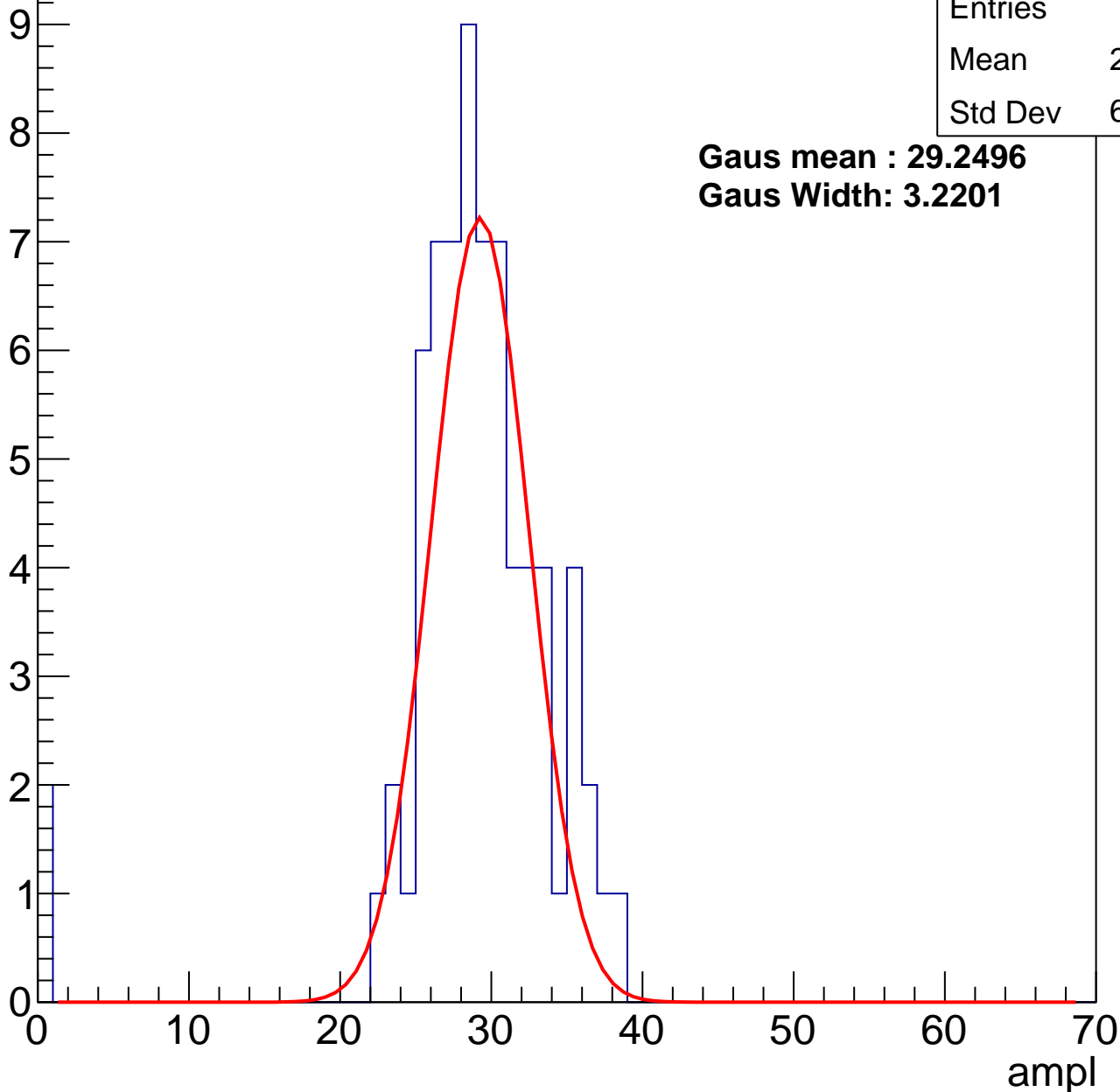
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.33
Std Dev	6.009

Gaus mean : 29.2496

Gaus Width: 3.2201



B1L101S, U1-ch81, adc1

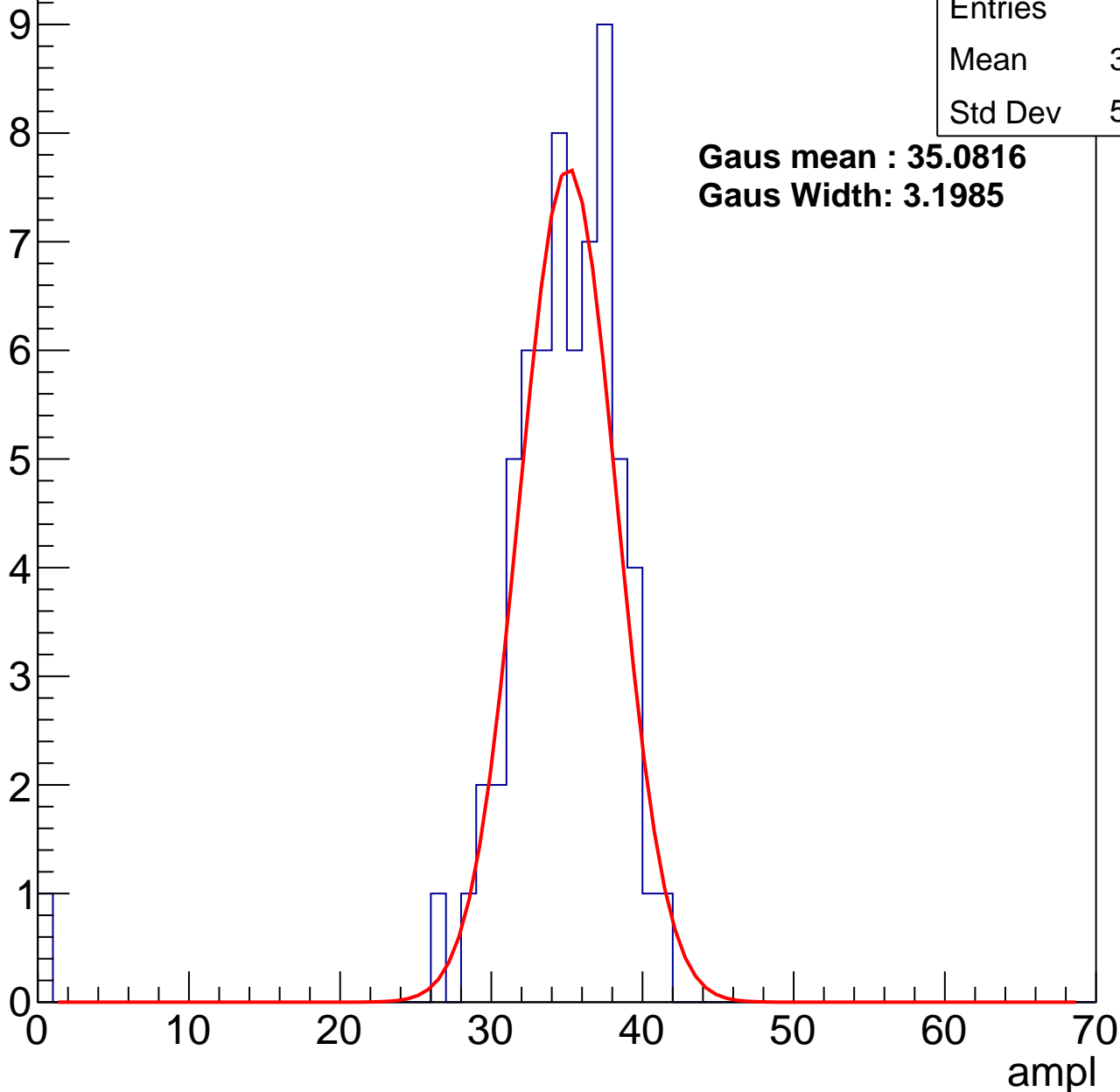
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	34.02
Std Dev	5.252

Gaus mean : 35.0816

Gaus Width: 3.1985



B1L101S, U1-ch81, adc2

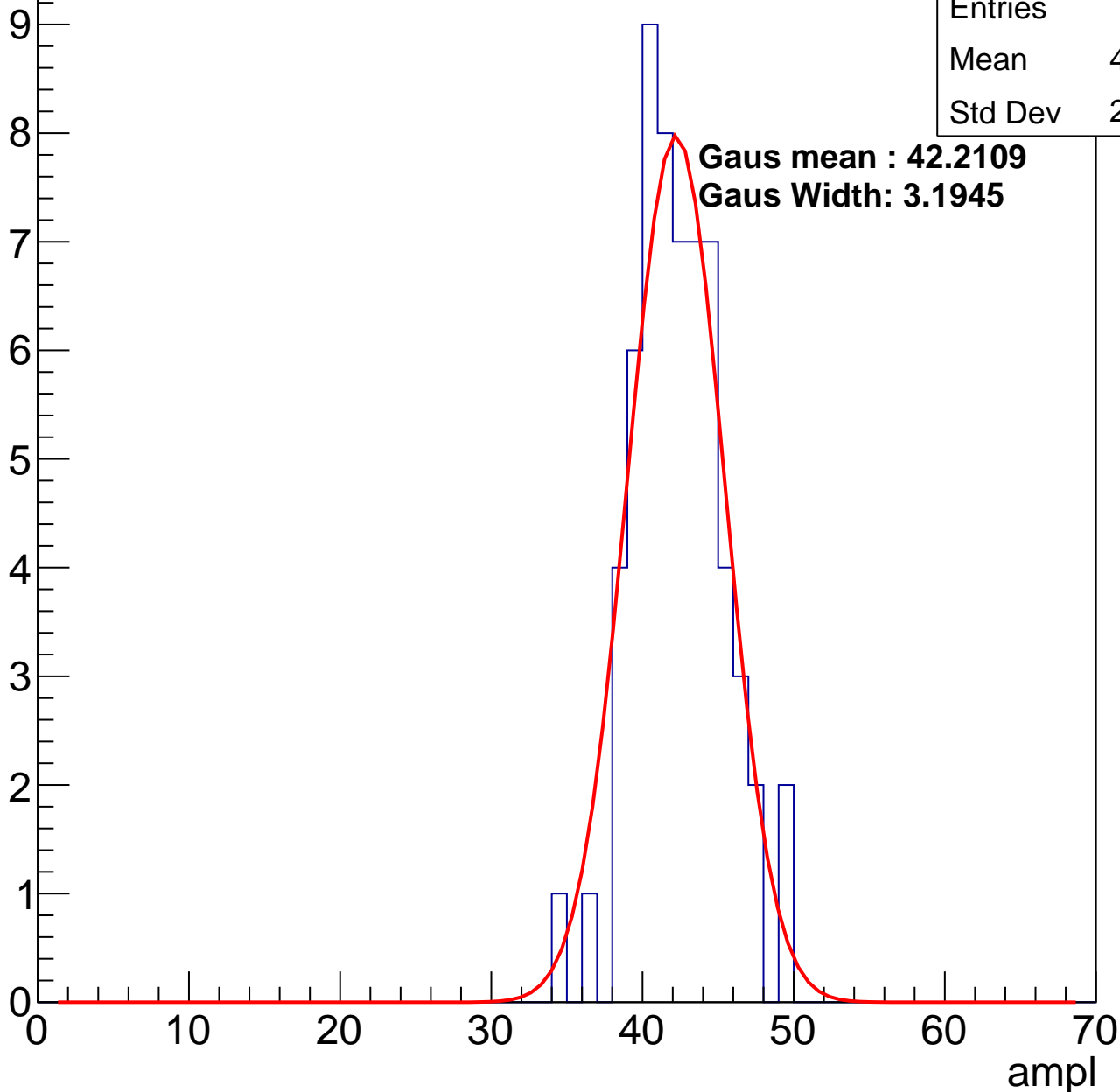
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.92
Std Dev	2.944

Gaus mean : 42.2109

Gaus Width: 3.1945

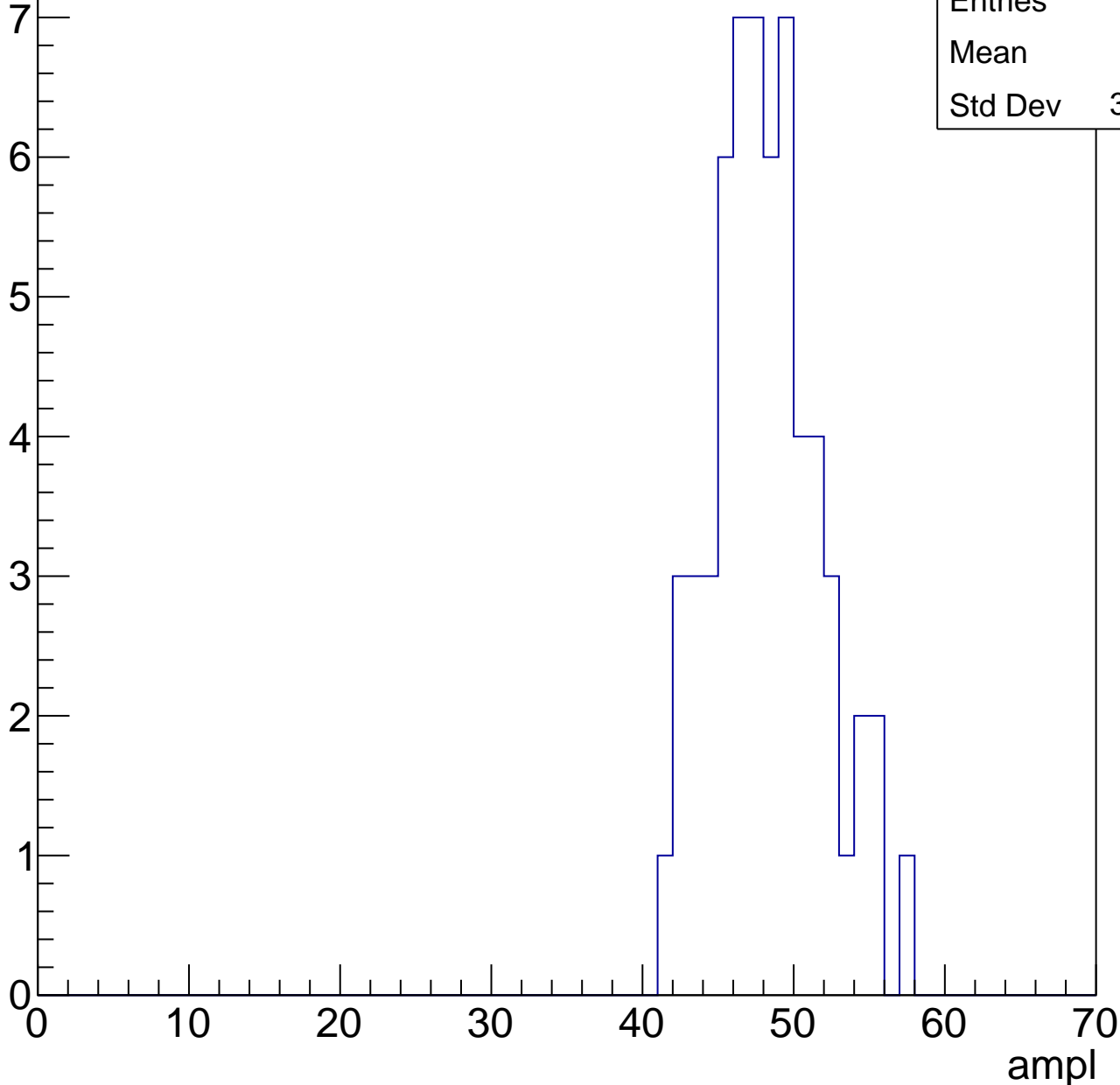


B1L101S, U1-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

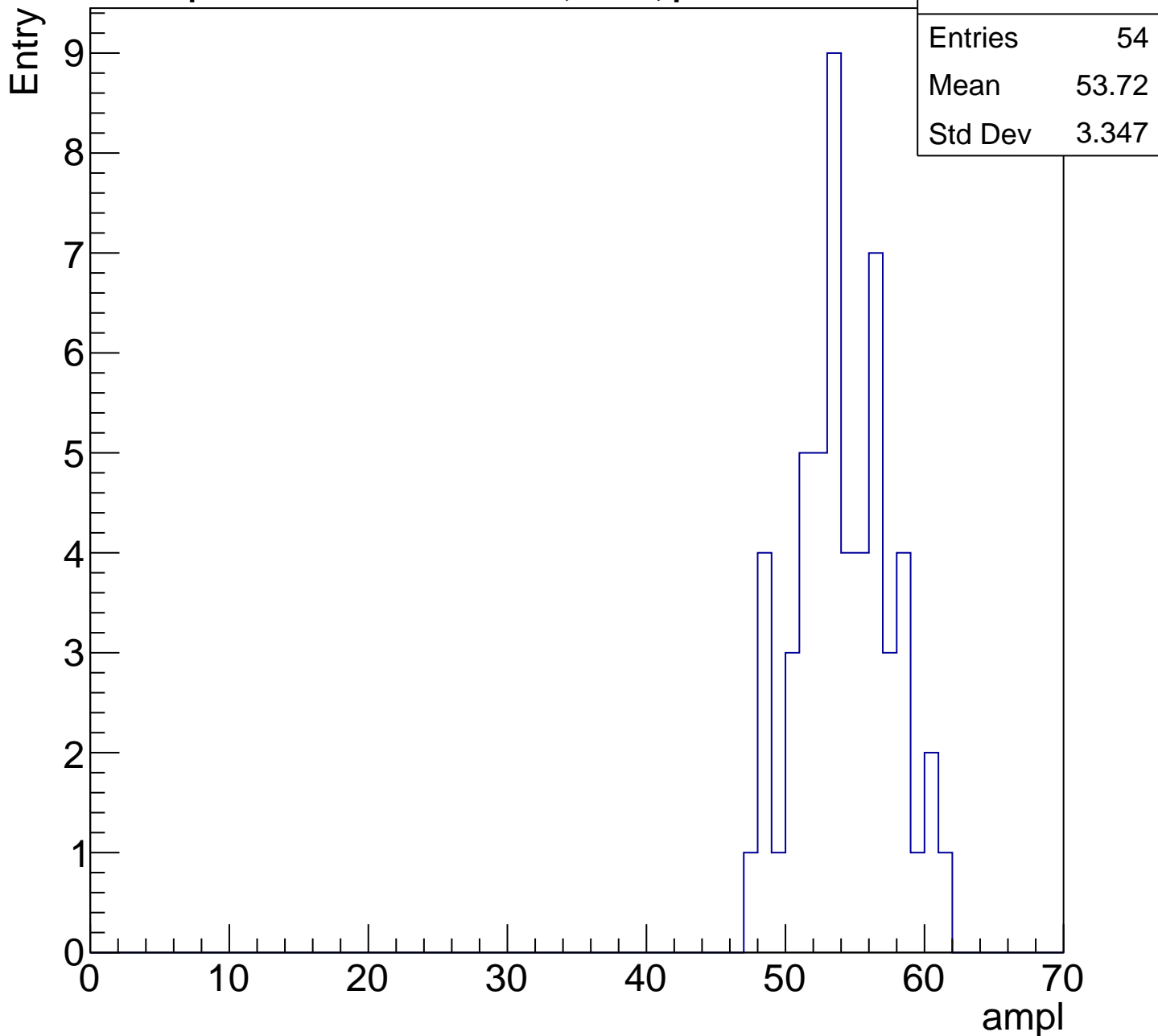
Entry

Entries	60
Mean	47.8
Std Dev	3.535



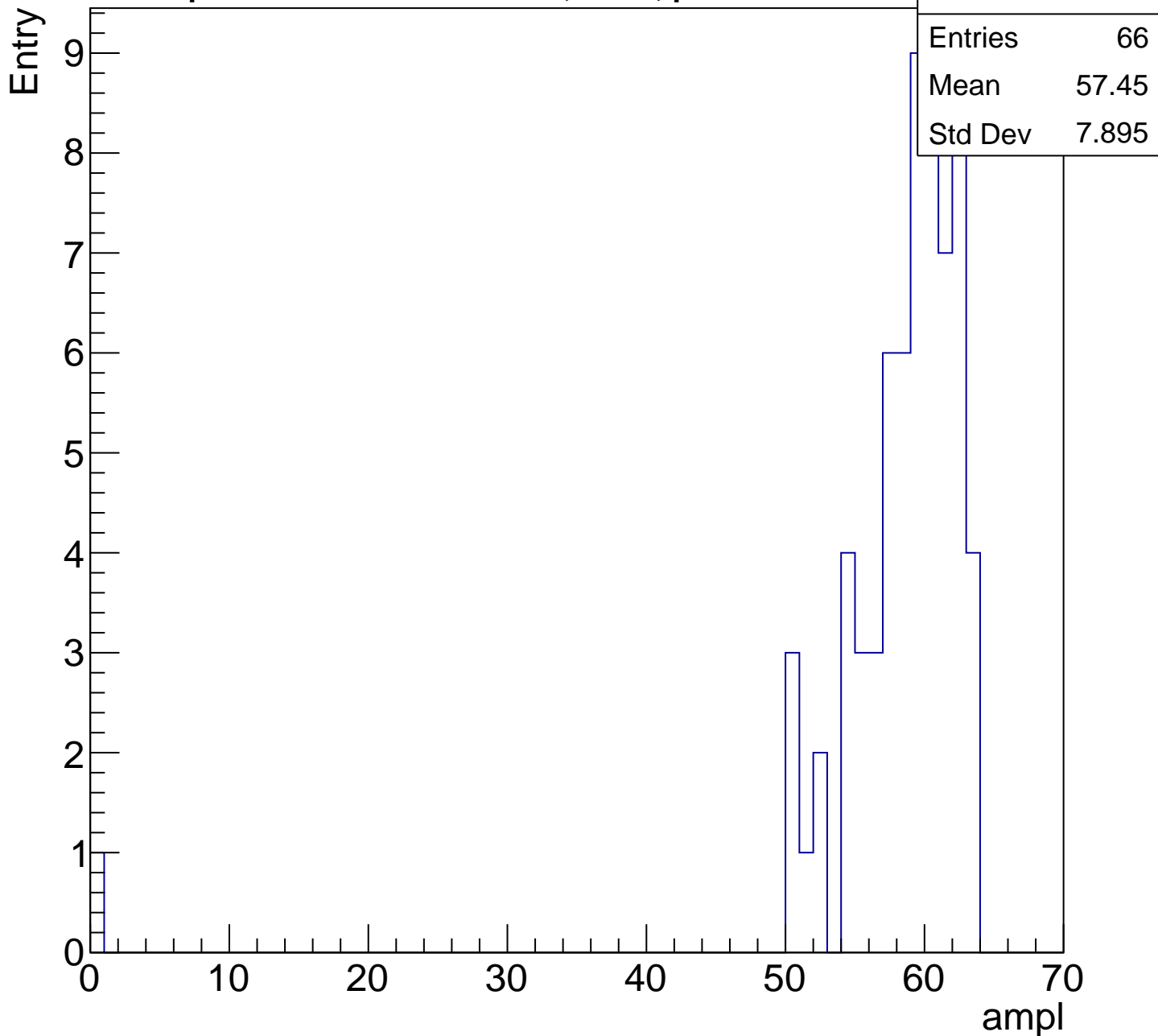
B1L101S, U1-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

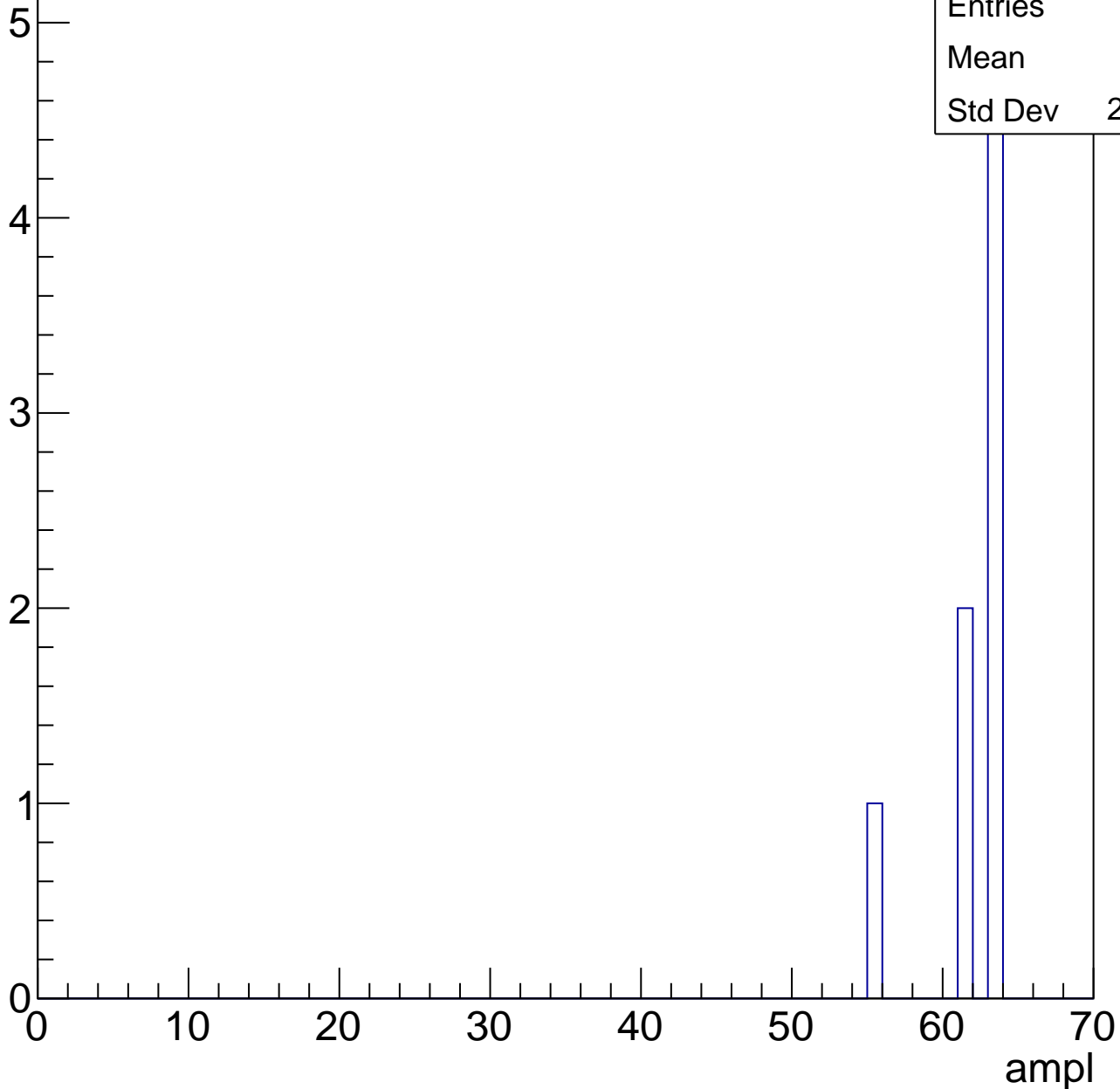


B1L101S, U1-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	61.5
Std Dev	2.598



B1L101S, U1-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch82, adc0

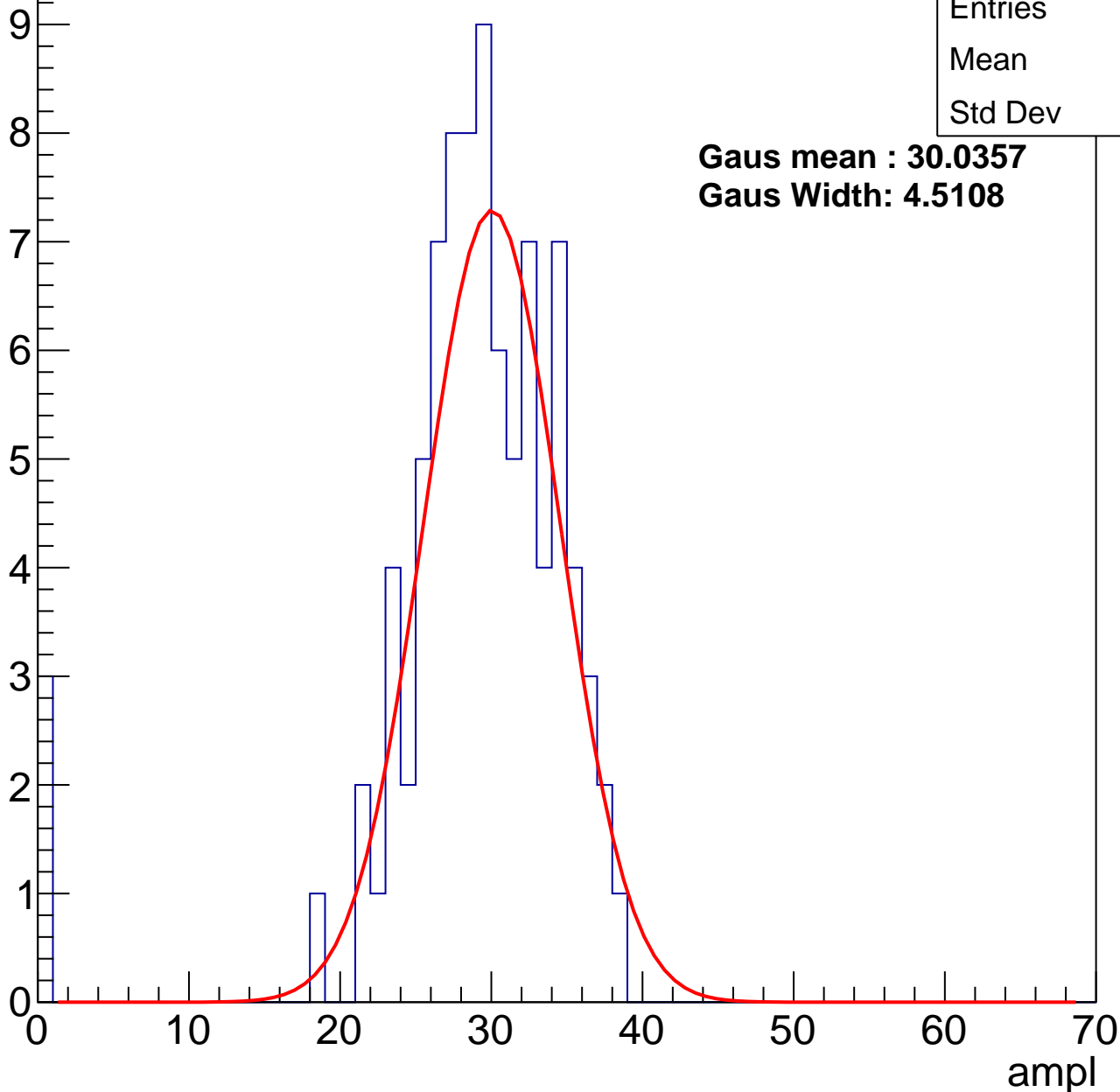
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	28.3
Std Dev	6.69

Gaus mean : 30.0357

Gaus Width: 4.5108



B1L101S, U1-ch82, adc1

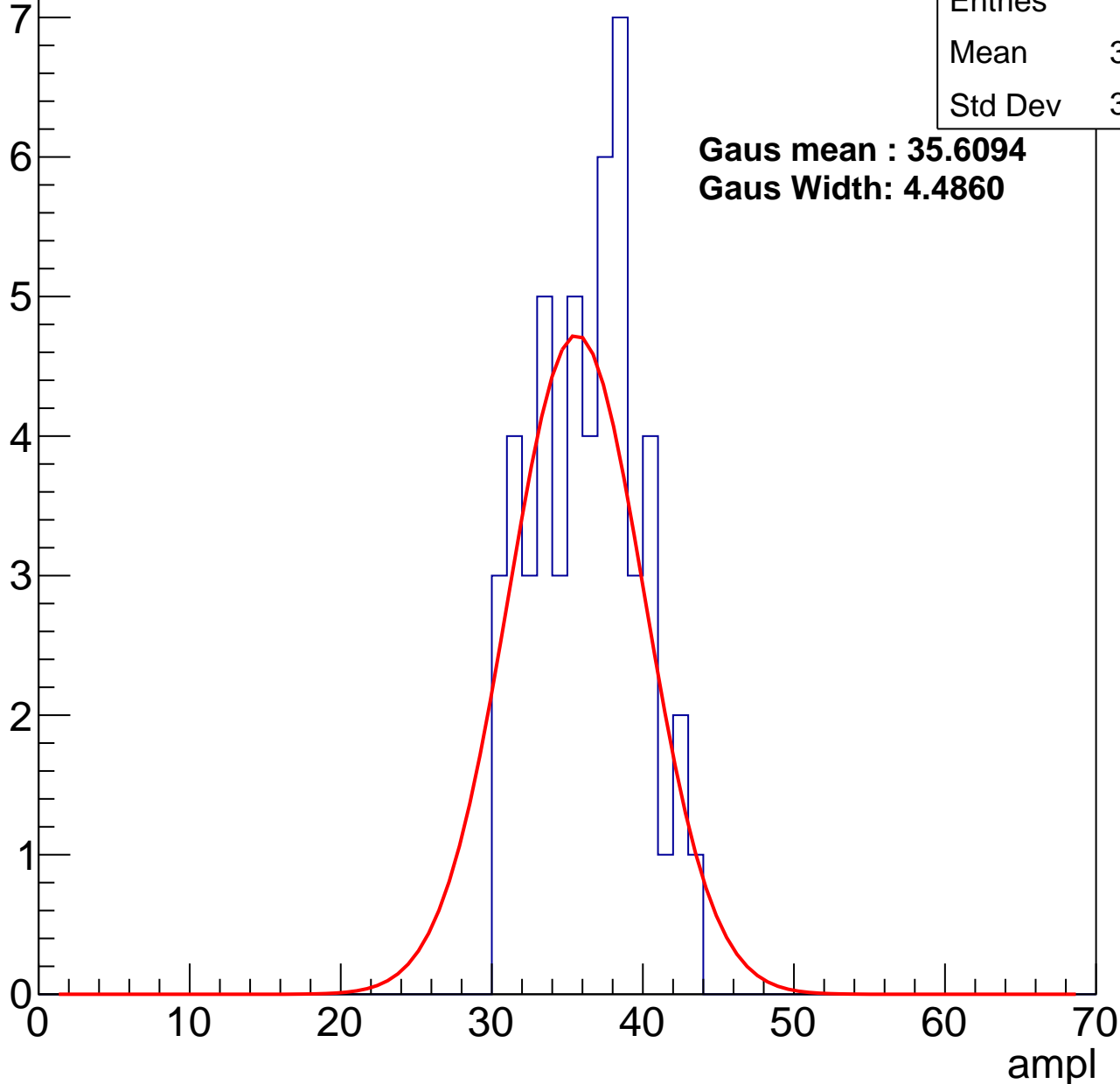
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.86
Std Dev	3.396

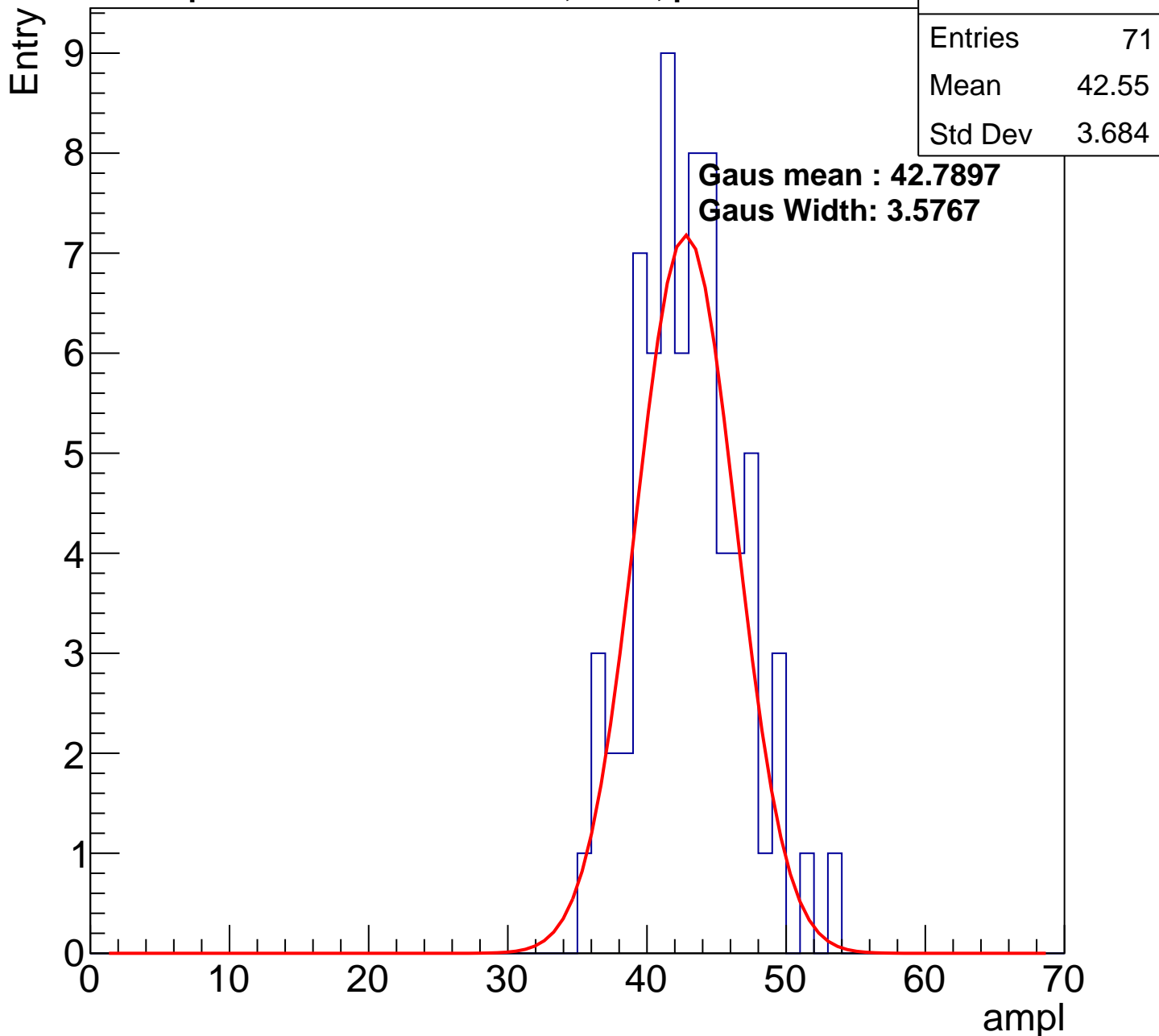
Gaus mean : 35.6094

Gaus Width: 4.4860



B1L101S, U1-ch82, adc2

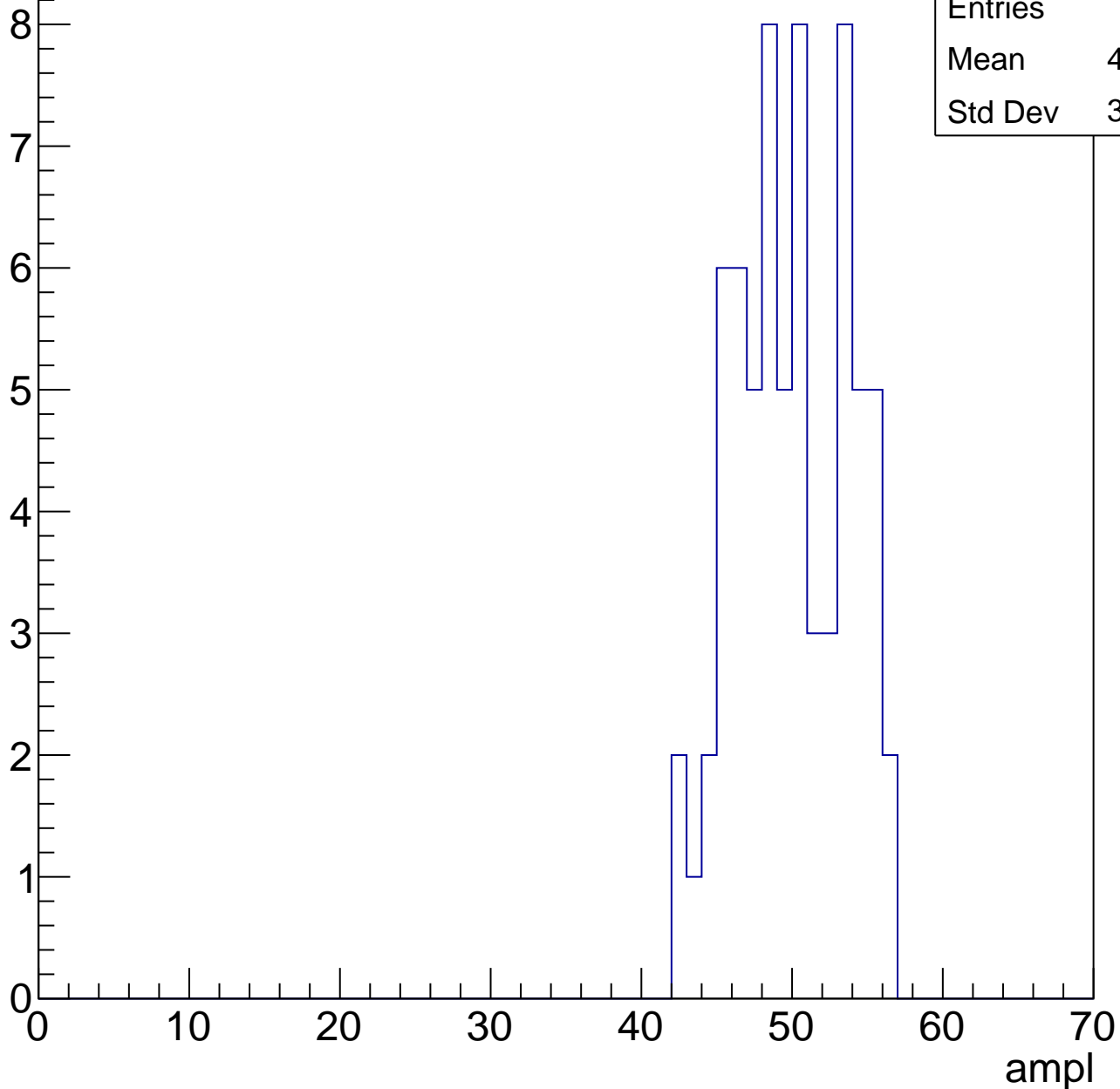
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



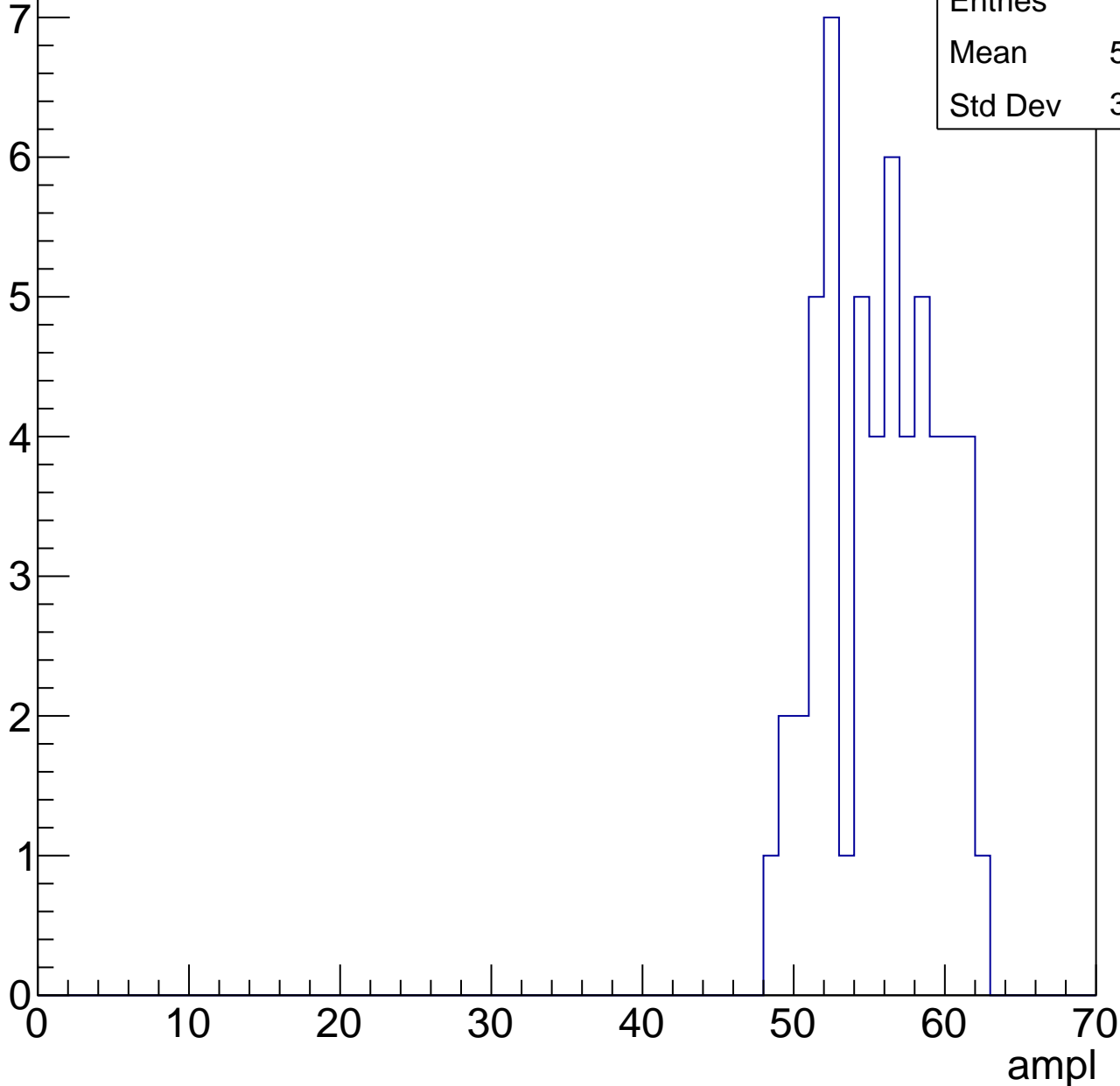
Entries	69
Mean	49.49
Std Dev	3.658

B1L101S, U1-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.35
Std Dev	3.669

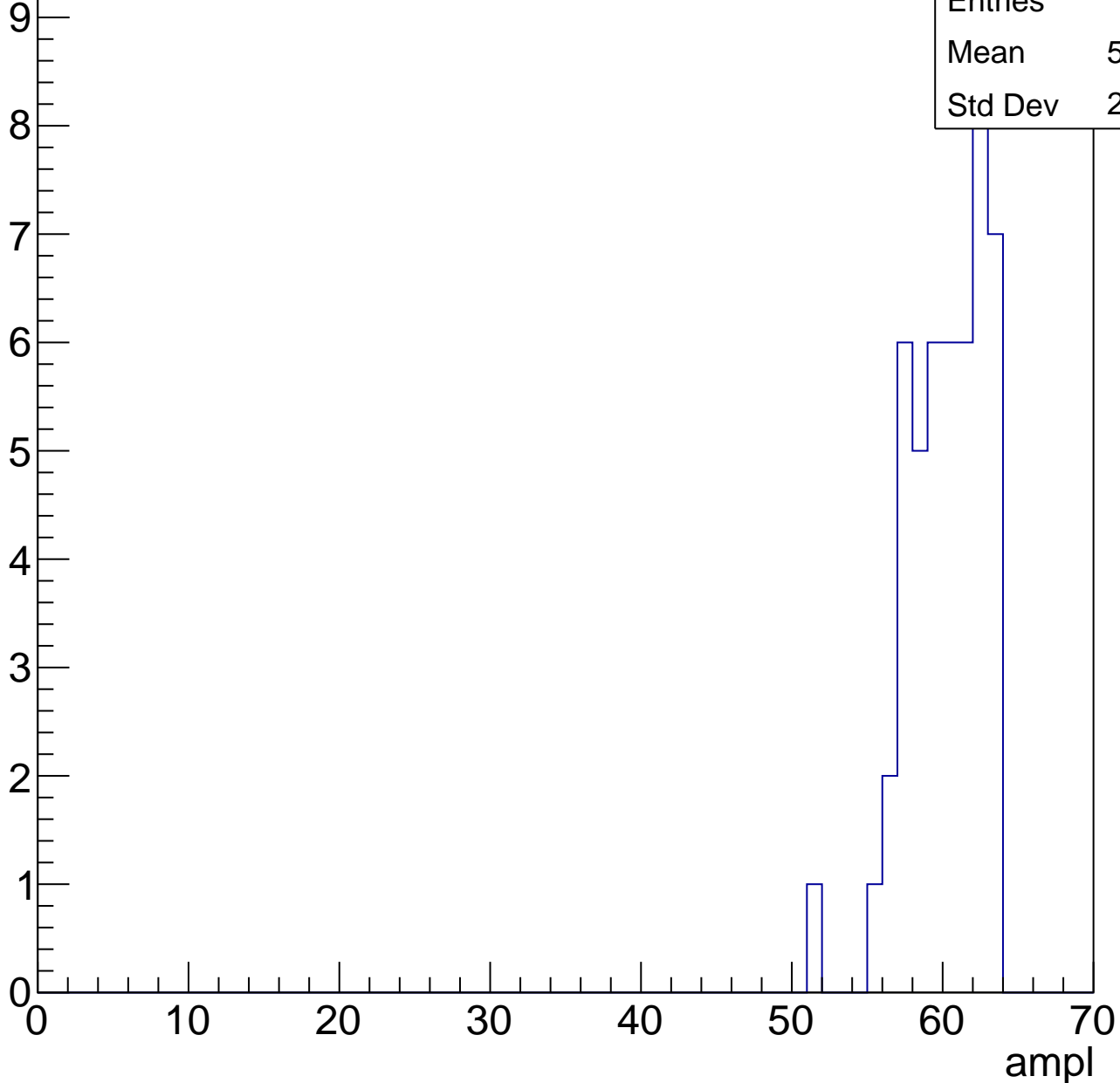


B1L101S, U1-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

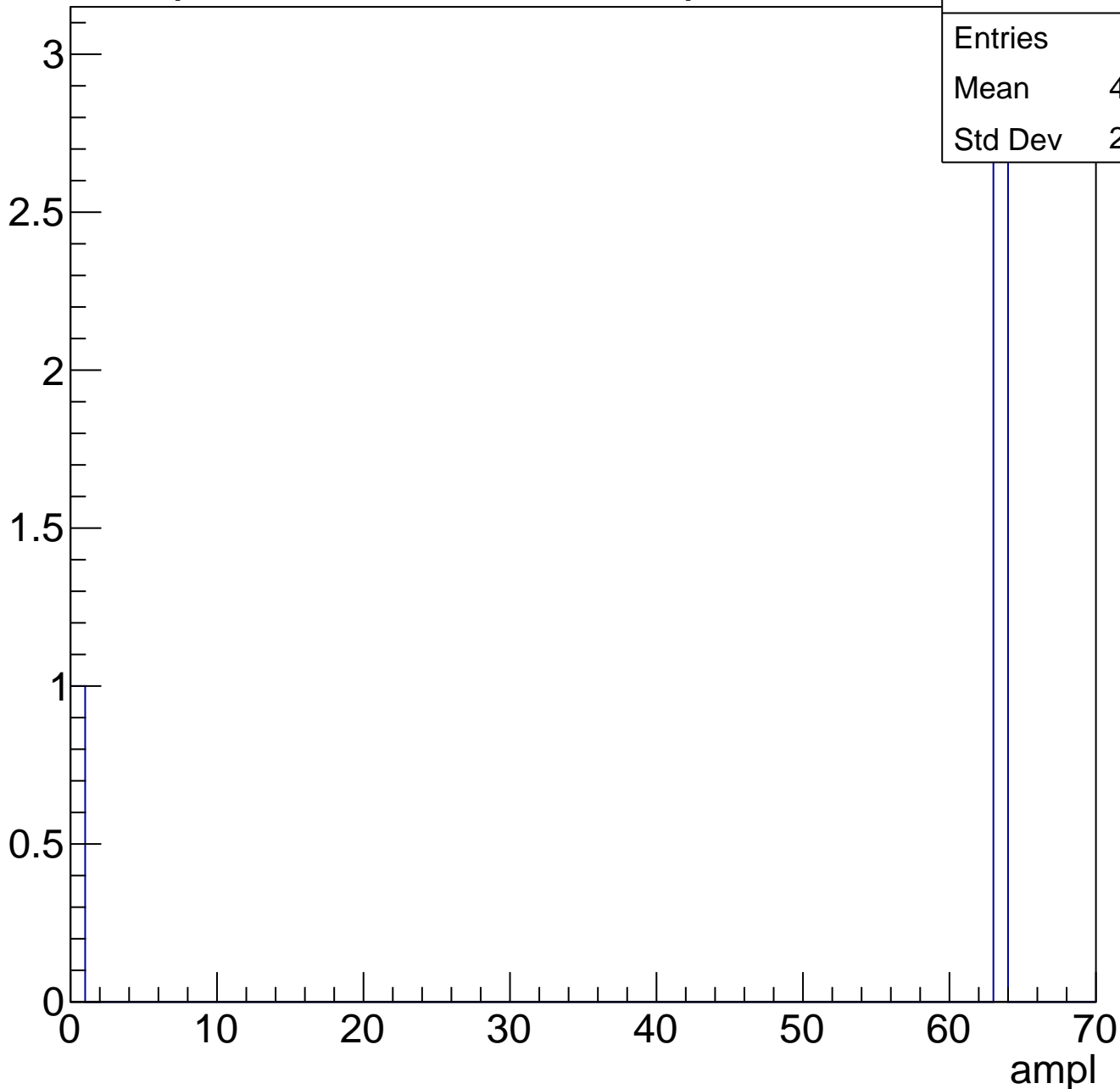
Entries	49
Mean	59.78
Std Dev	2.558



B1L101S, U1-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch83, adc0

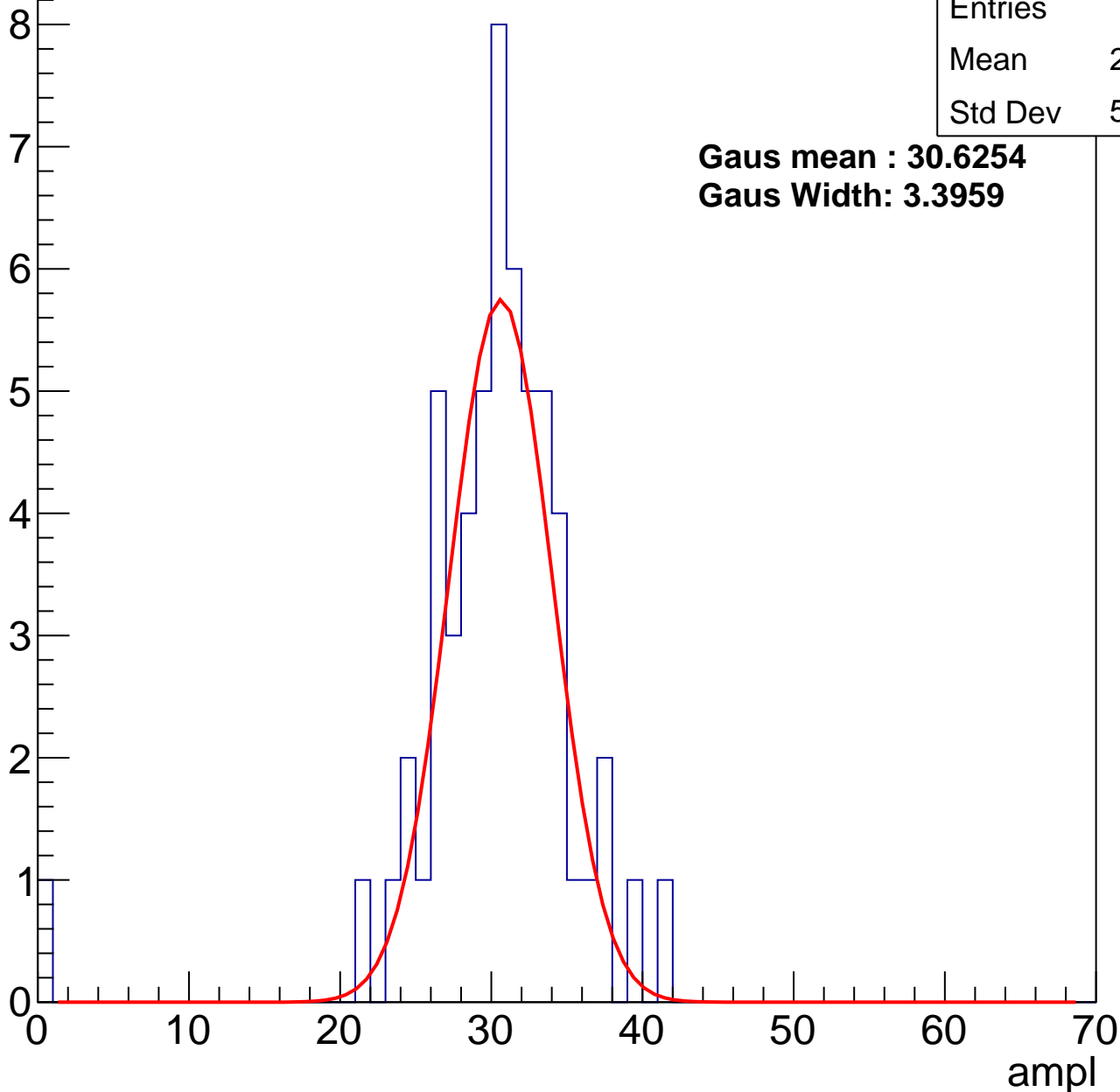
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	29.77
Std Dev	5.538

Gaus mean : 30.6254

Gaus Width: 3.3959



B1L101S, U1-ch83, adc1

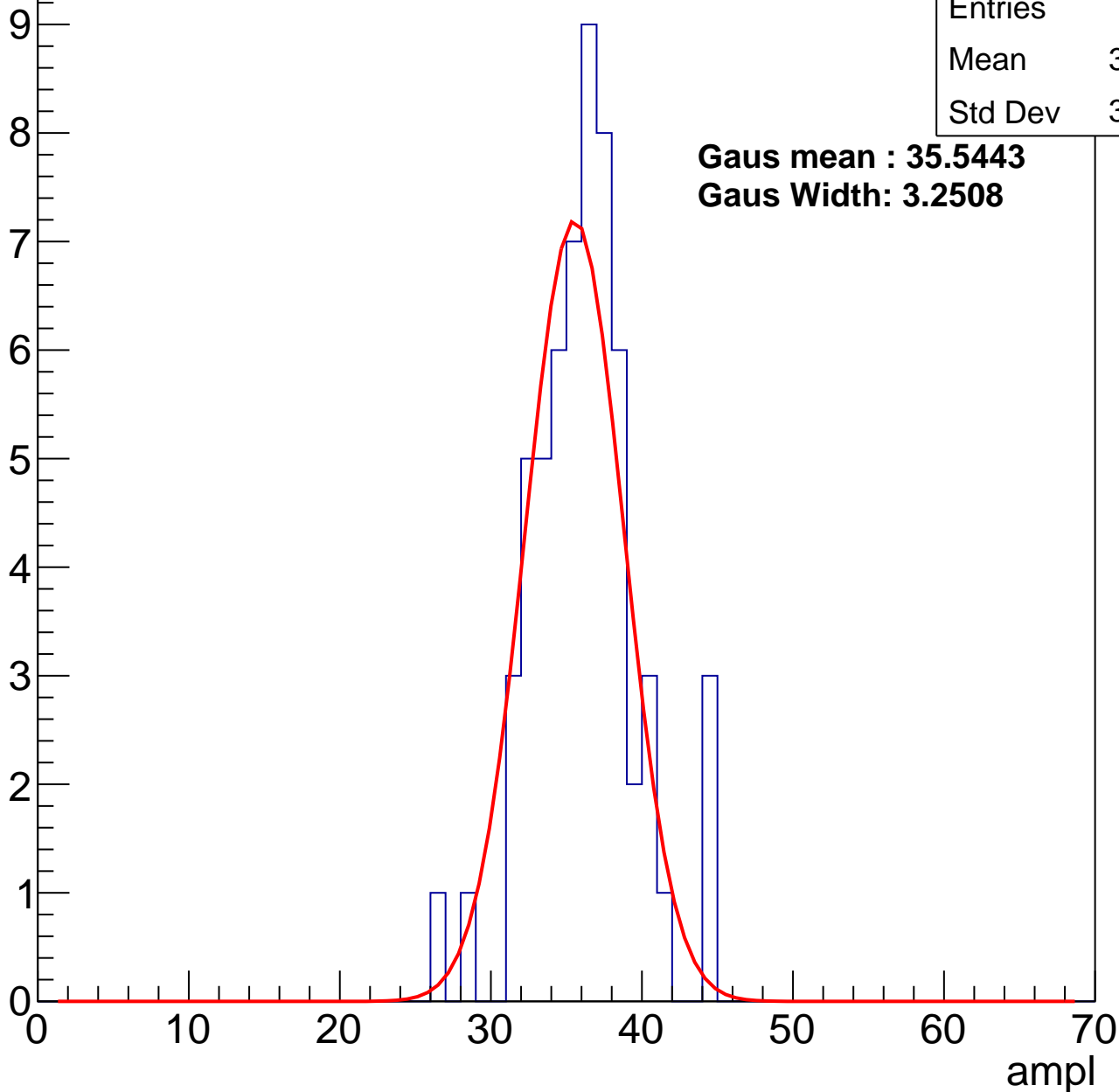
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.67
Std Dev	3.424

Gaus mean : 35.5443

Gaus Width: 3.2508



B1L101S, U1-ch83, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	42.84
Std Dev	3.491

Gaus mean : 42.5909

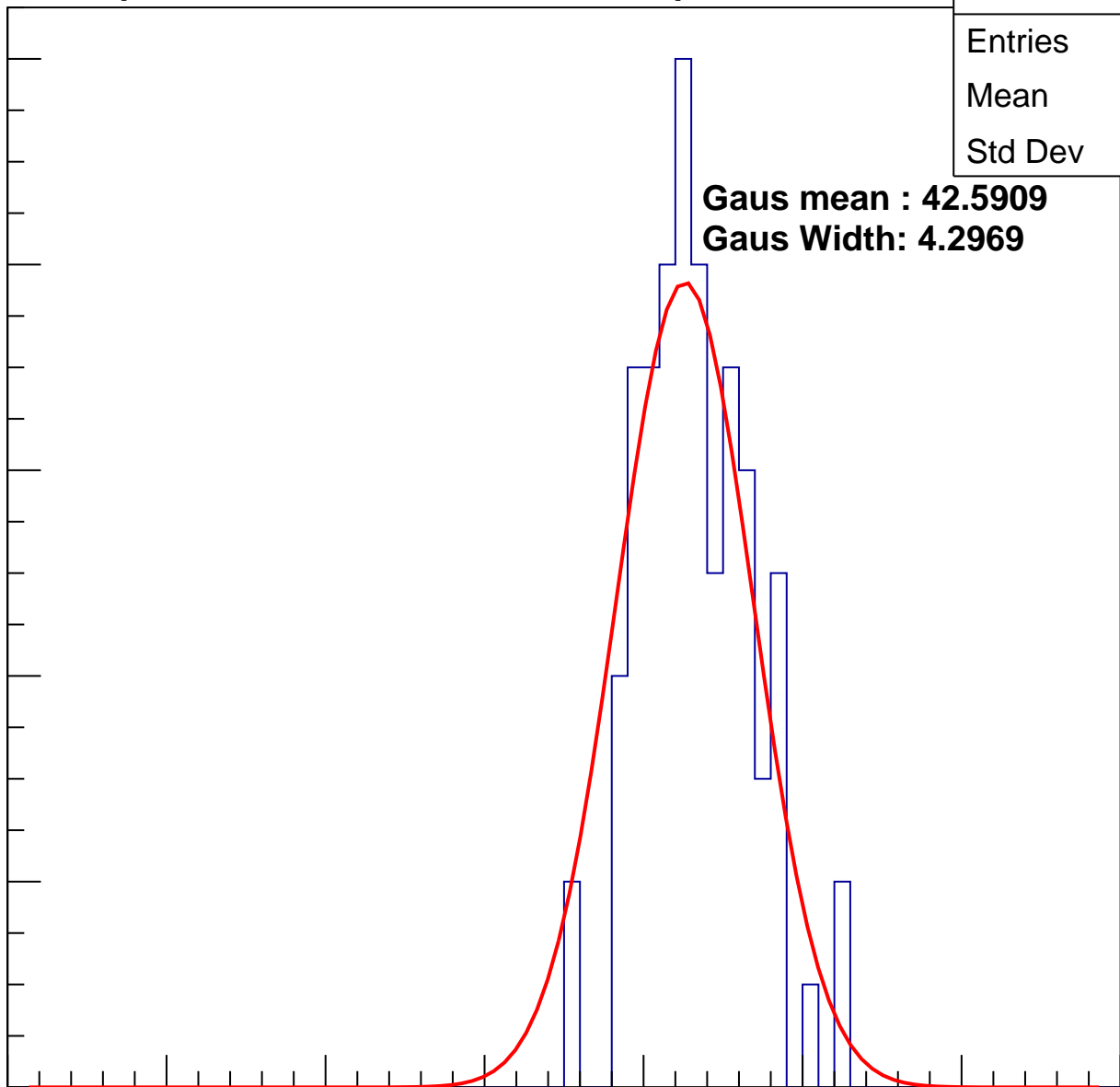
Gaus Width: 4.2969

Entry

10
8
6
4
2
0

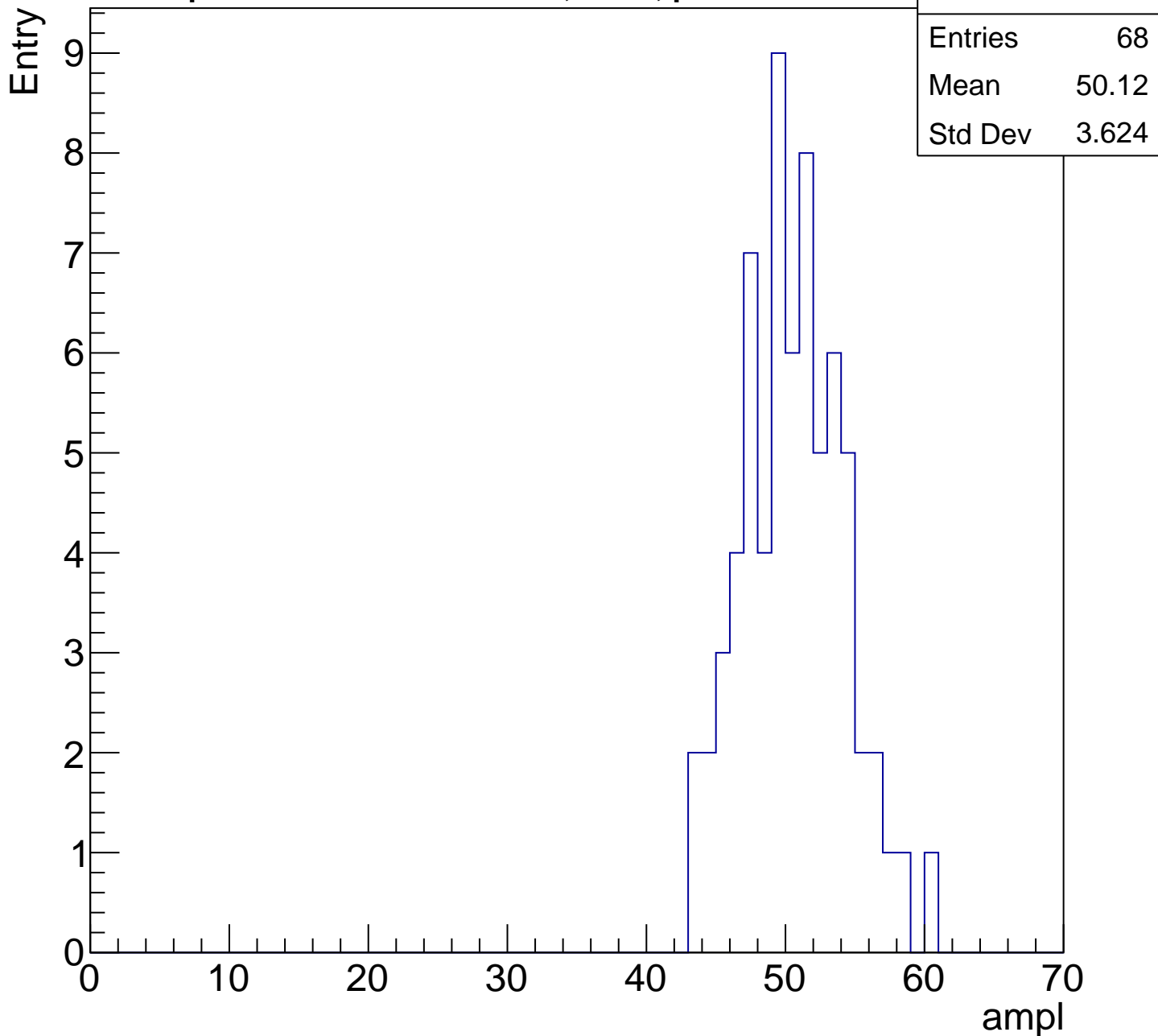
ampl

0 10 20 30 40 50 60 70



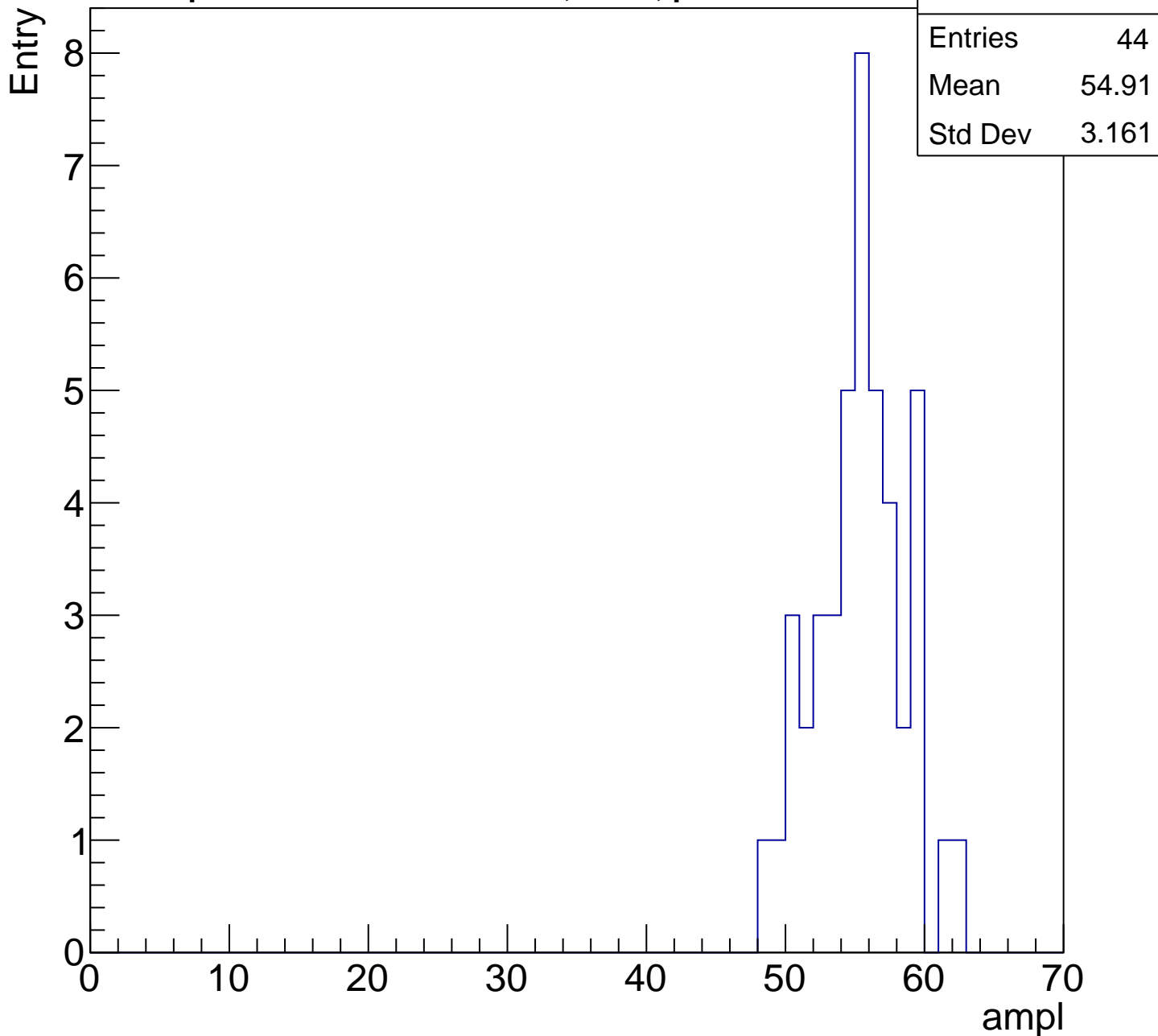
B1L101S, U1-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch83, adc4

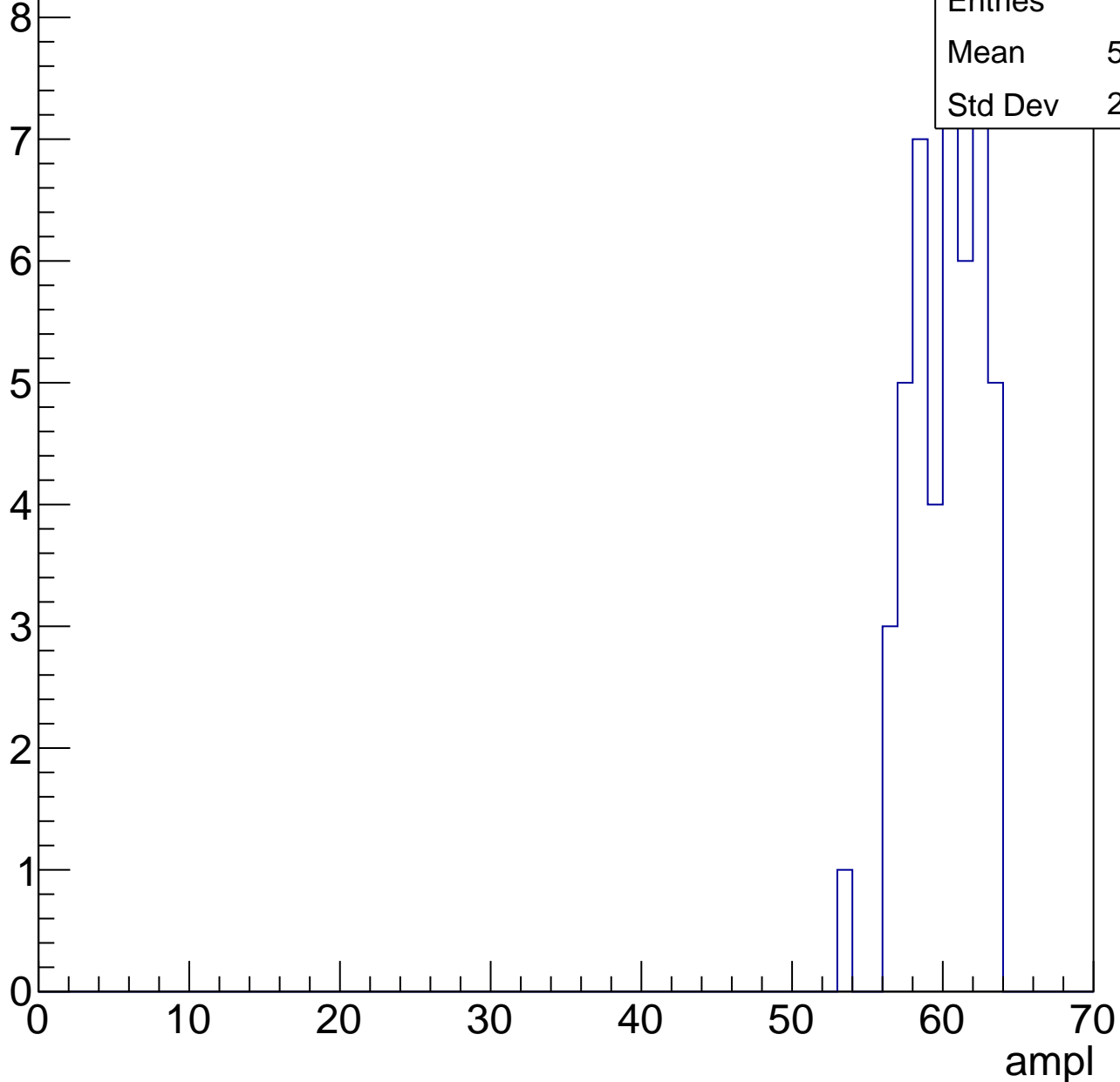
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

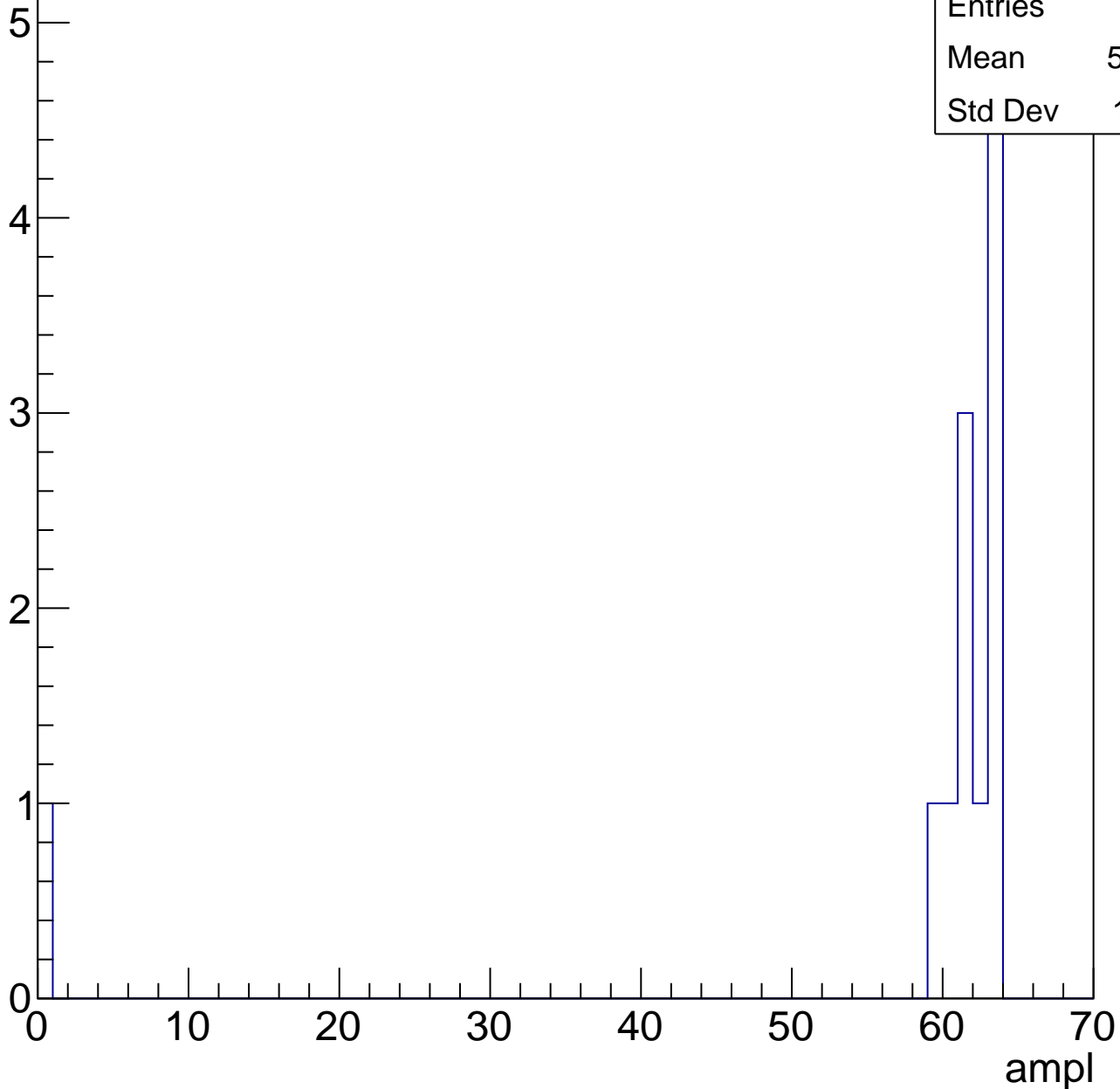


B1L101S, U1-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	56.58
Std Dev	17.11



B1L101S, U1-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch84, adc0

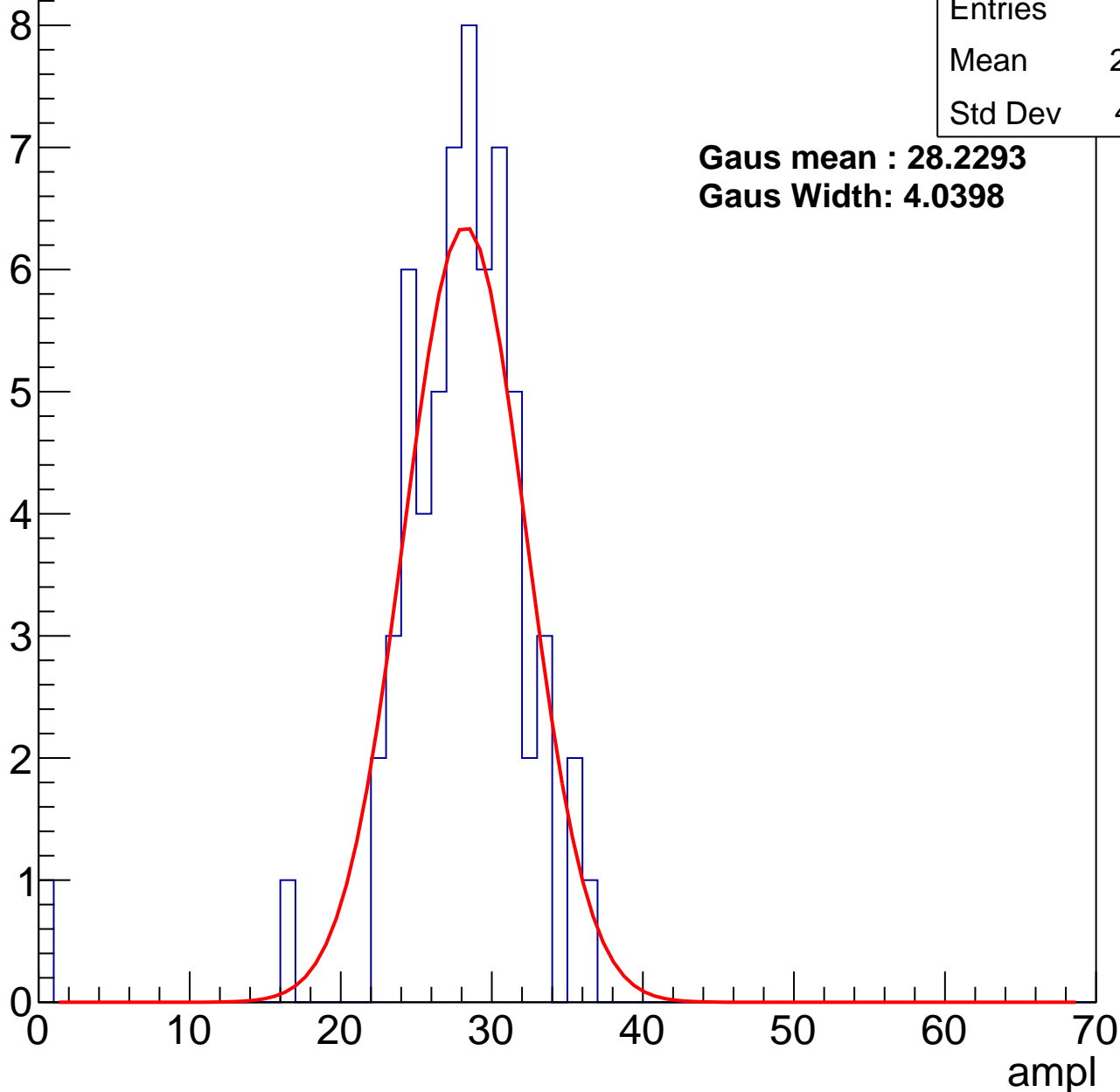
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.37
Std Dev	4.971

Gaus mean : 28.2293

Gaus Width: 4.0398



B1L101S, U1-ch84, adc1

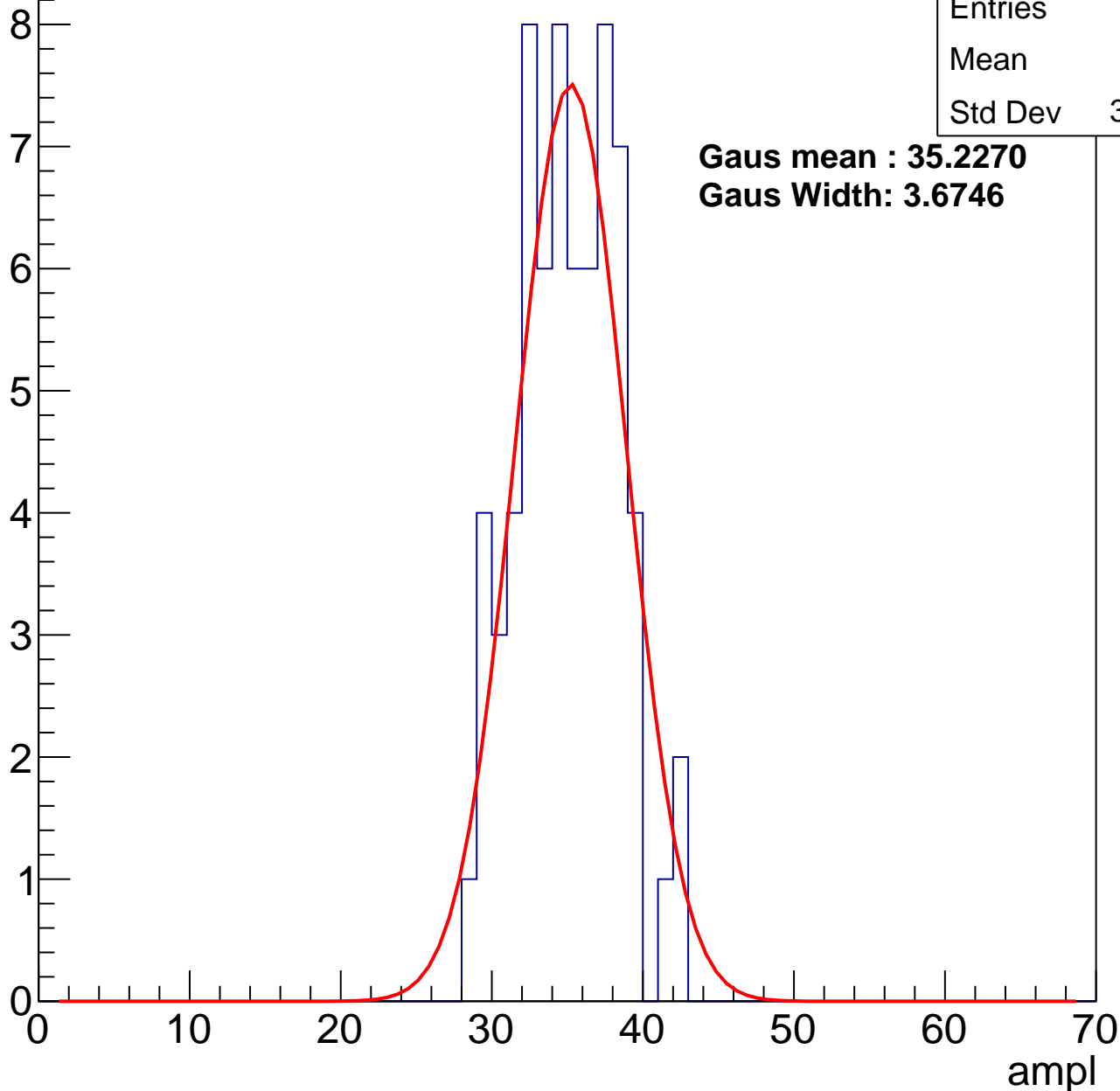
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	34.6
Std Dev	3.264

Gaus mean : 35.2270

Gaus Width: 3.6746



B1L101S, U1-ch84, adc2

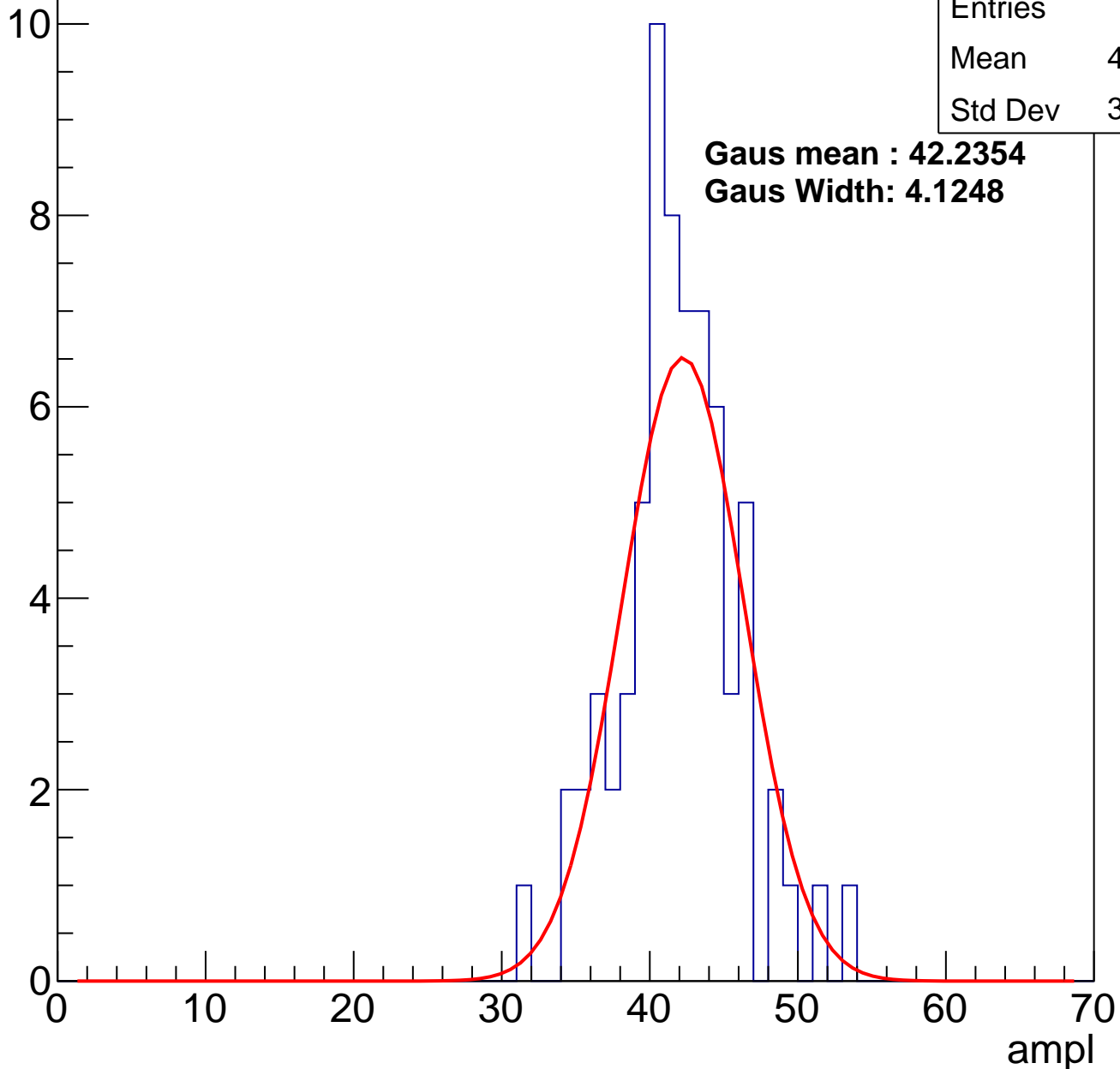
calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	41.46
Std Dev	3.969

Gaus mean : 42.2354

Gaus Width: 4.1248

Entry

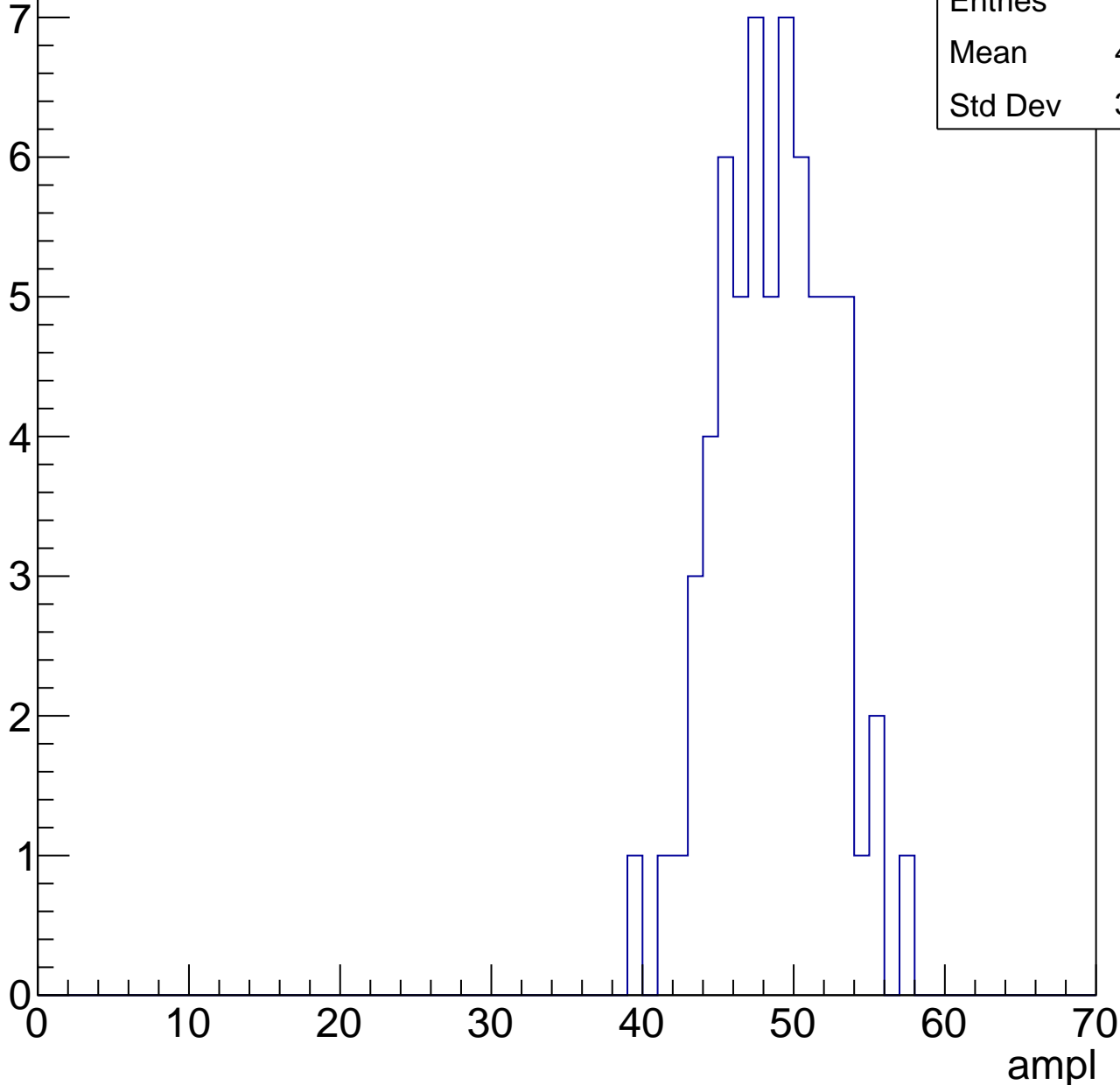


B1L101S, U1-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

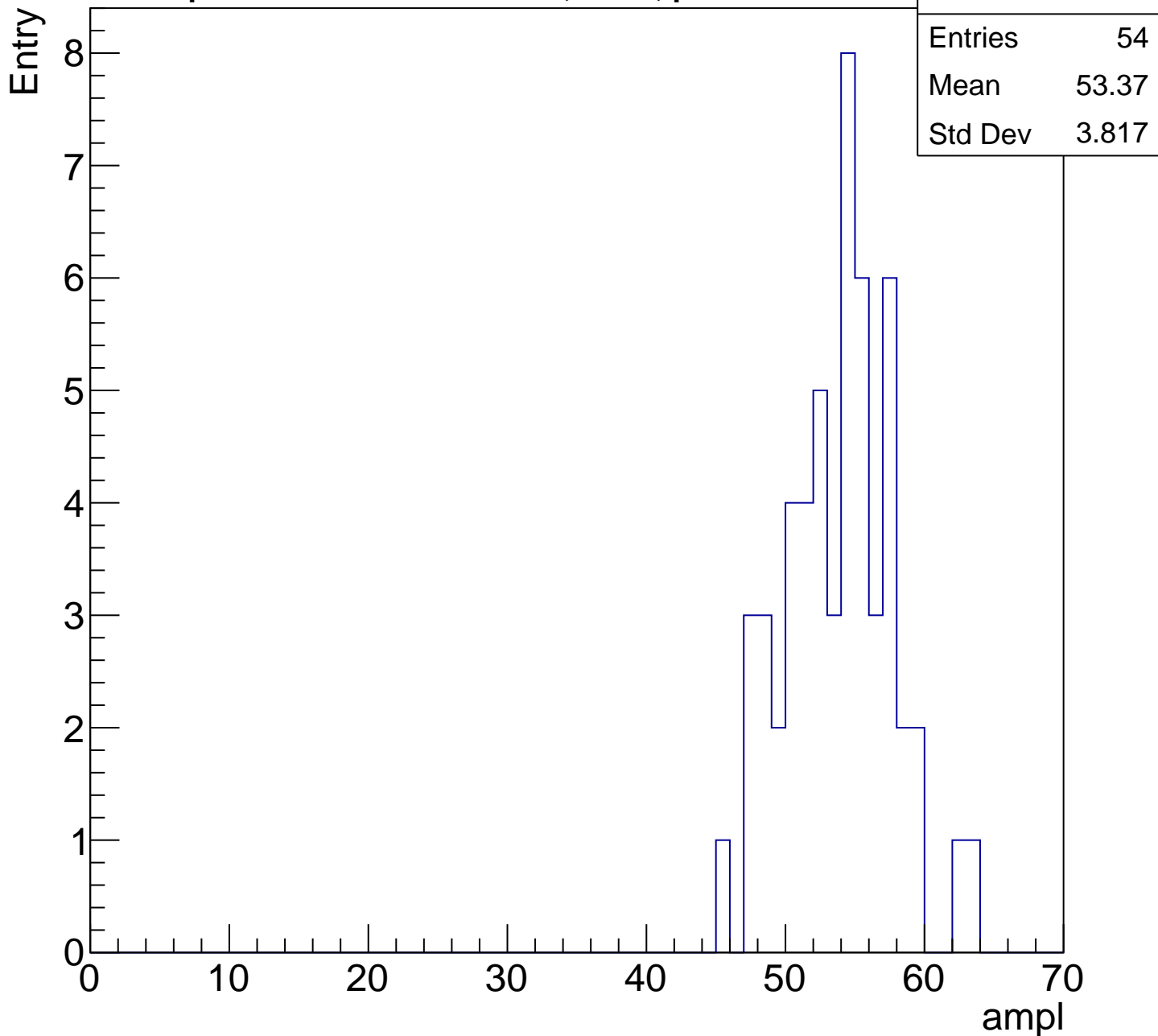
Entry

Entries	65
Mean	48.31
Std Dev	3.671



B1L101S, U1-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 58

Mean 59.21

Std Dev 2.462

8

6

4

2

0

0

10

20

30

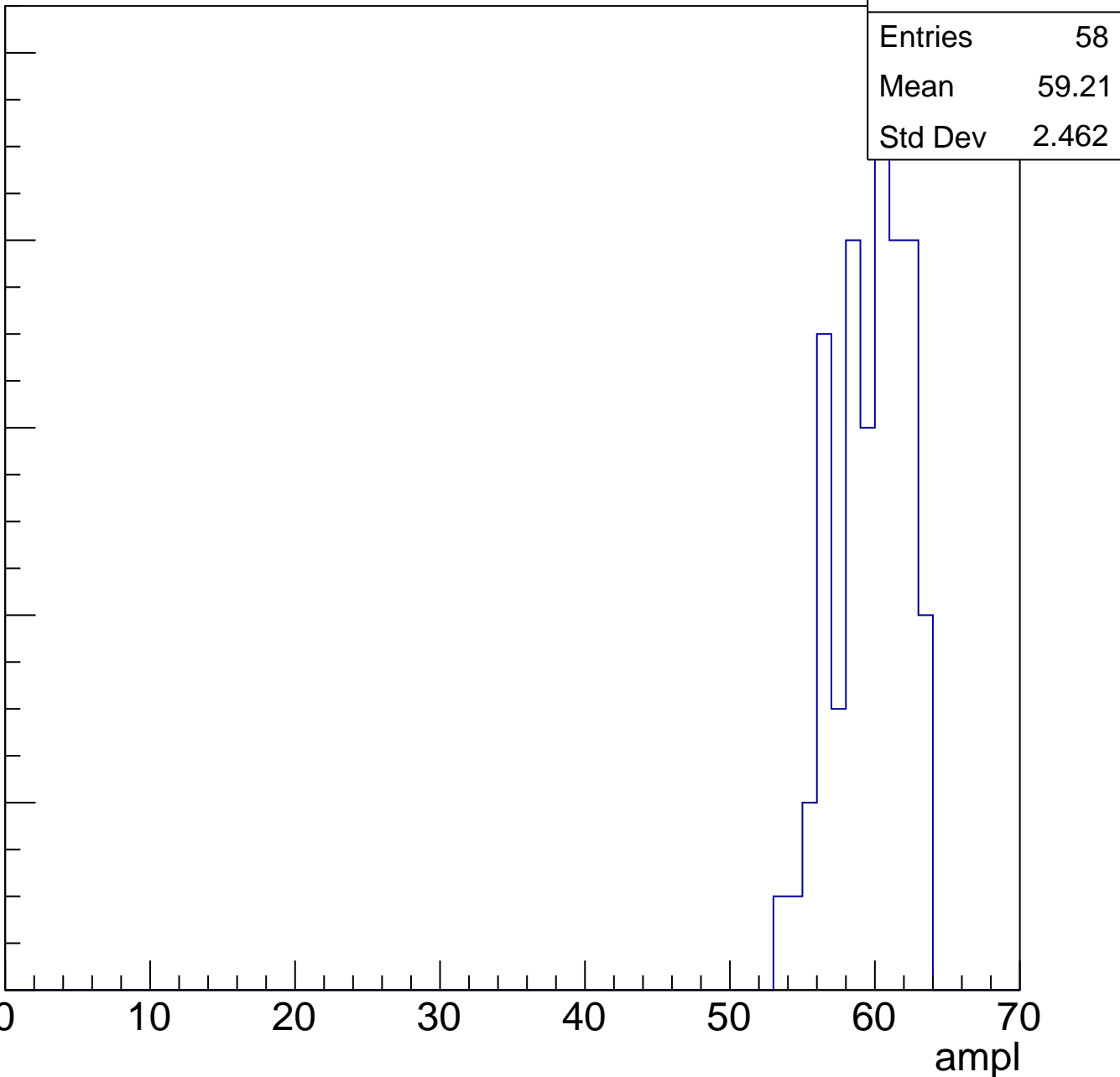
40

50

60

70

ampl

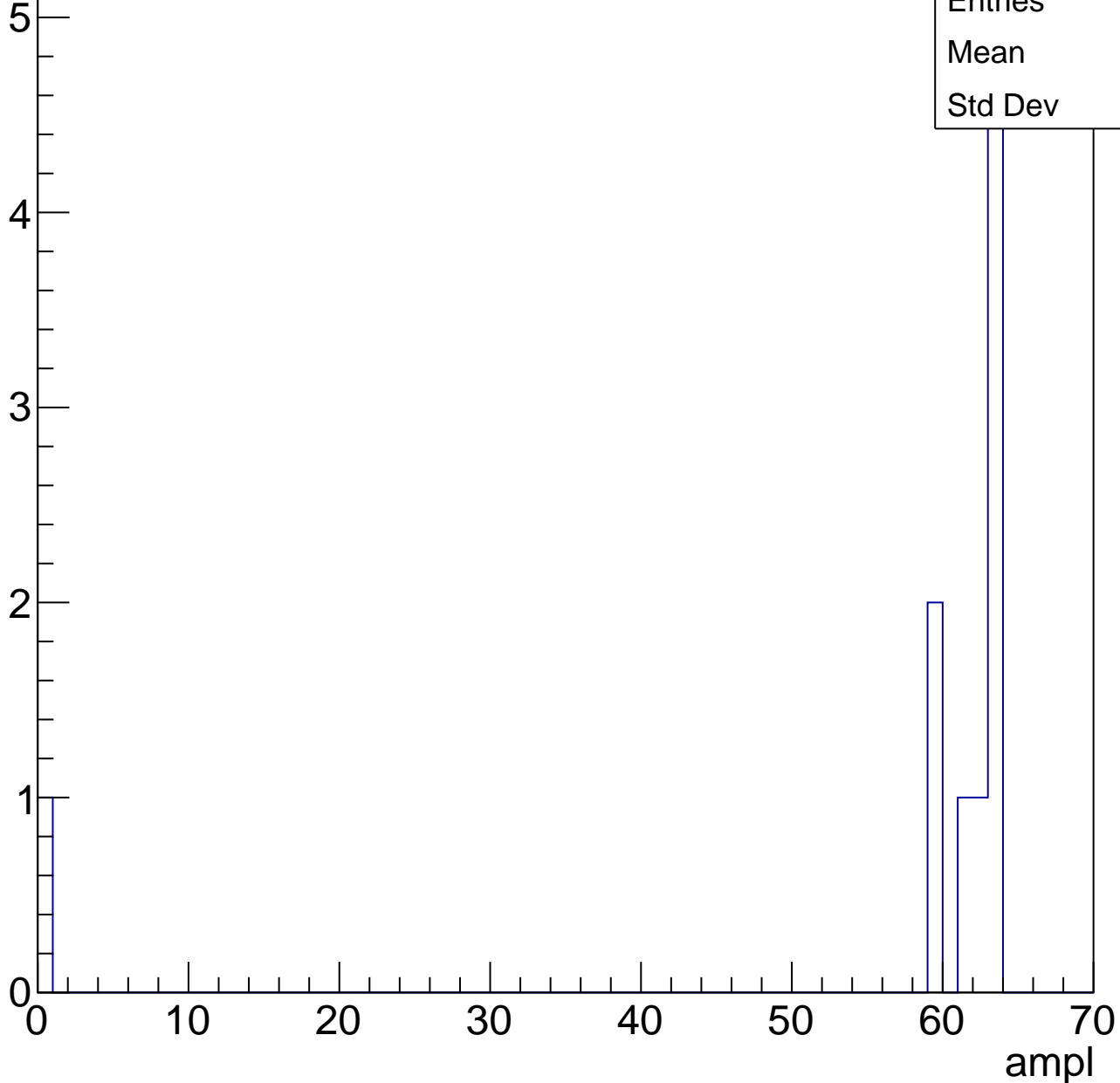


B1L101S, U1-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

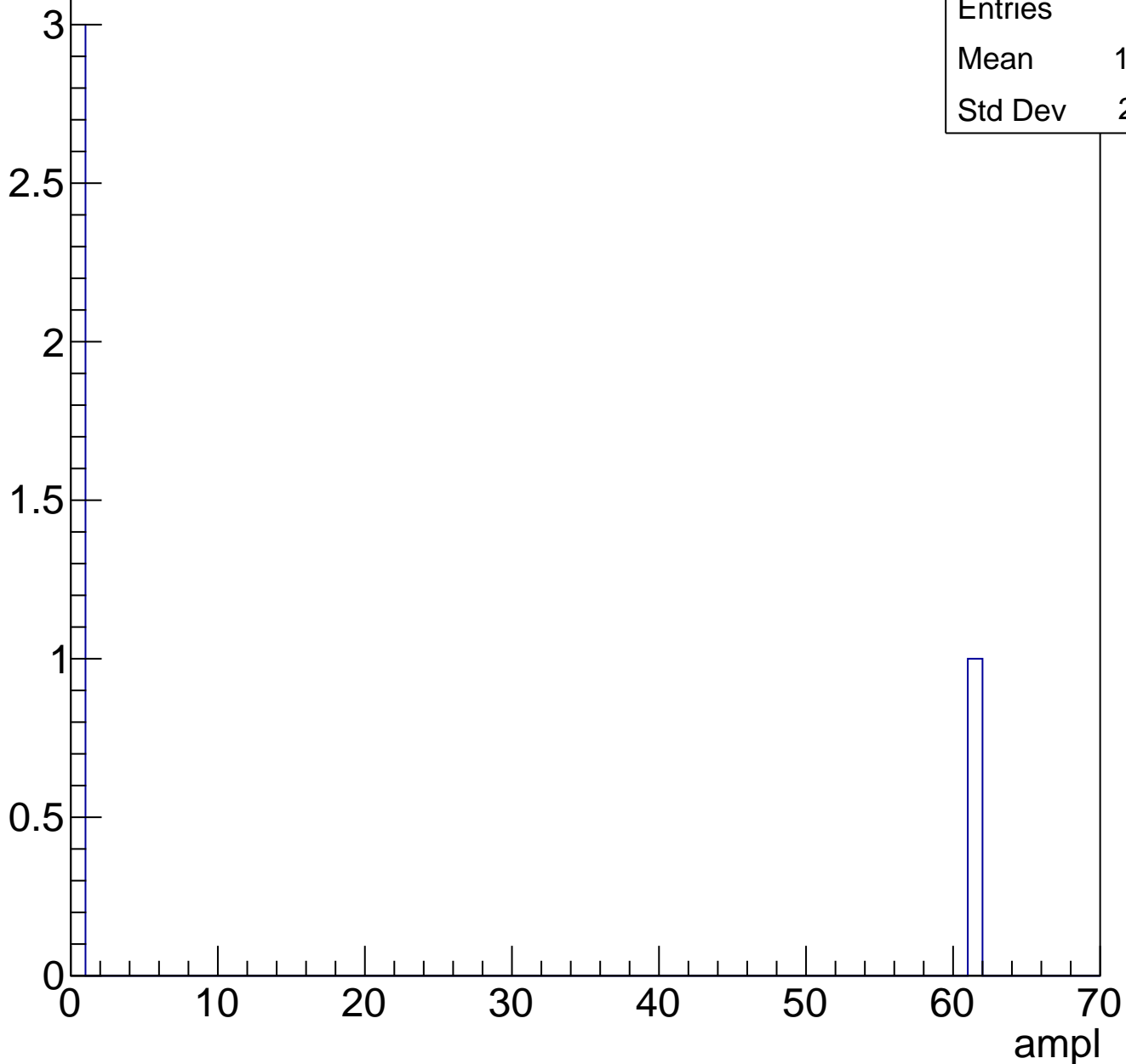
Entries	10
Mean	55.6
Std Dev	18.6



B1L101S, U1-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch85, adc0

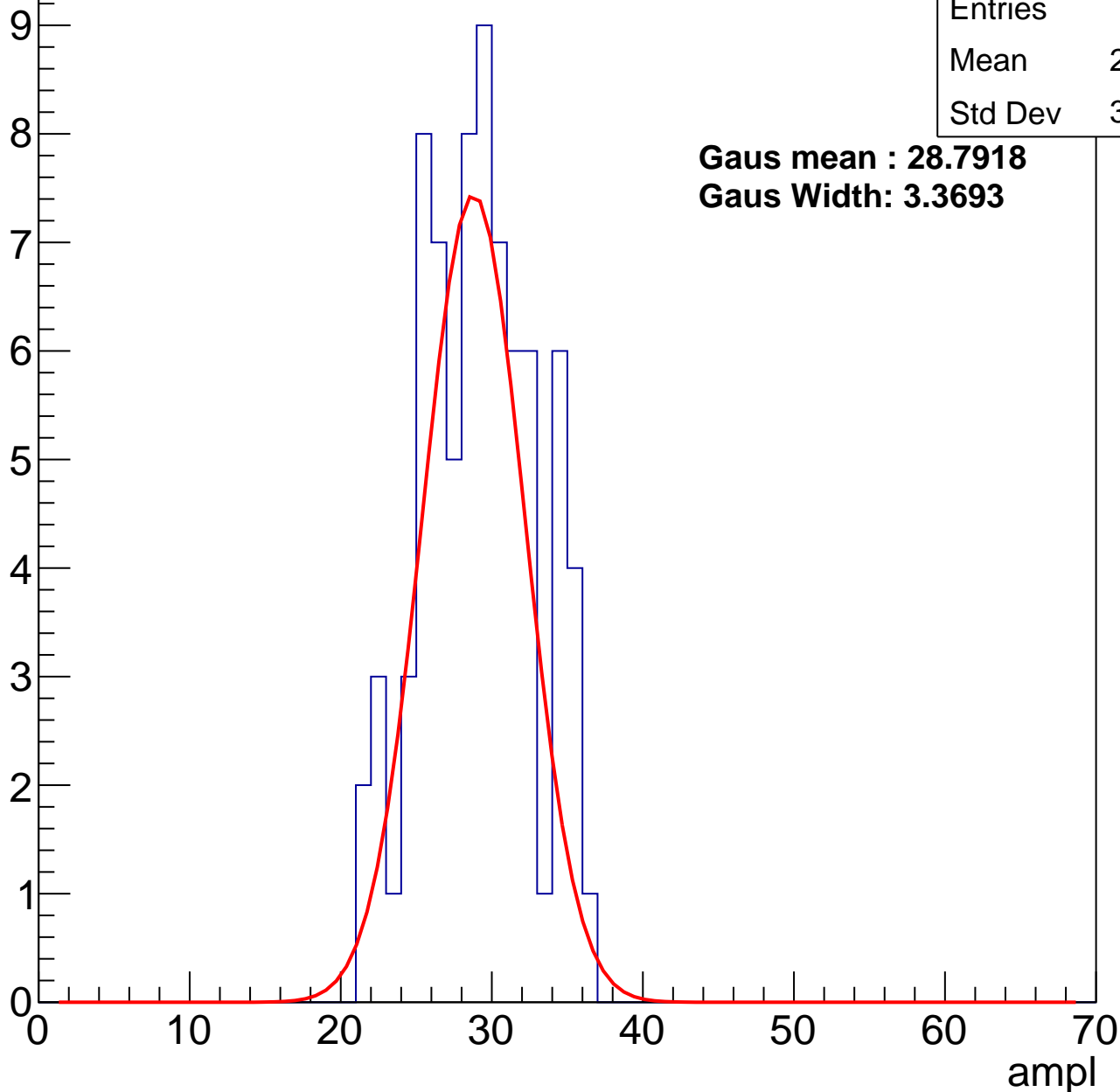
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.65
Std Dev	3.674

Gaus mean : 28.7918

Gaus Width: 3.3693



B1L101S, U1-ch85, adc1

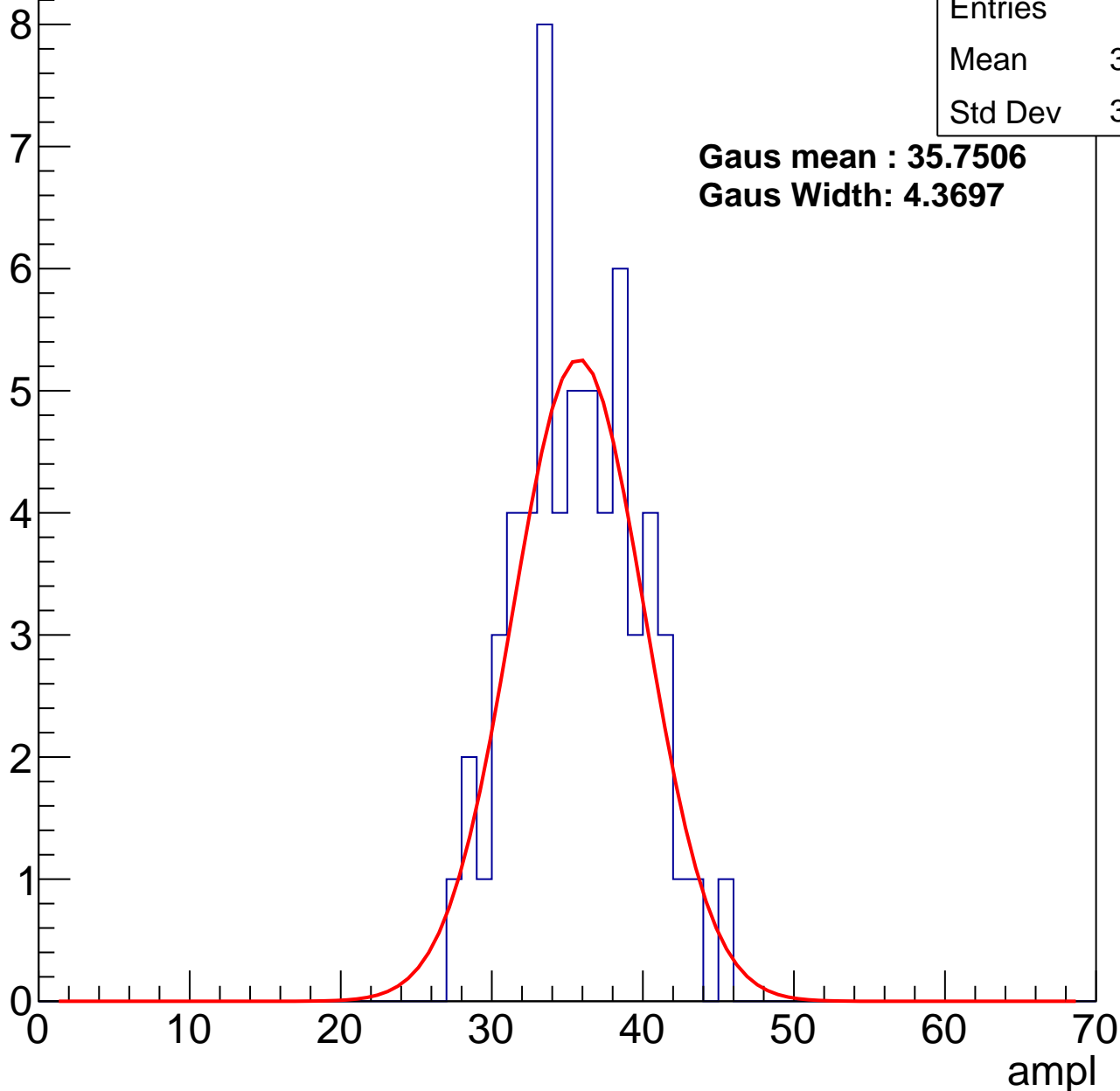
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.25
Std Dev	3.978

Gaus mean : 35.7506

Gaus Width: 4.3697



B1L101S, U1-ch85, adc2

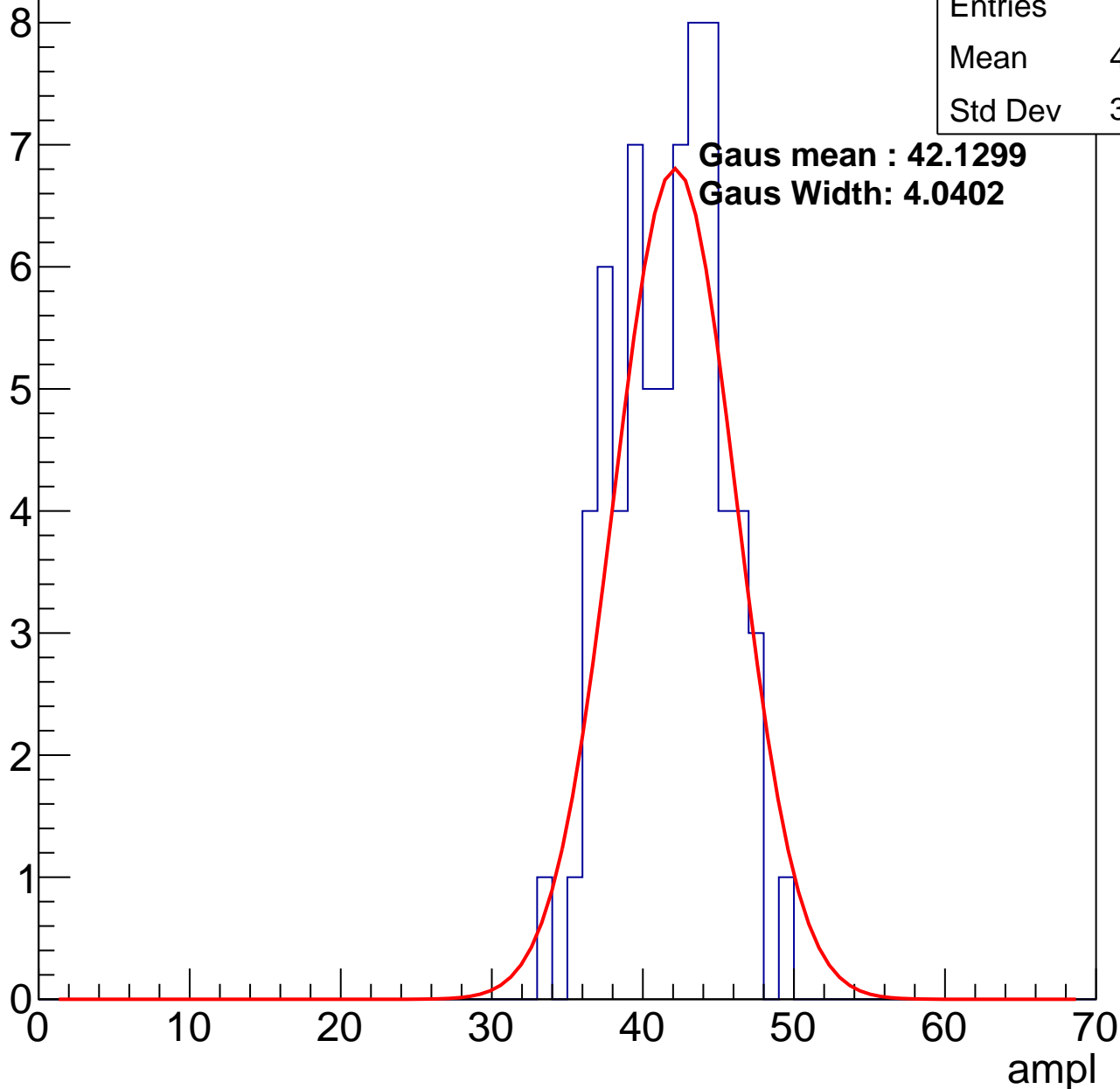
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.29
Std Dev	3.443

Gaus mean : 42.1299

Gaus Width: 4.0402

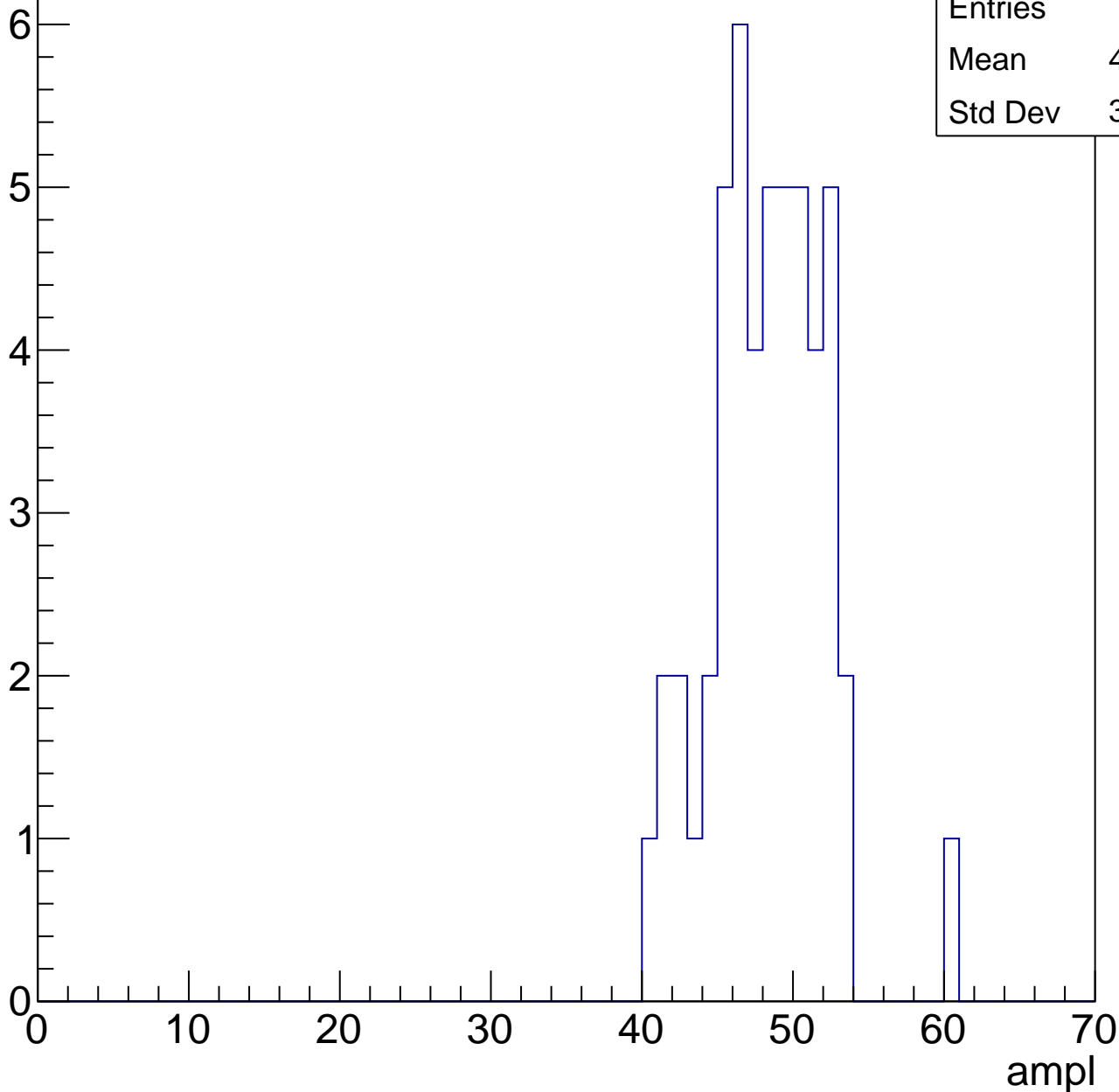


B1L101S, U1-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	47.82
Std Dev	3.735

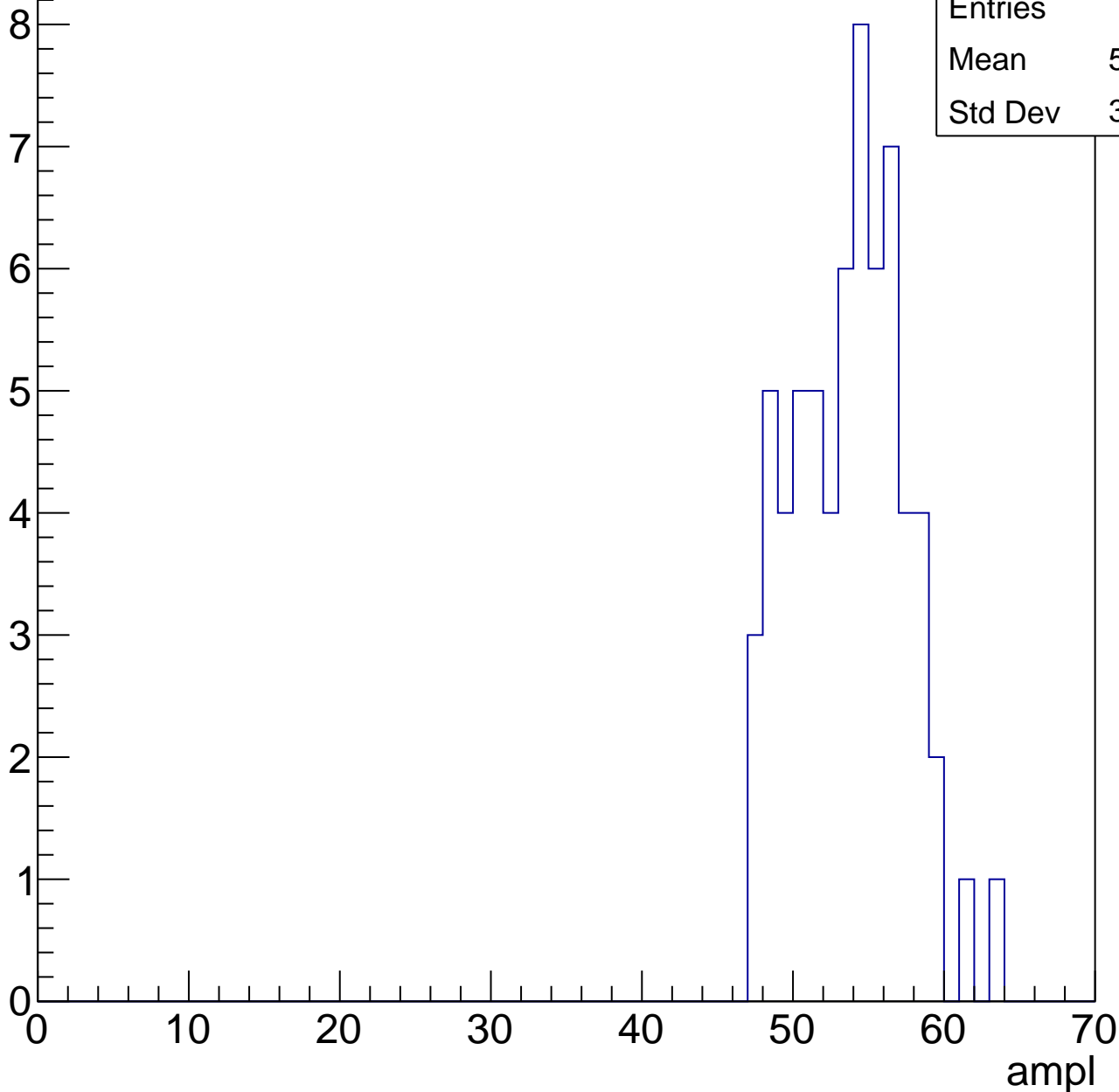


B1L101S, U1-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

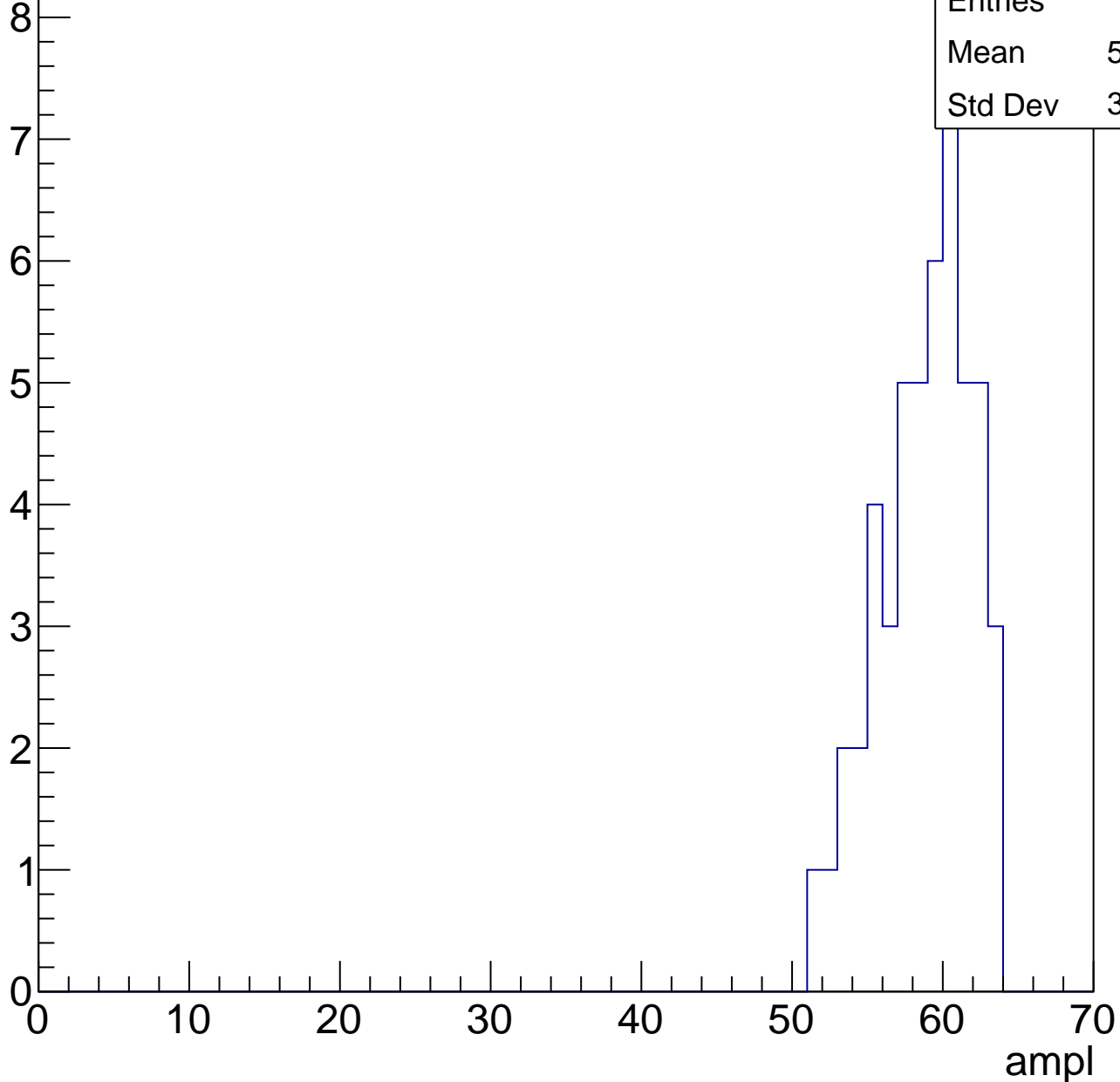
Entries	65
Mean	53.29
Std Dev	3.628



B1L101S, U1-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



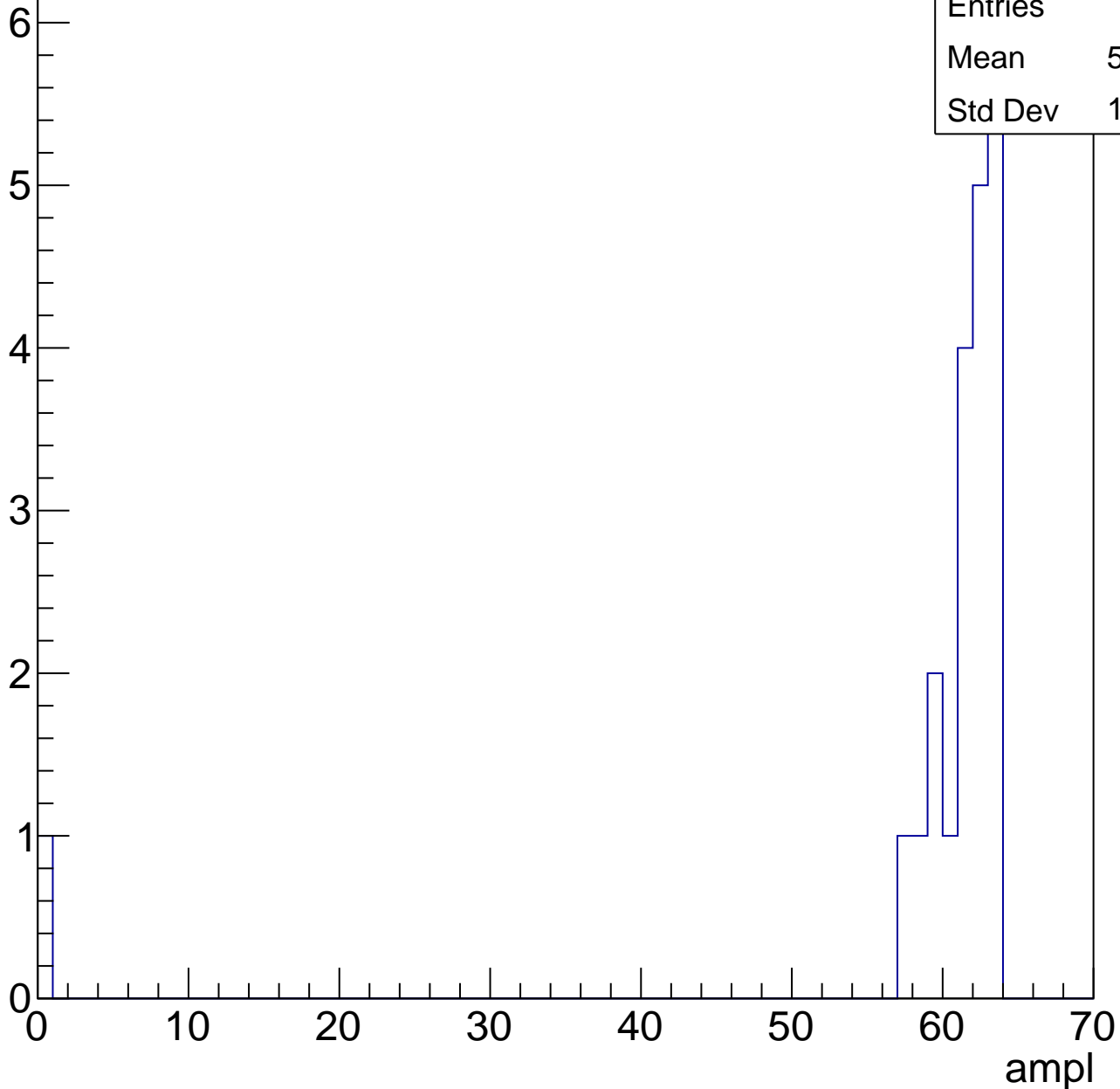
Entries	50
Mean	58.36
Std Dev	3.005

B1L101S, U1-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.33
Std Dev	13.16



B1L101S, U1-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch86, adc0

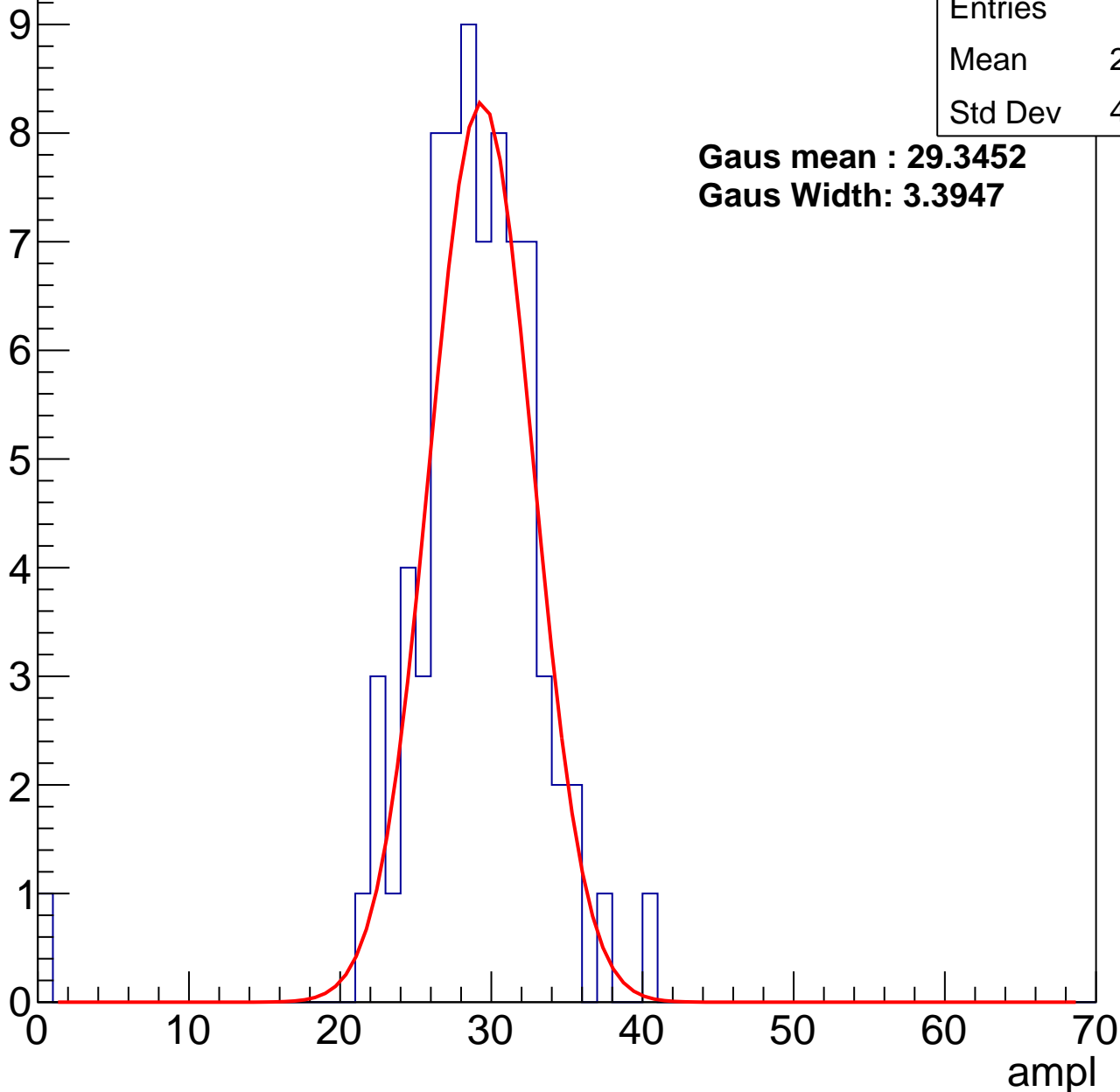
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.36
Std Dev	4.828

Gaus mean : 29.3452

Gaus Width: 3.3947



B1L101S, U1-ch86, adc1

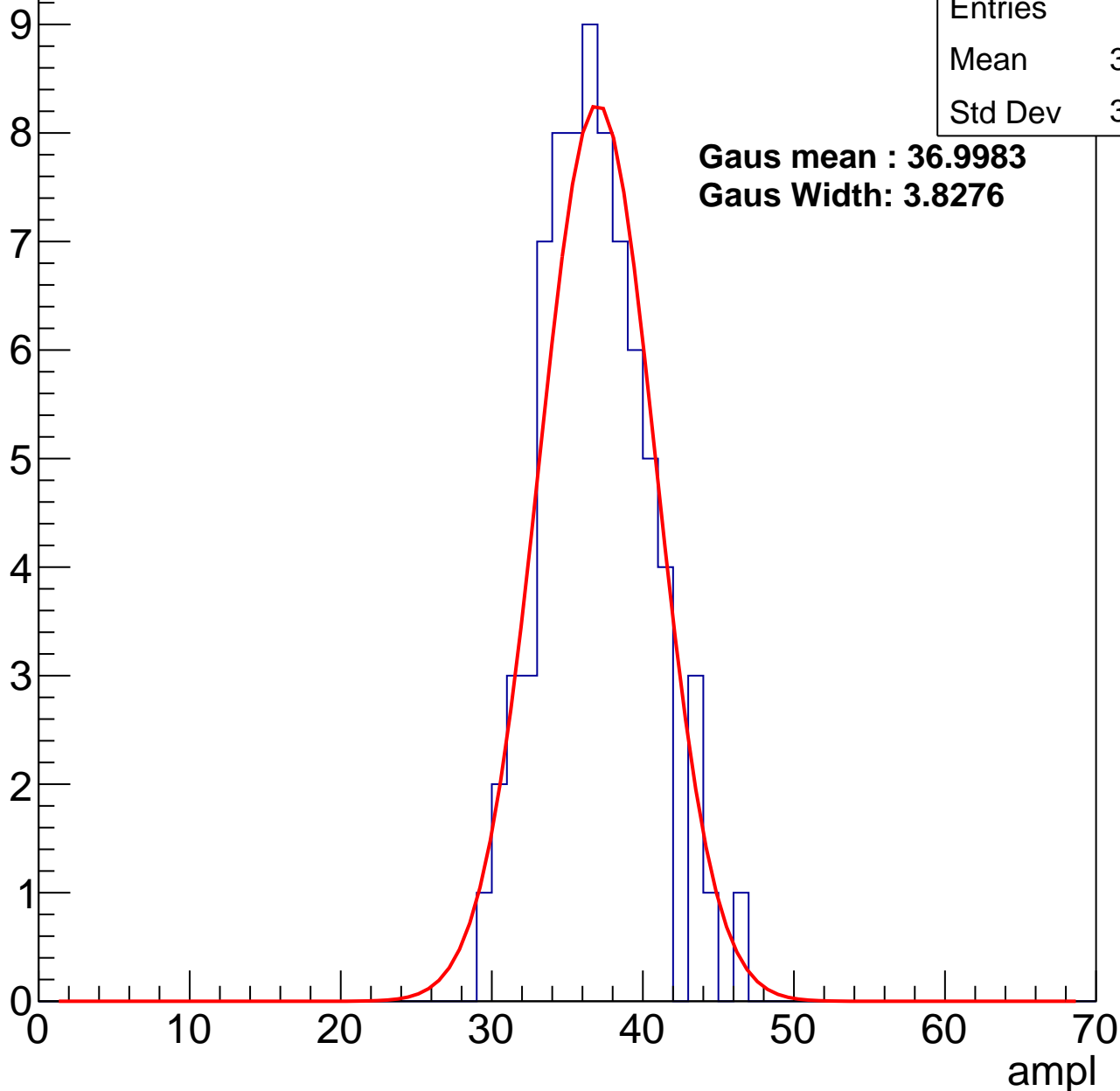
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.37
Std Dev	3.475

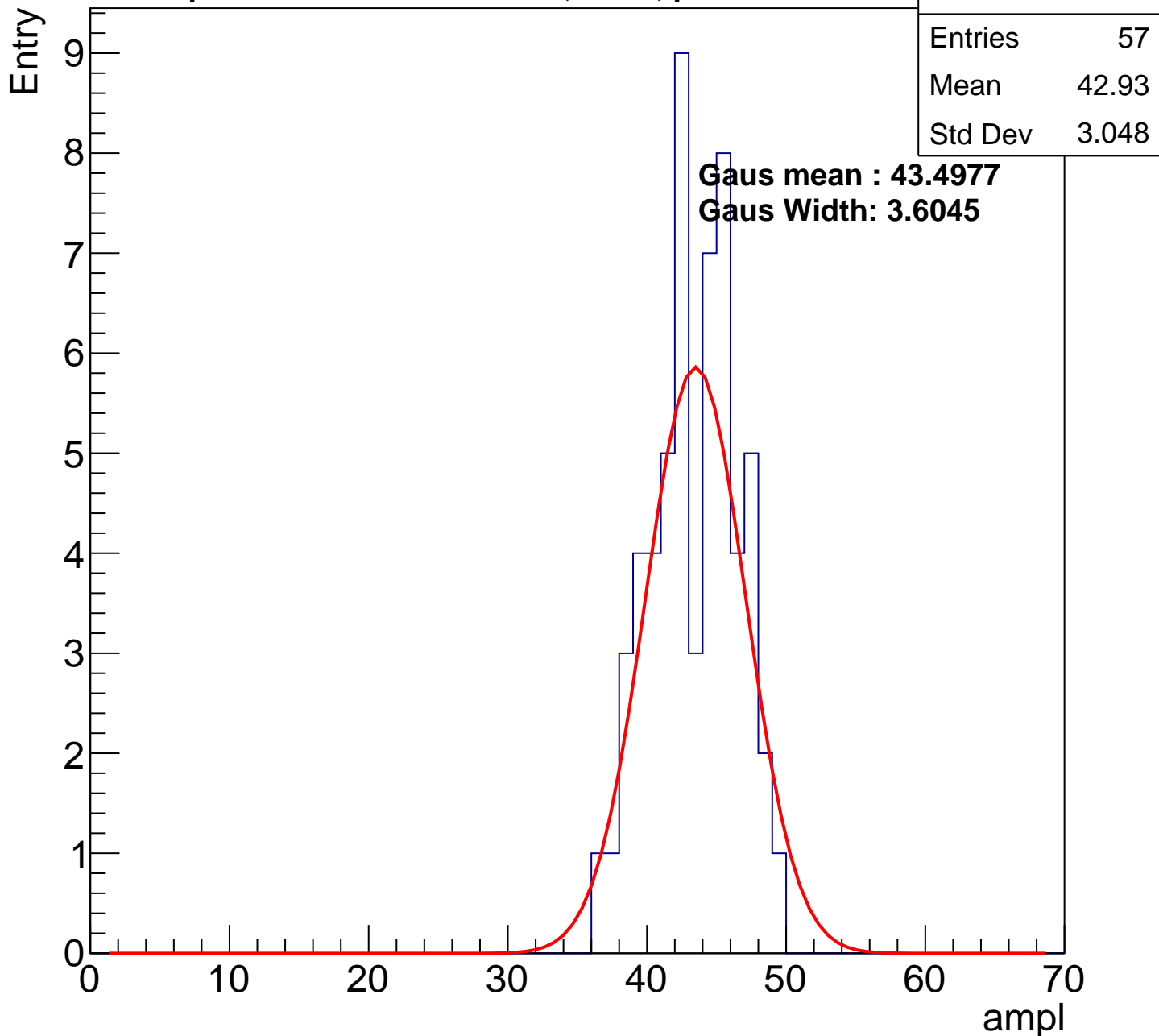
Gaus mean : 36.9983

Gaus Width: 3.8276



B1L101S, U1-ch86, adc2

calib_packv5_042523_0143.root, FC#0, port D2

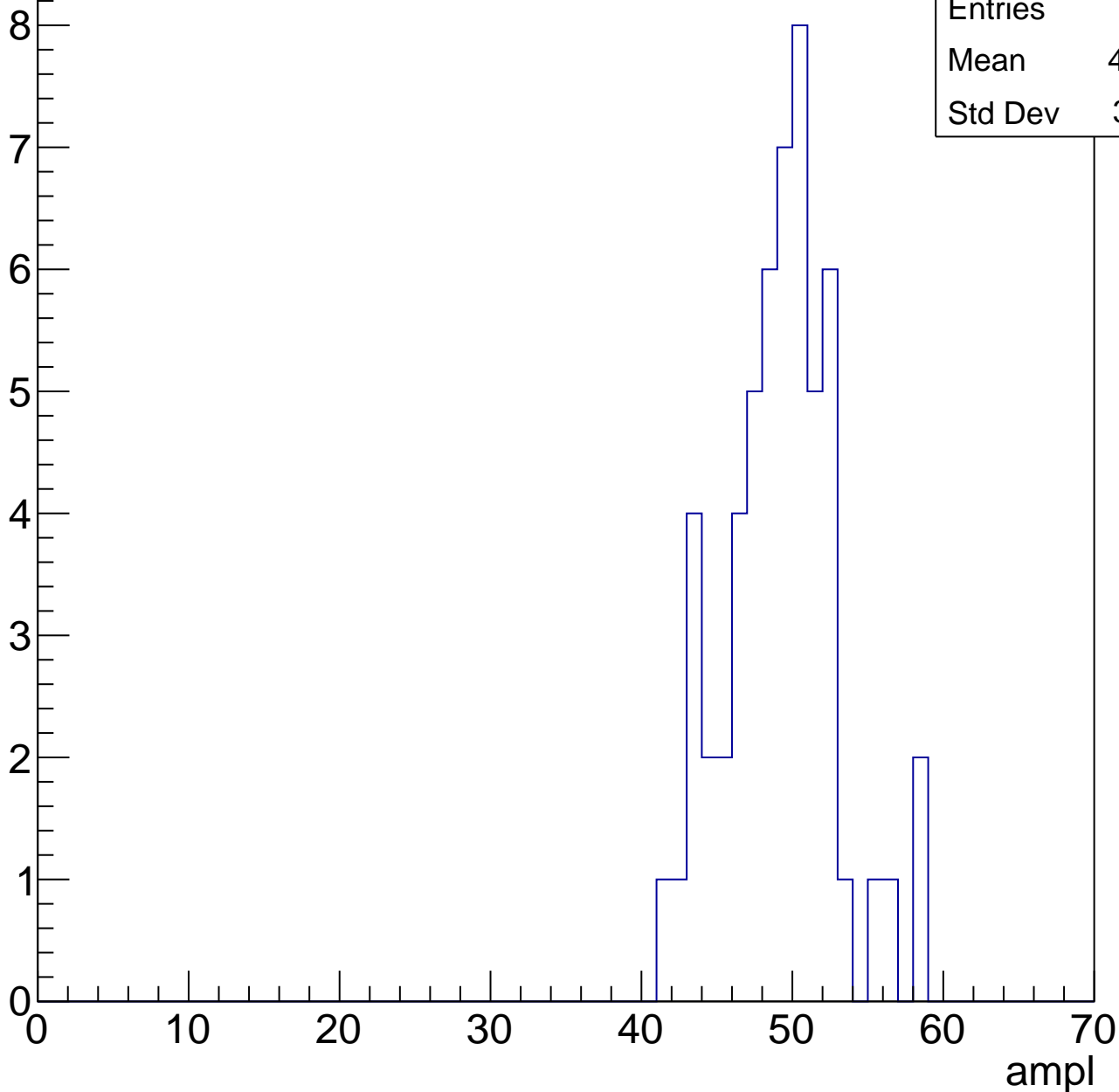


B1L101S, U1-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

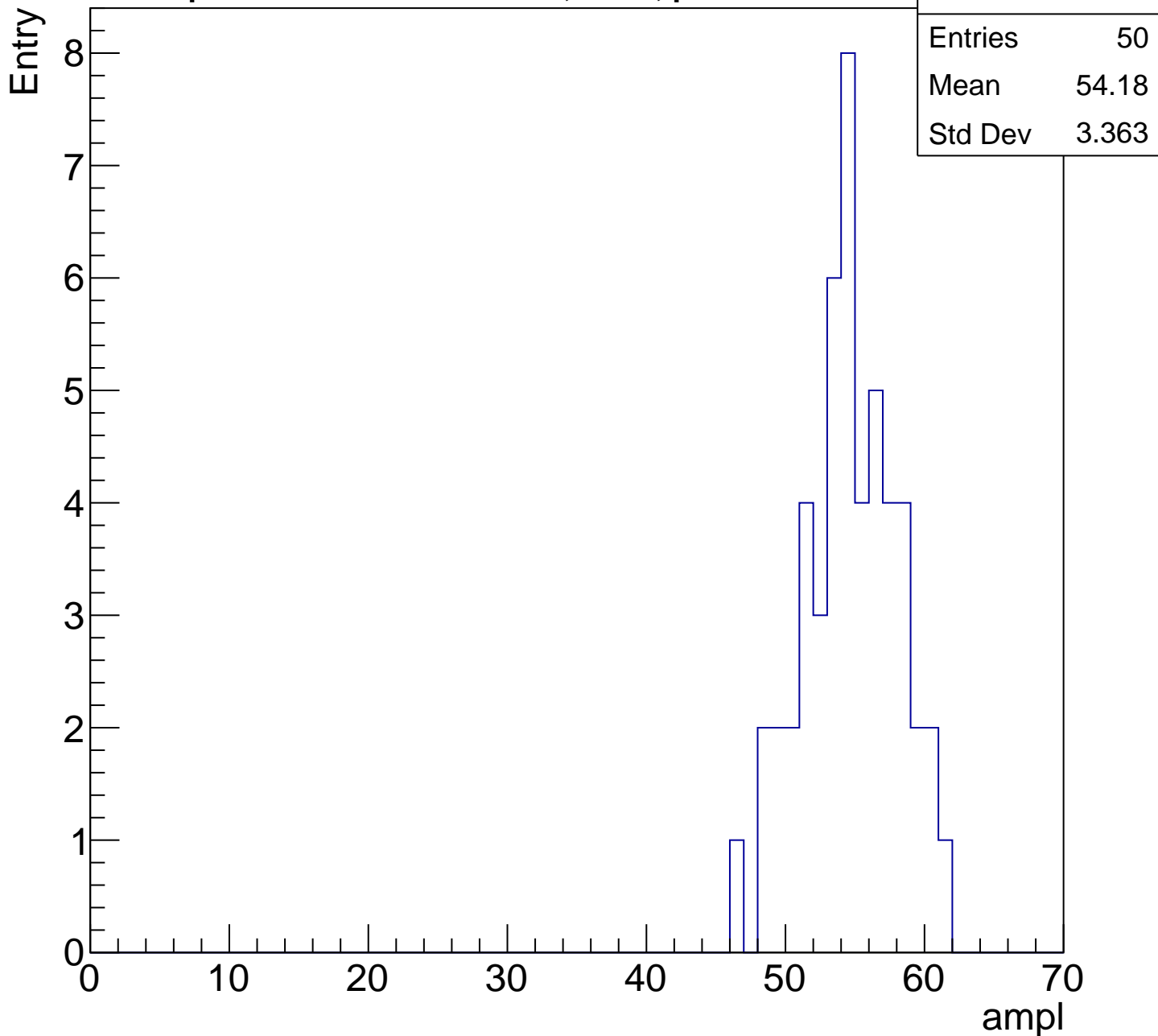
Entry

Entries	56
Mean	48.75
Std Dev	3.641



B1L101S, U1-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

56

Mean

58.41

Std Dev

3.184

0

10

20

30

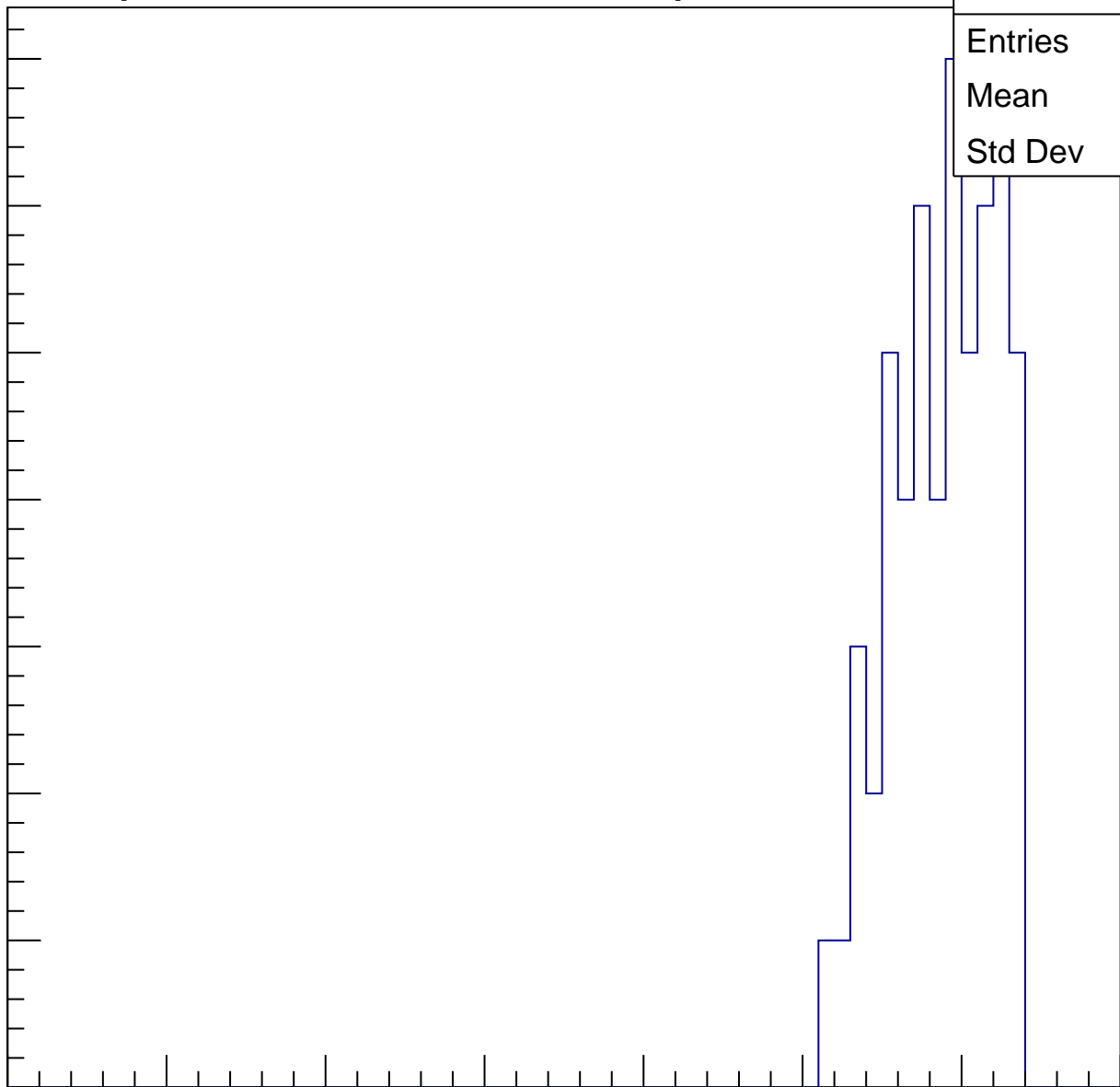
40

50

60

70

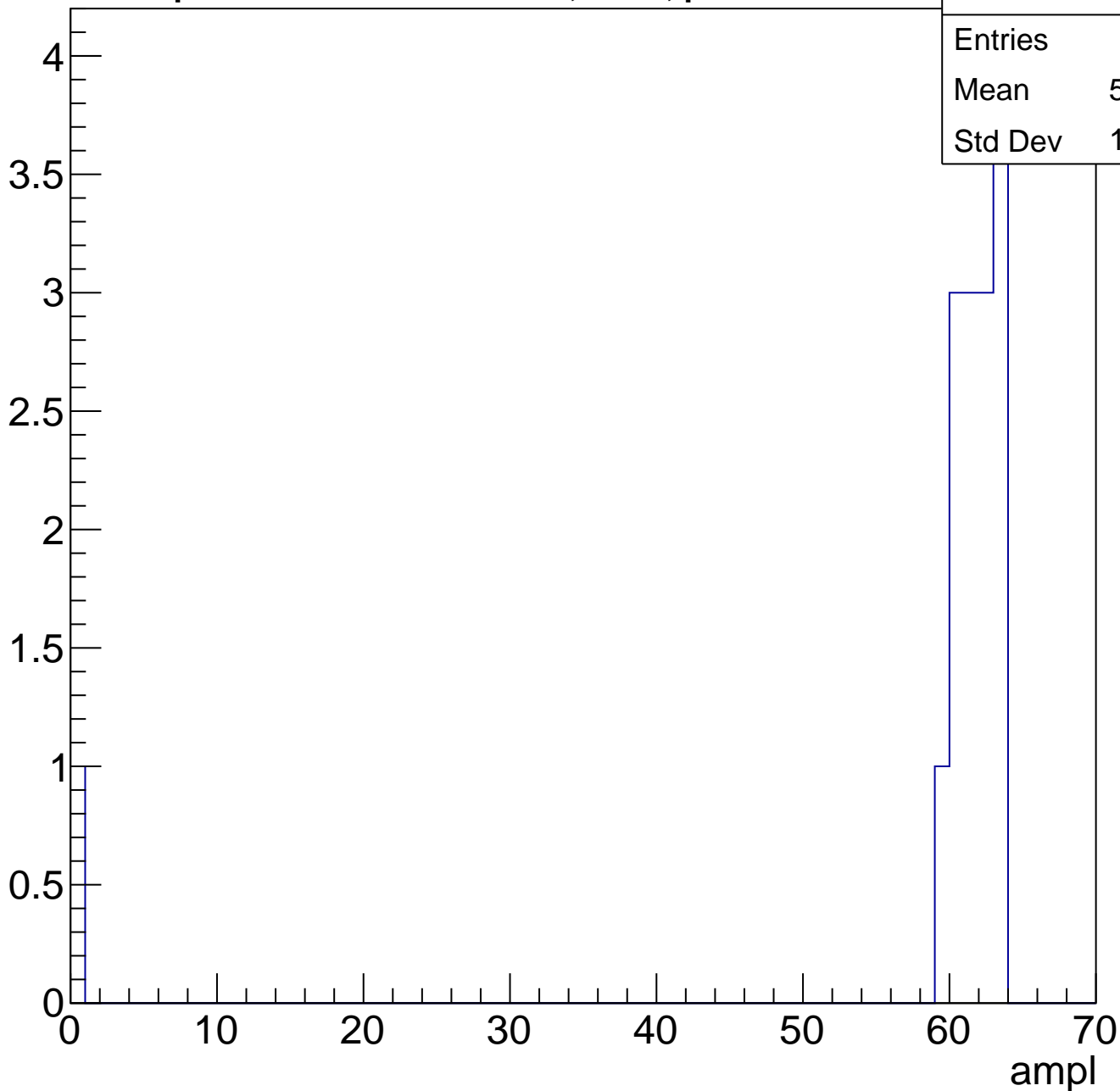
ampl



B1L101S, U1-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch87, adc0

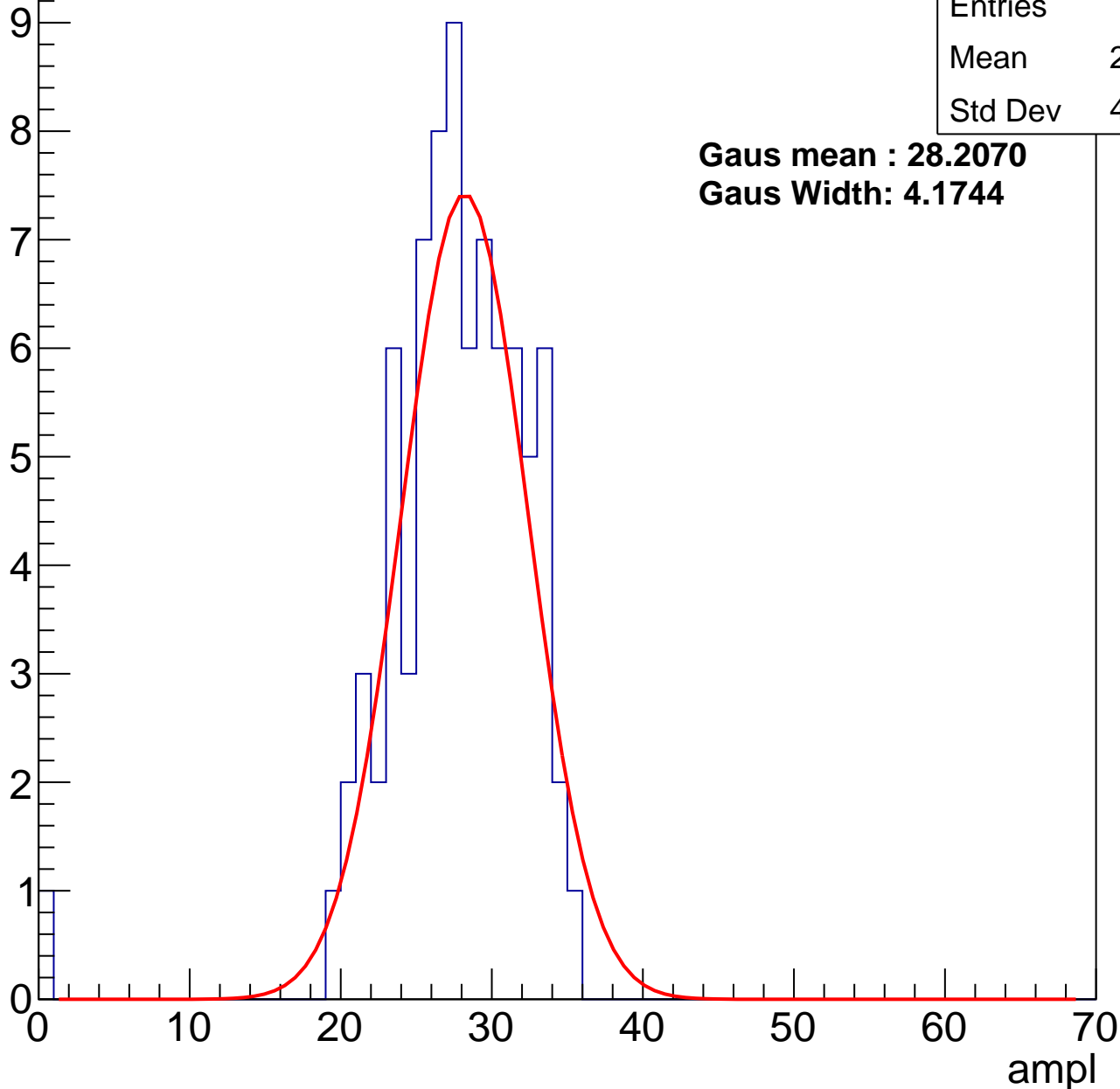
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	27.16
Std Dev	4.834

Gaus mean : 28.2070

Gaus Width: 4.1744



B1L101S, U1-ch87, adc1

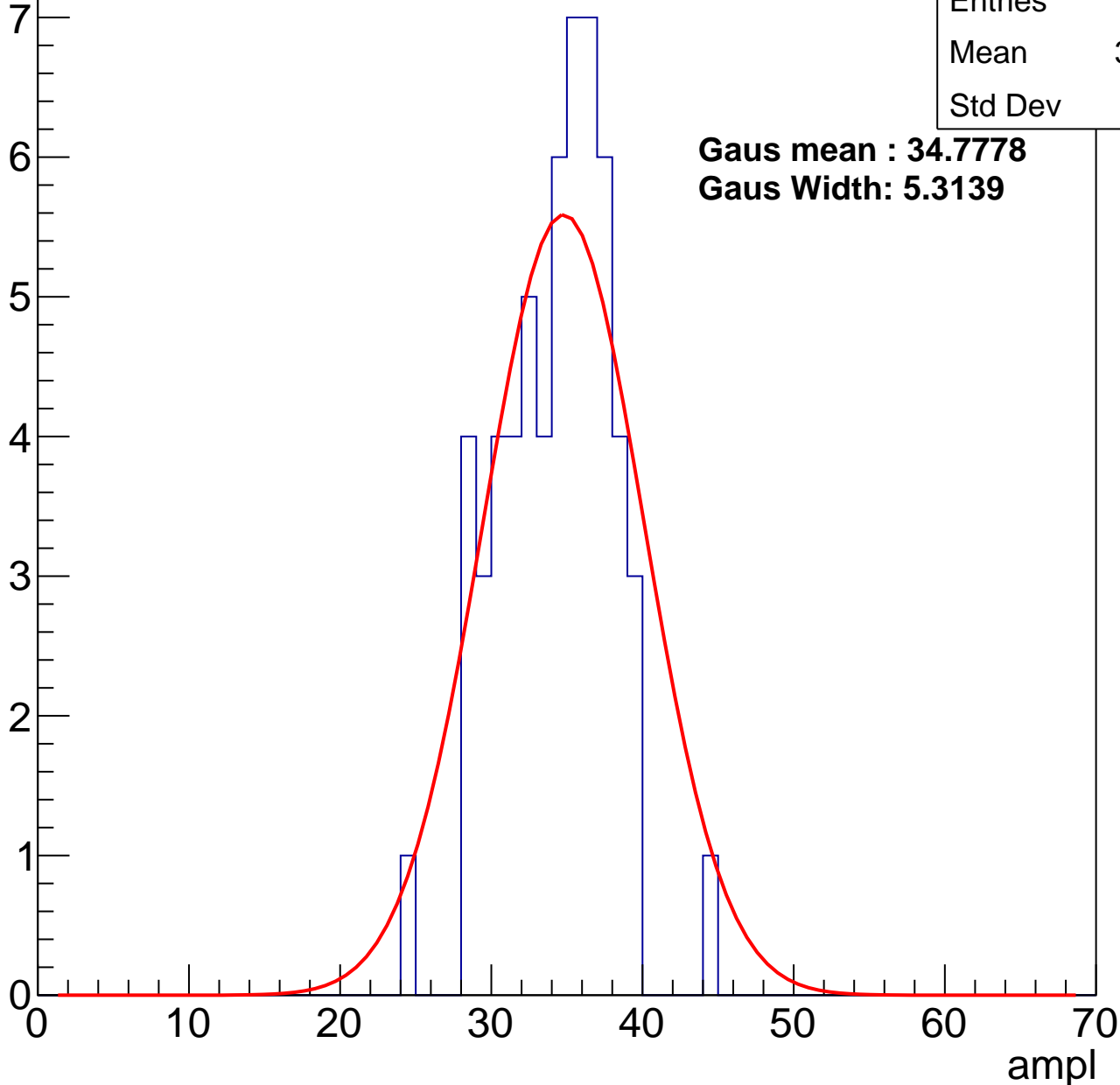
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	33.81
Std Dev	3.61

Gaus mean : 34.7778

Gaus Width: 5.3139



B1L101S, U1-ch87, adc2

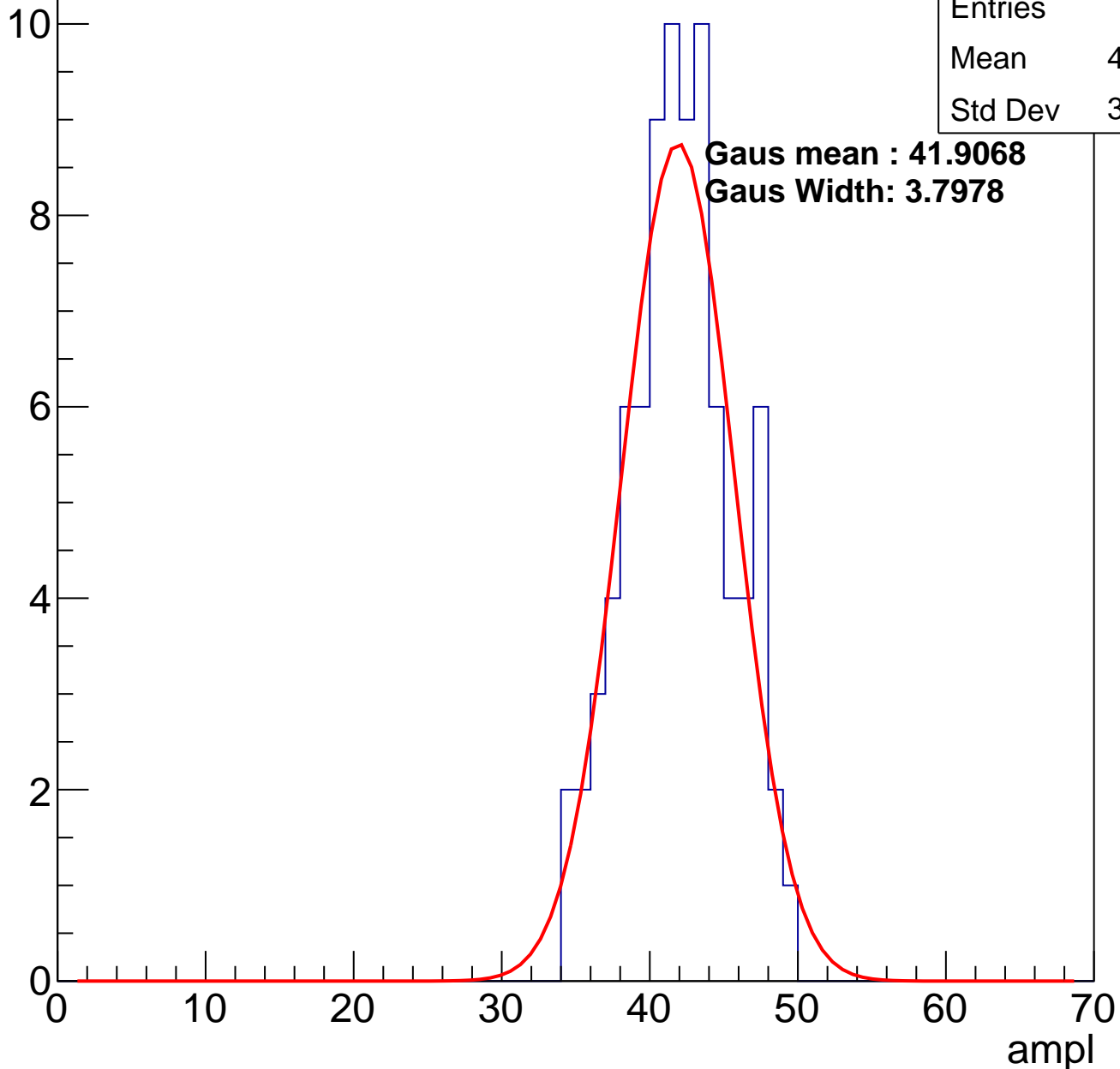
calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	41.54
Std Dev	3.479

Gaus mean : 41.9068

Gaus Width: 3.7978

Entry



B1L101S, U1-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	58
Mean	48.71
Std Dev	3.625

Entry

10

8

6

4

2

0

0

10

20

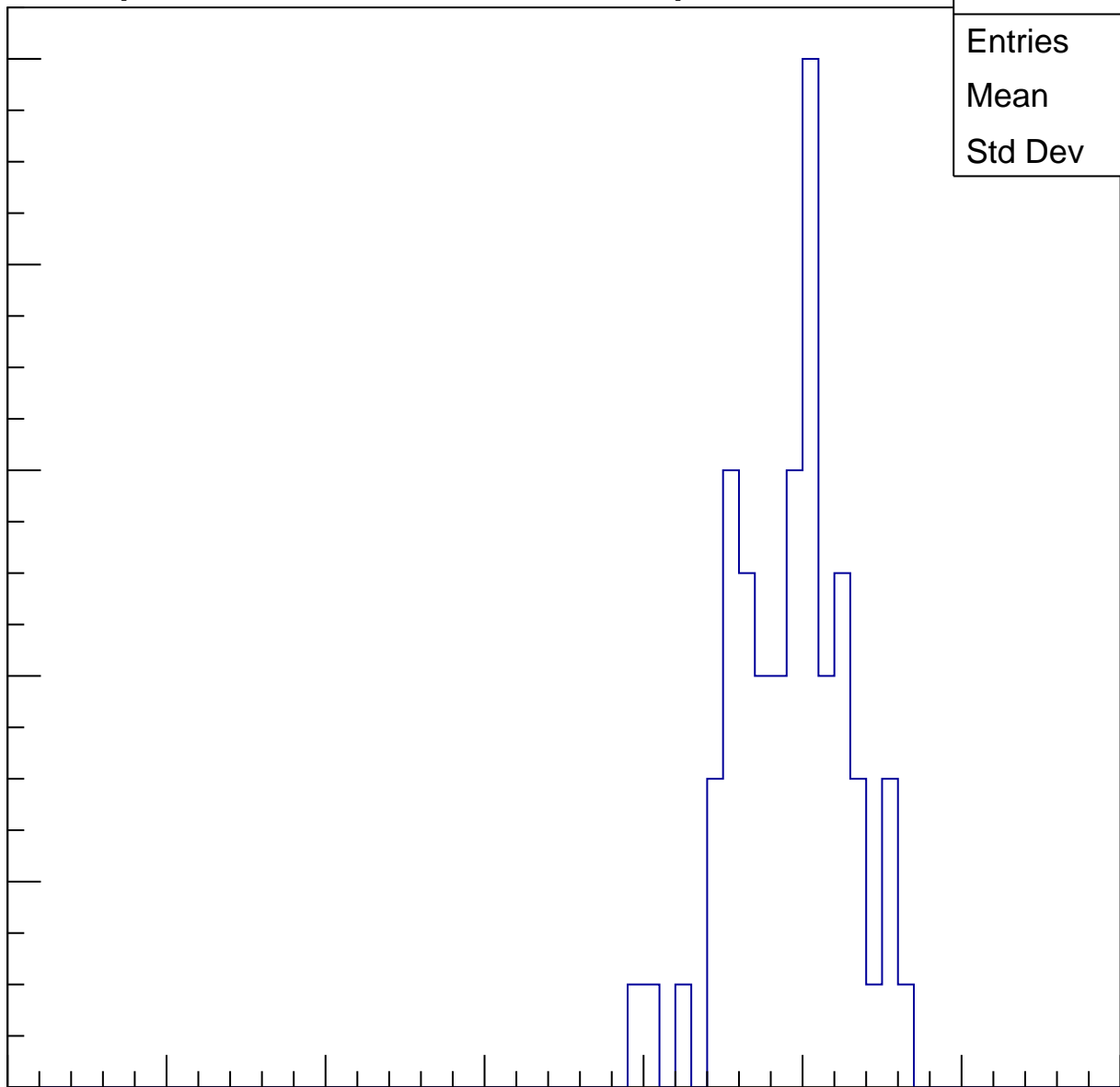
30

40

50

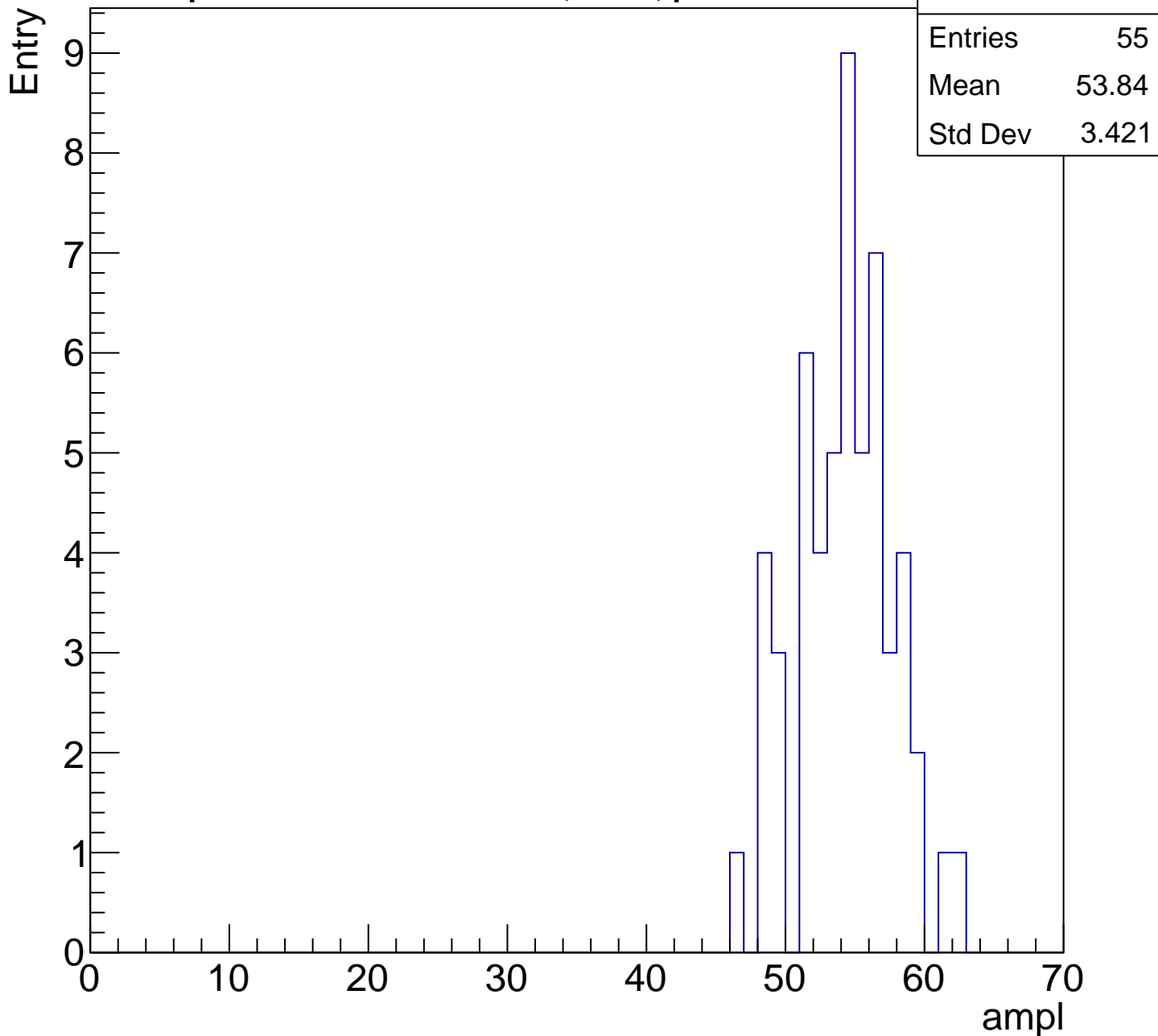
60

ampl



B1L101S, U1-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

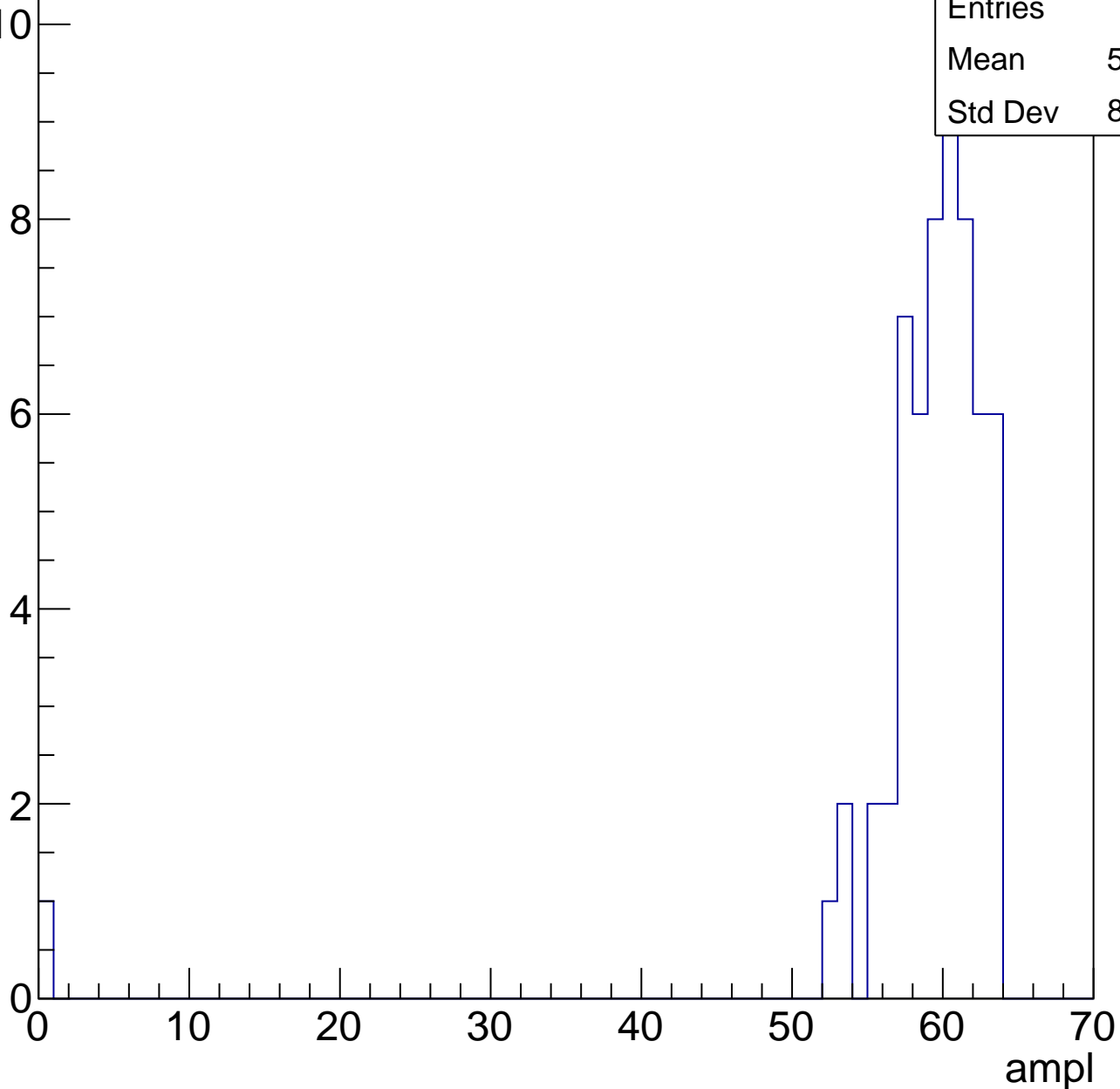


B1L101S, U1-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	58.25
Std Dev	8.073



B1L101S, U1-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

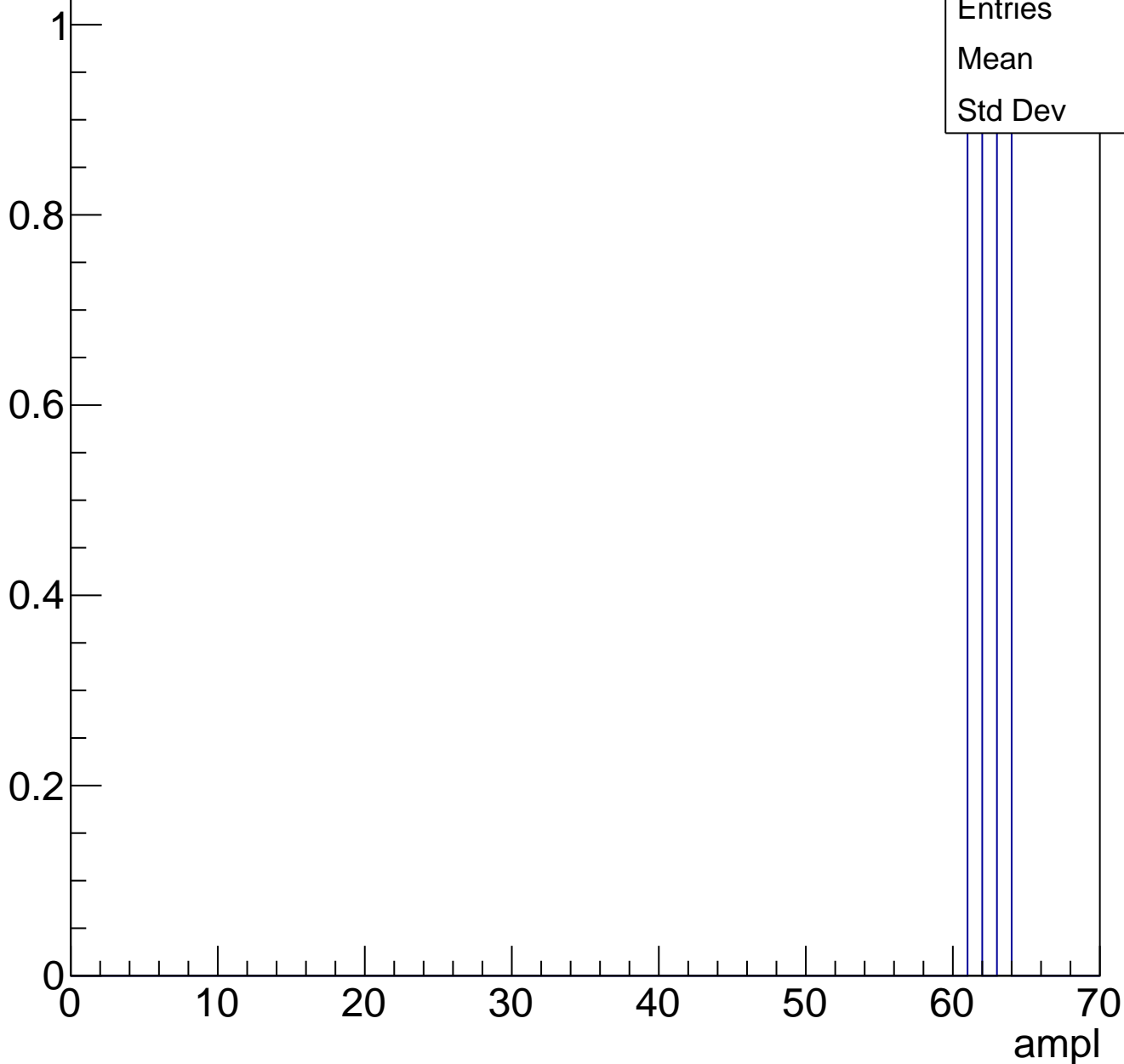
Entry



B1L101S, U1-ch87, adc7

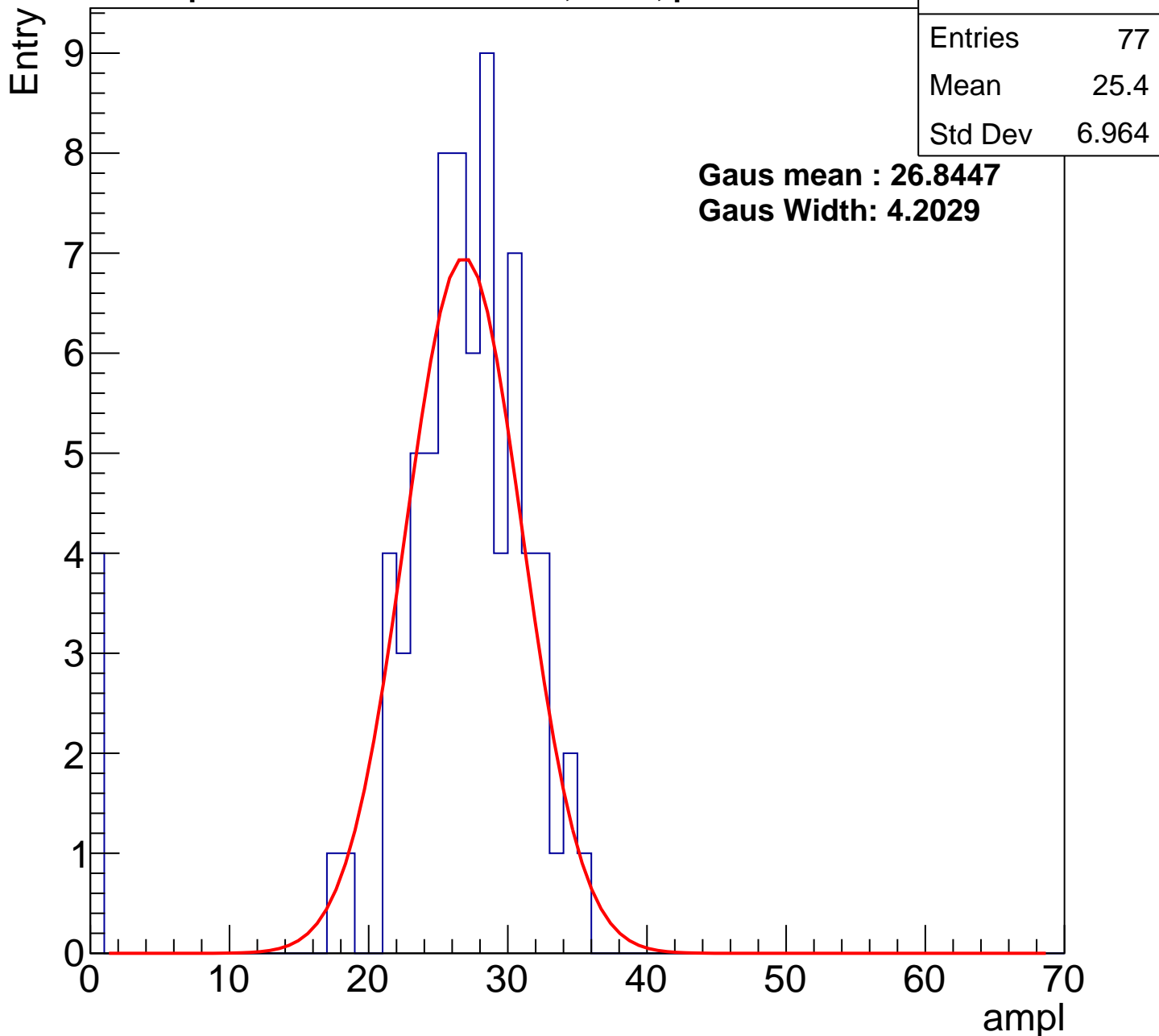
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch88, adc0

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch88, adc1

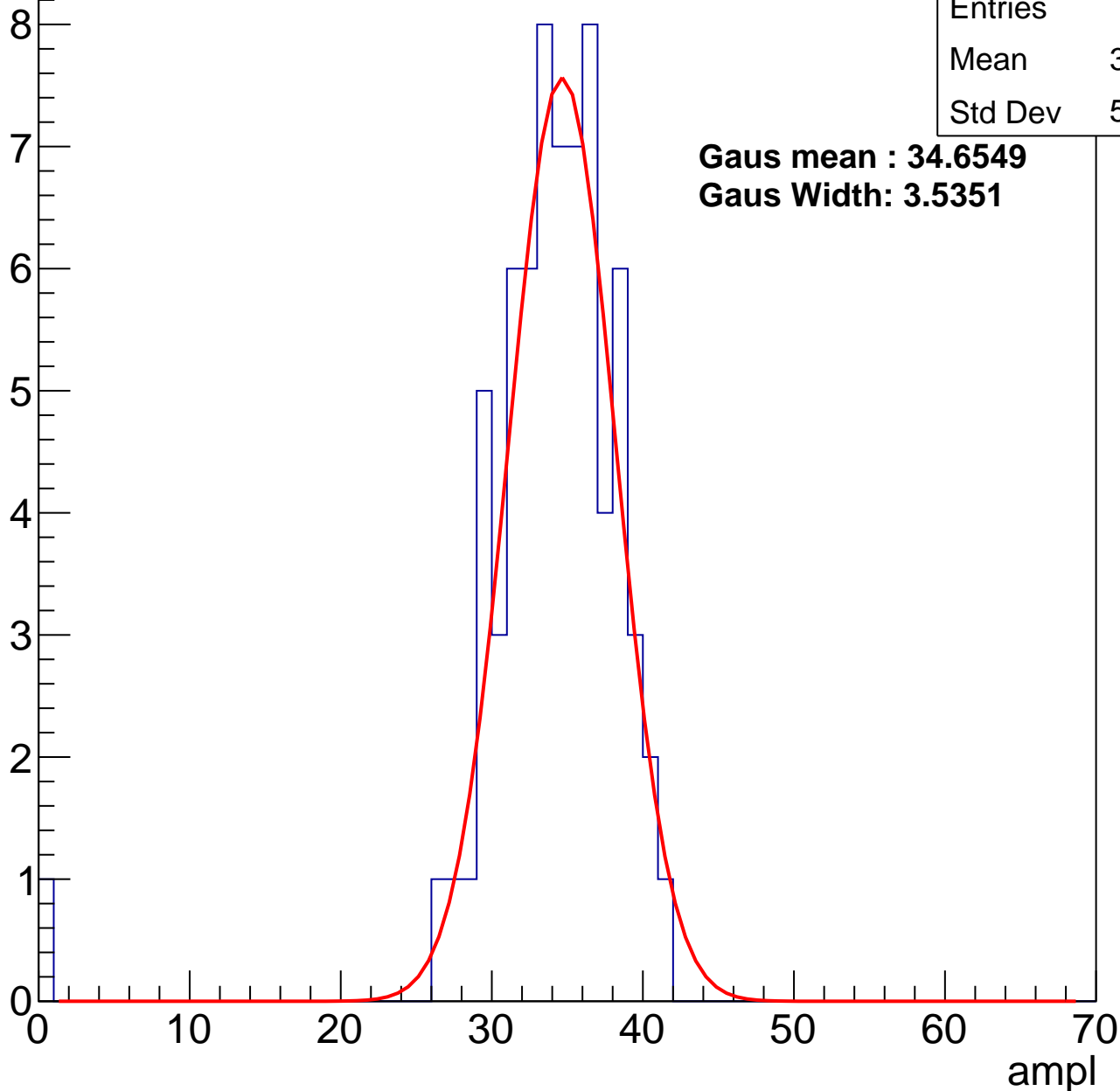
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	33.47
Std Dev	5.226

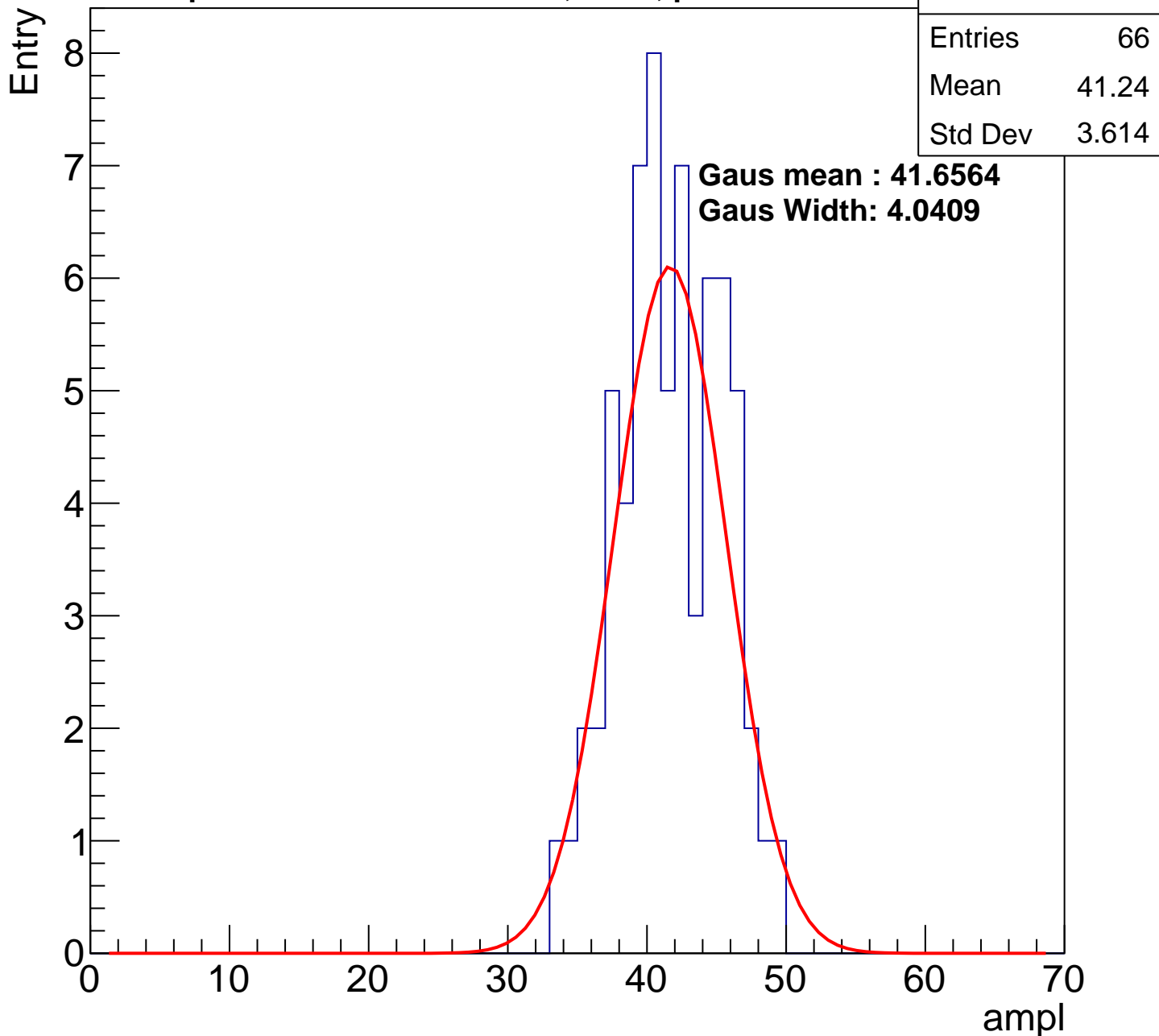
Gaus mean : 34.6549

Gaus Width: 3.5351



B1L101S, U1-ch88, adc2

calib_packv5_042523_0143.root, FC#0, port D2

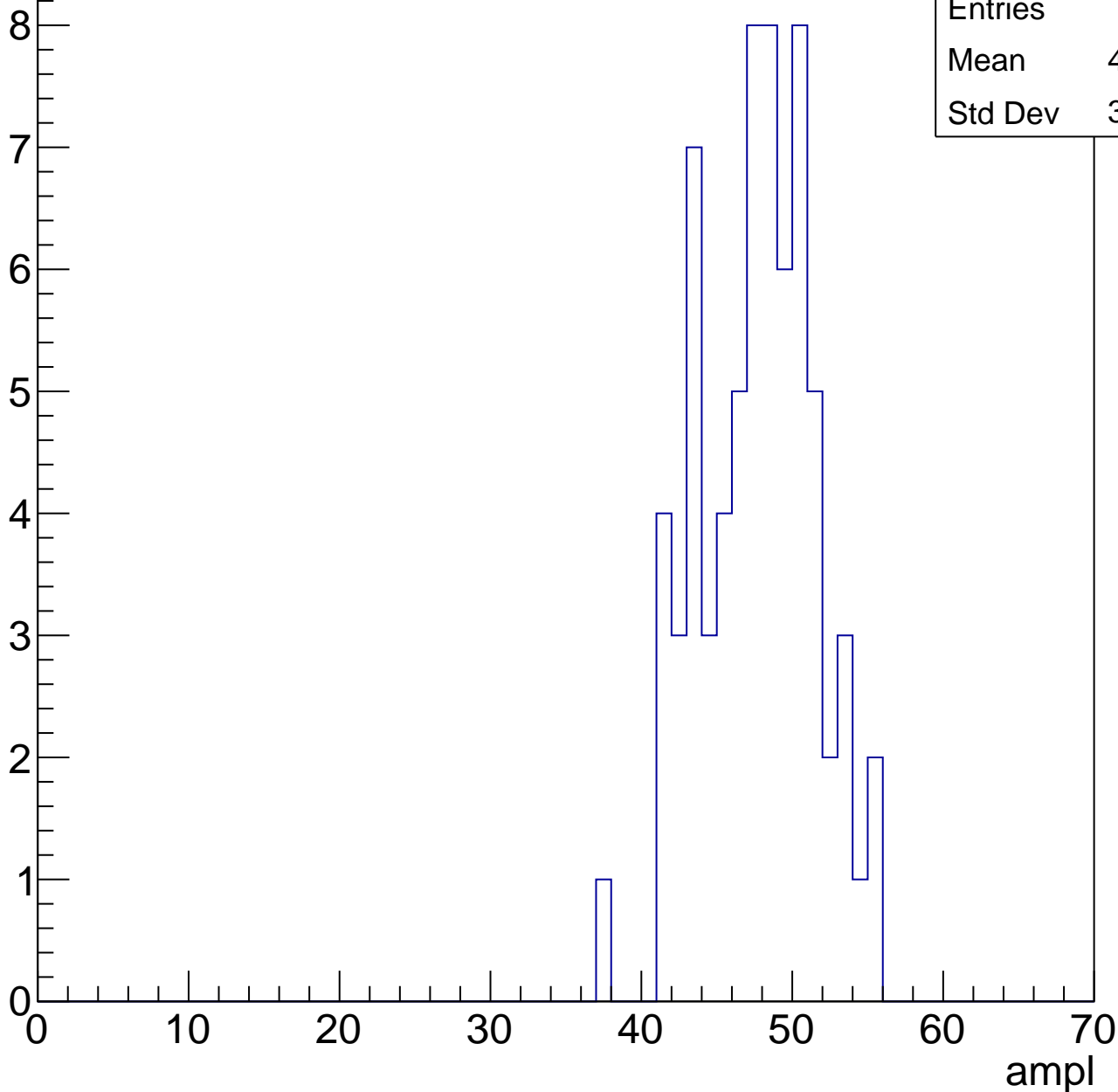


B1L101S, U1-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

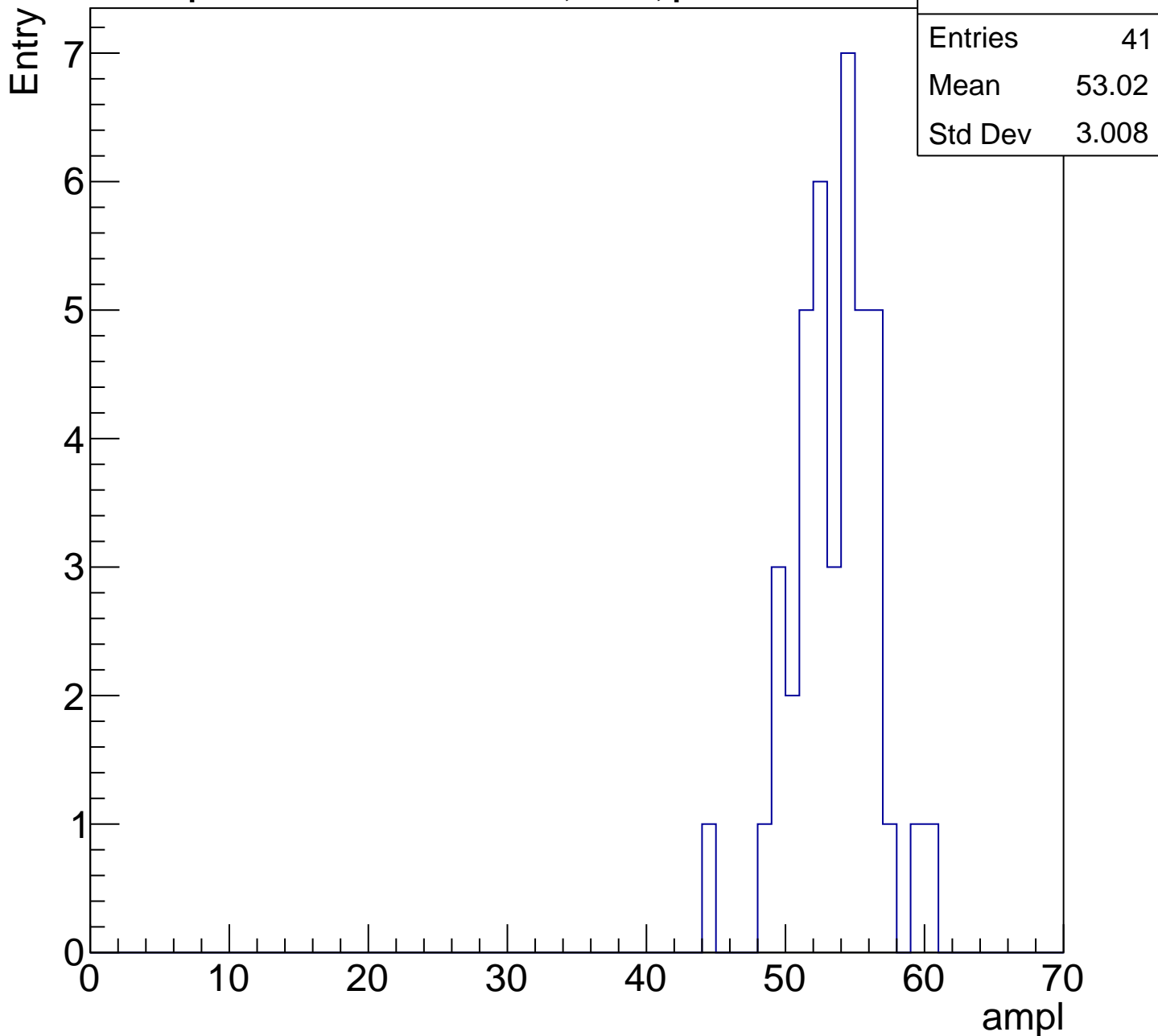
Entry

Entries	70
Mean	47.23
Std Dev	3.769



B1L101S, U1-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch88, adc5

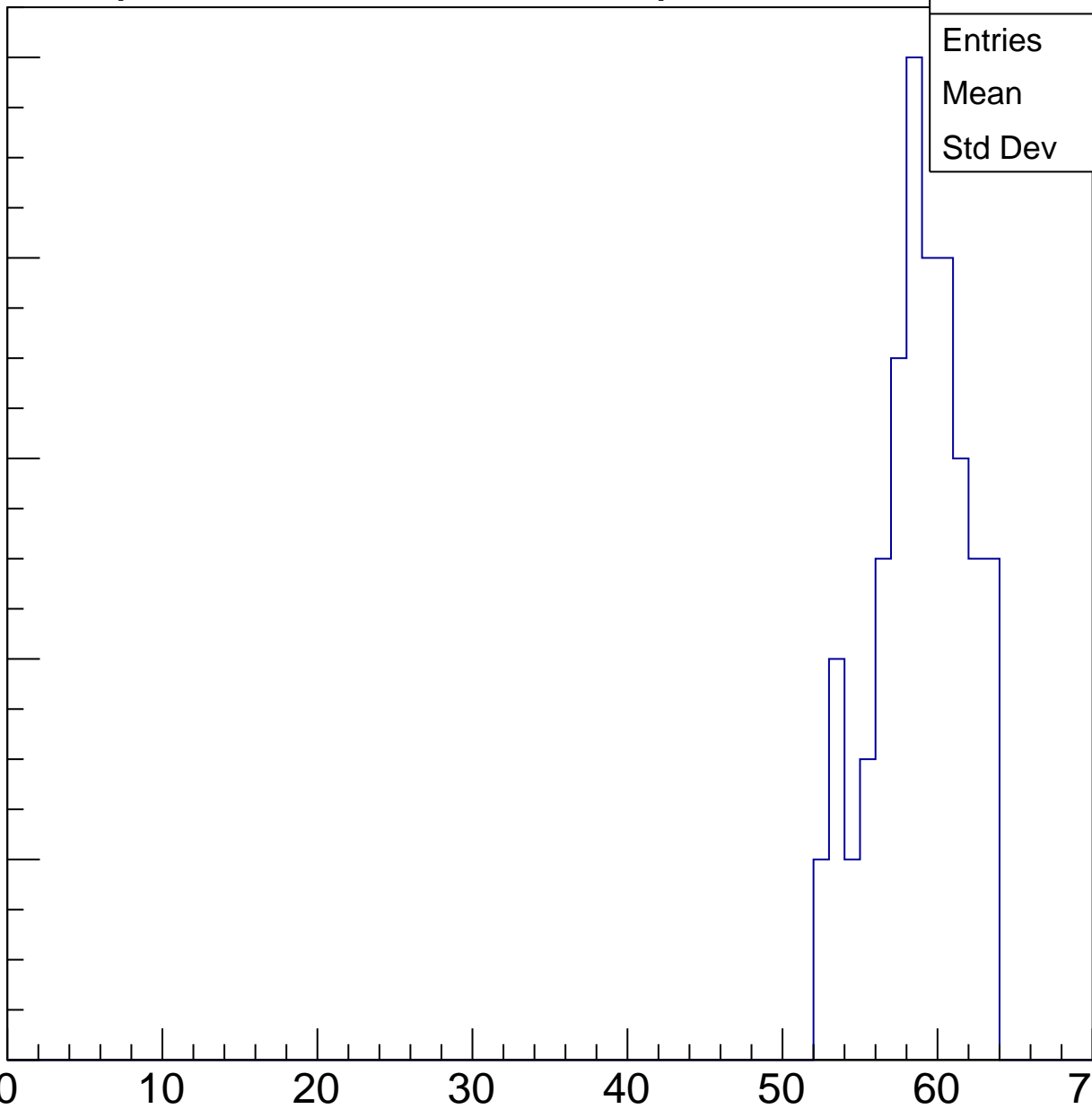
calib_packv5_042523_0143.root, FC#0, port D2

Entry

10
8
6
4
2
0

Entries	65
Mean	58.32
Std Dev	2.909

ampl

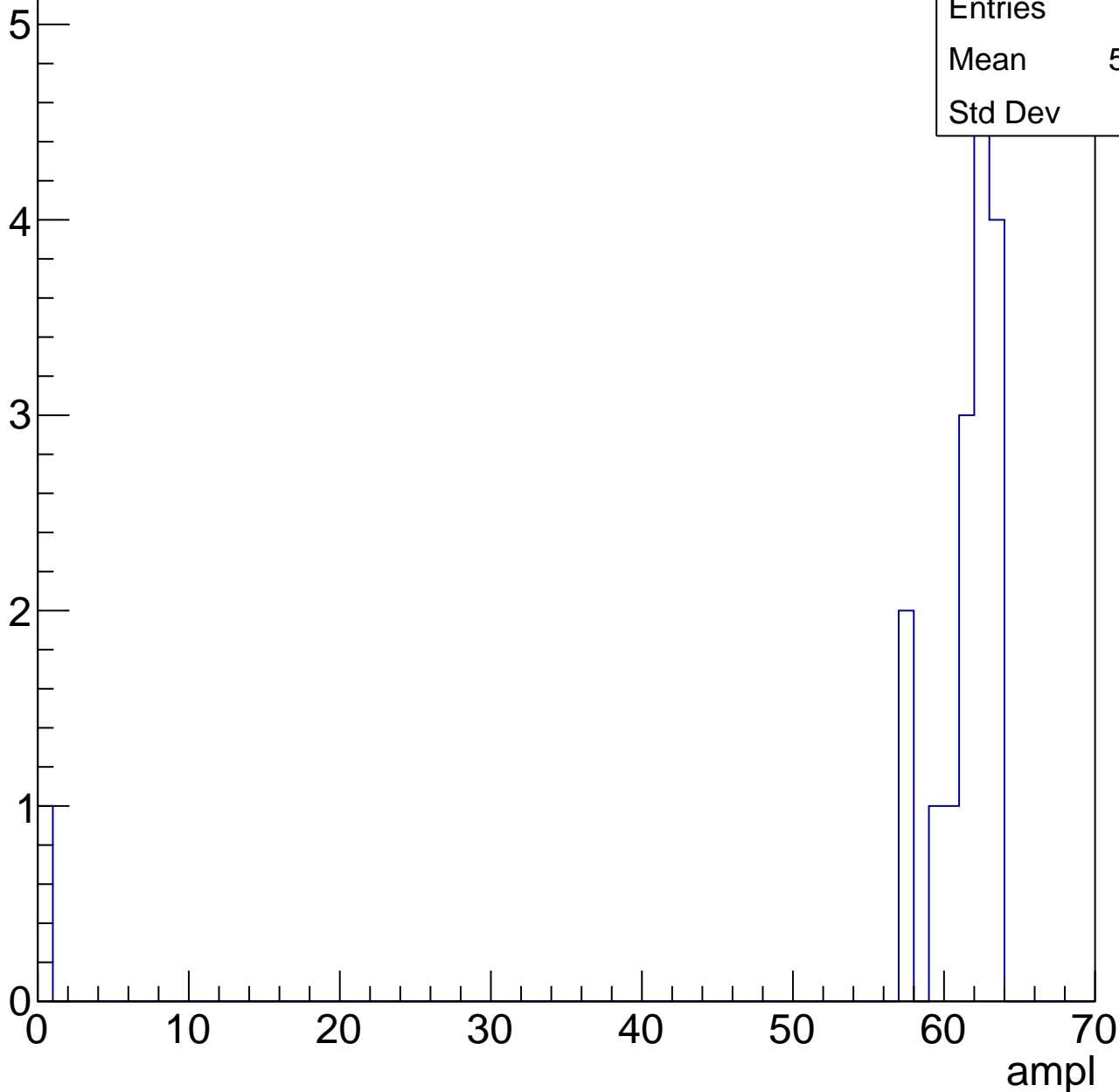


B1L101S, U1-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

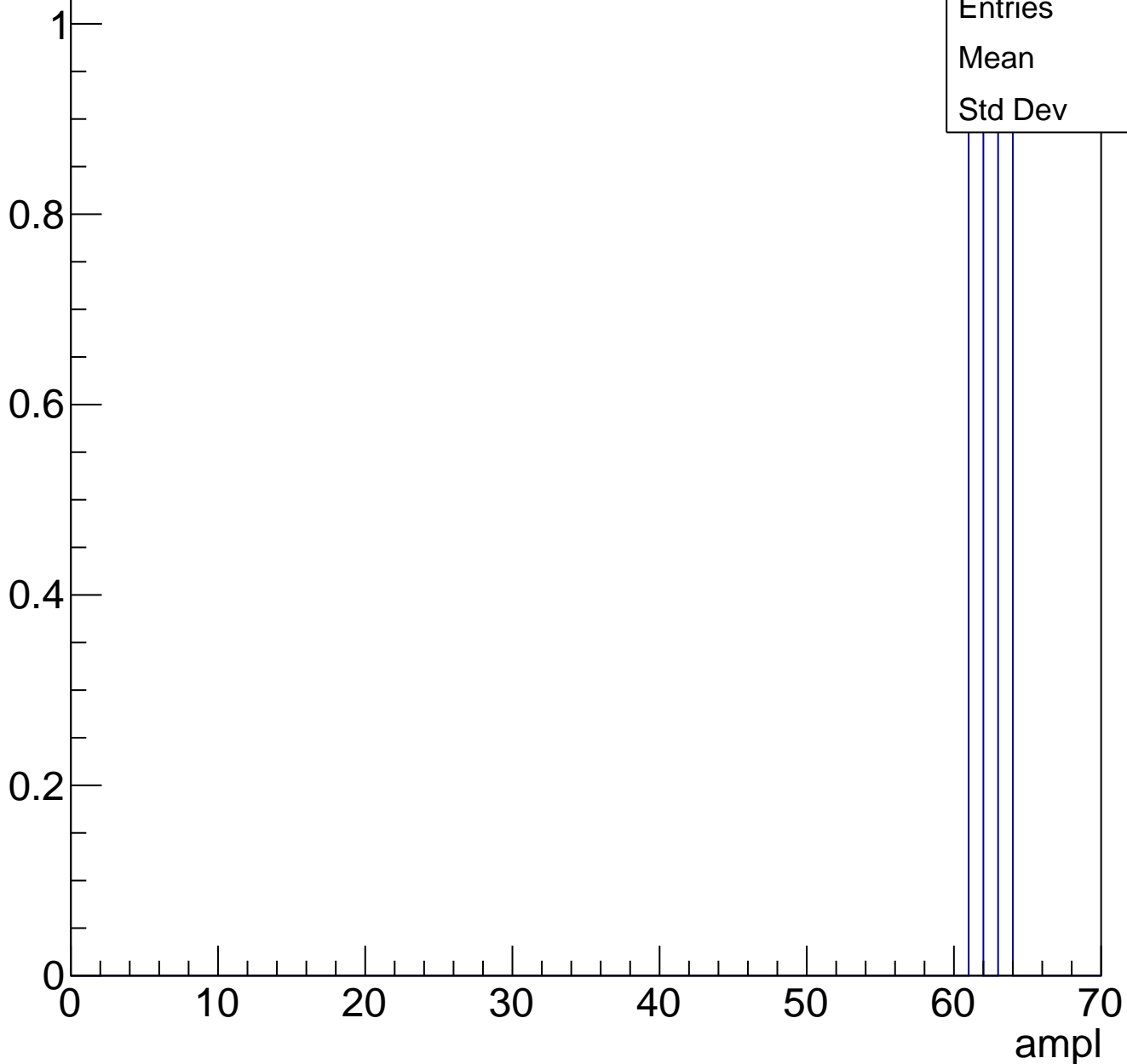
Entries	17
Mean	57.53
Std Dev	14.5



B1L101S, U1-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch89, adc0

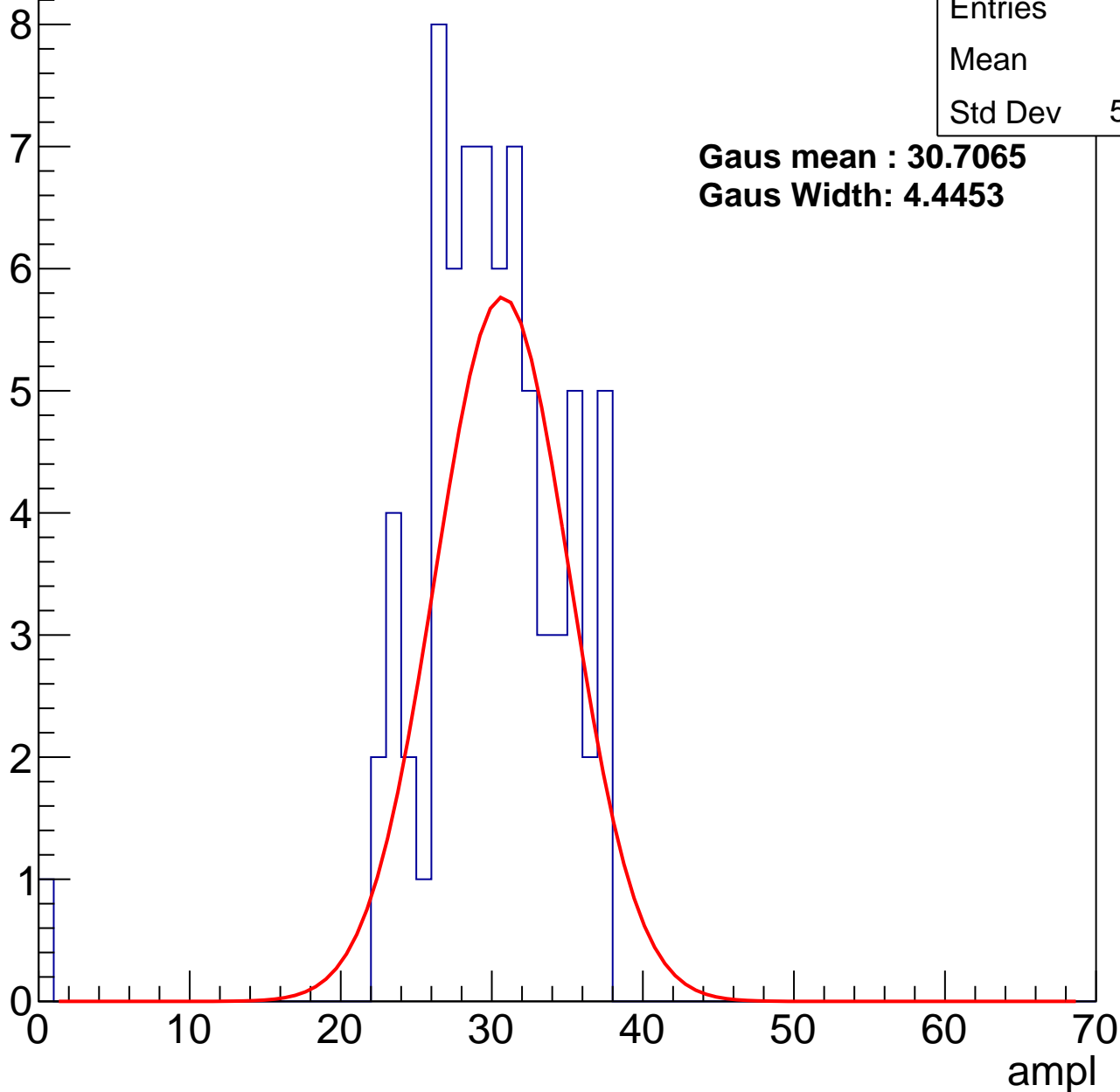
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.3
Std Dev	5.258

Gaus mean : 30.7065

Gaus Width: 4.4453



B1L101S, U1-ch89, adc1

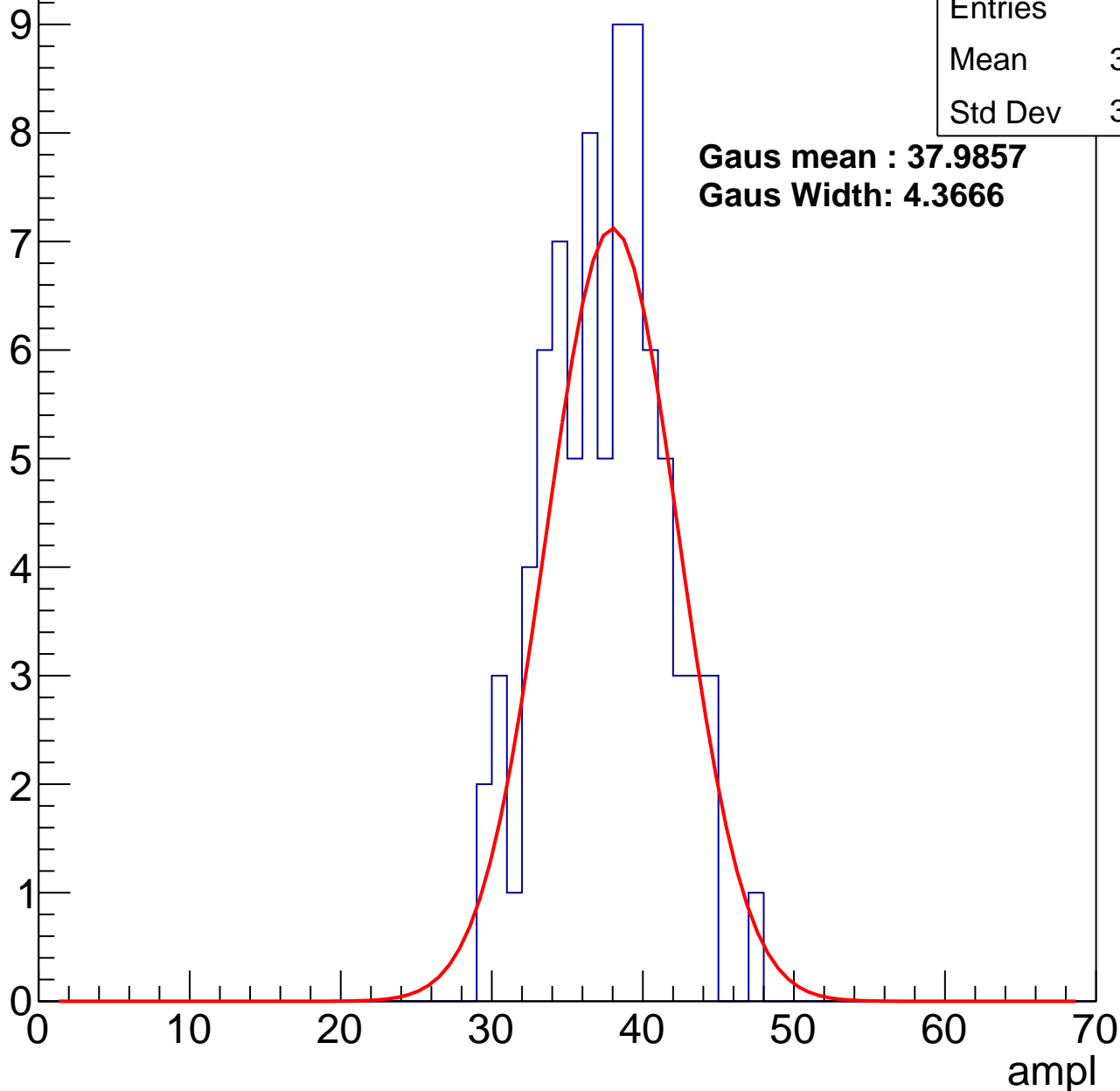
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	37.04
Std Dev	3.884

Gaus mean : 37.9857

Gaus Width: 4.3666

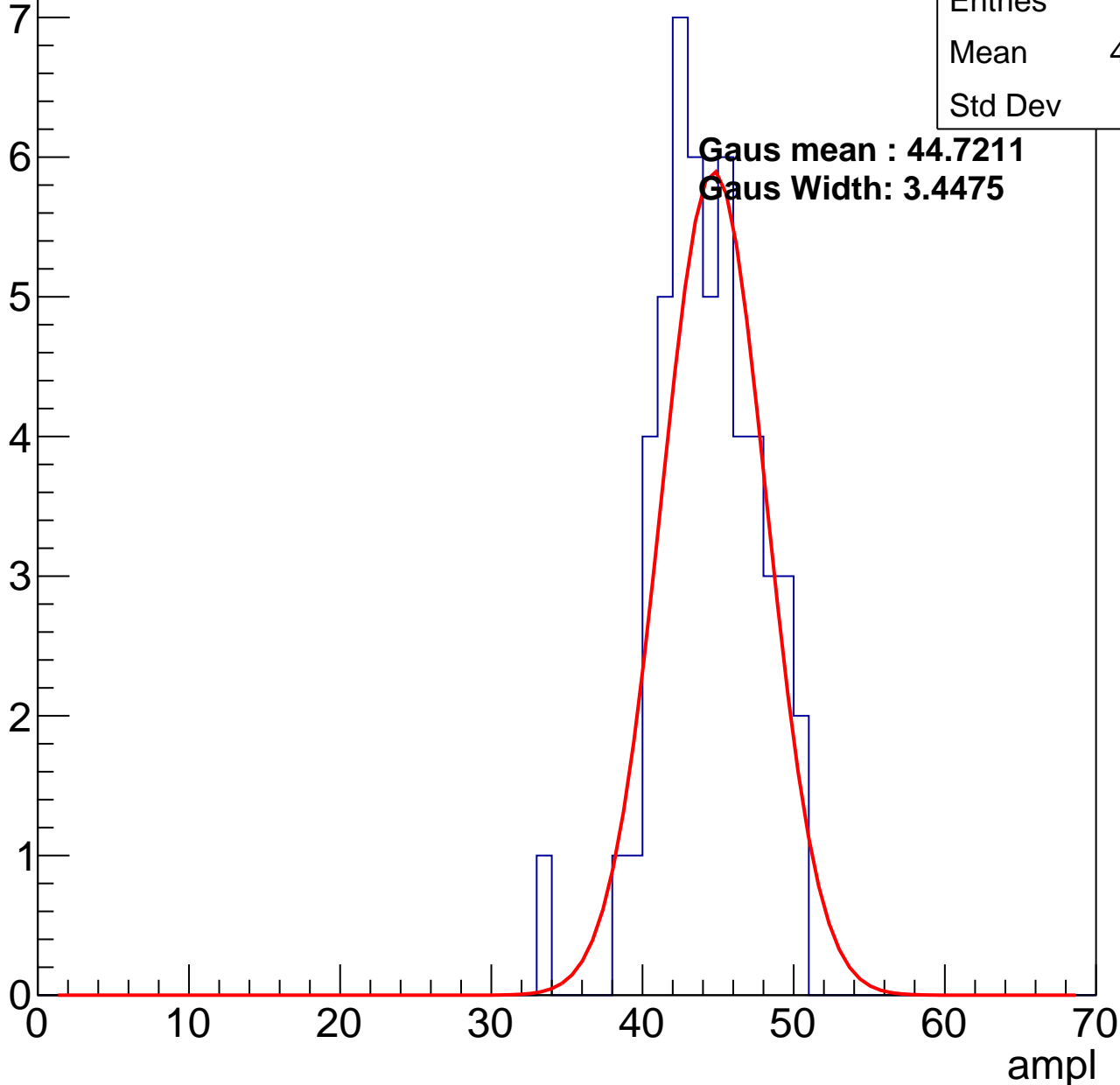


B1L101S, U1-ch89, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.85
Std Dev	3.33



B1L101S, U1-ch89, adc3

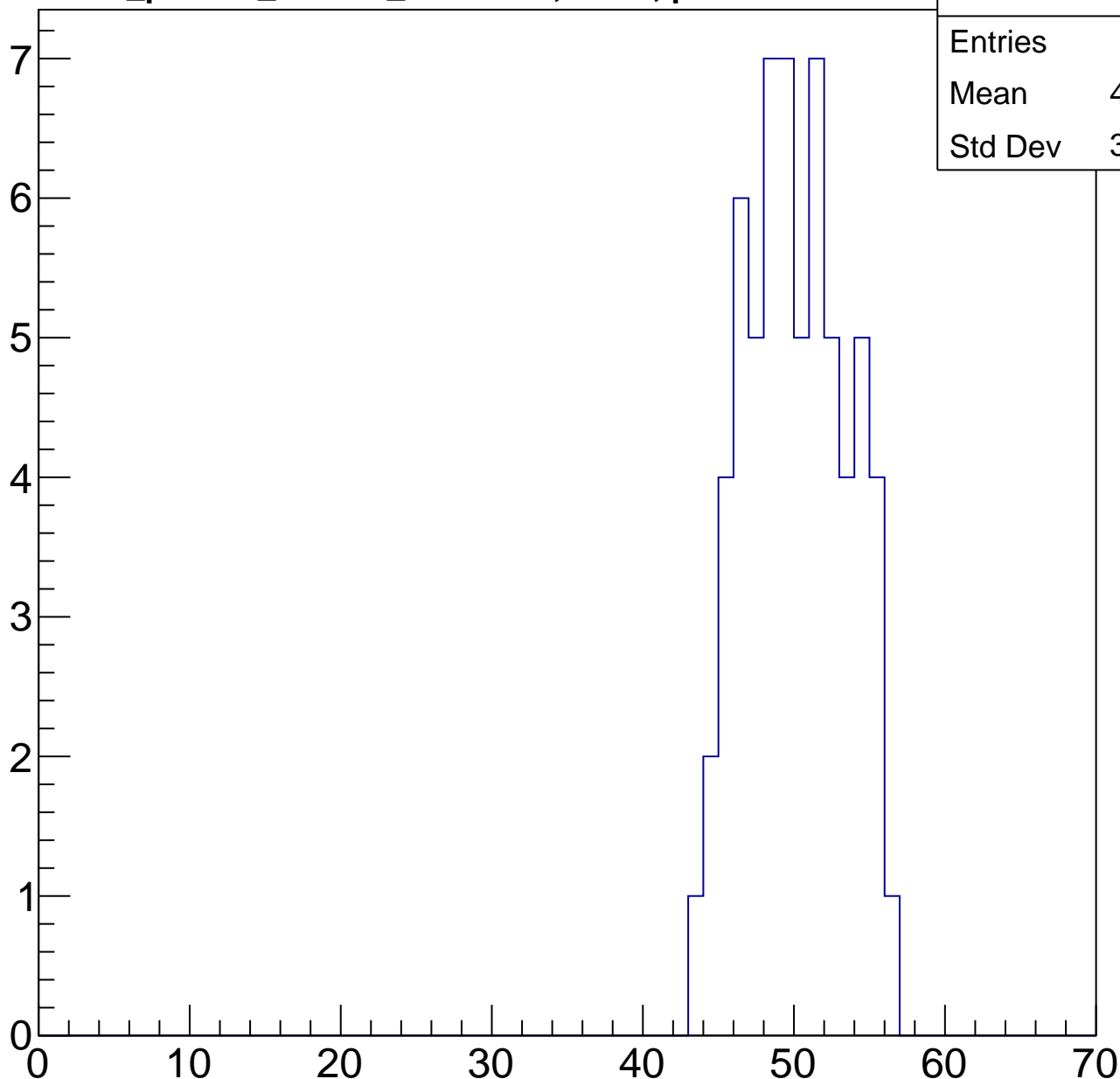
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

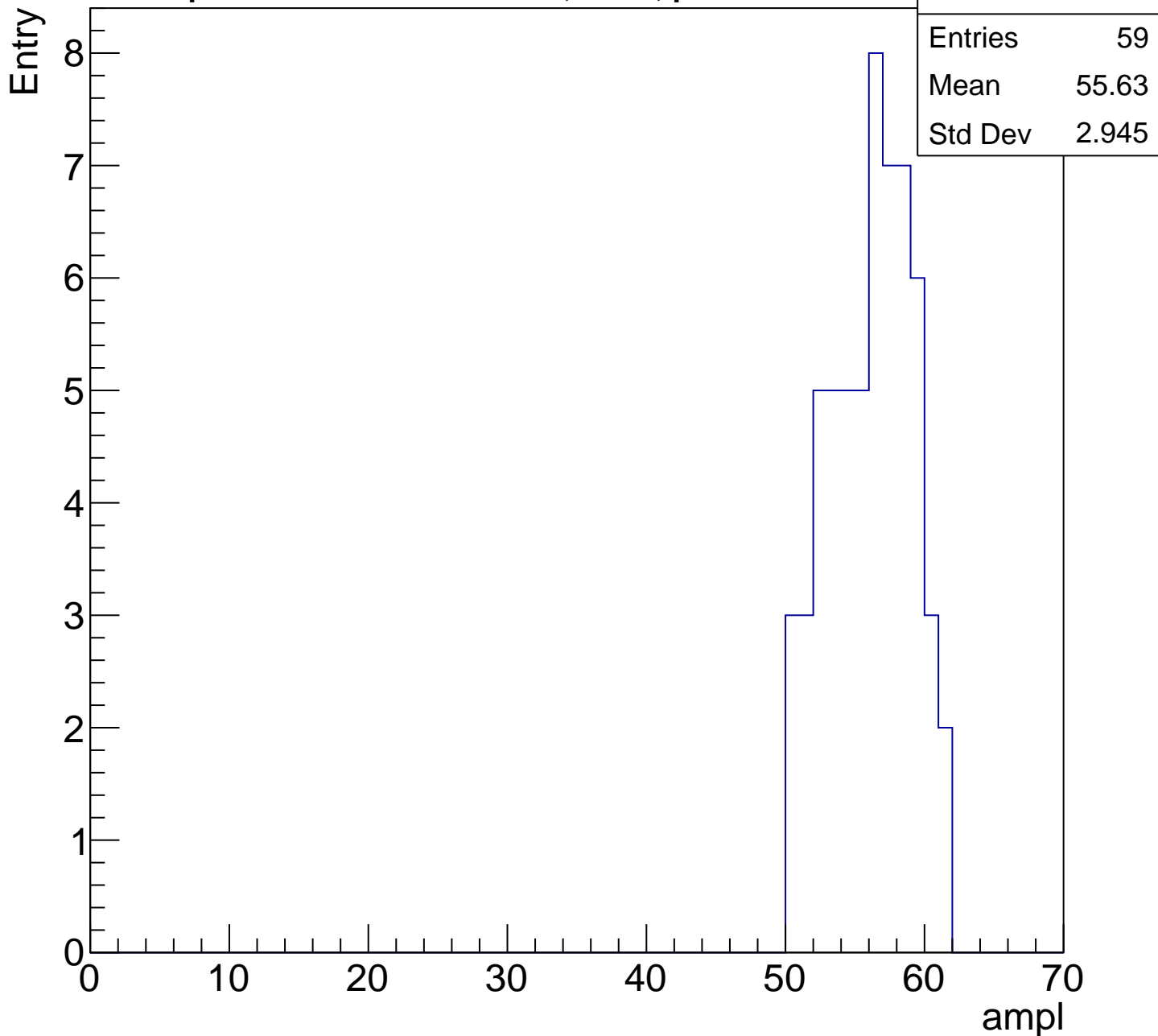
Entries	63
Mean	49.62
Std Dev	3.253

ampl



B1L101S, U1-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

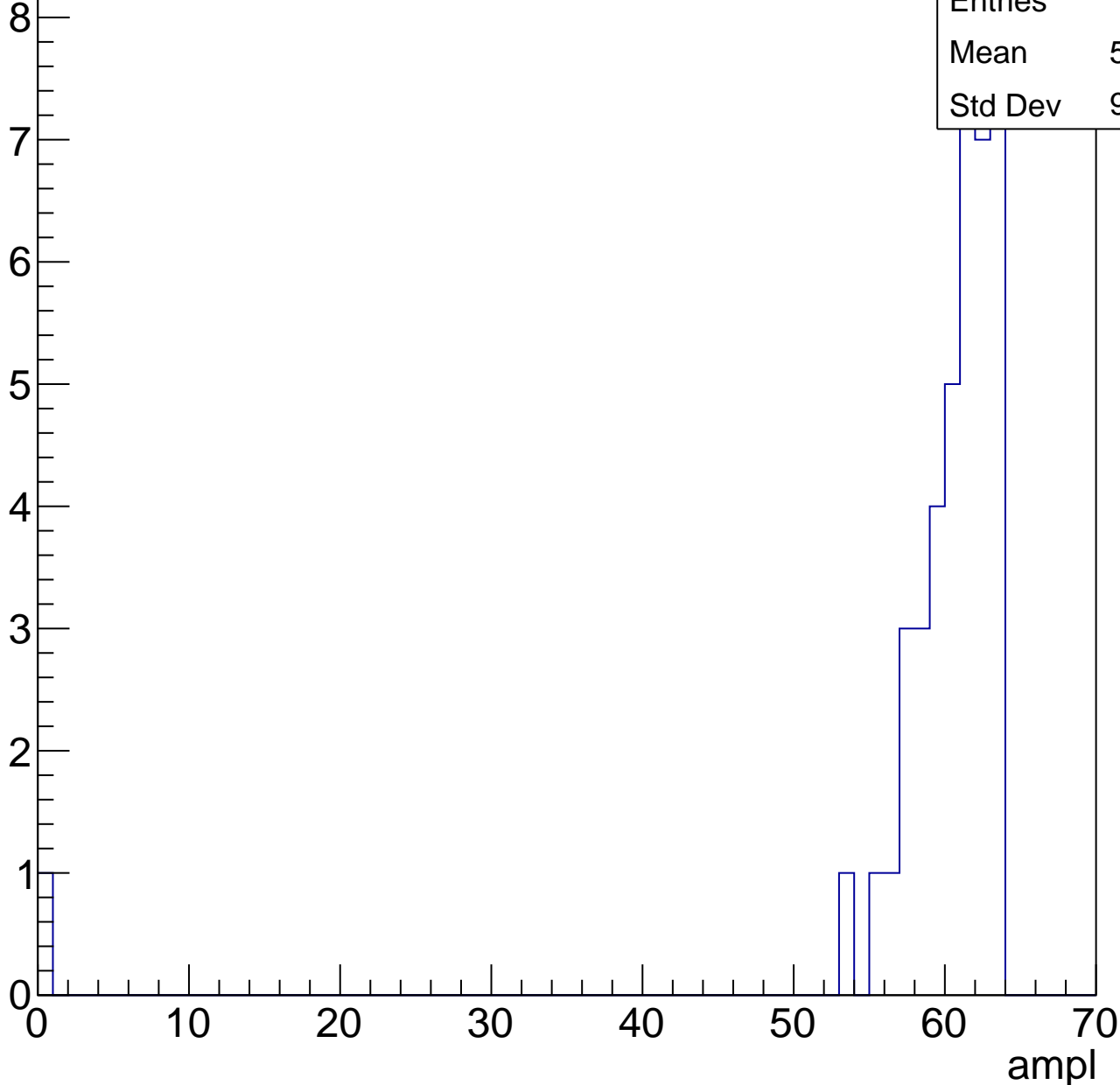


B1L101S, U1-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

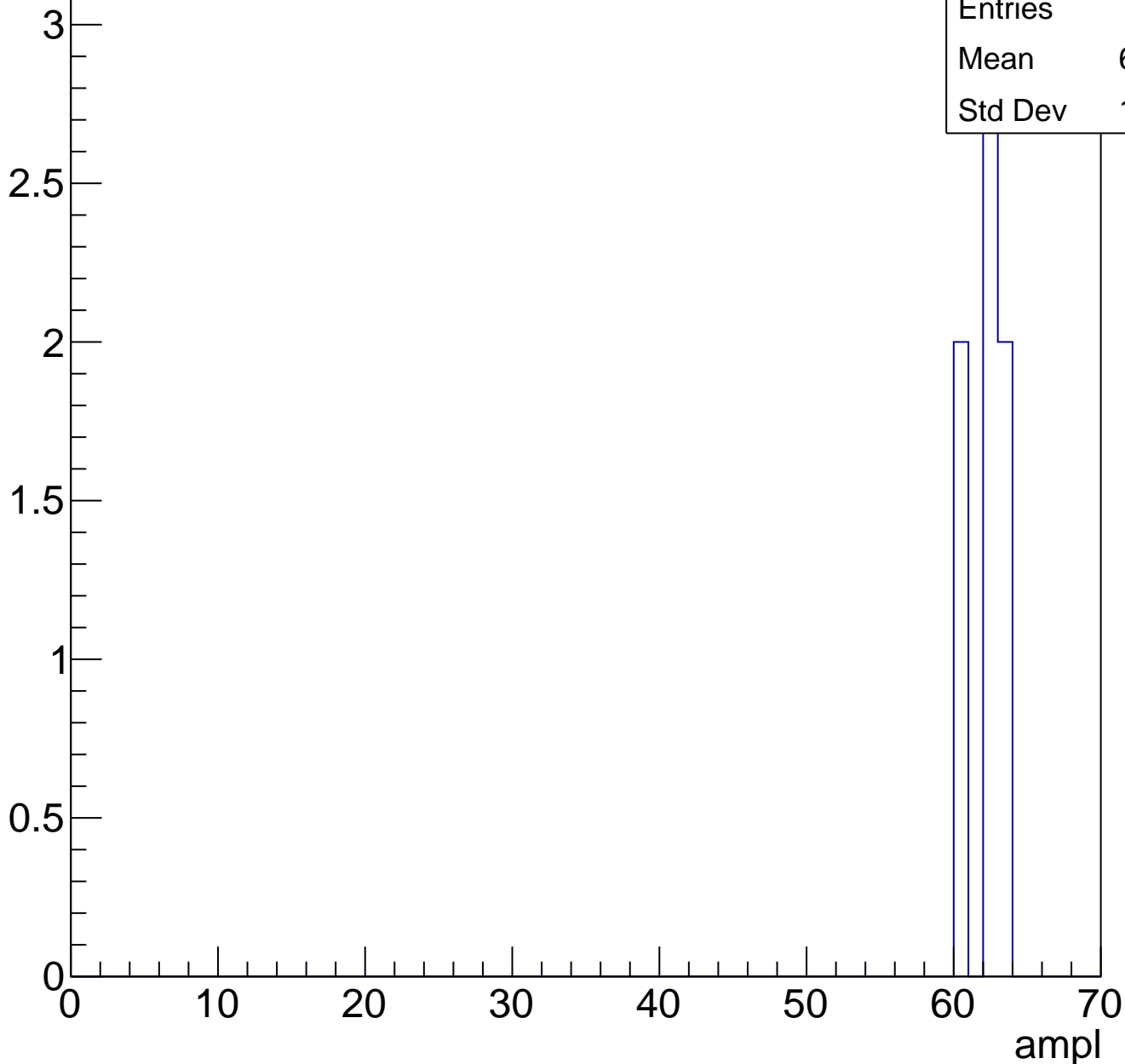
Entries	42
Mean	58.83
Std Dev	9.492



B1L101S, U1-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch90, adc0

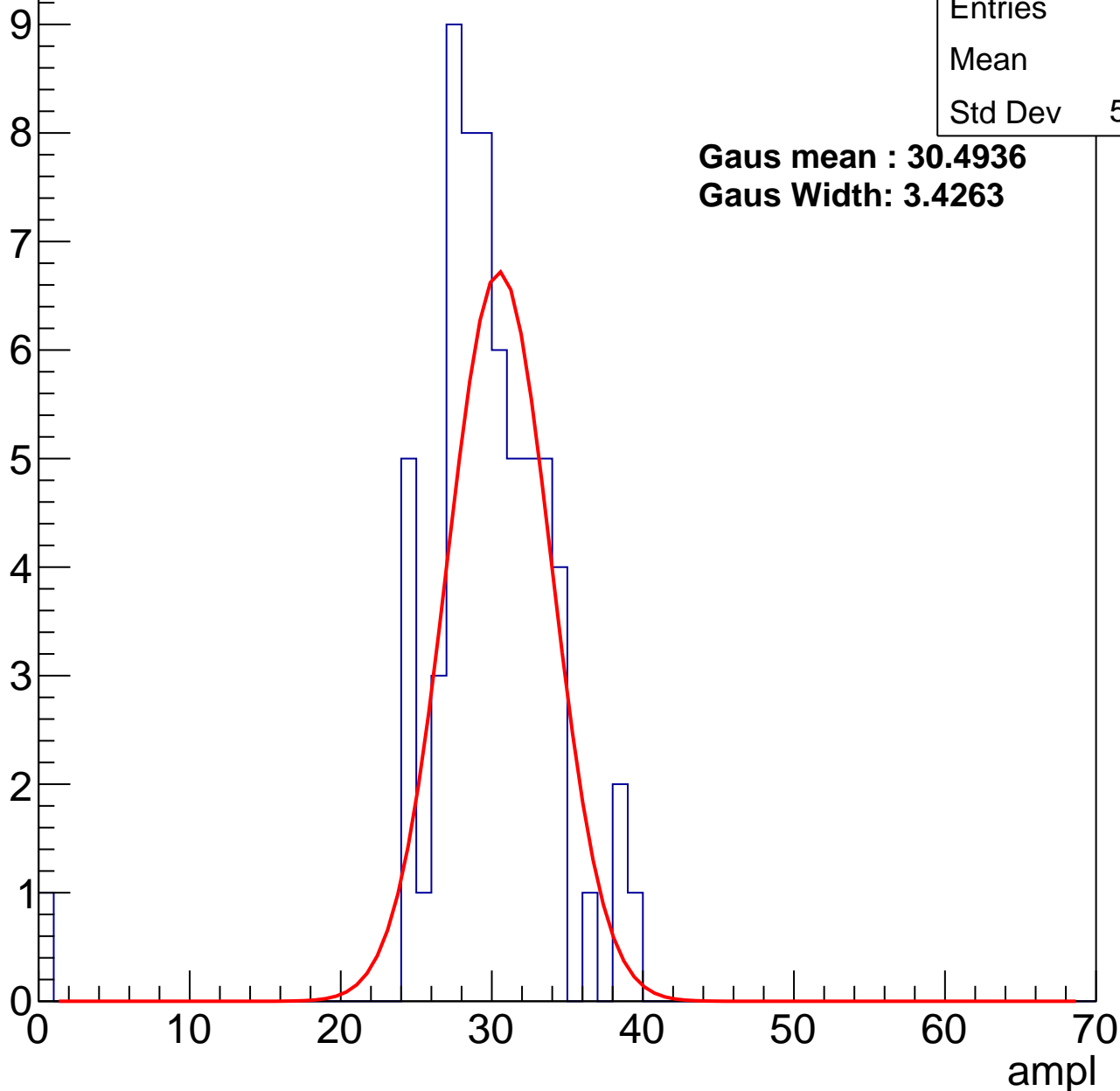
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.2
Std Dev	5.019

Gaus mean : 30.4936

Gaus Width: 3.4263



B1L101S, U1-ch90, adc1

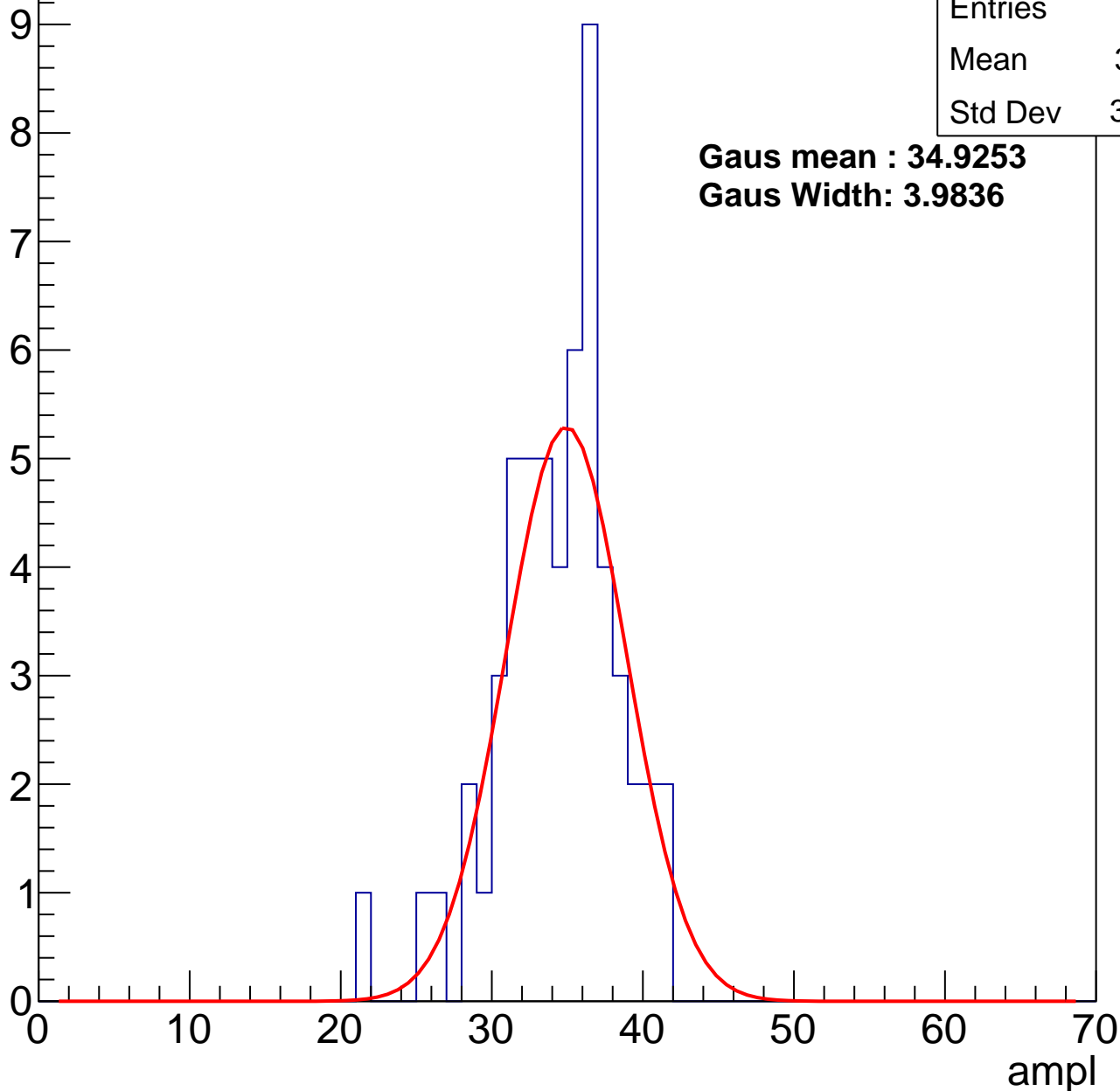
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	33.91
Std Dev	3.952

Gaus mean : 34.9253

Gaus Width: 3.9836



B1L101S, U1-ch90, adc2

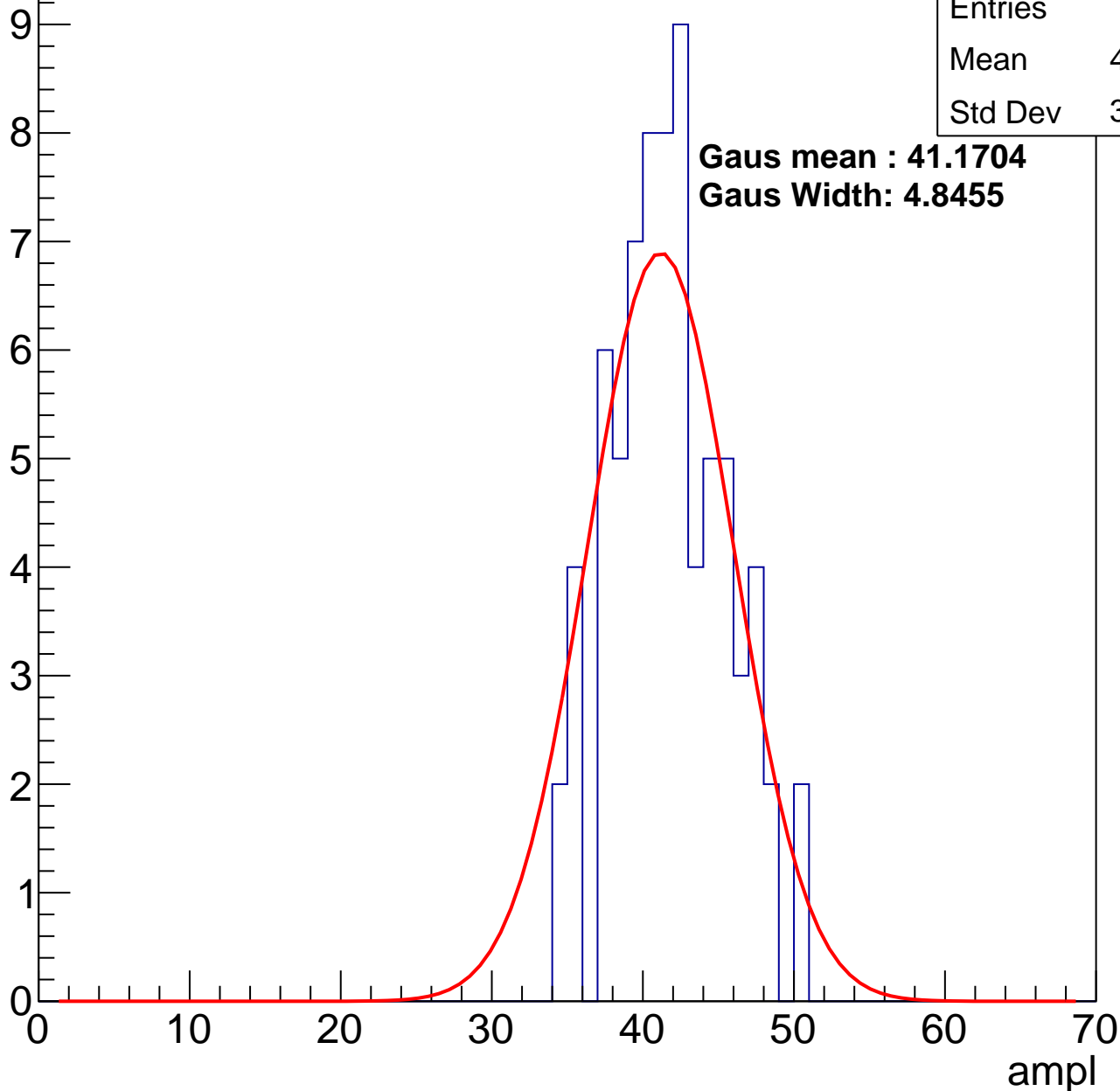
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	41.32
Std Dev	3.749

Gaus mean : 41.1704

Gaus Width: 4.8455

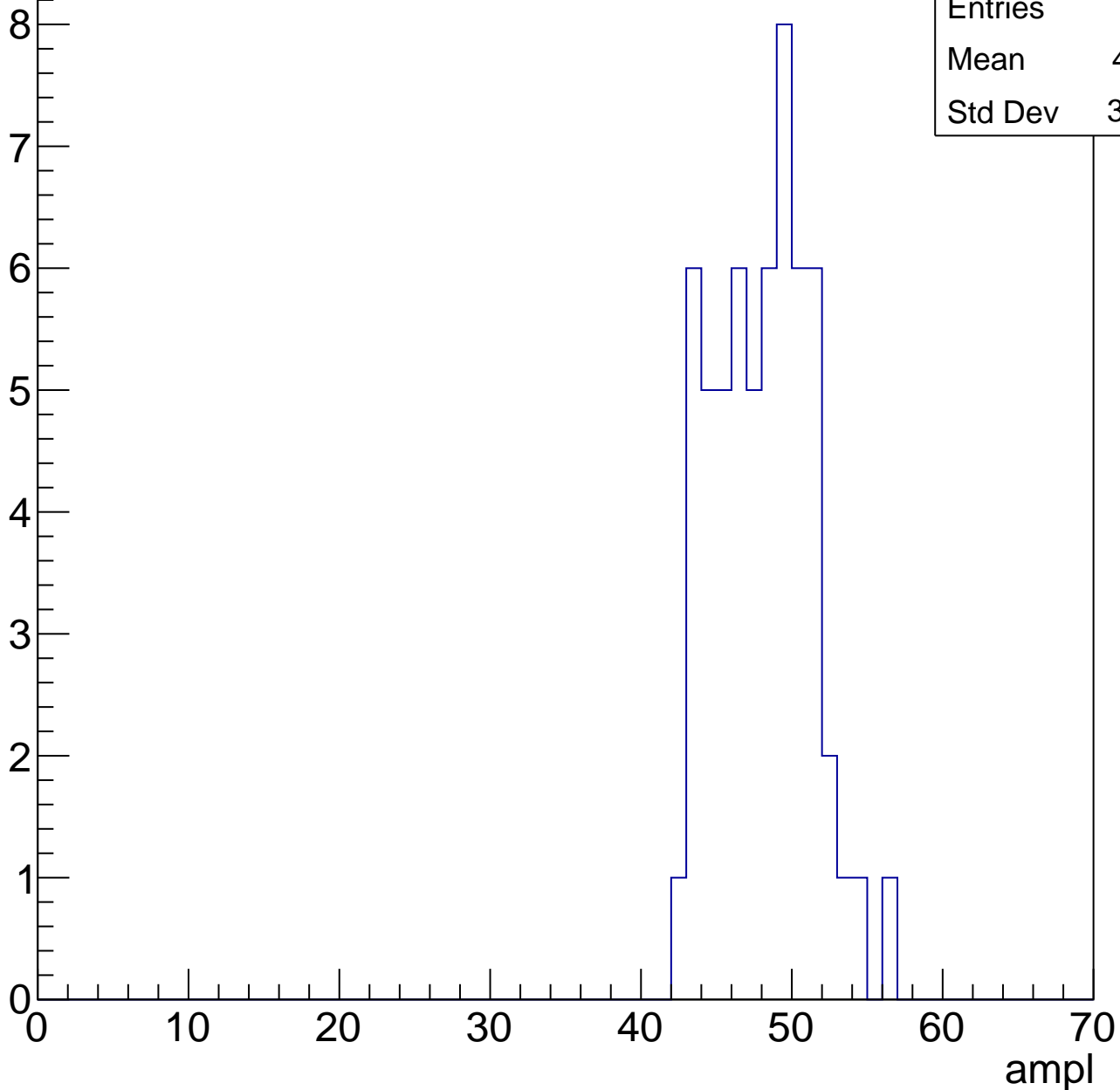


B1L101S, U1-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	47.61
Std Dev	3.119

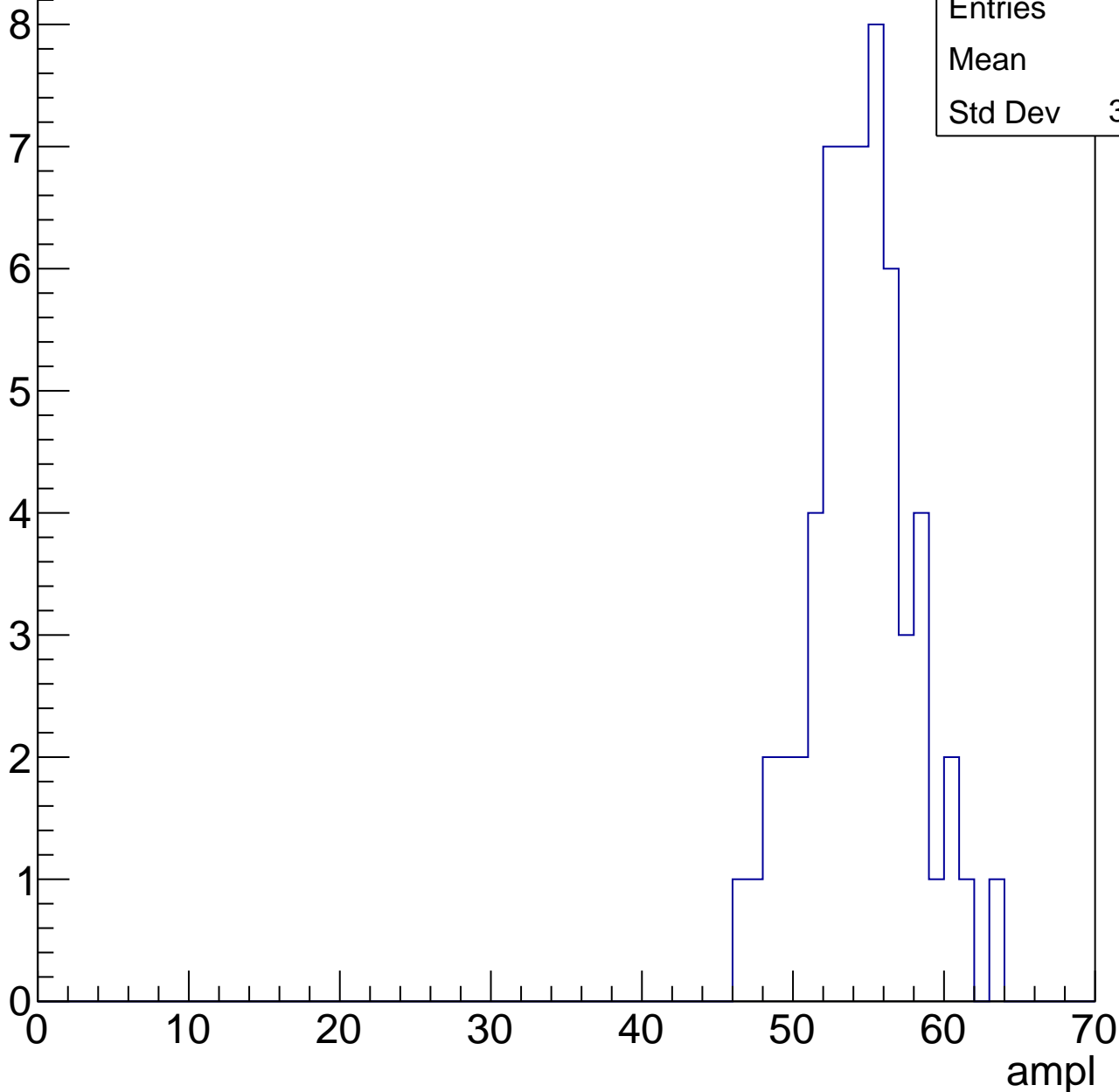


B1L101S, U1-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

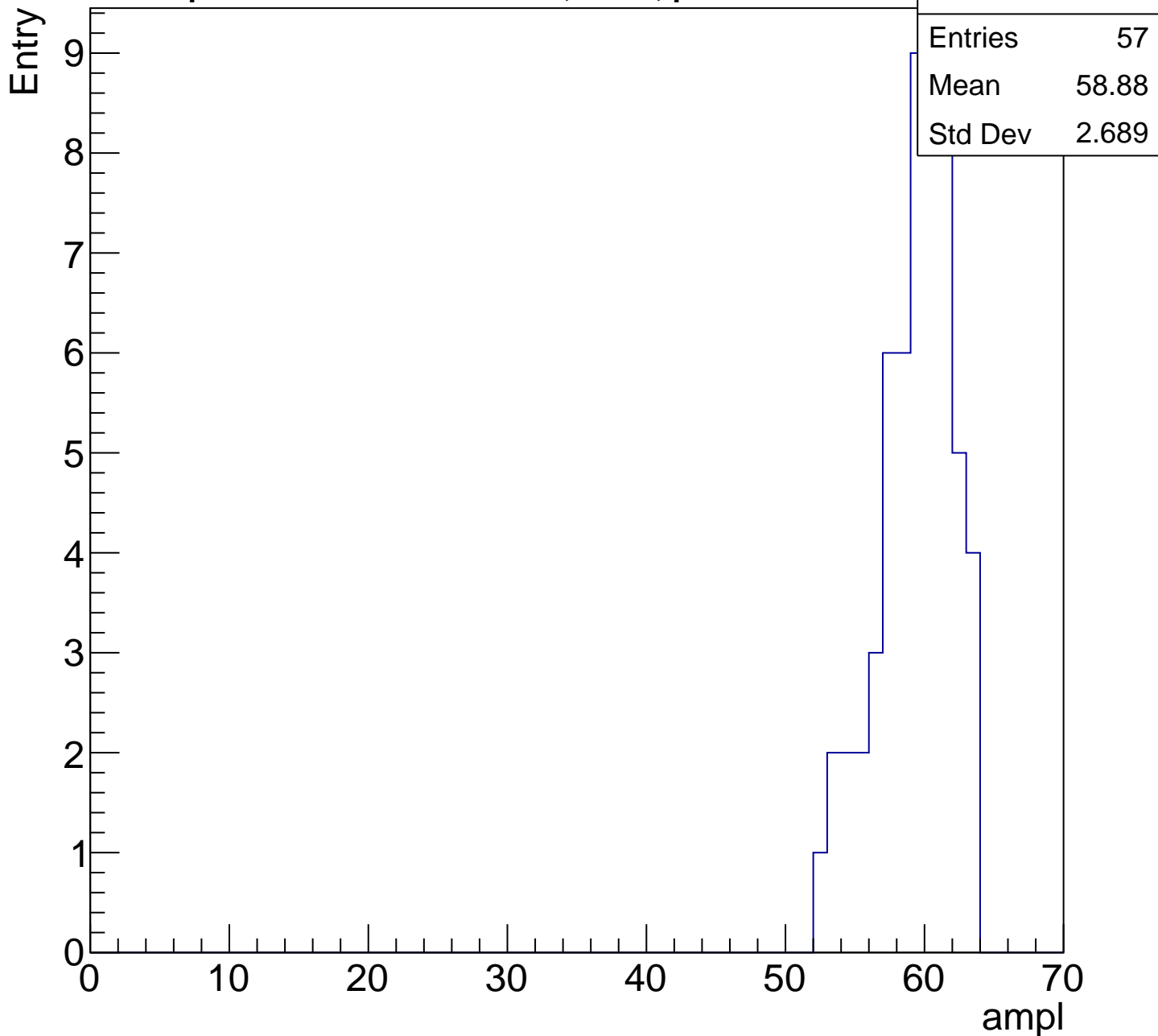
Entry

Entries	59
Mean	54
Std Dev	3.415



B1L101S, U1-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

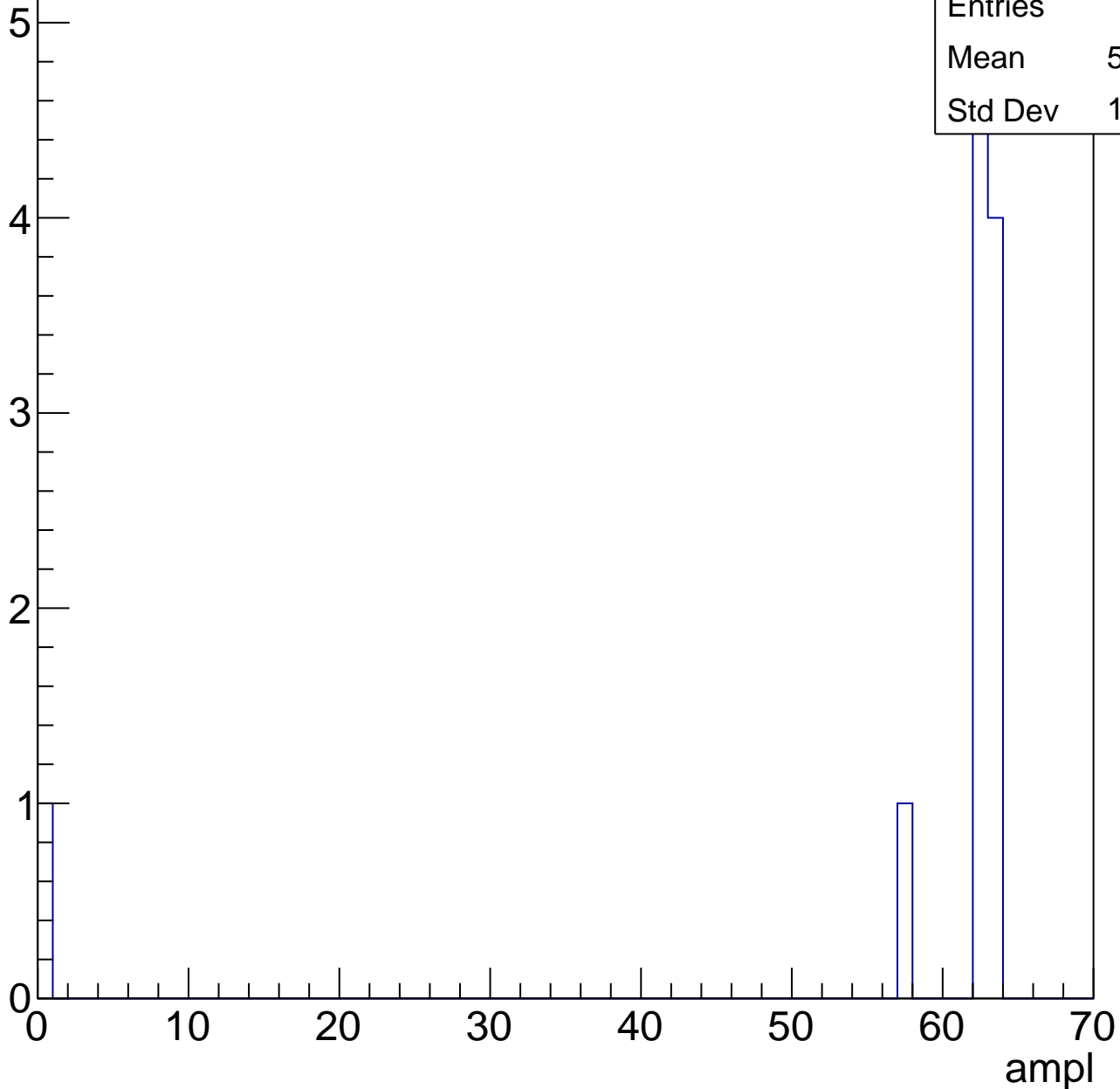


B1L101S, U1-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	56.27
Std Dev	17.87



B1L101S, U1-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch91, adc0

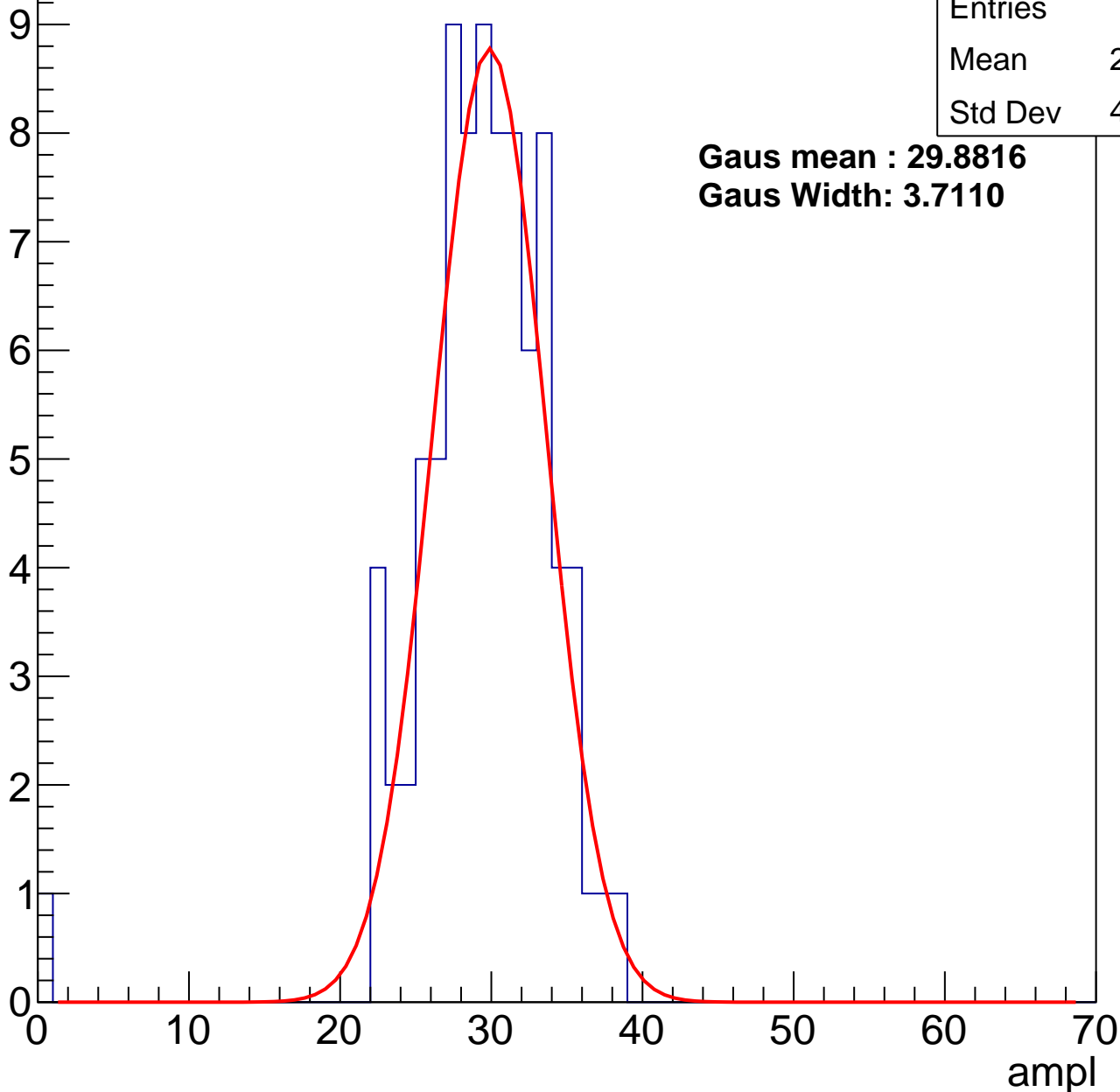
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.02
Std Dev	4.808

Gaus mean : 29.8816

Gaus Width: 3.7110



B1L101S, U1-ch91, adc1

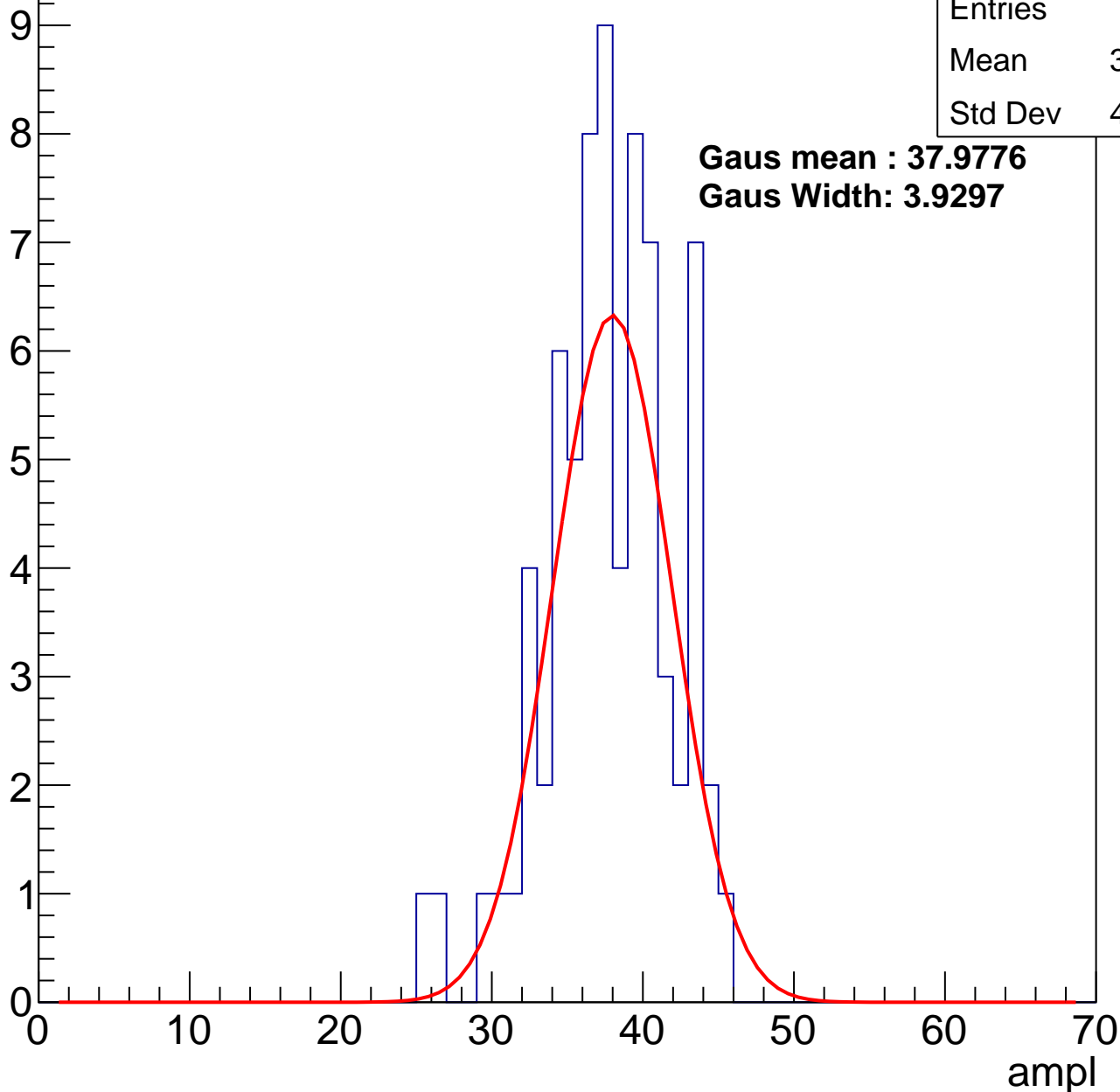
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	37.26
Std Dev	4.112

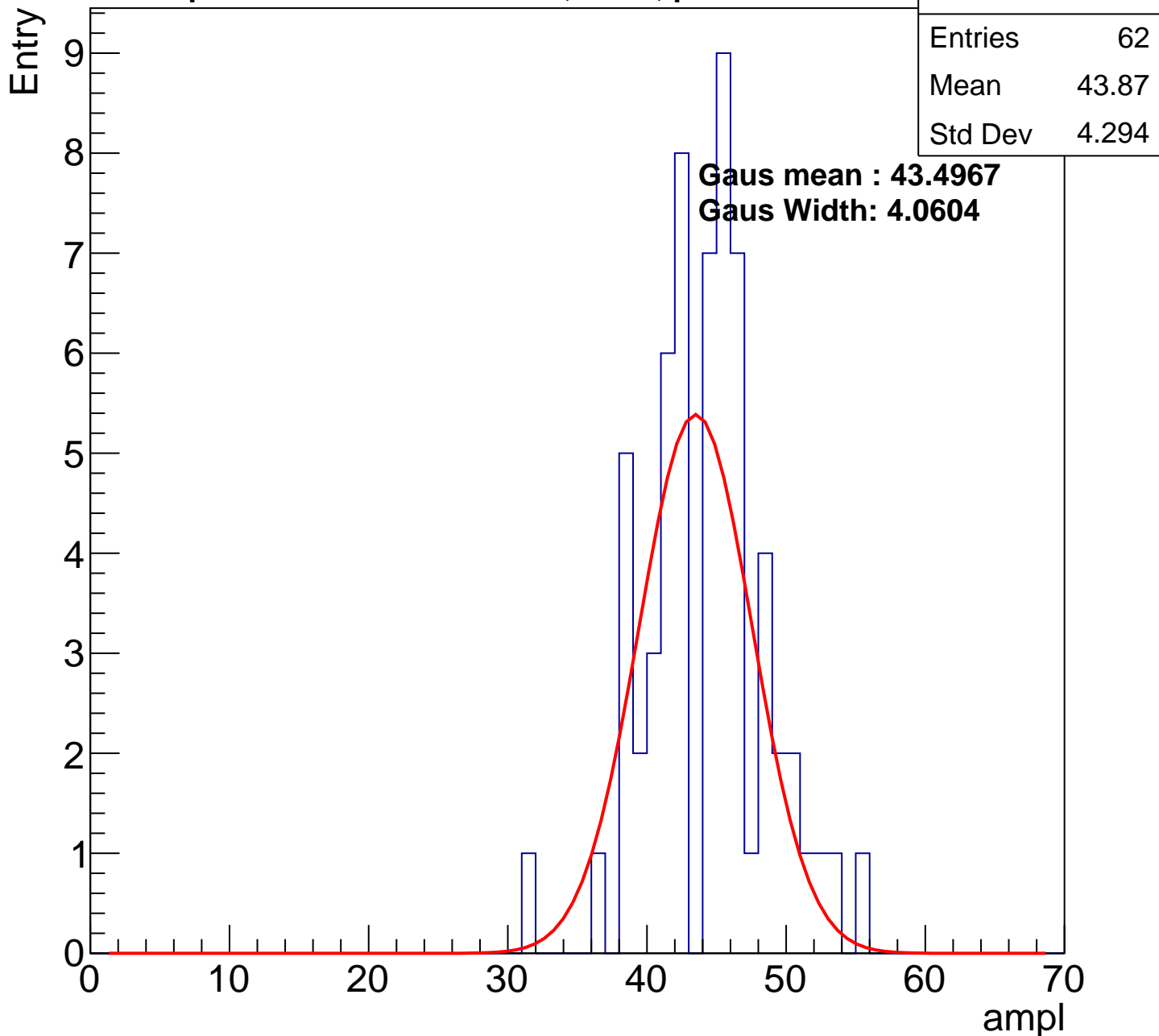
Gaus mean : 37.9776

Gaus Width: 3.9297



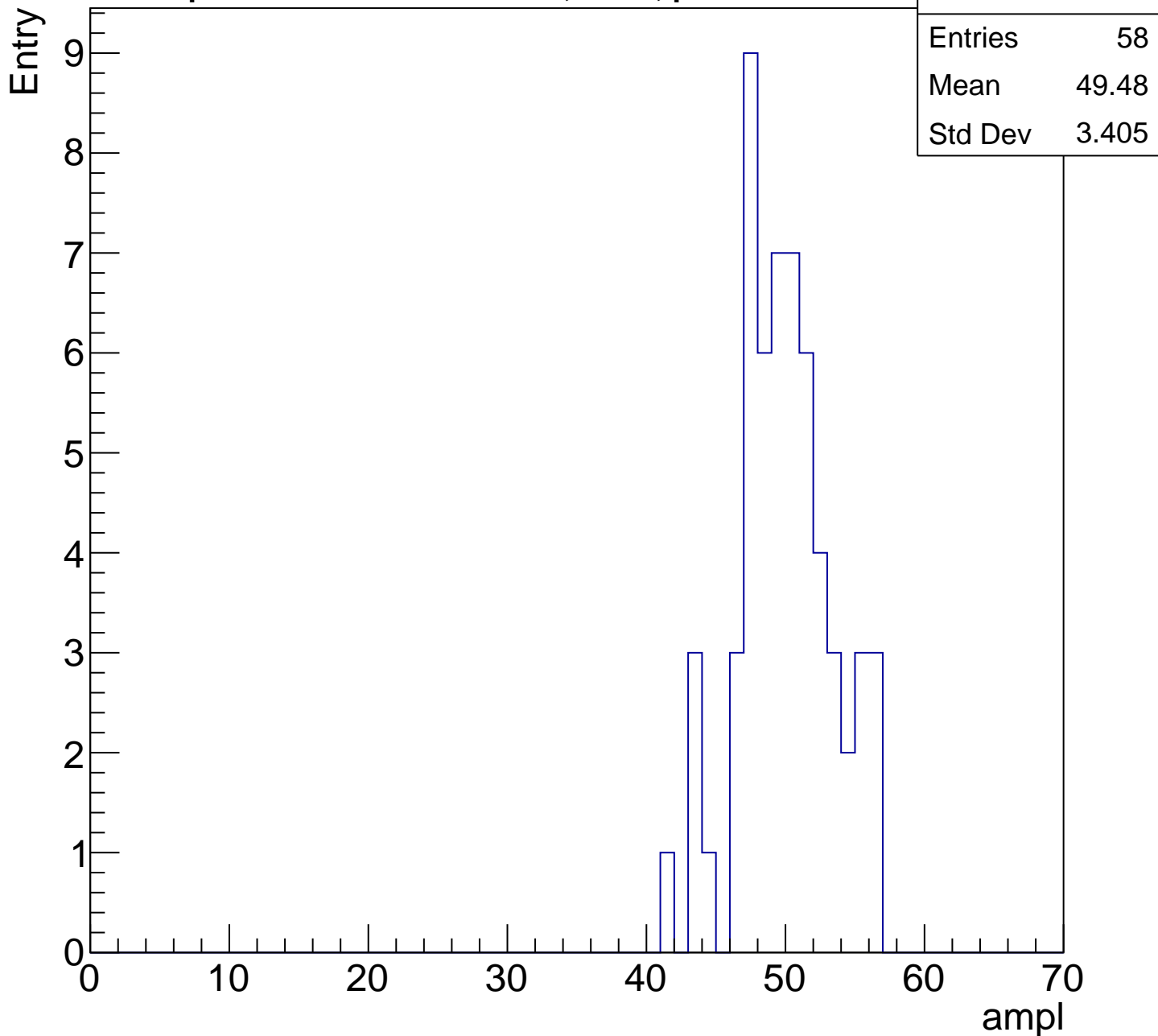
B1L101S, U1-ch91, adc2

calib_packv5_042523_0143.root, FC#0, port D2



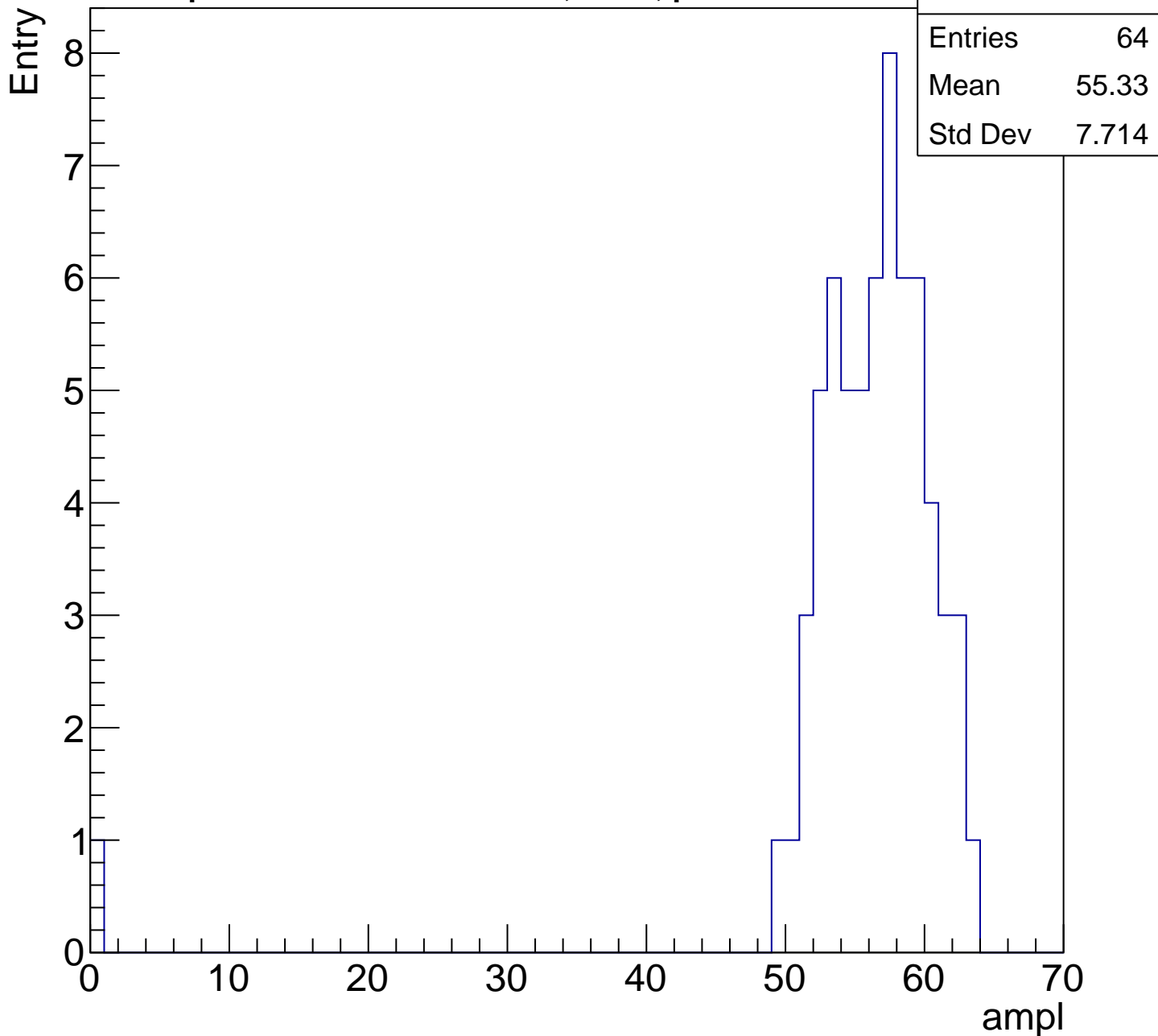
B1L101S, U1-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

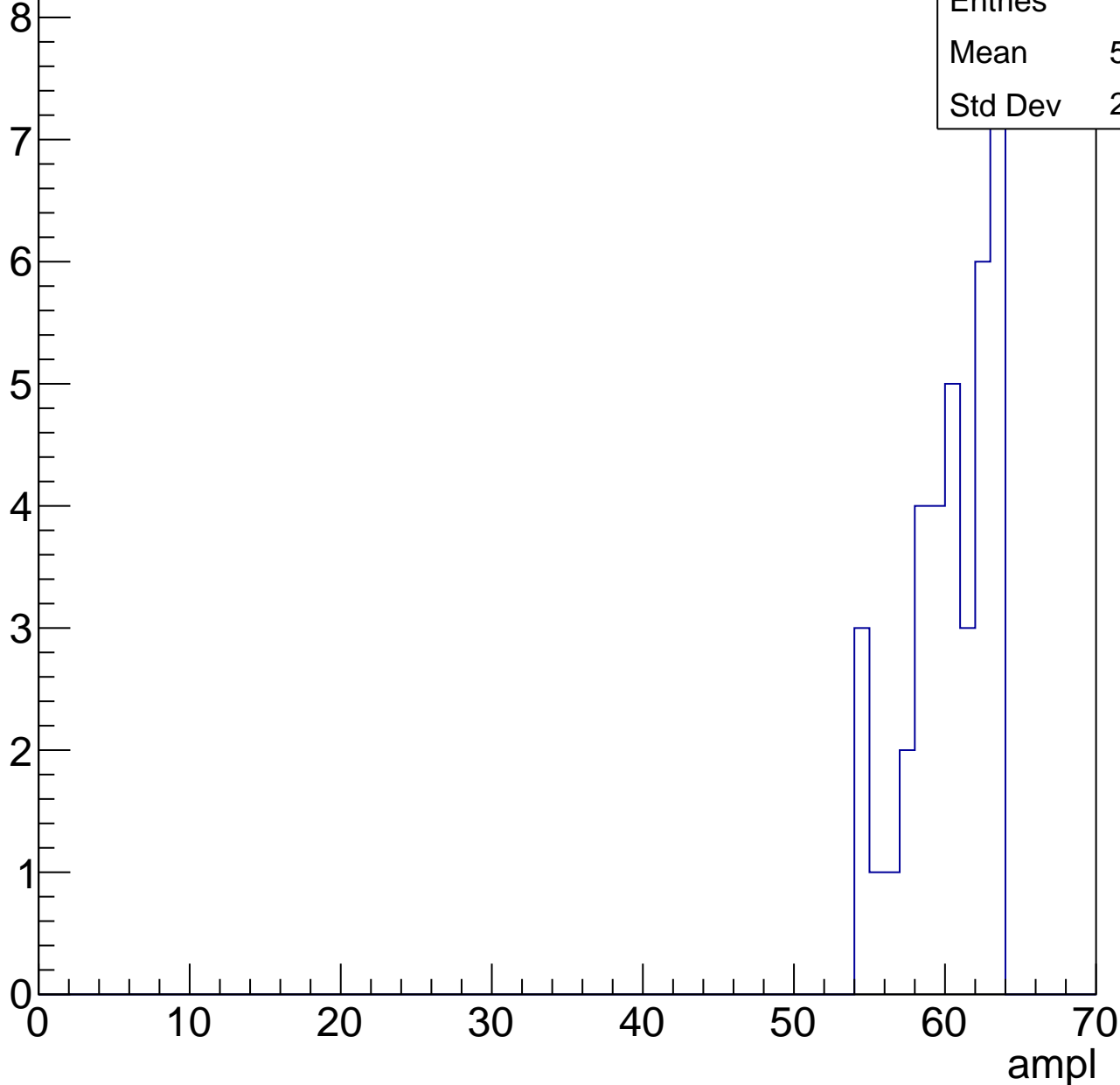


B1L101S, U1-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

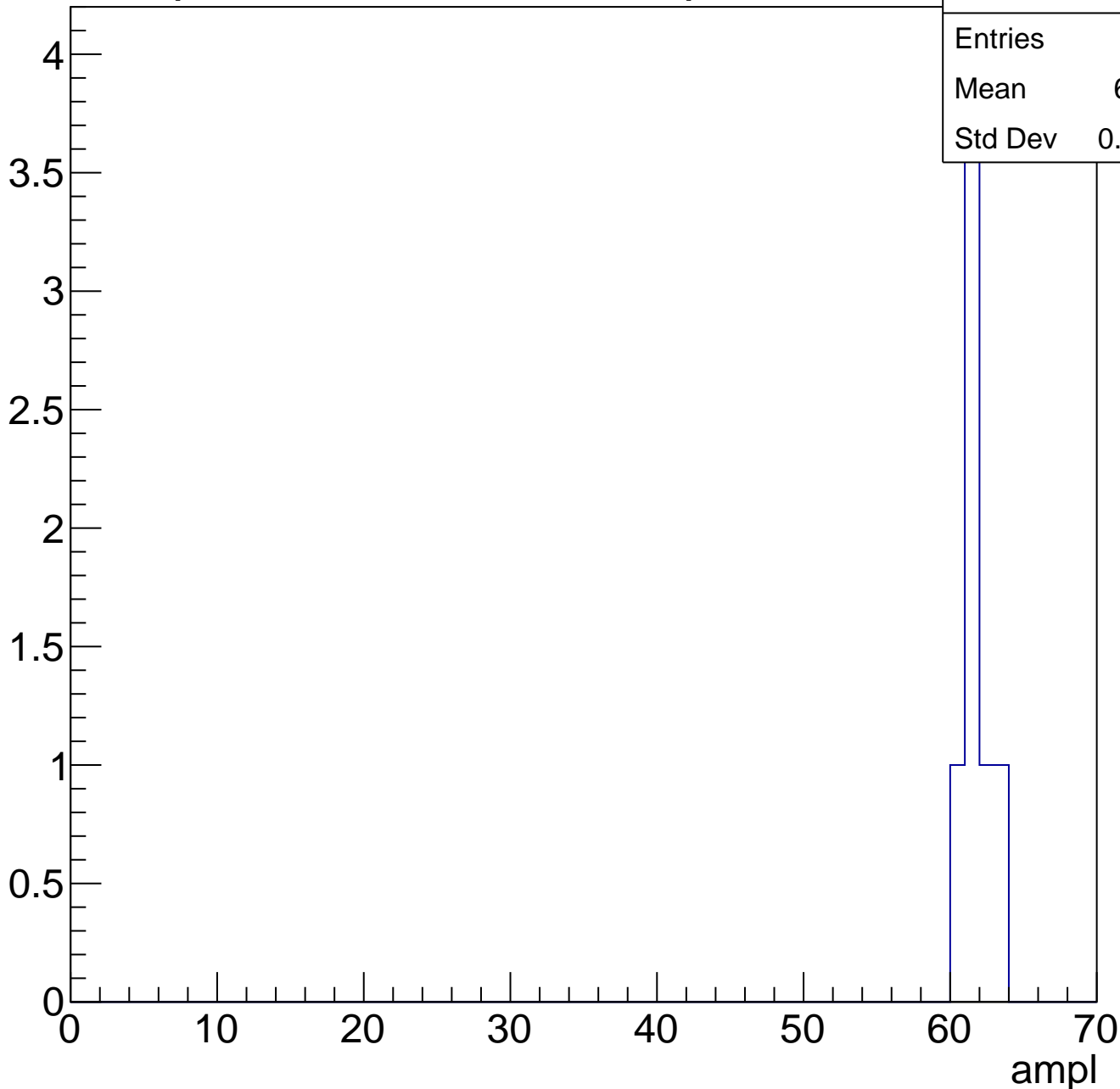
Entries	37
Mean	59.84
Std Dev	2.776



B1L101S, U1-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch92, adc0

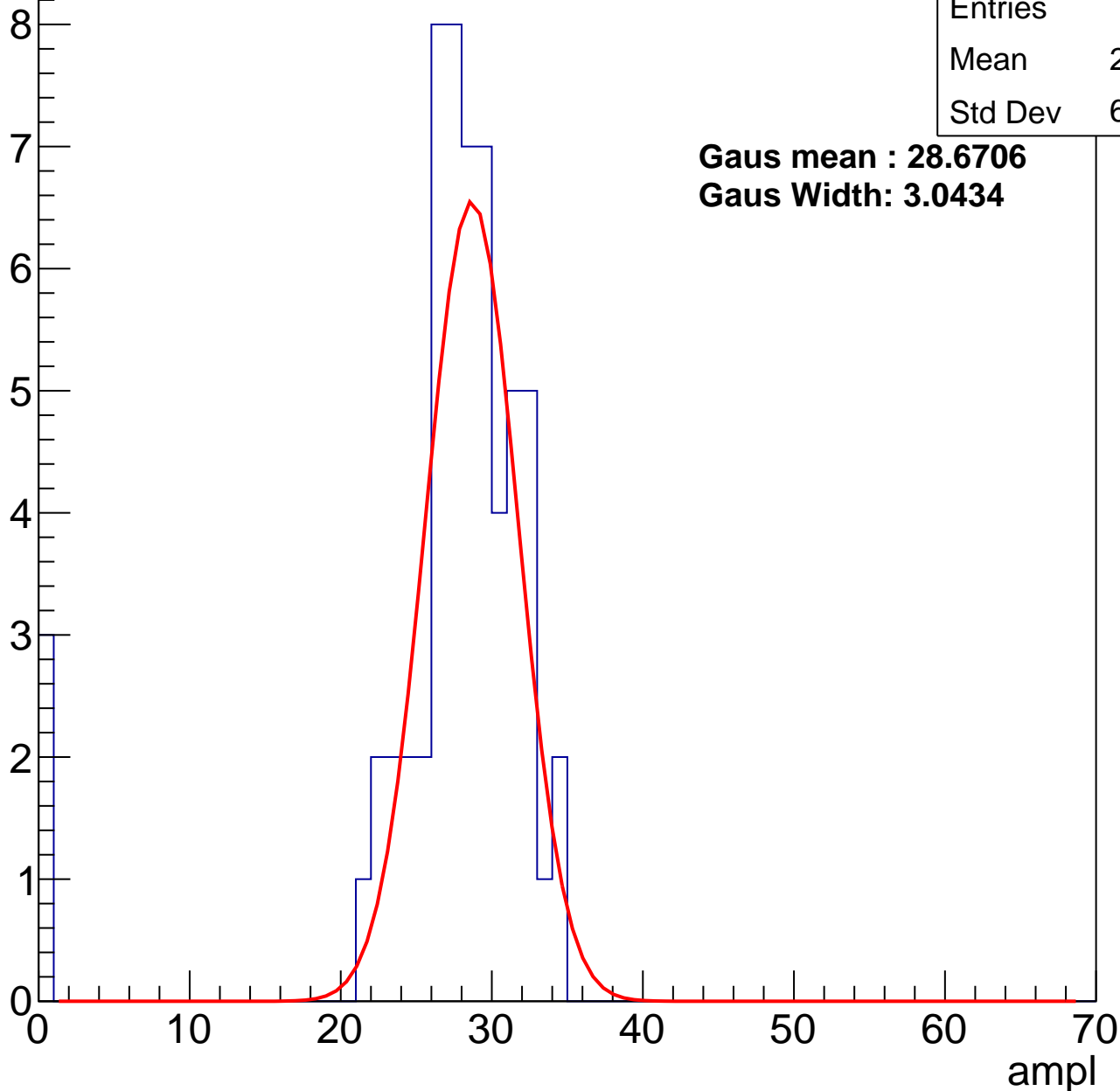
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	26.58
Std Dev	6.813

Gaus mean : 28.6706

Gaus Width: 3.0434



B1L101S, U1-ch92, adc1

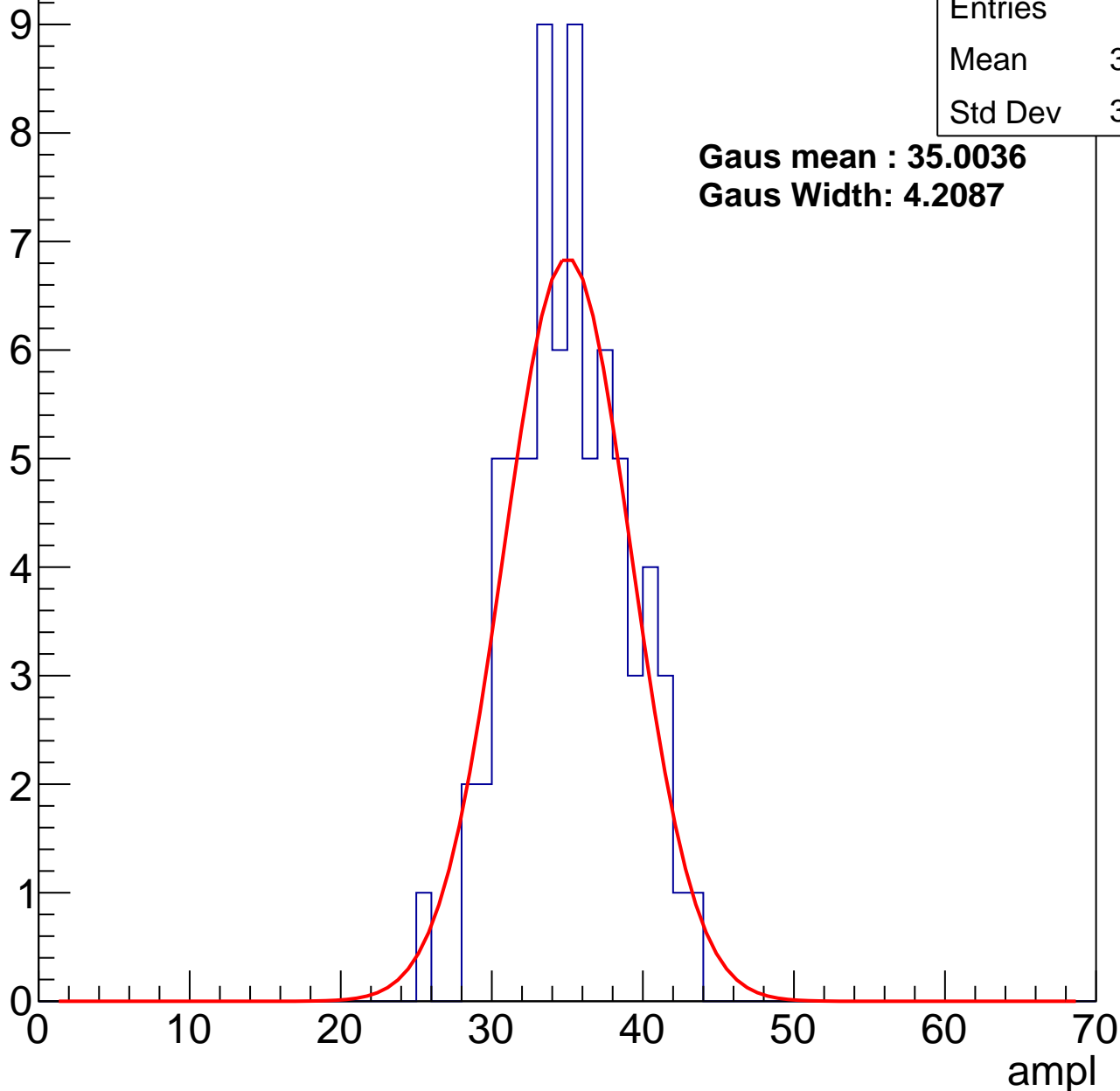
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	34.68
Std Dev	3.722

Gaus mean : 35.0036

Gaus Width: 4.2087



B1L101S, U1-ch92, adc2

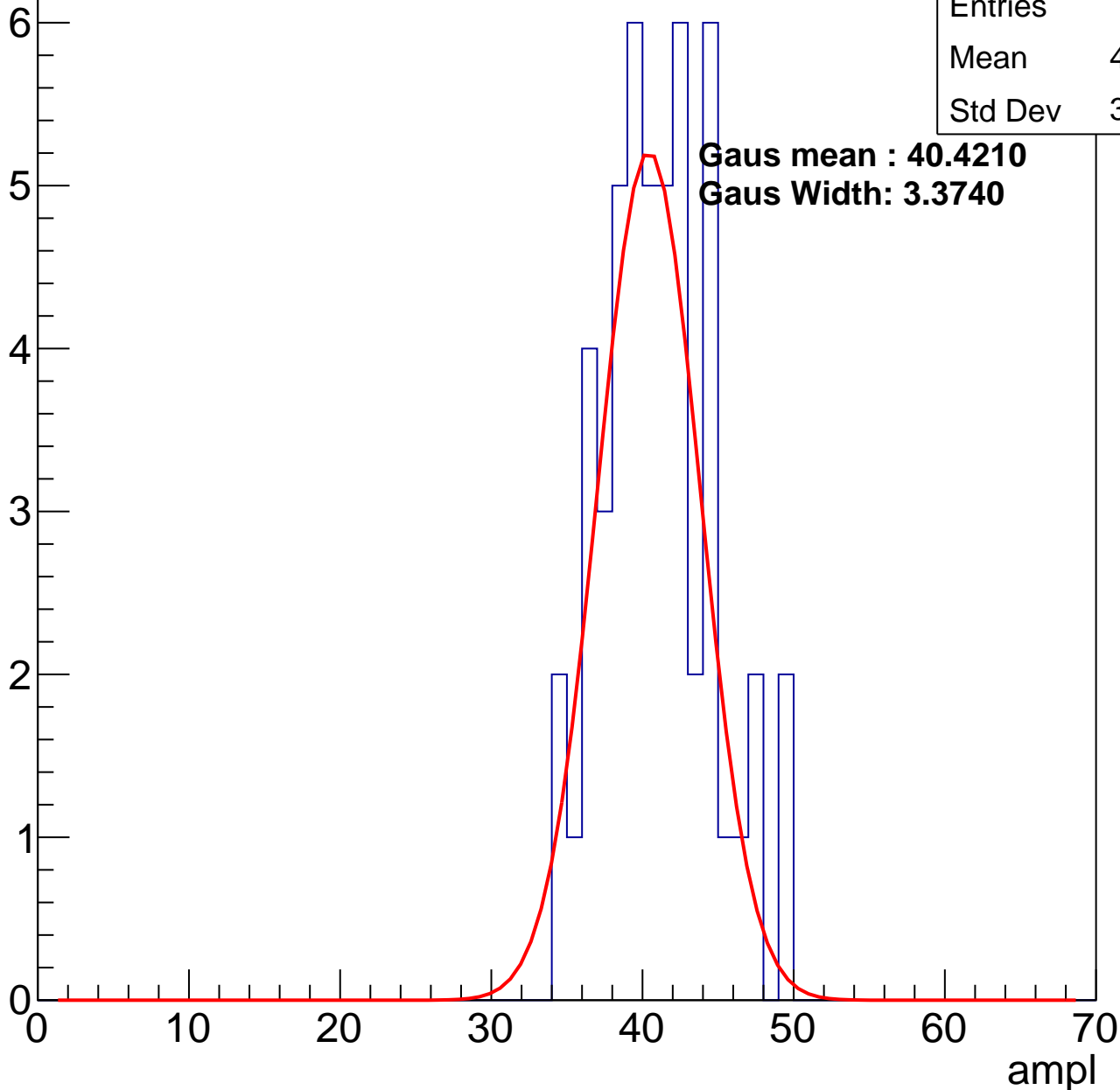
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	40.63
Std Dev	3.592

Gaus mean : 40.4210

Gaus Width: 3.3740

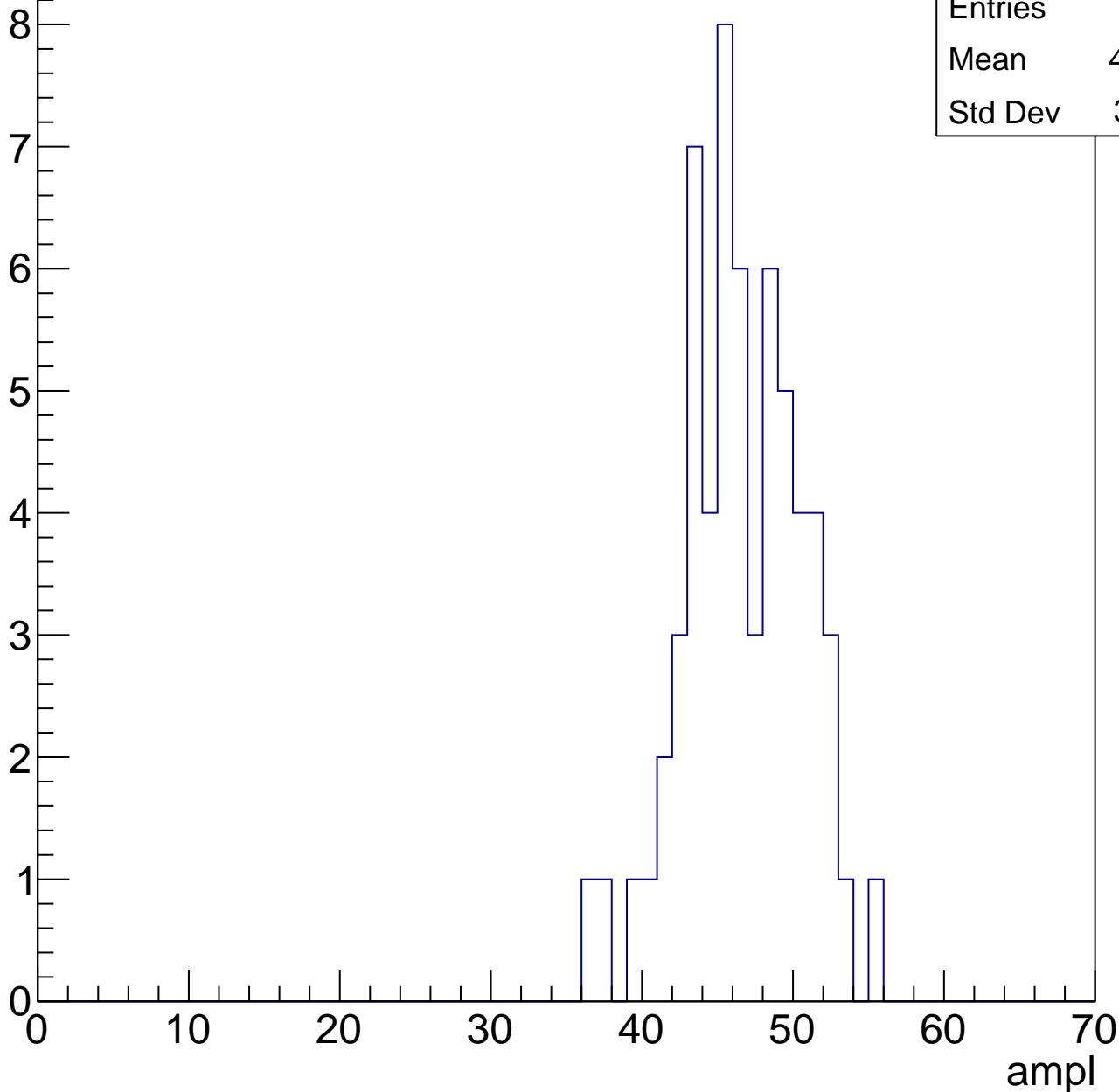


B1L101S, U1-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

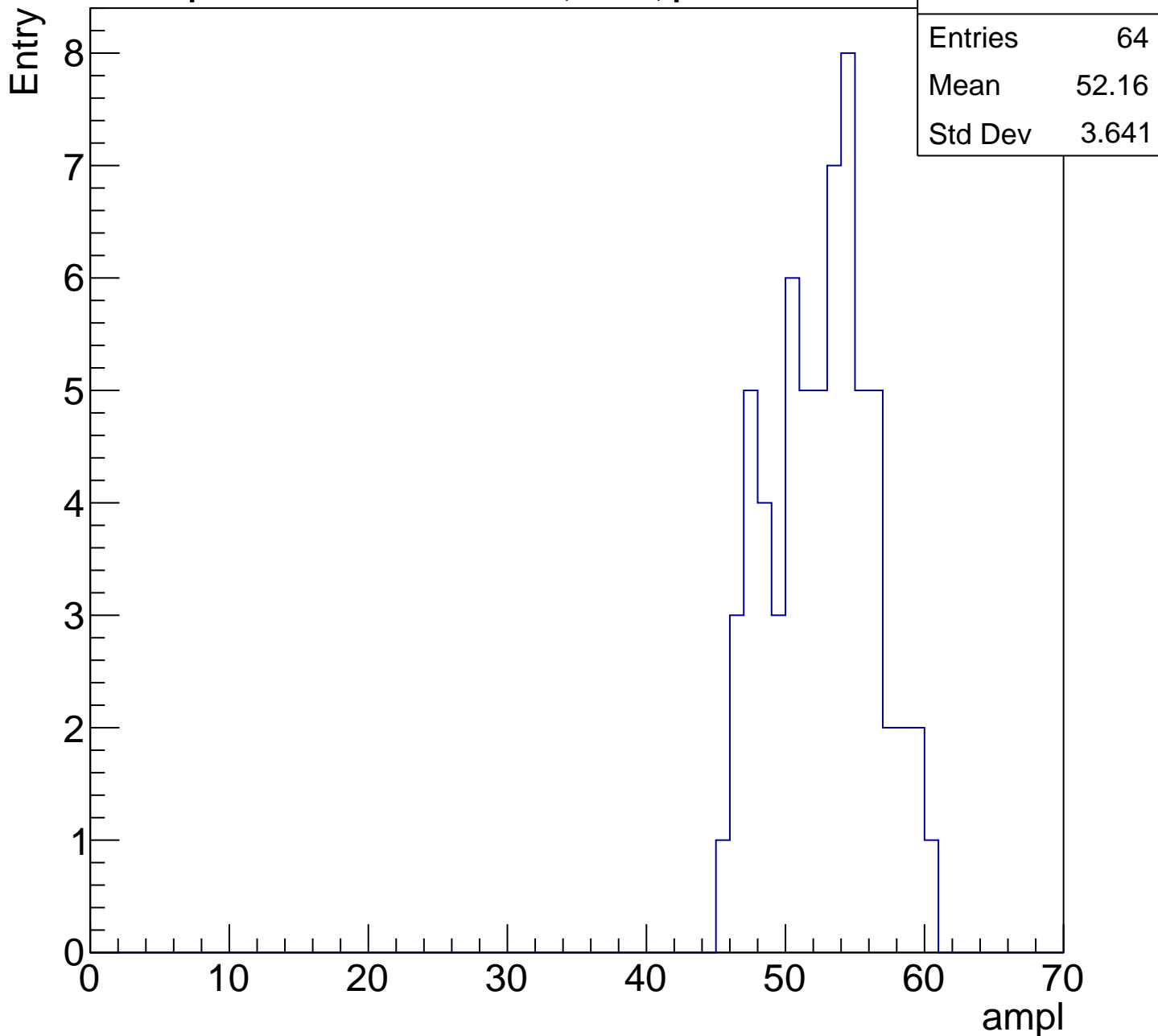
Entry

Entries	61
Mean	46.15
Std Dev	3.891



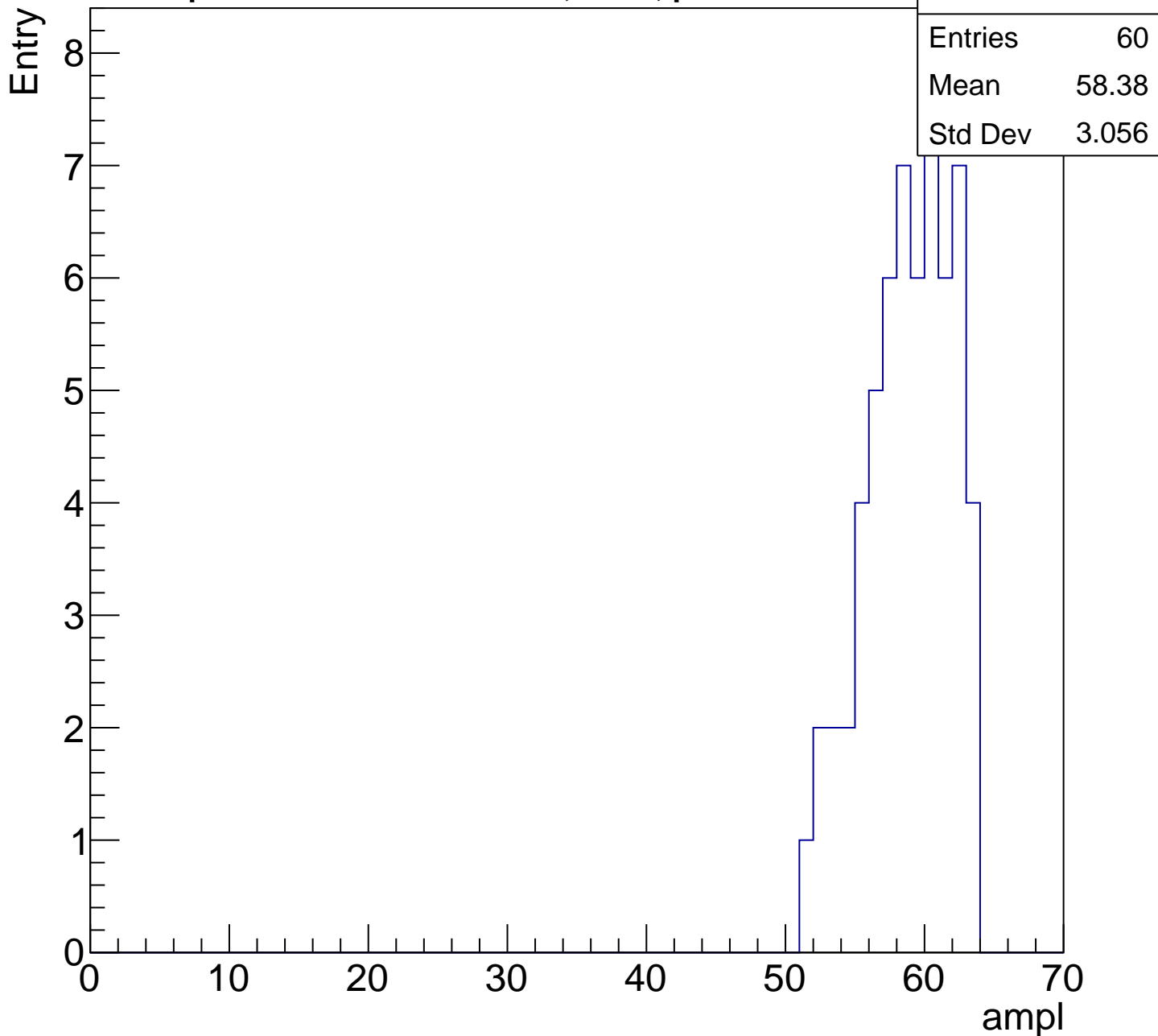
B1L101S, U1-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

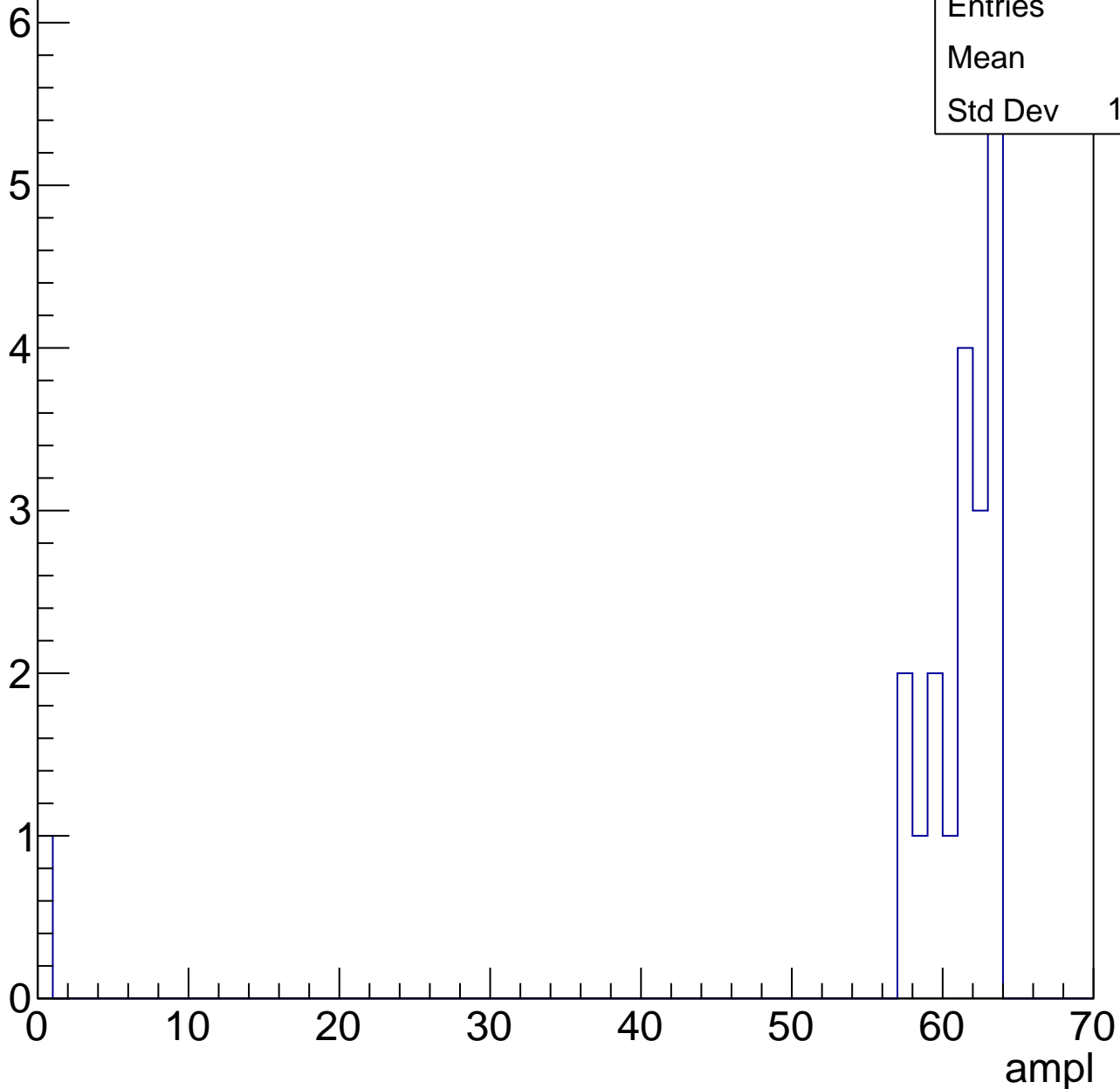


B1L101S, U1-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	57.9
Std Dev	13.43



B1L101S, U1-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch93, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	29.46
Std Dev	5.089

Gaus mean : 30.1990

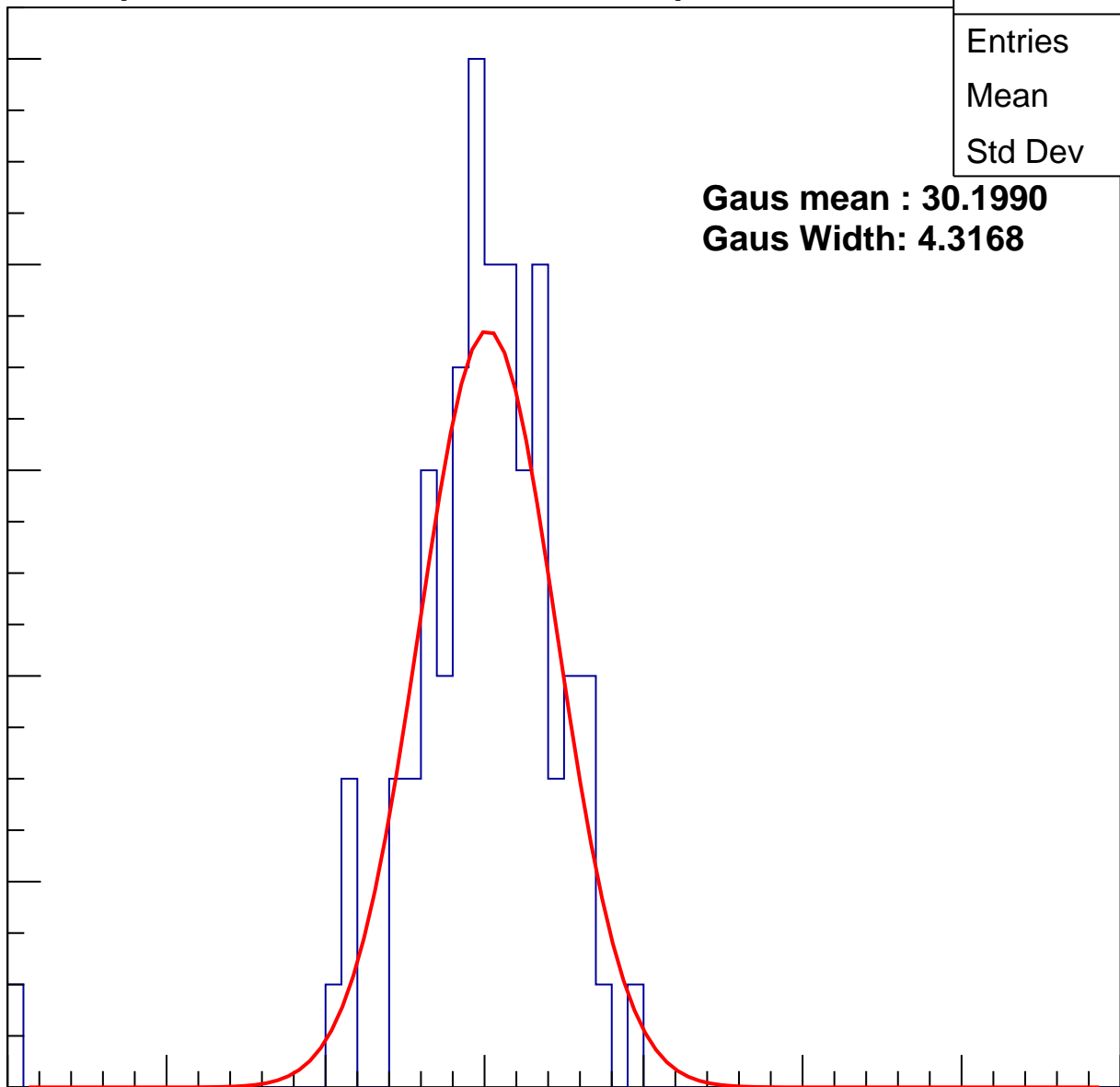
Gaus Width: 4.3168

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch93, adc1

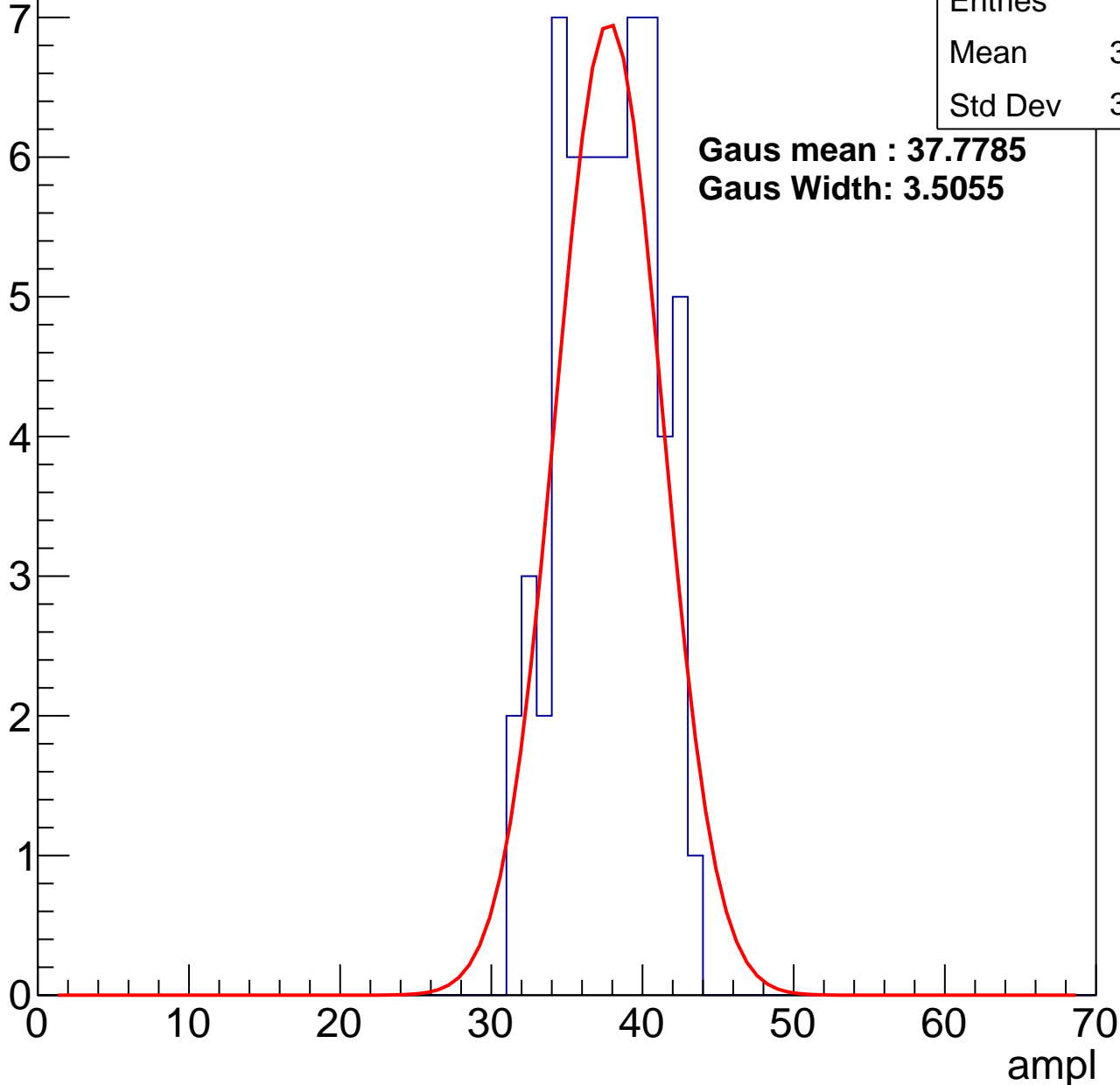
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.23
Std Dev	3.087

Gaus mean : 37.7785

Gaus Width: 3.5055



B1L101S, U1-ch93, adc2

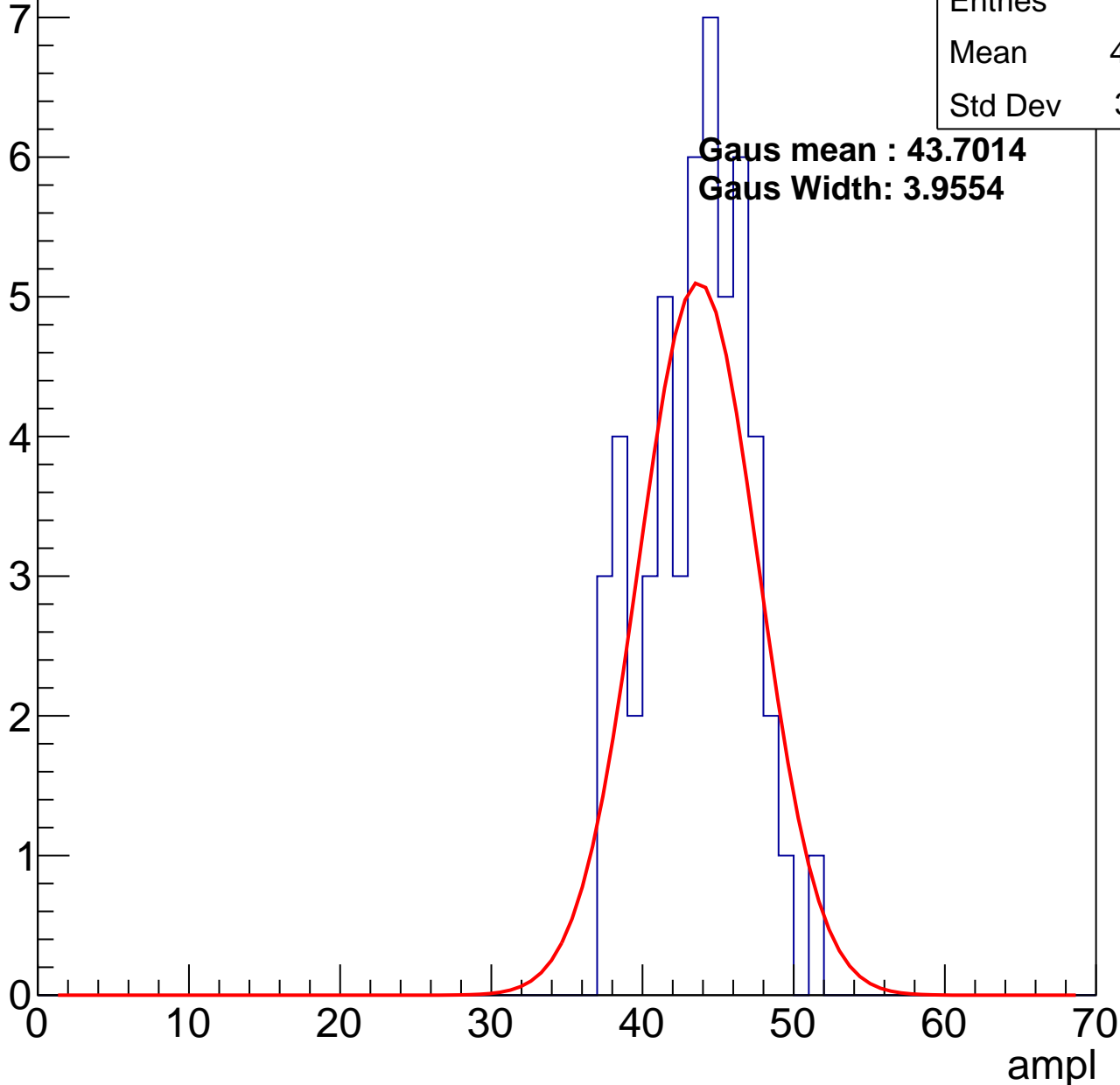
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.13
Std Dev	3.351

Gaus mean : 43.7014

Gaus Width: 3.9554

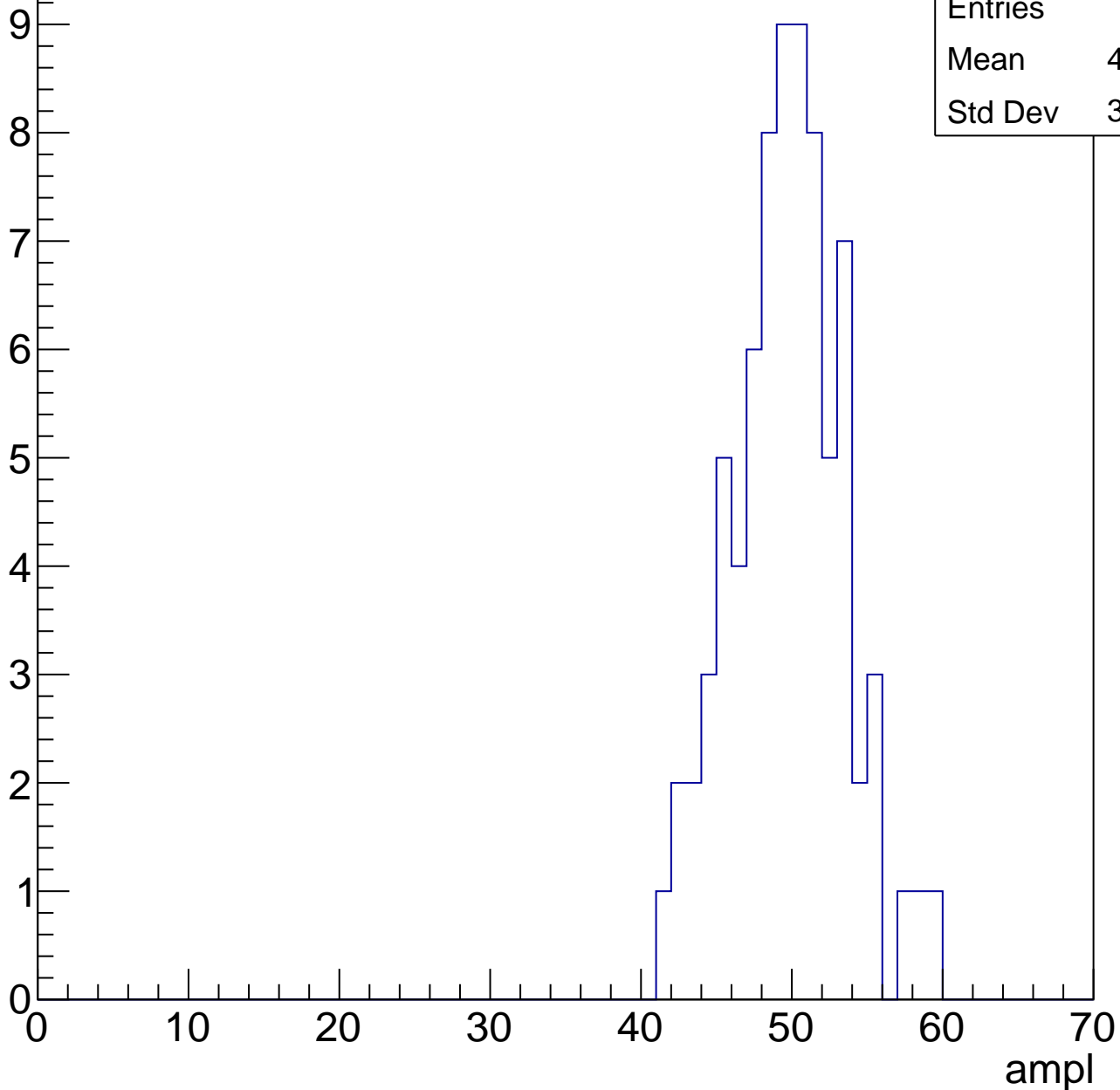


B1L101S, U1-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	49.29
Std Dev	3.696

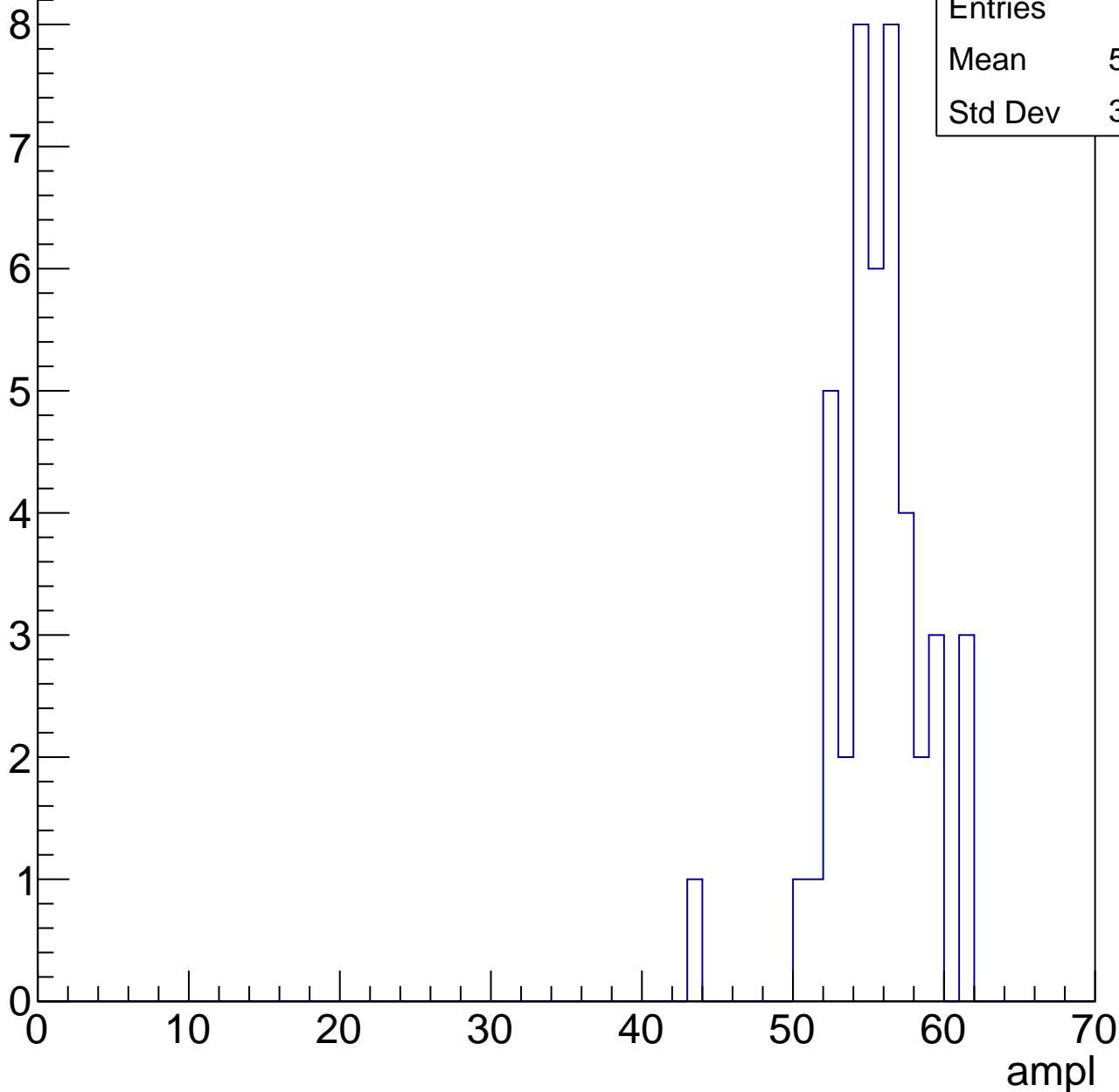


B1L101S, U1-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	55.09
Std Dev	3.175

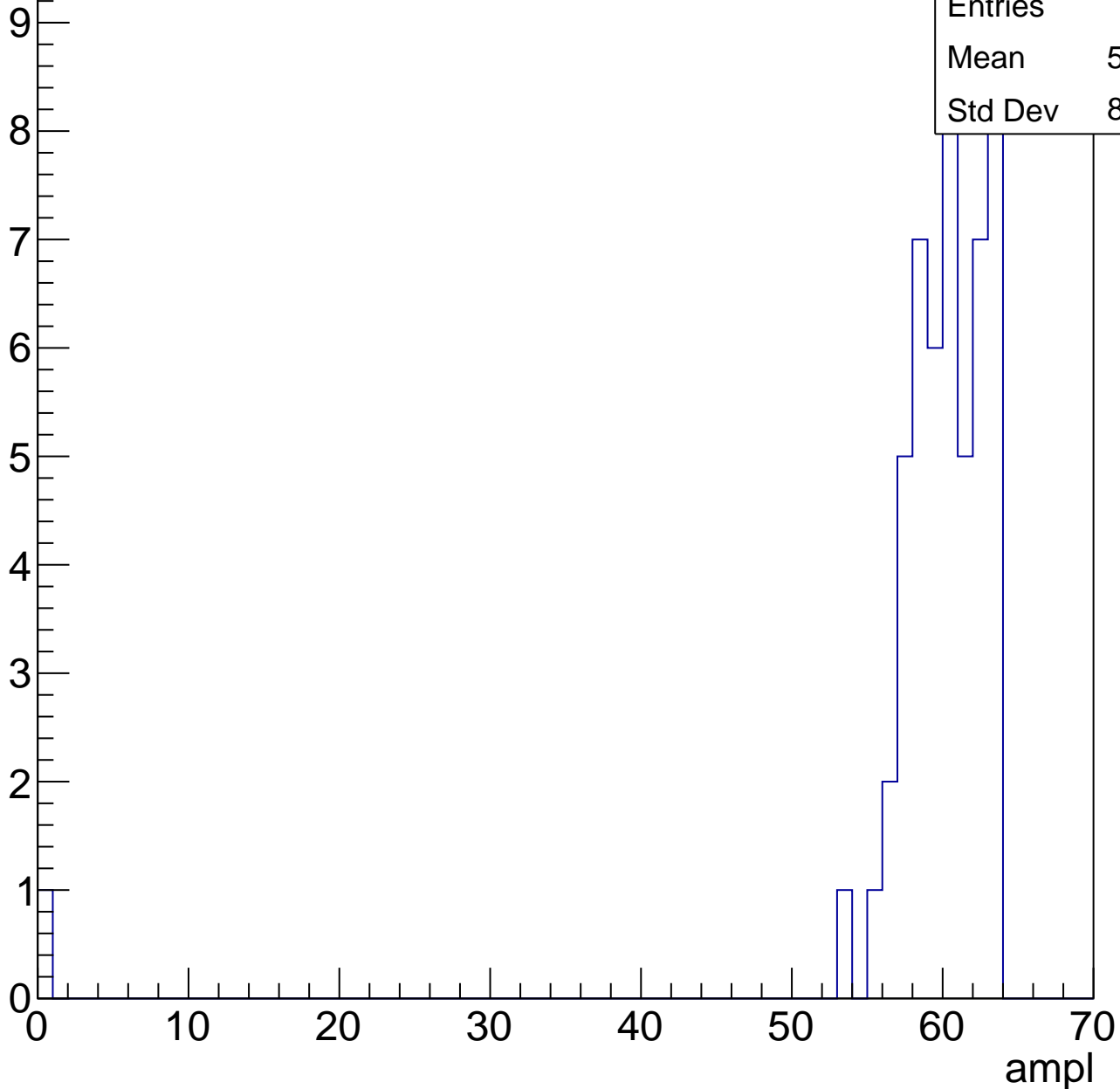


B1L101S, U1-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

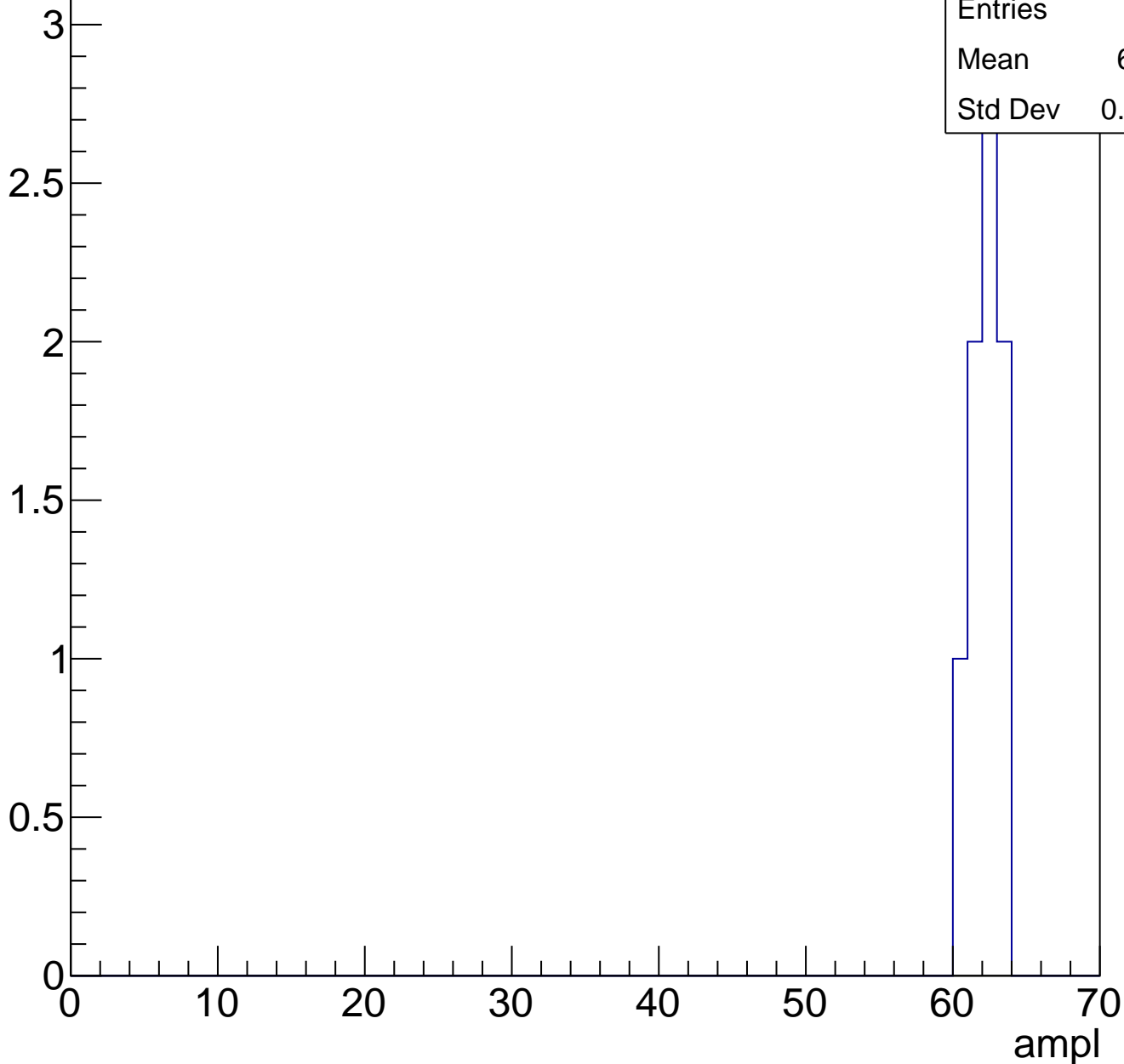
Entries	52
Mean	58.62
Std Dev	8.538



B1L101S, U1-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch94, adc0

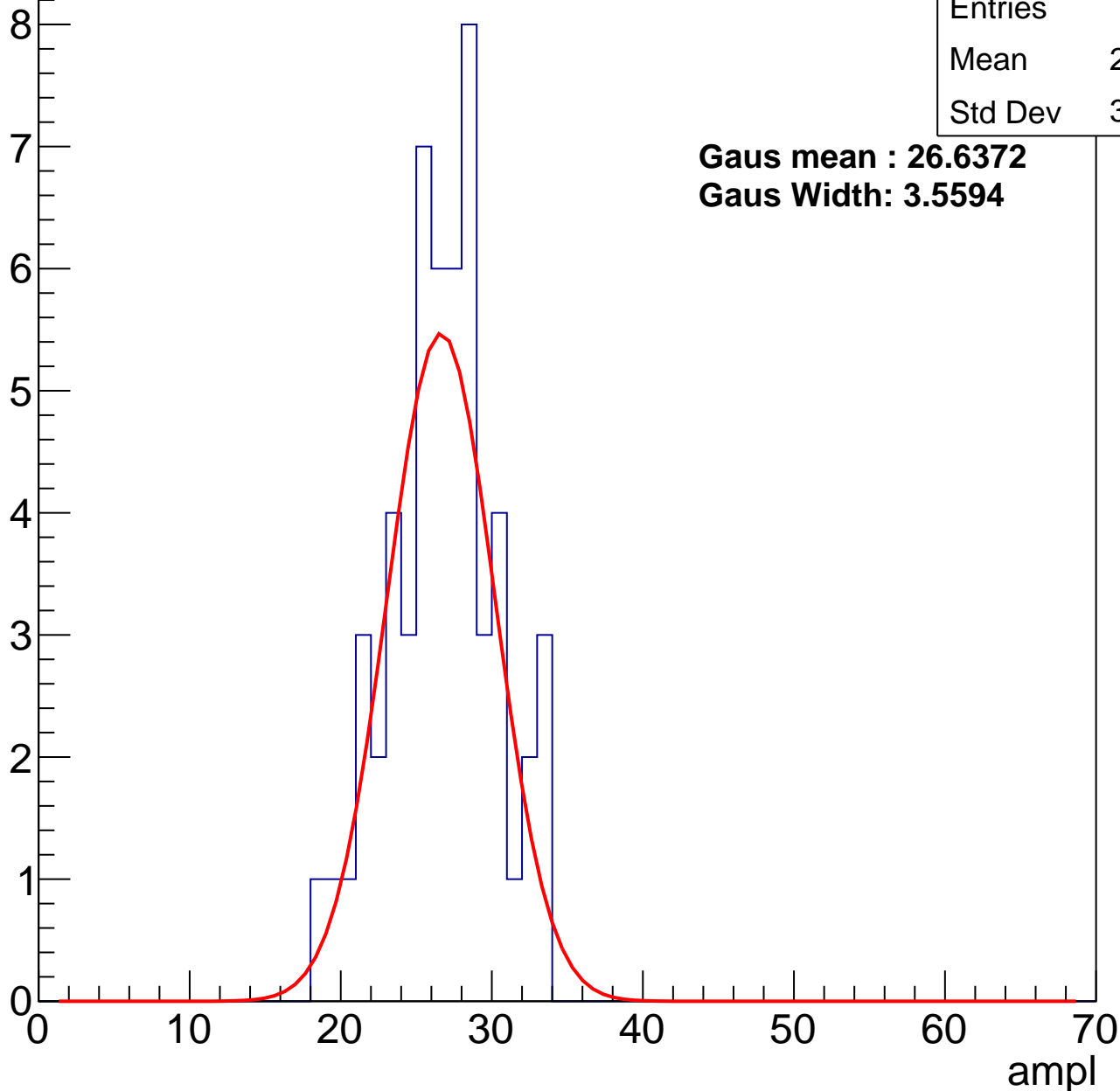
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	26.29
Std Dev	3.515

Gaus mean : 26.6372

Gaus Width: 3.5594



B1L101S, U1-ch94, adc1

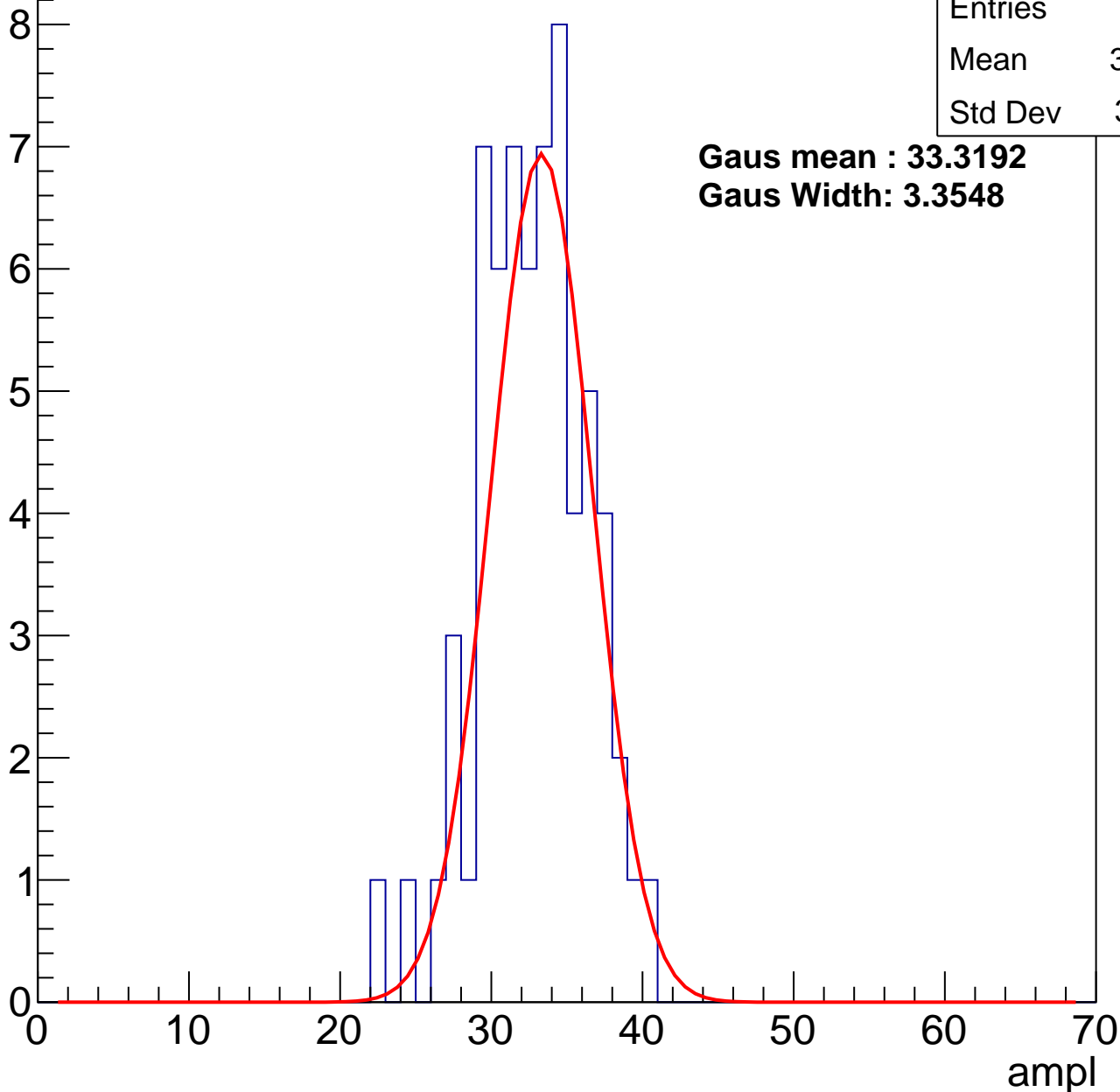
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	32.29
Std Dev	3.551

Gaus mean : 33.3192

Gaus Width: 3.3548



B1L101S, U1-ch94, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	39.62
Std Dev	3.641

Gaus mean : 40.0220

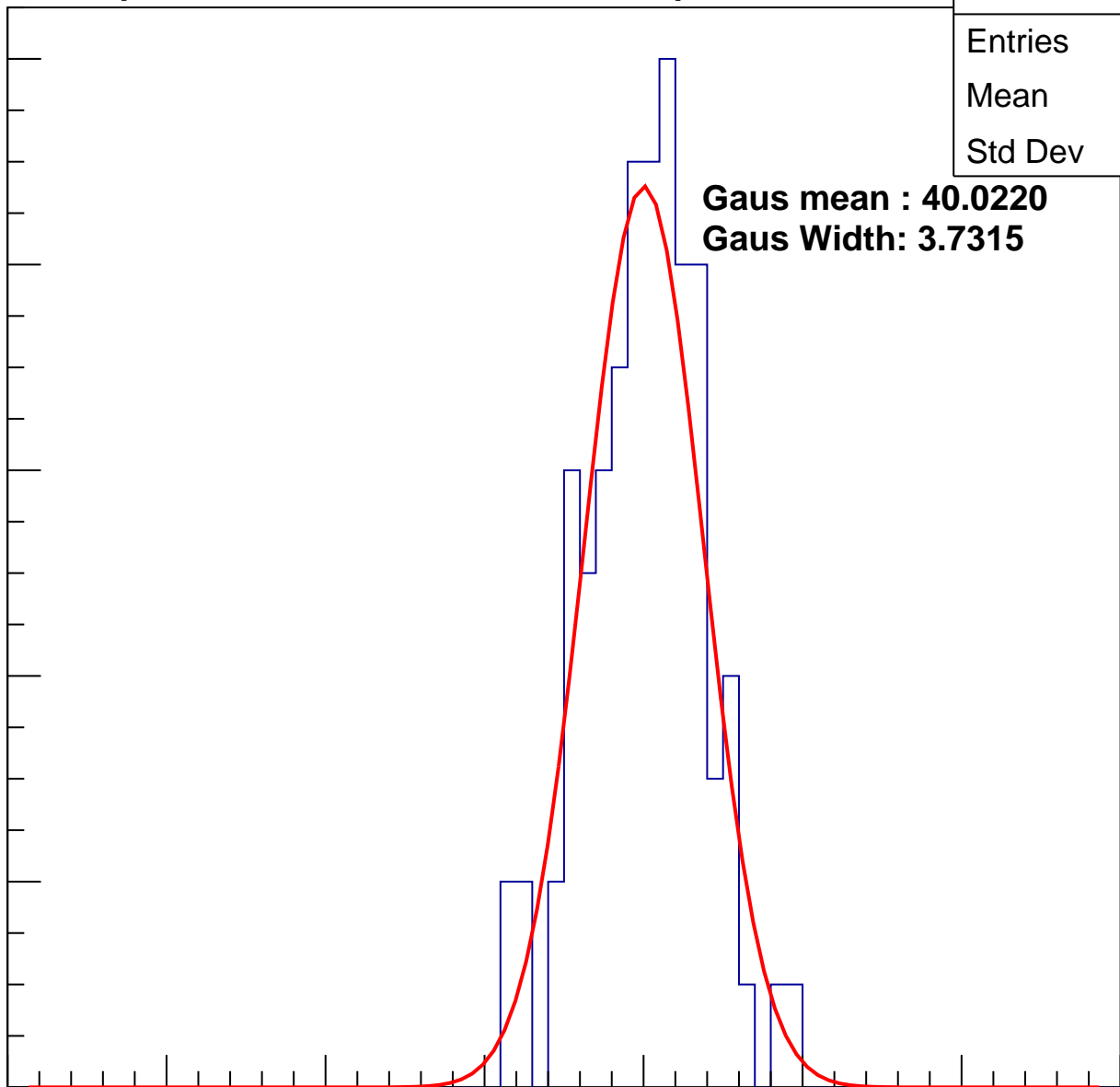
Gaus Width: 3.7315

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

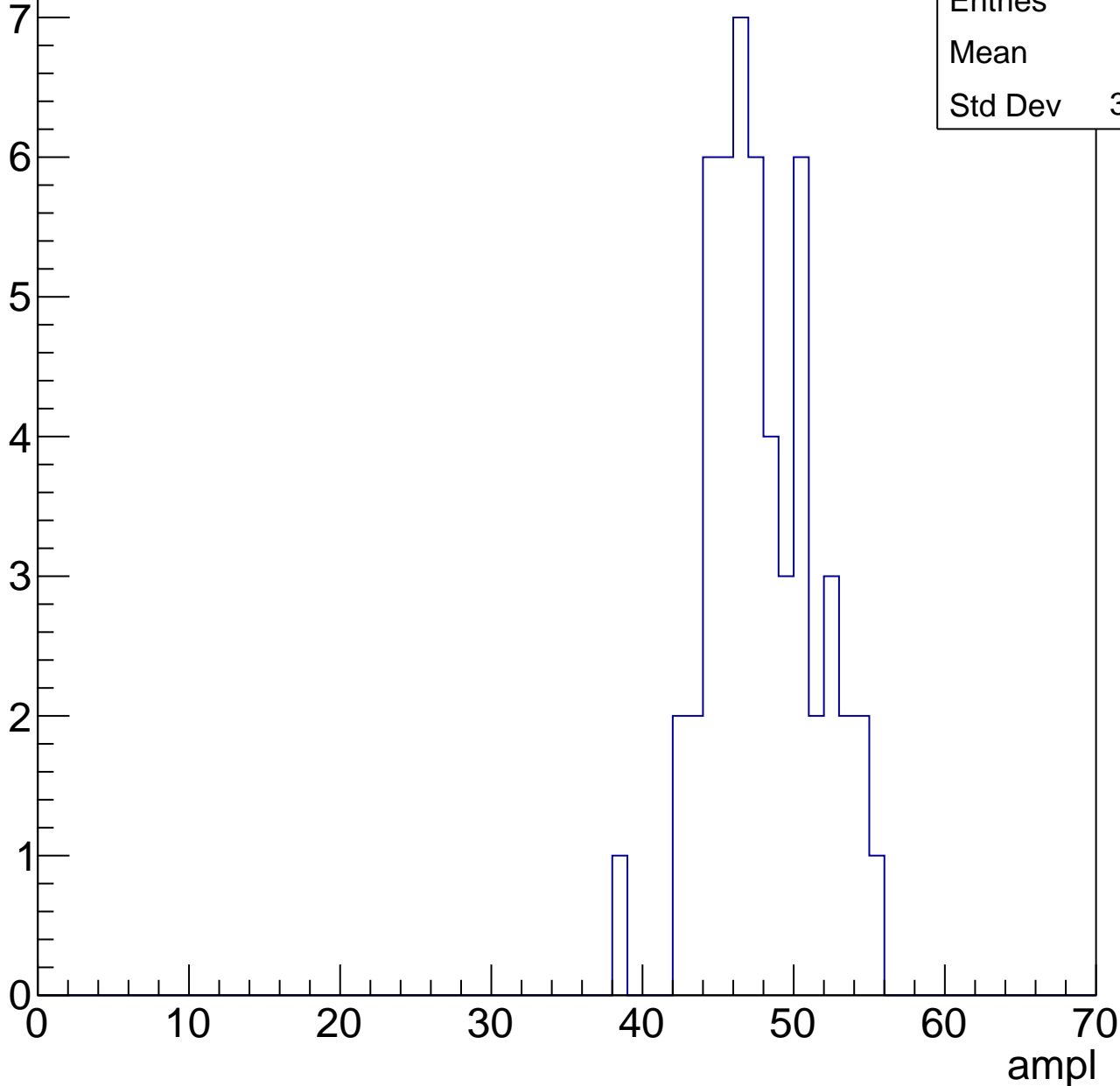


B1L101S, U1-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	47.4
Std Dev	3.504

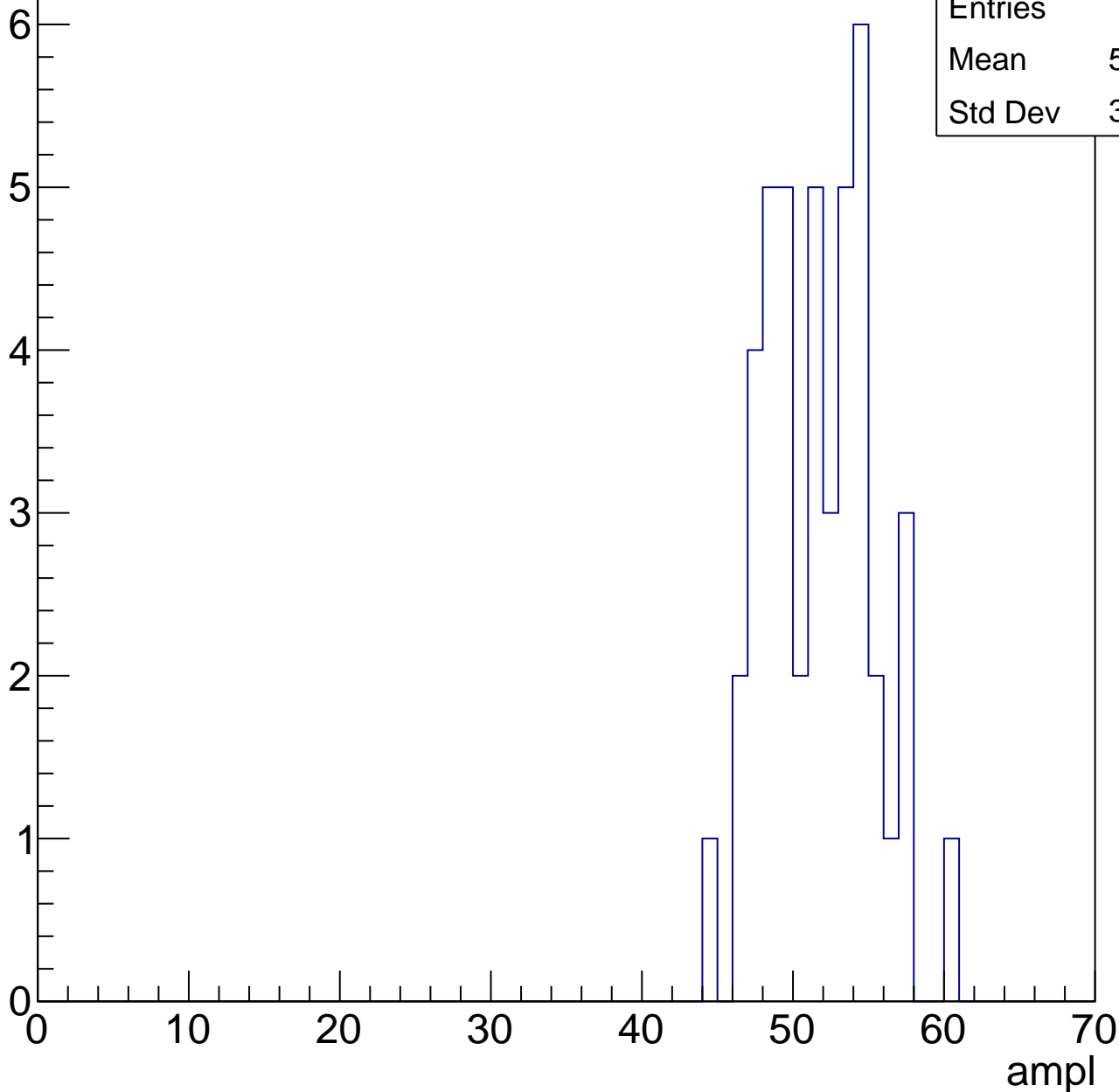


B1L101S, U1-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

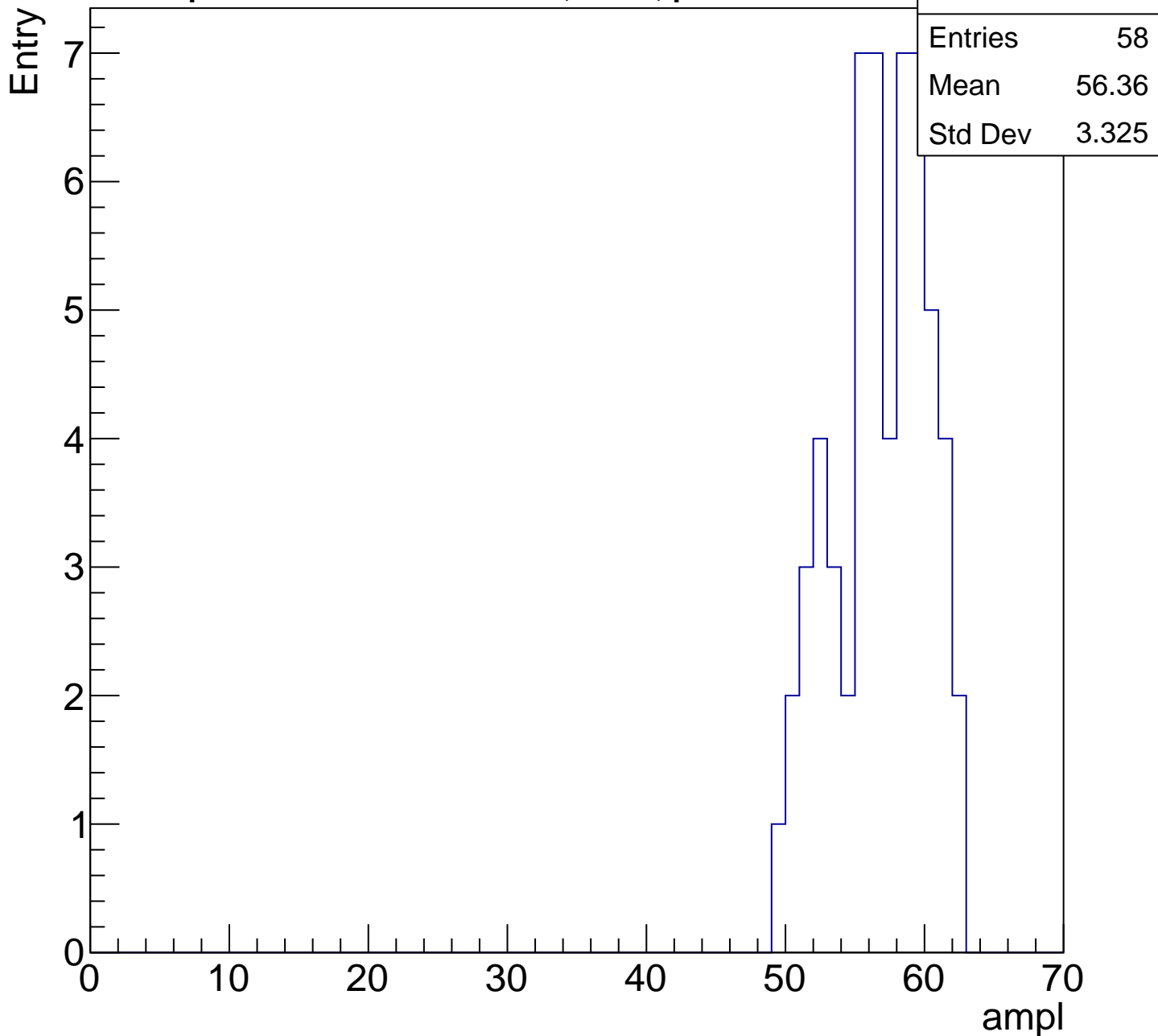
Entry

Entries	45
Mean	51.24
Std Dev	3.497



B1L101S, U1-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

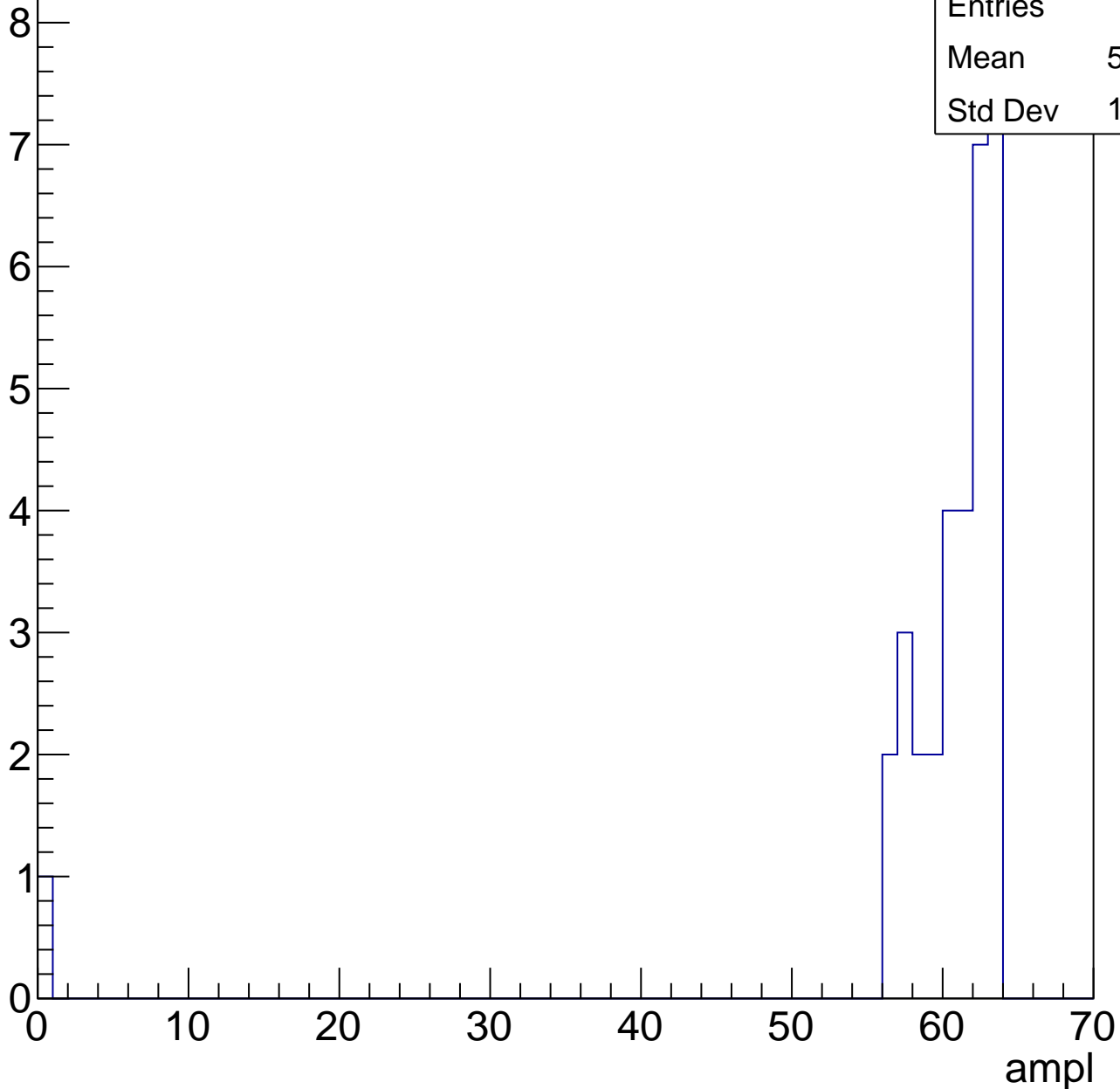


B1L101S, U1-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.76
Std Dev	10.62



B1L101S, U1-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

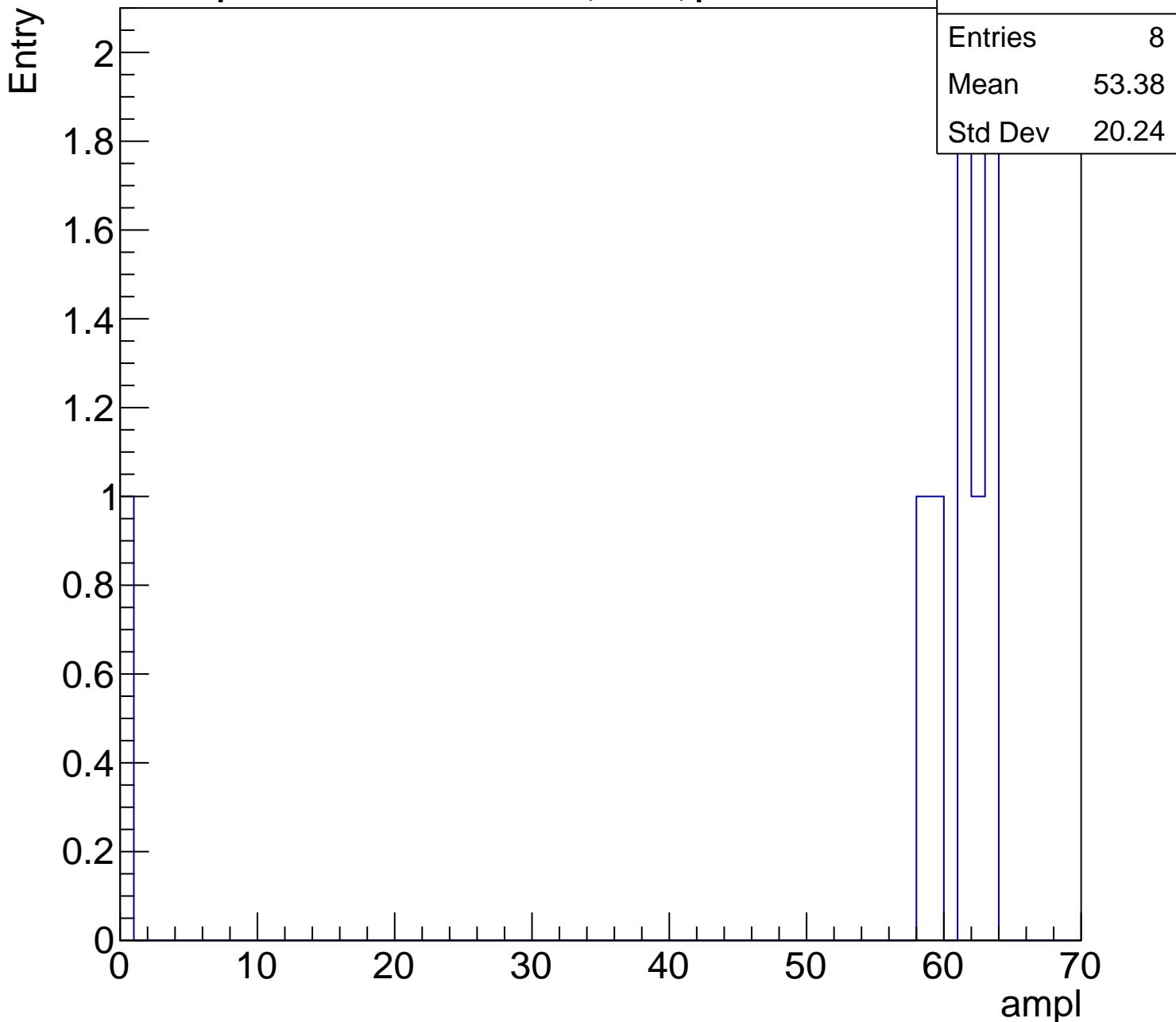
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.38
Std Dev	20.24

0 10 20 30 40 50 60 70

ampl



B1L101S, U1-ch95, adc0

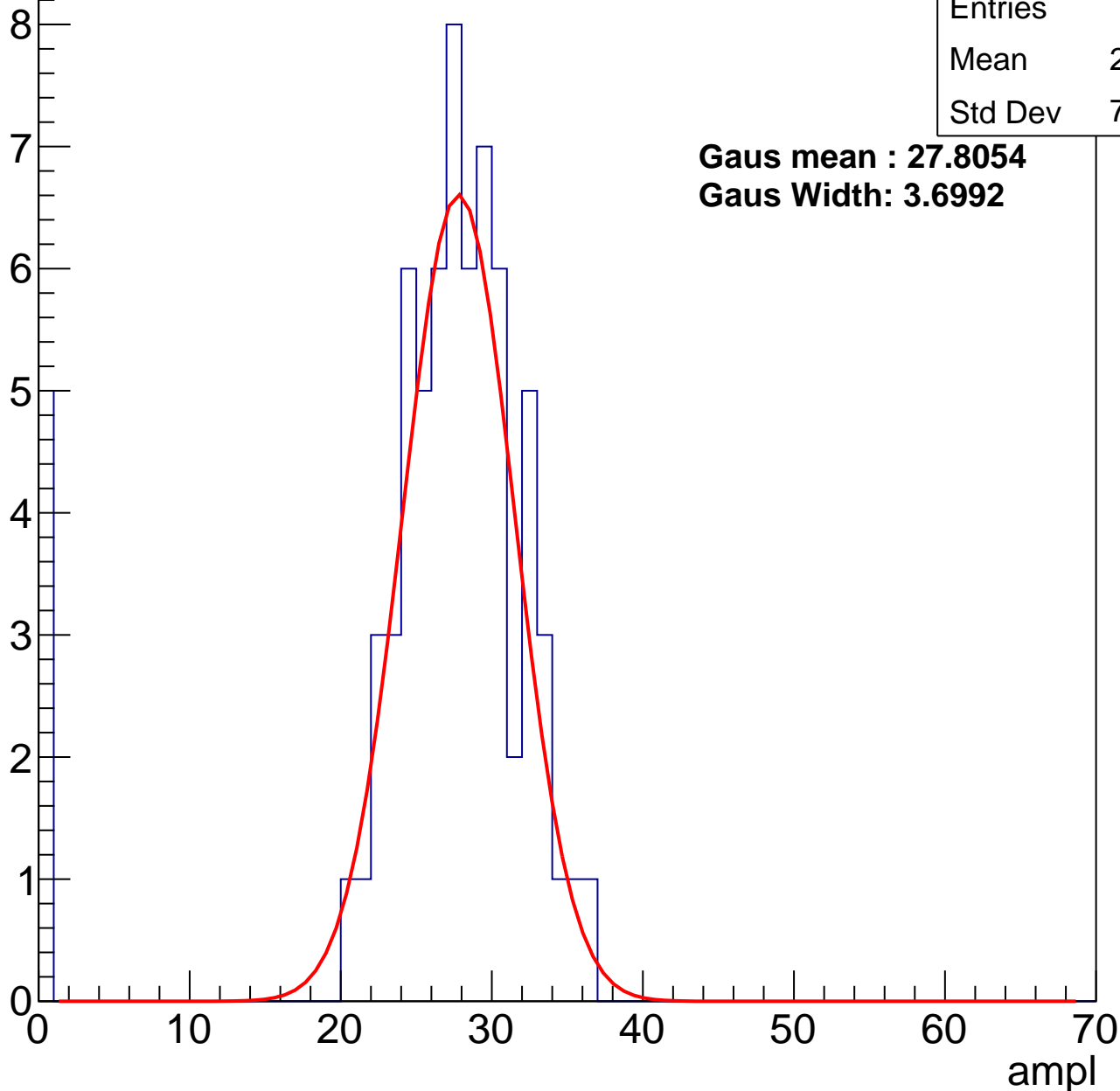
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	25.63
Std Dev	7.882

Gaus mean : 27.8054

Gaus Width: 3.6992



B1L101S, U1-ch95, adc1

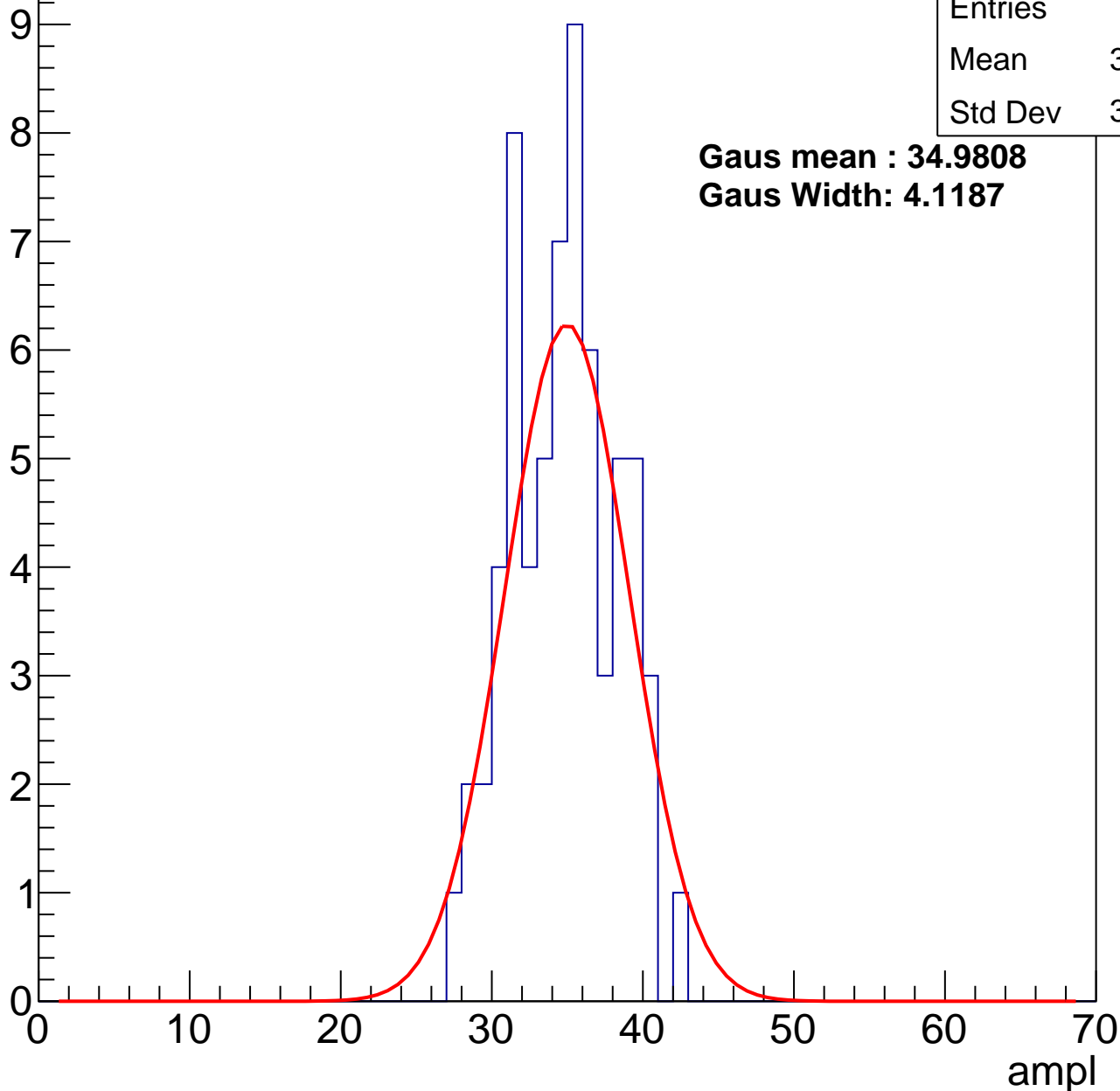
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	34.29
Std Dev	3.418

Gaus mean : 34.9808

Gaus Width: 4.1187

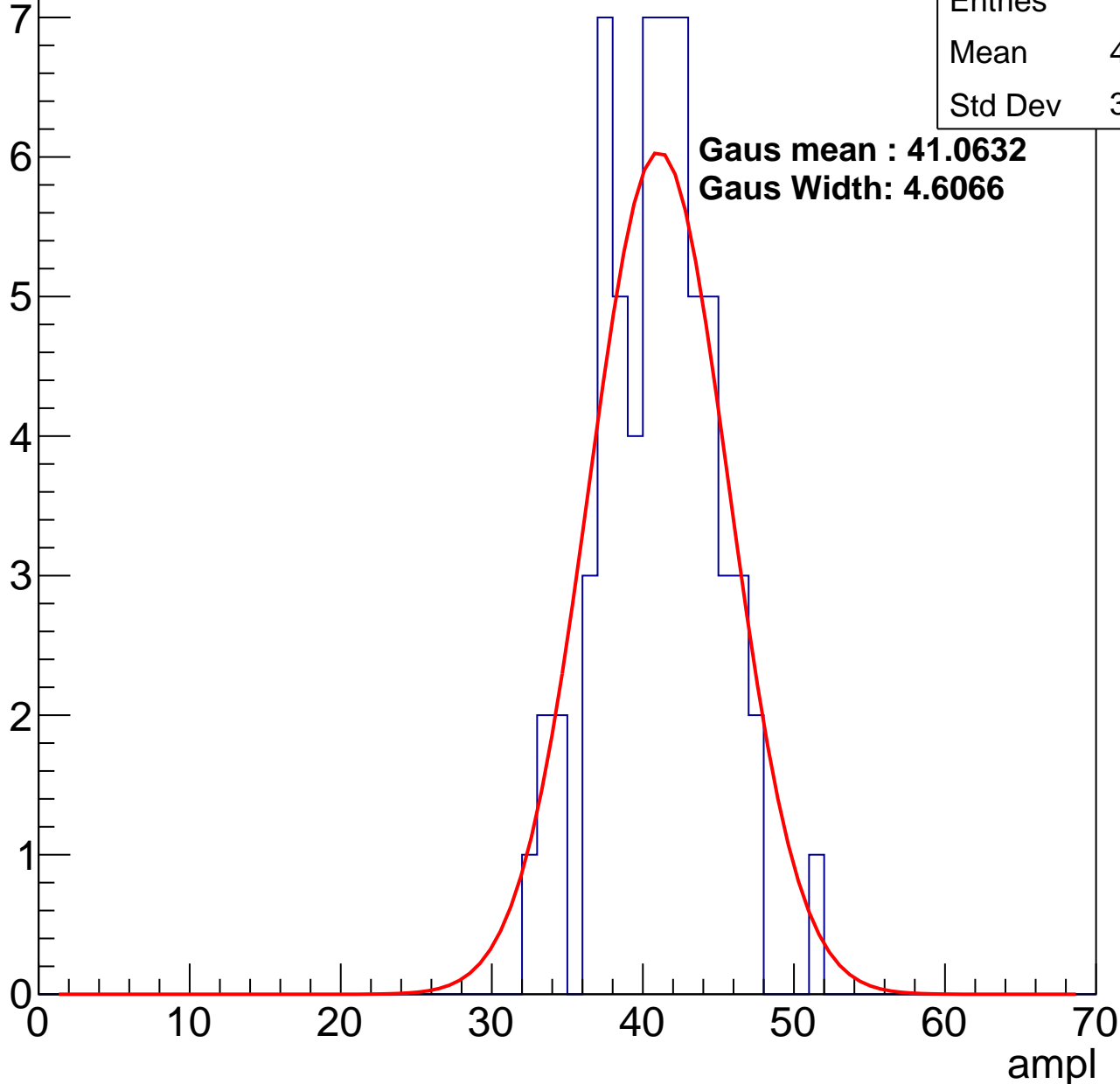


B1L101S, U1-ch95, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	40.52
Std Dev	3.779

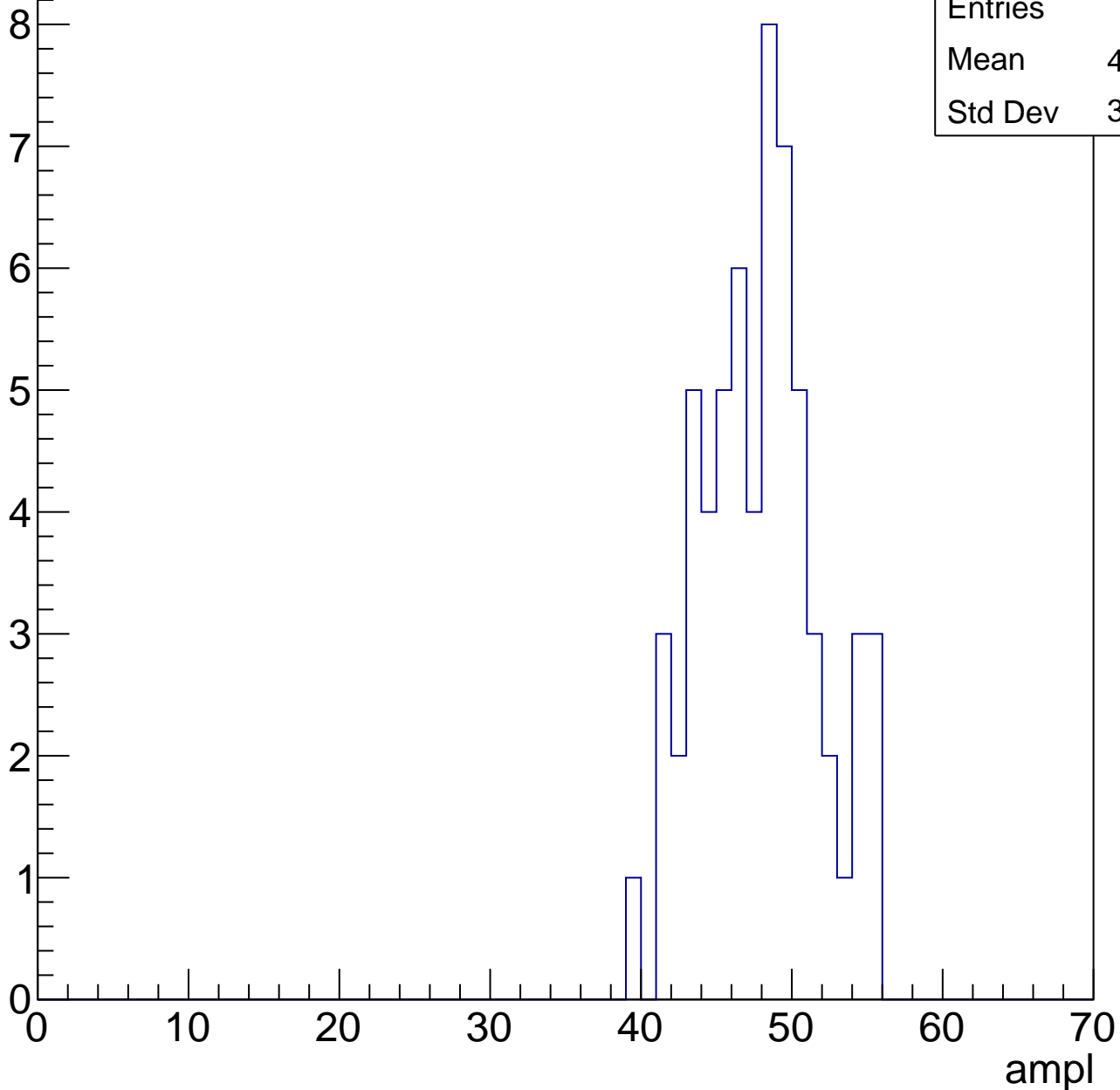


B1L101S, U1-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	47.42
Std Dev	3.825

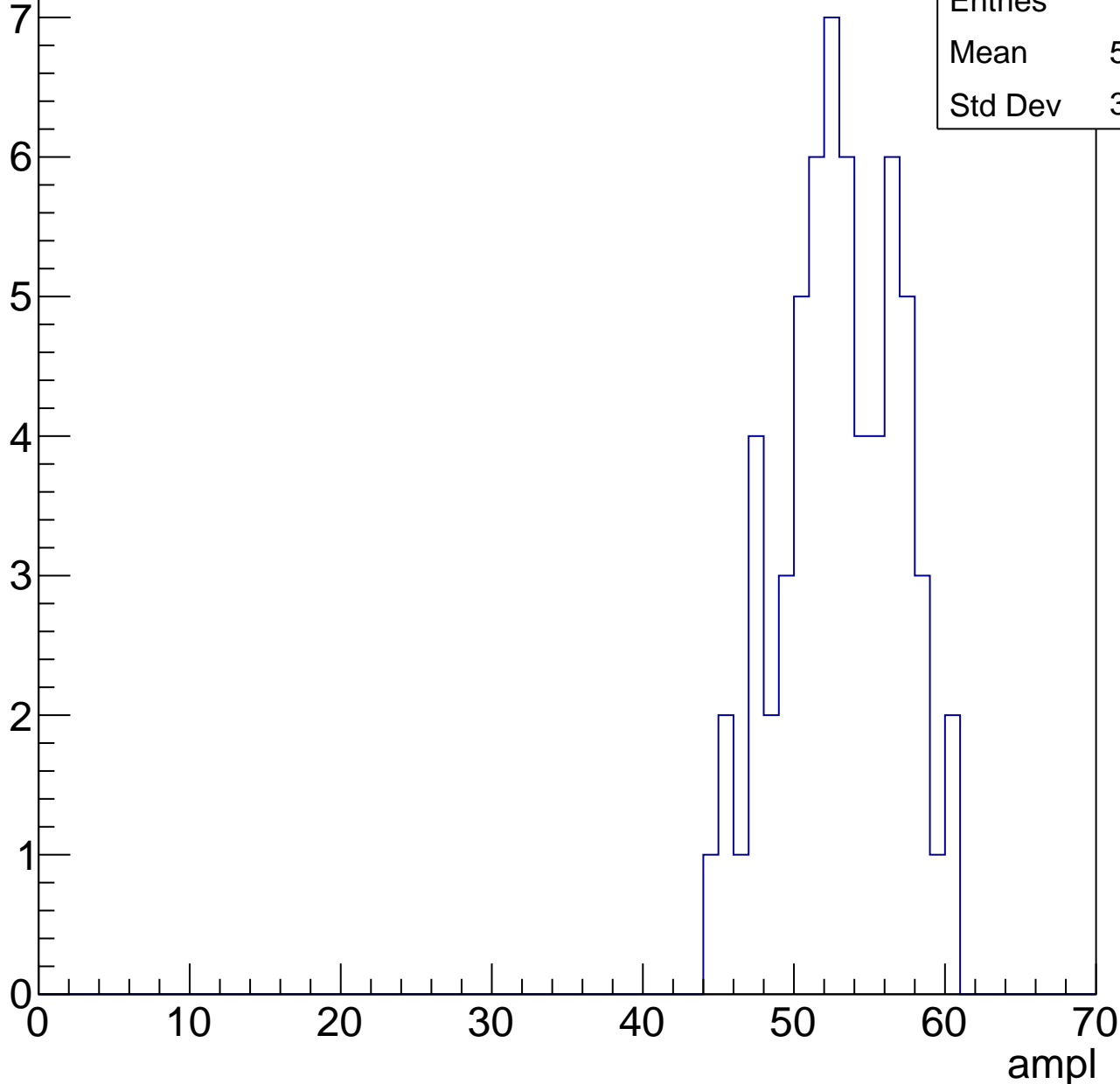


B1L101S, U1-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

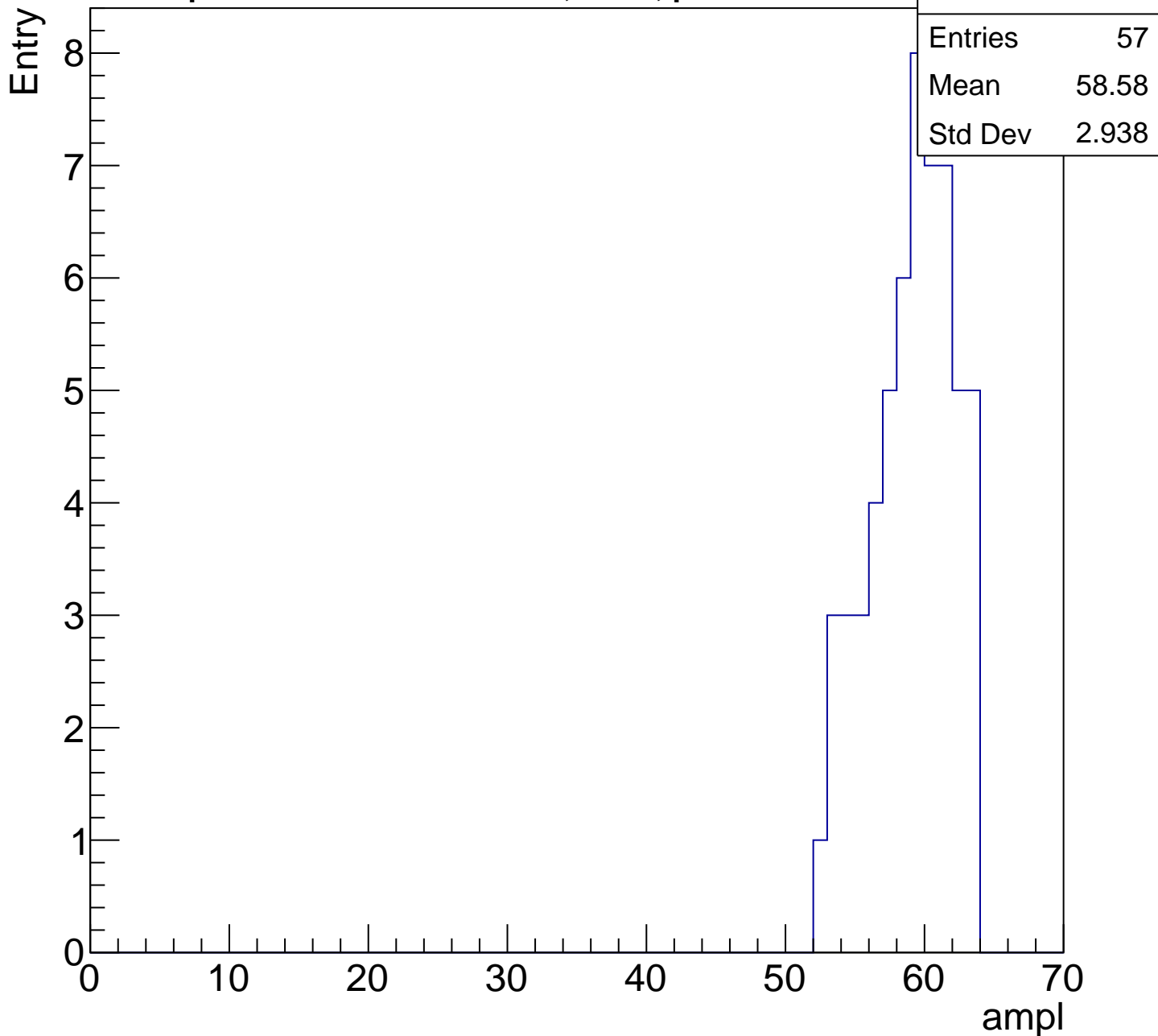
Entry

Entries	62
Mean	52.56
Std Dev	3.867



B1L101S, U1-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

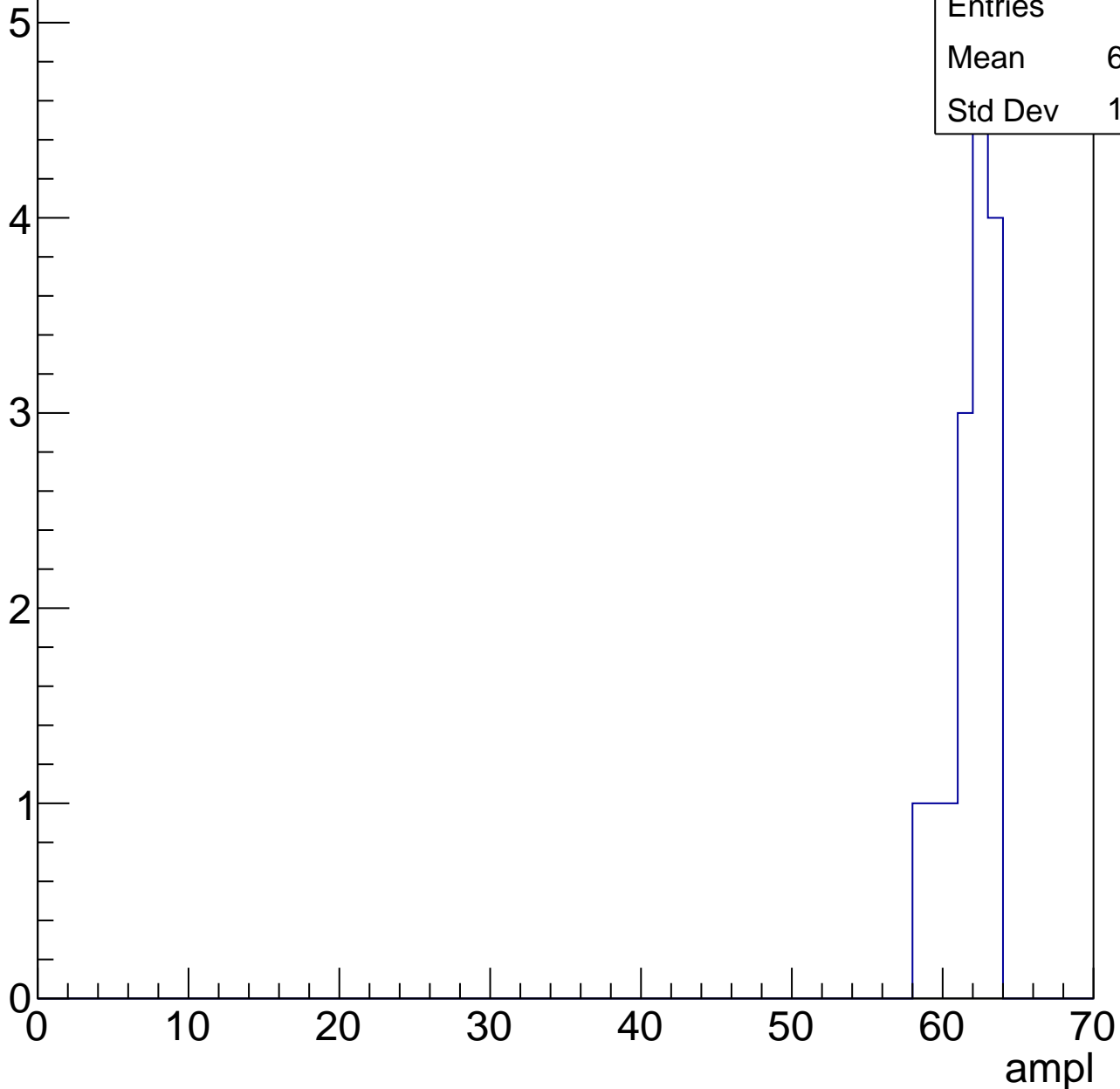


B1L101S, U1-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.47
Std Dev	1.454



B1L101S, U1-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch96, adc0

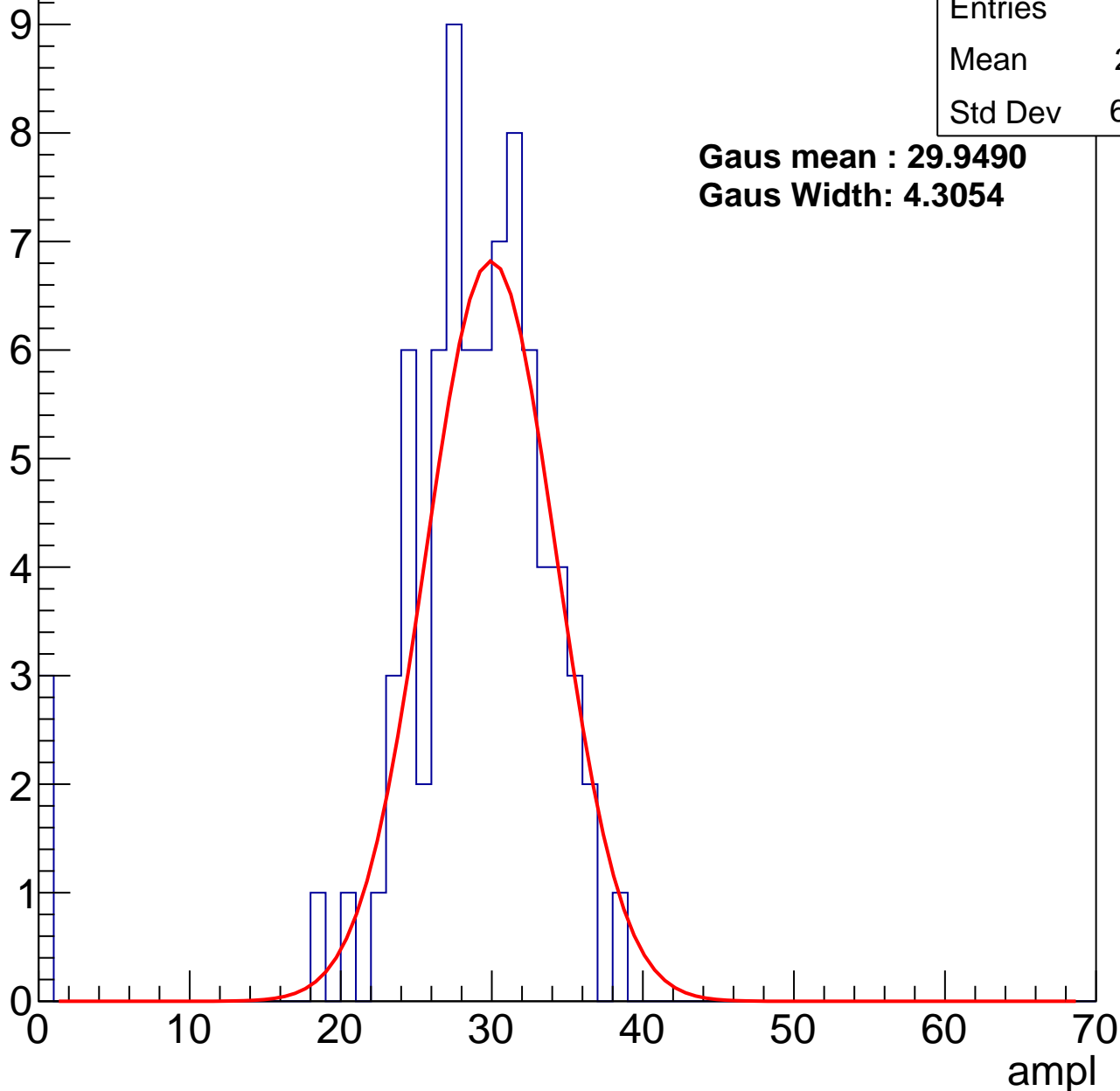
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	27.81
Std Dev	6.743

Gaus mean : 29.9490

Gaus Width: 4.3054



B1L101S, U1-ch96, adc1

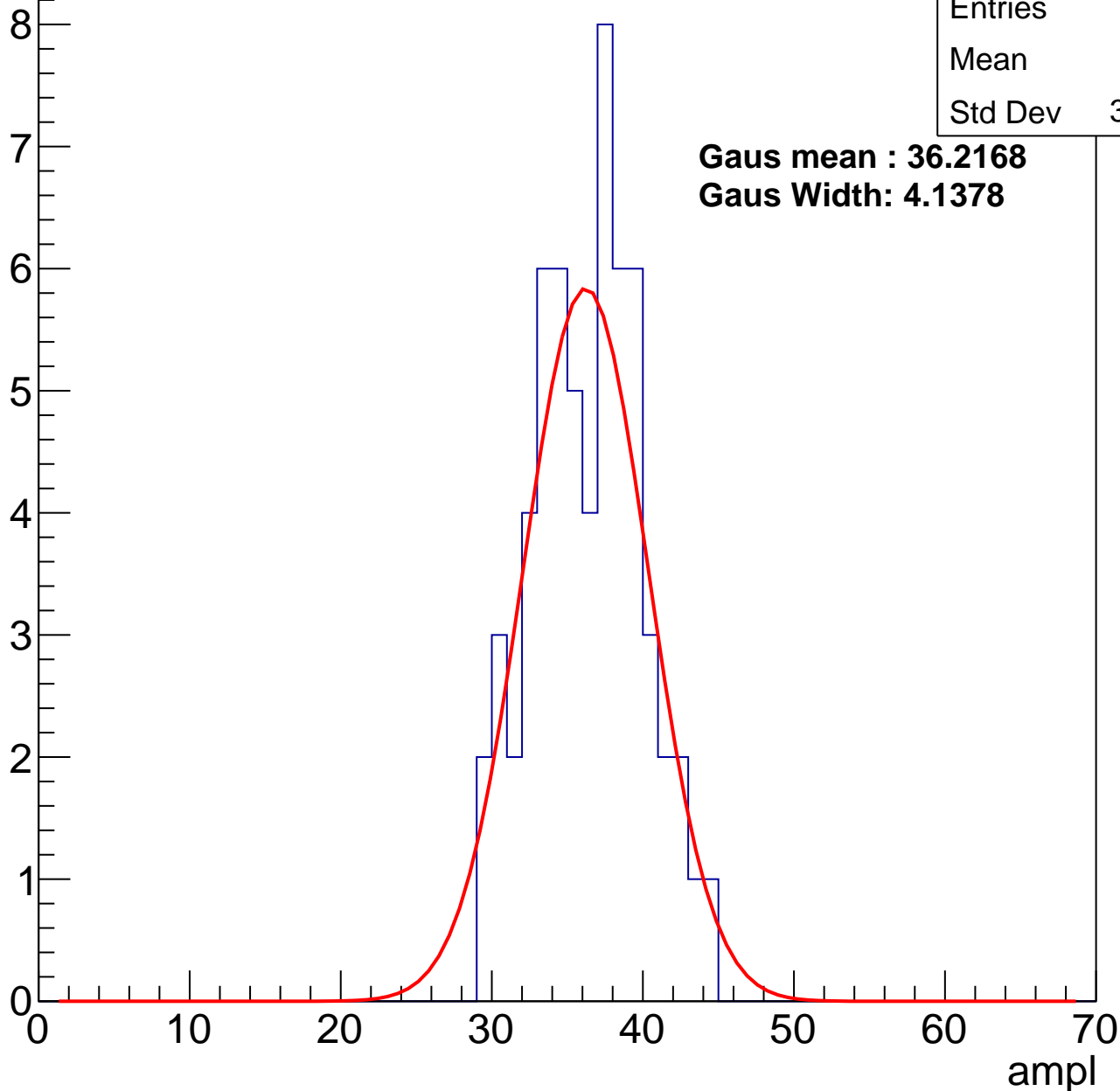
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.9
Std Dev	3.556

Gaus mean : 36.2168

Gaus Width: 4.1378



B1L101S, U1-ch96, adc2

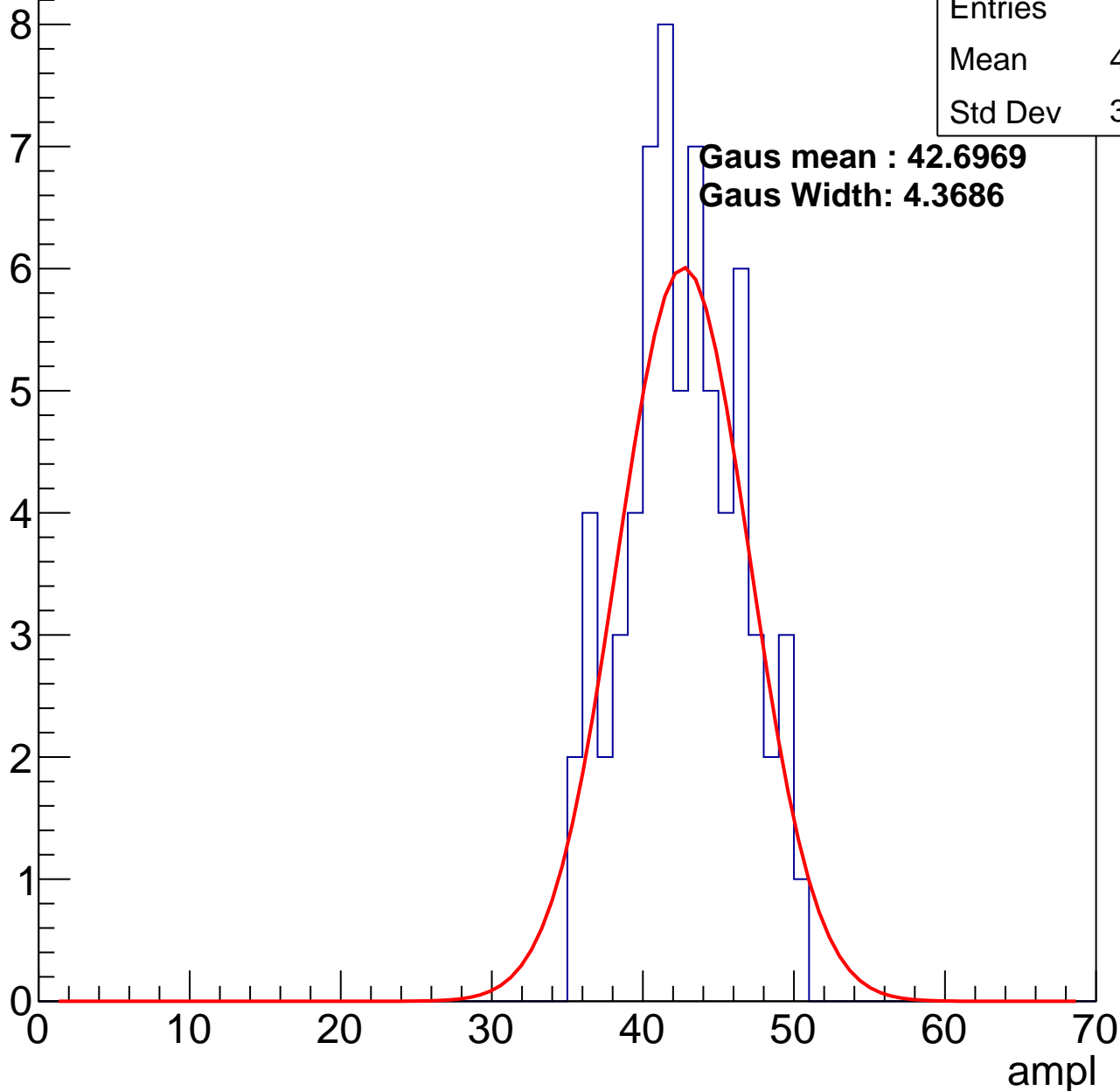
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.23
Std Dev	3.745

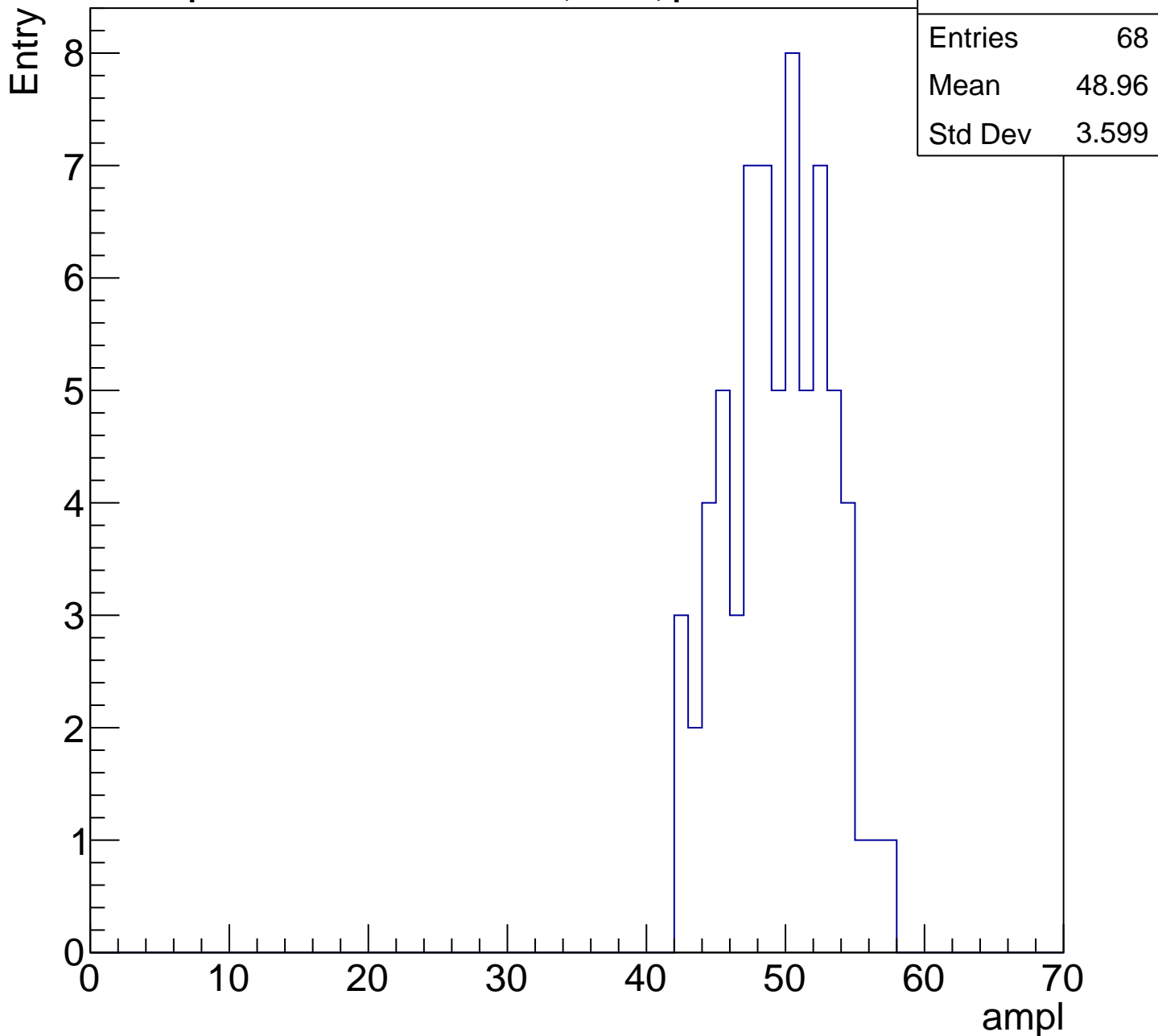
Gaus mean : 42.6969

Gaus Width: 4.3686



B1L101S, U1-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

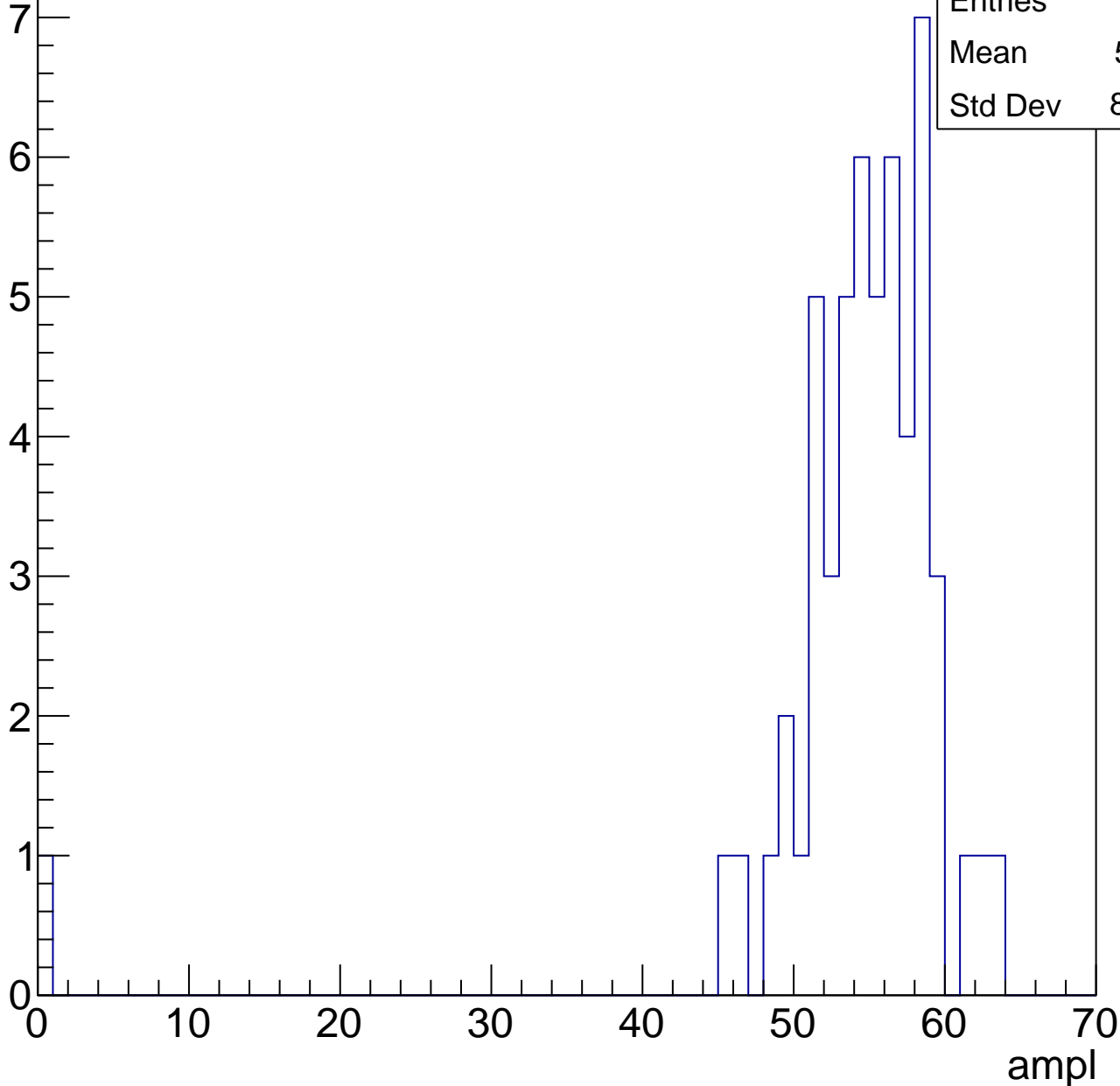


B1L101S, U1-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	53.61
Std Dev	8.236

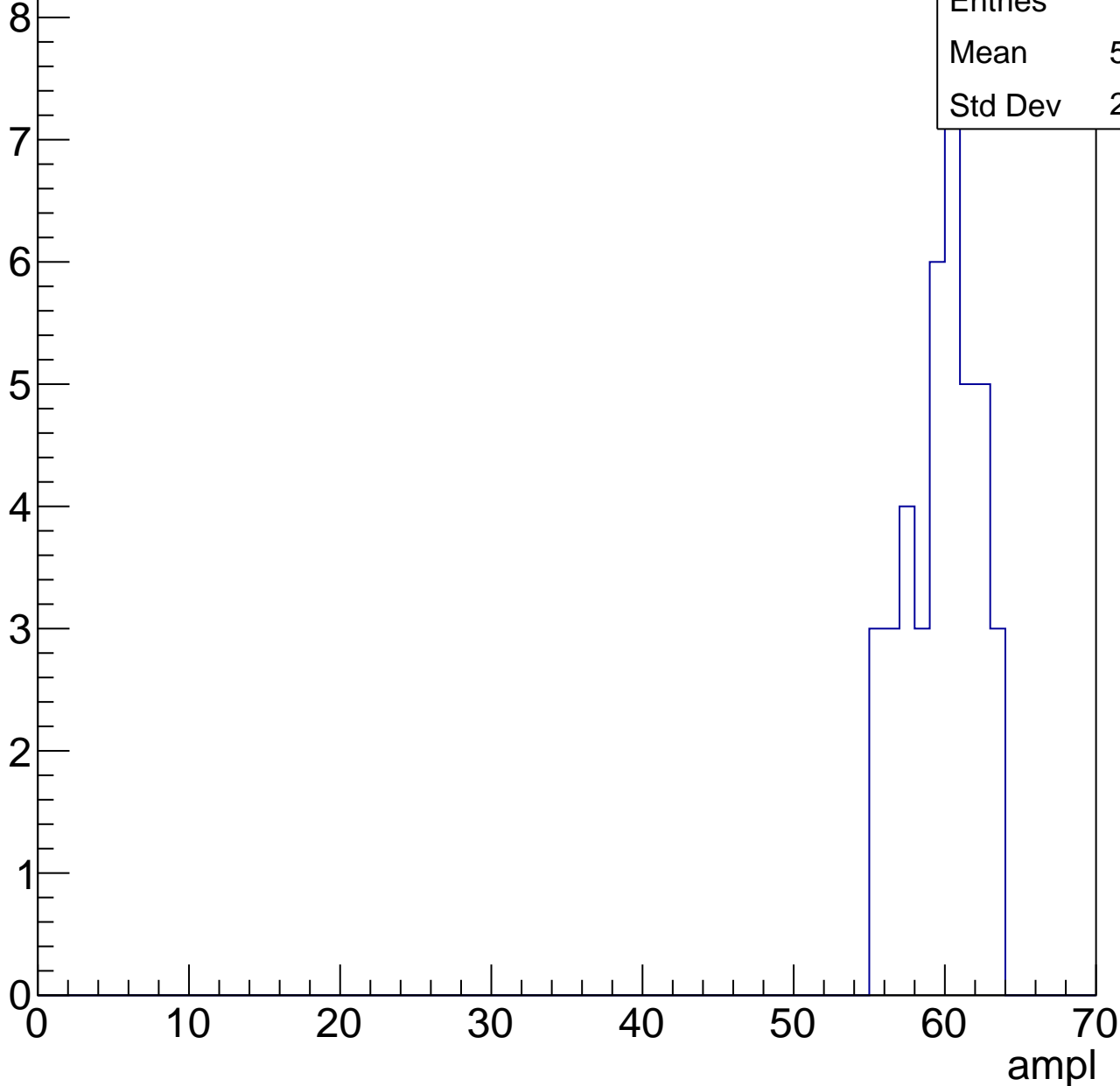


B1L101S, U1-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

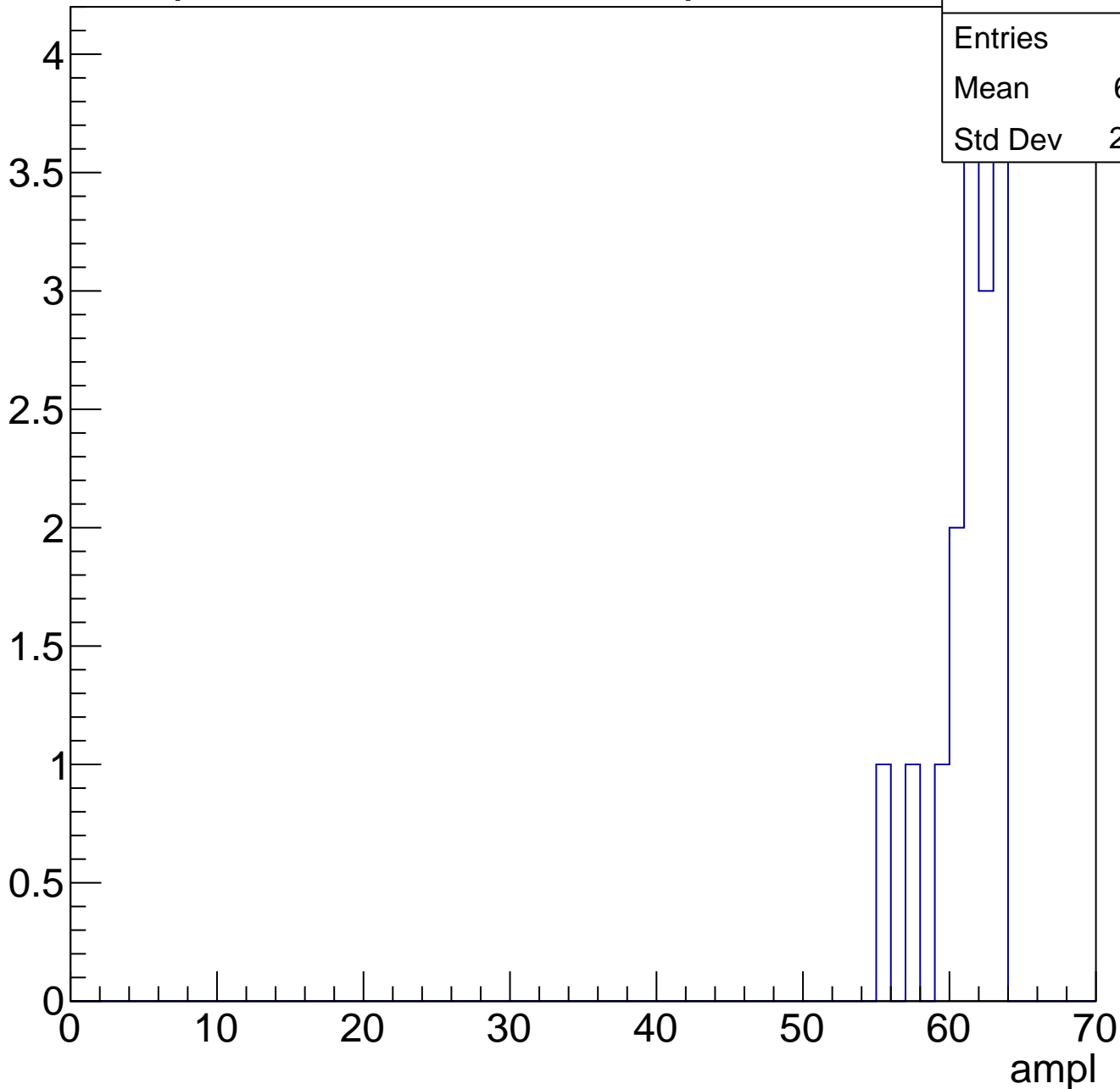
Entries	40
Mean	59.33
Std Dev	2.296



B1L101S, U1-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	16
Mean	60.81
Std Dev	2.186

B1L101S, U1-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch97, adc0

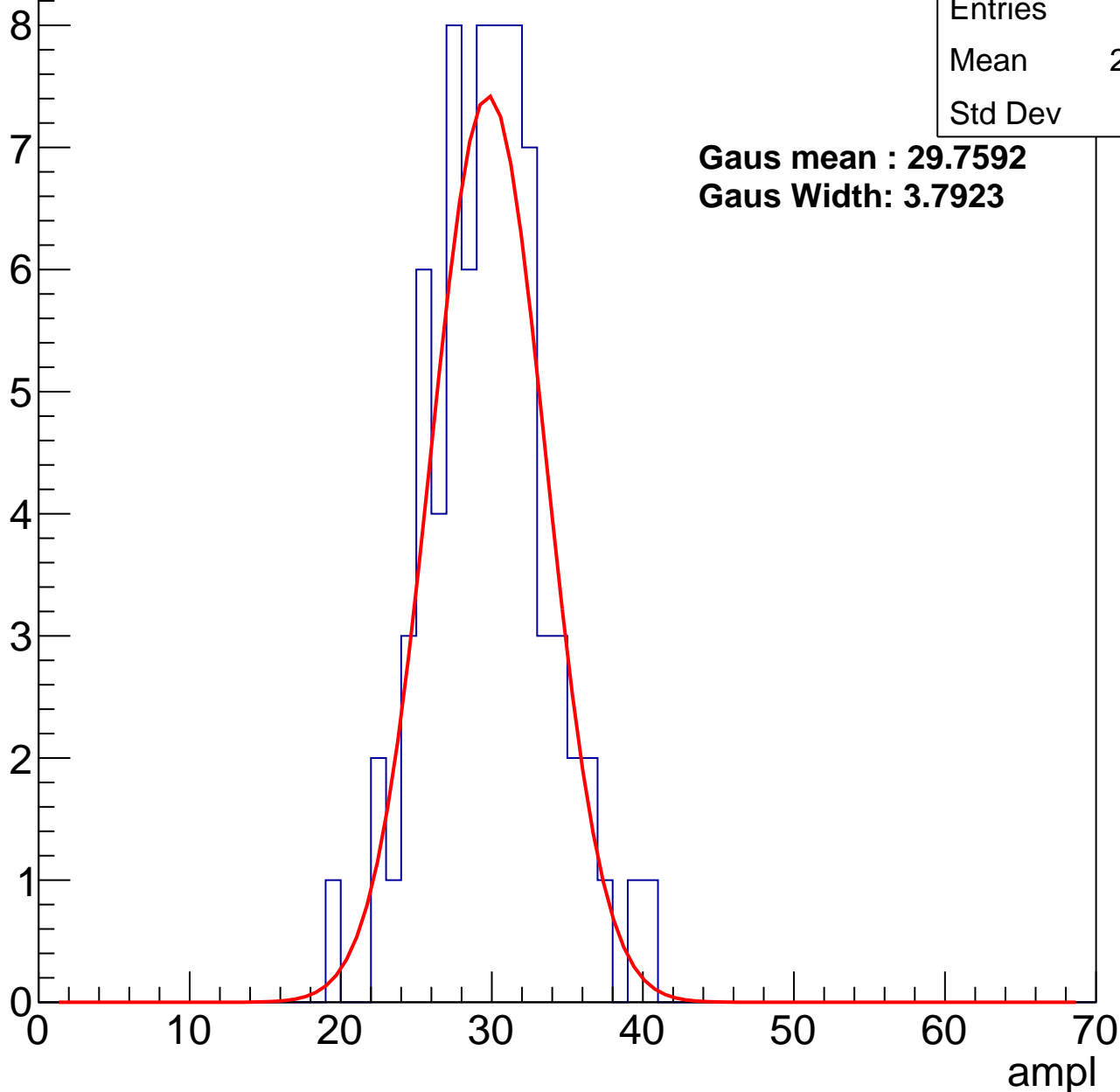
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.32
Std Dev	3.92

Gaus mean : 29.7592

Gaus Width: 3.7923



B1L101S, U1-ch97, adc1

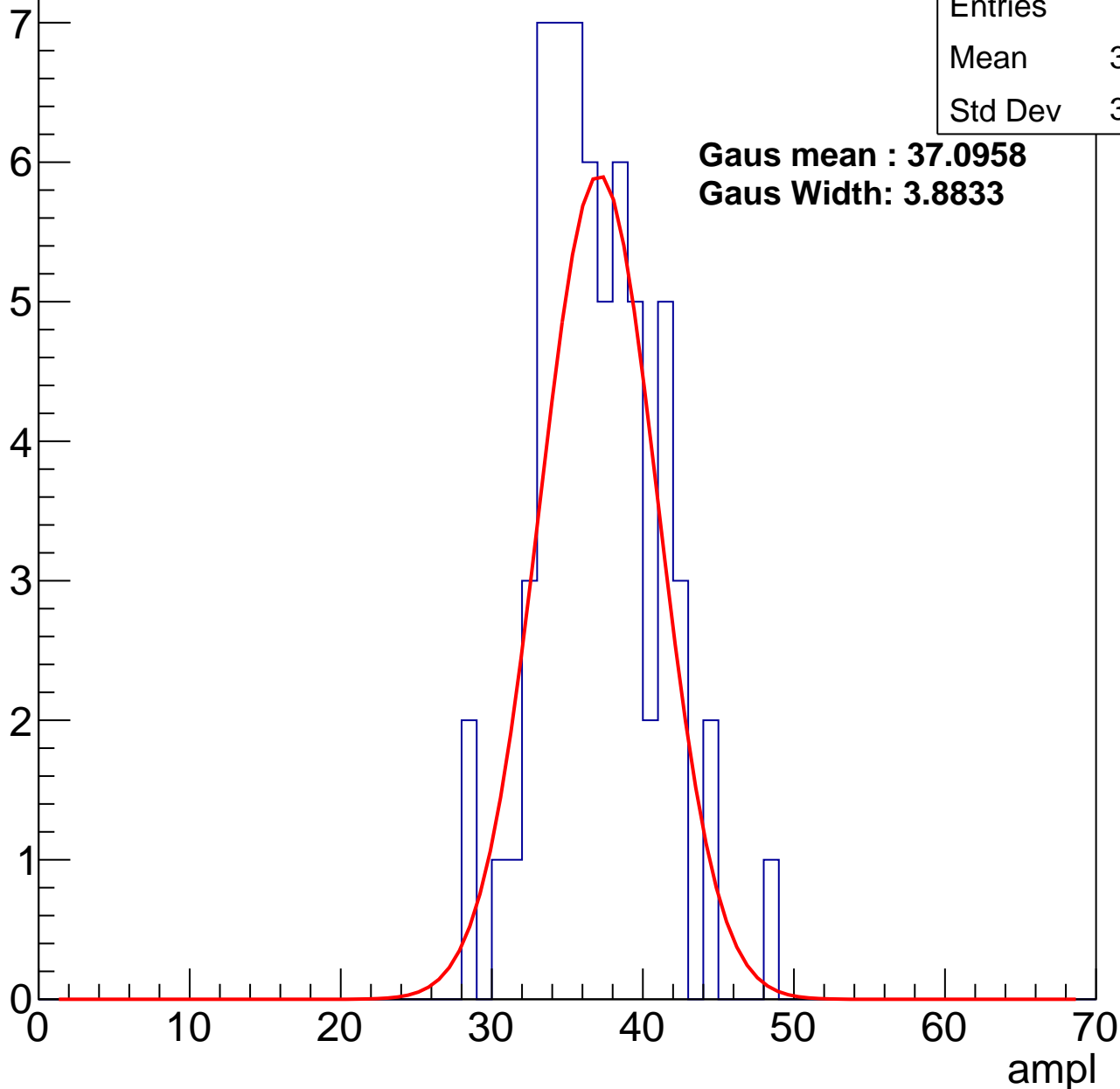
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.48
Std Dev	3.846

Gaus mean : 37.0958

Gaus Width: 3.8833



B1L101S, U1-ch97, adc2

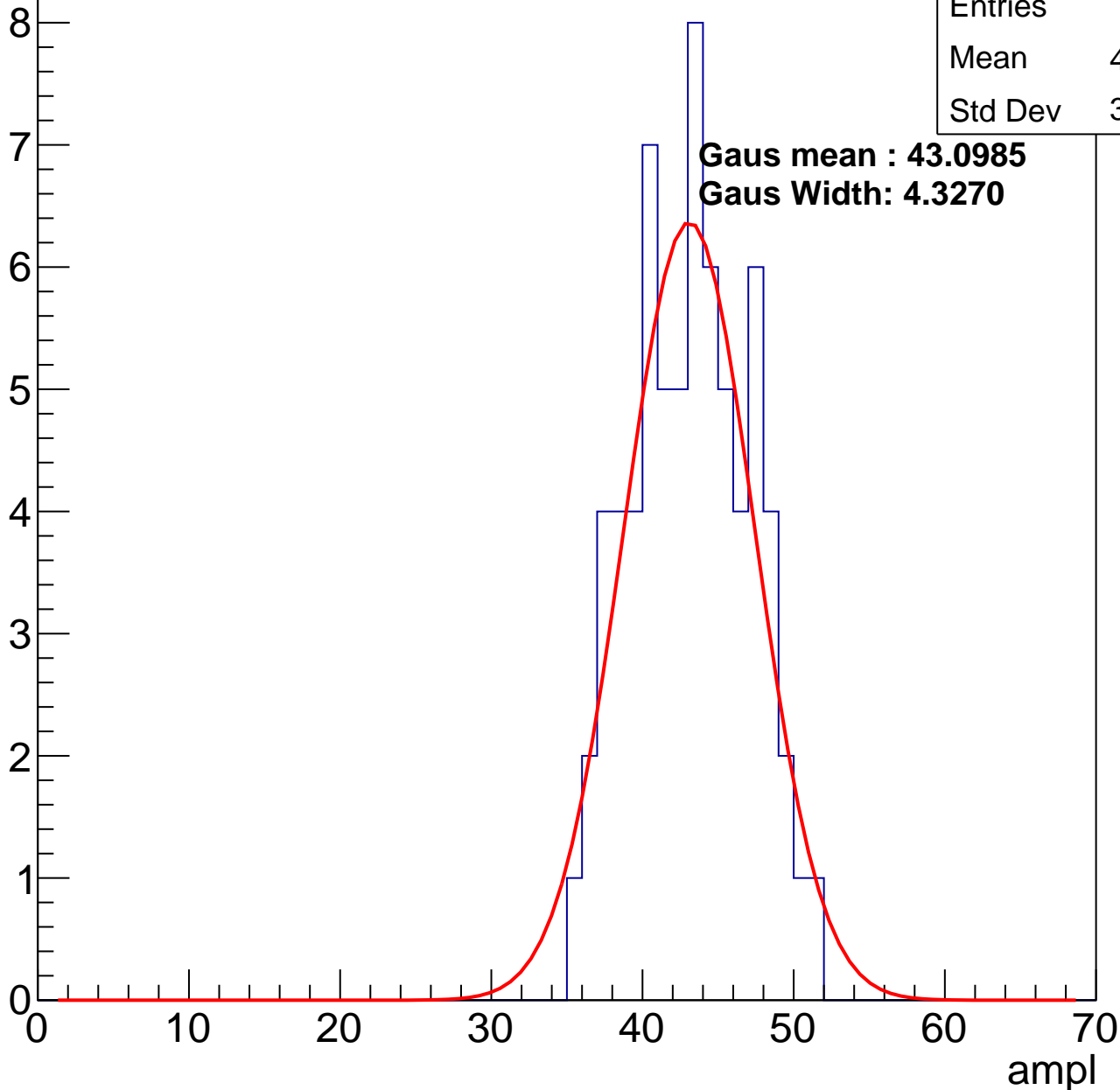
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.72
Std Dev	3.799

Gaus mean : 43.0985

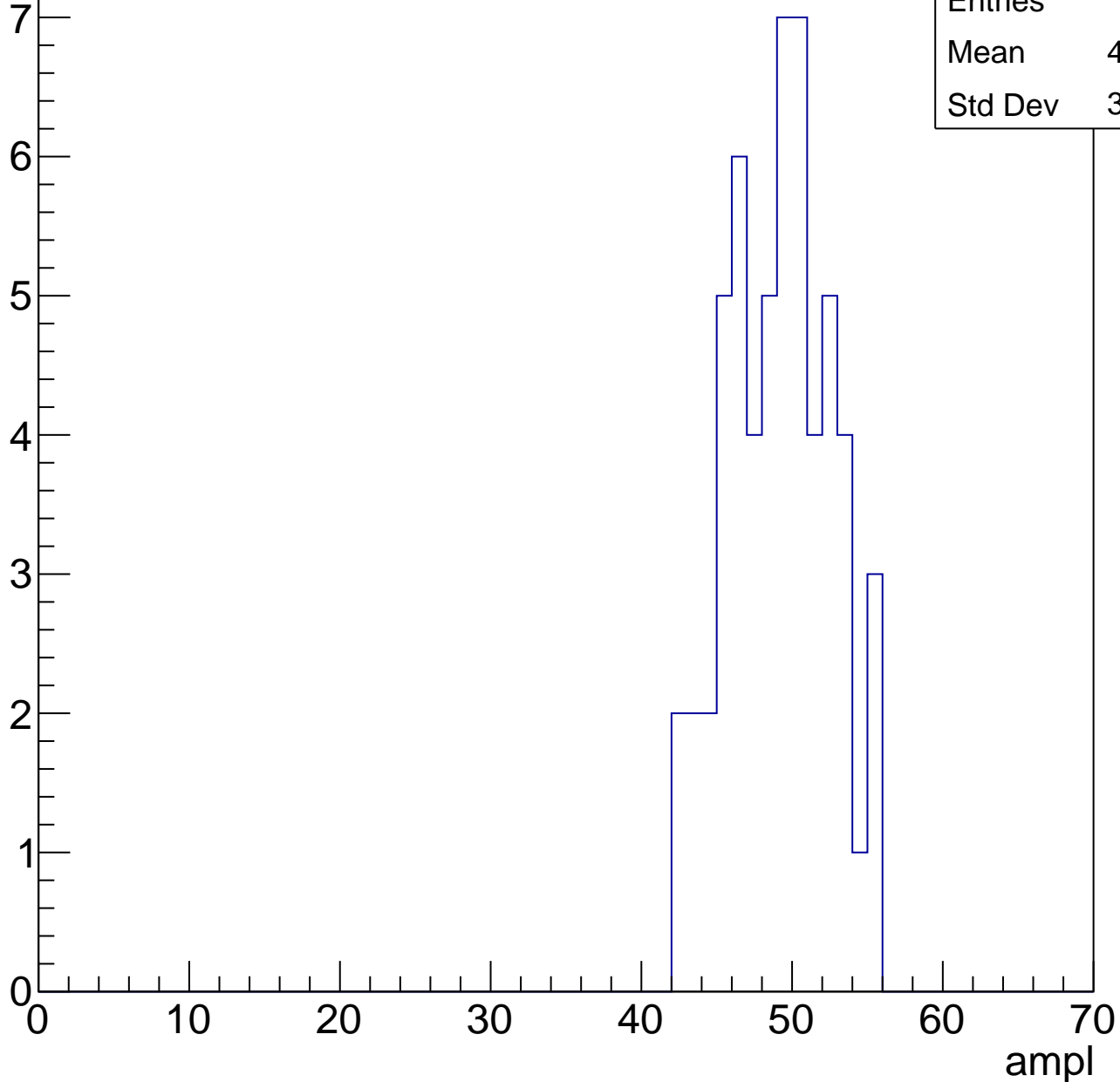
Gaus Width: 4.3270



B1L101S, U1-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

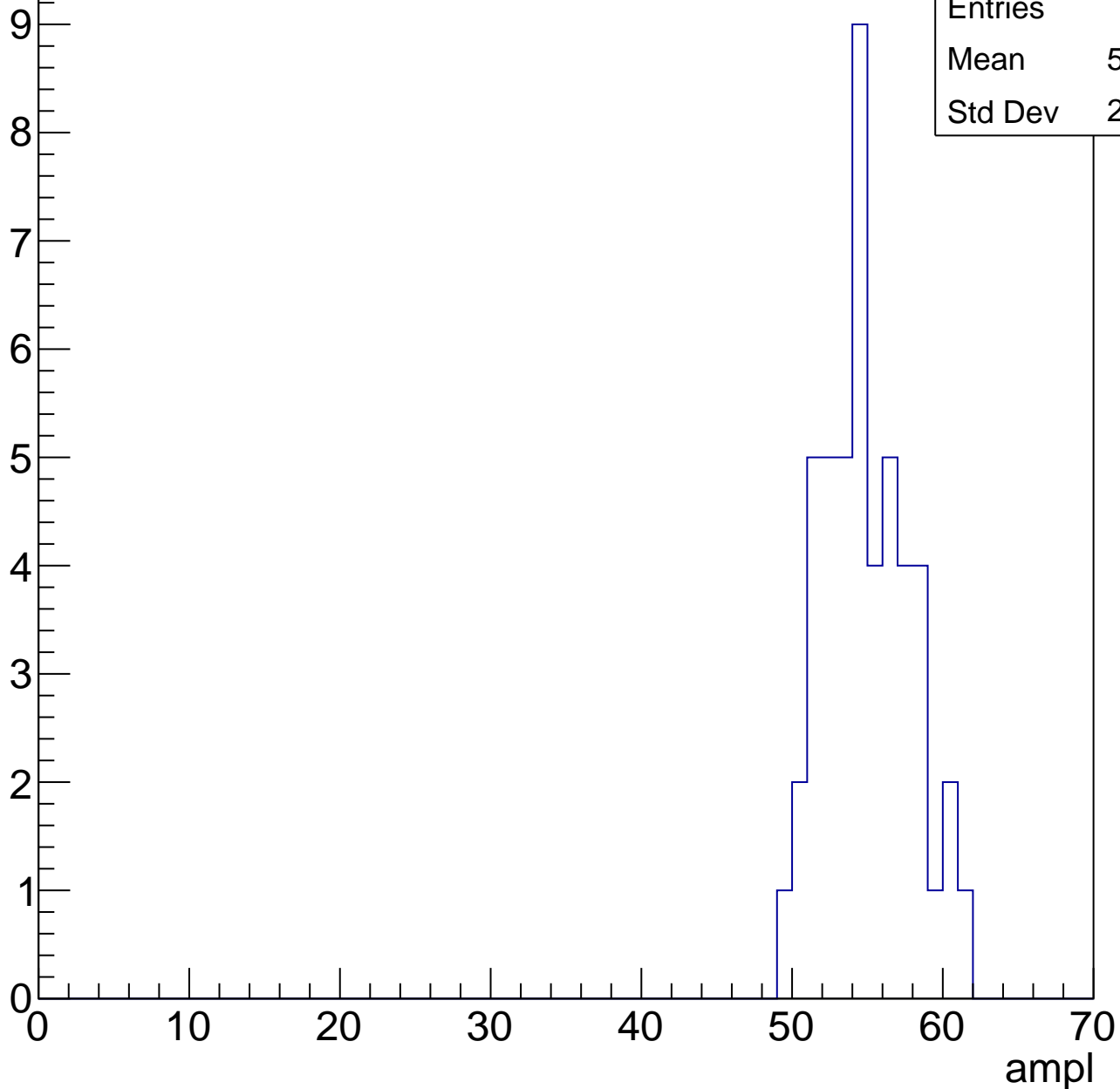


Entries	57
Mean	48.68
Std Dev	3.336

B1L101S, U1-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

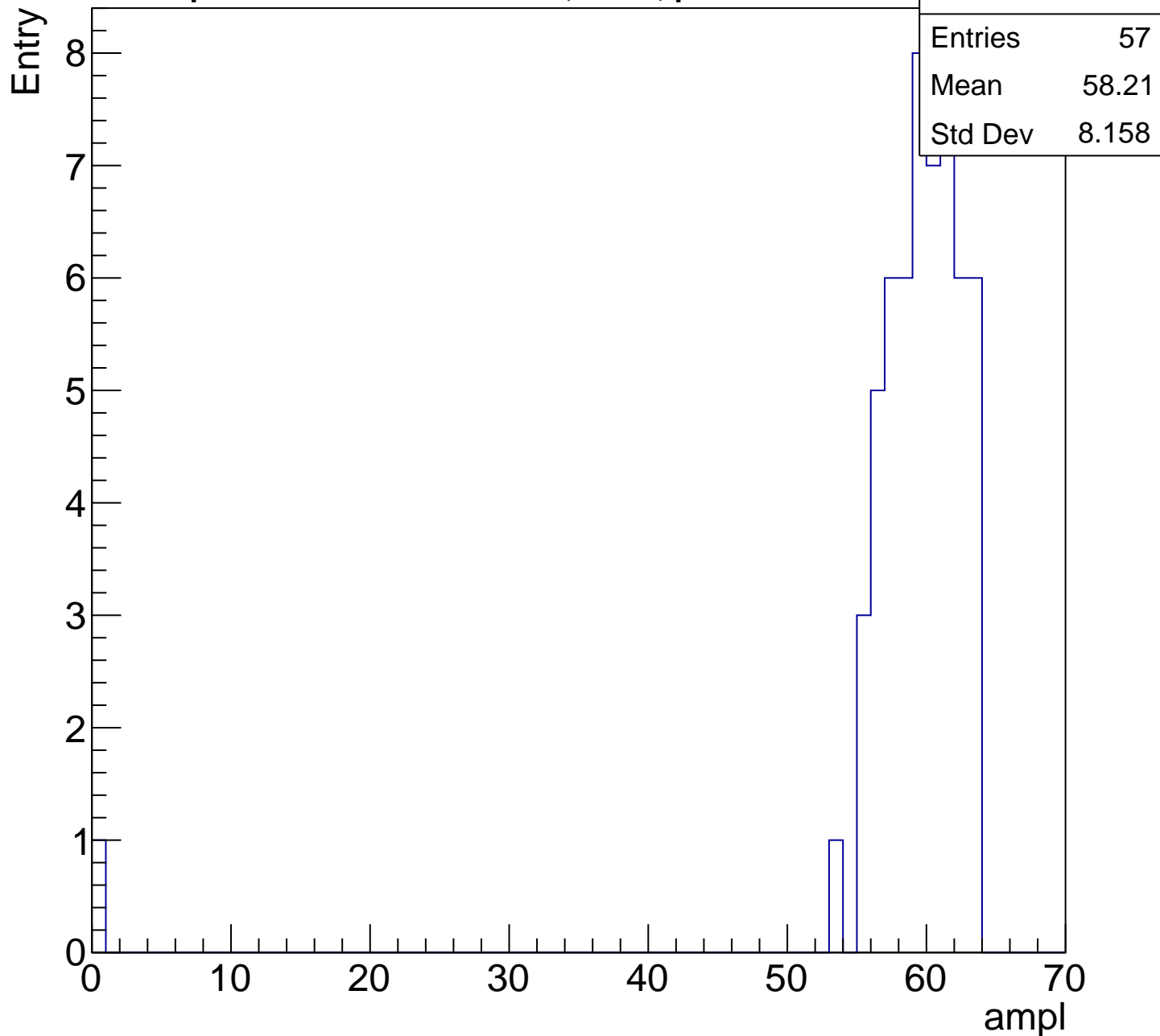
Entry



Entries	48
Mean	54.48
Std Dev	2.836

B1L101S, U1-ch97, adc5

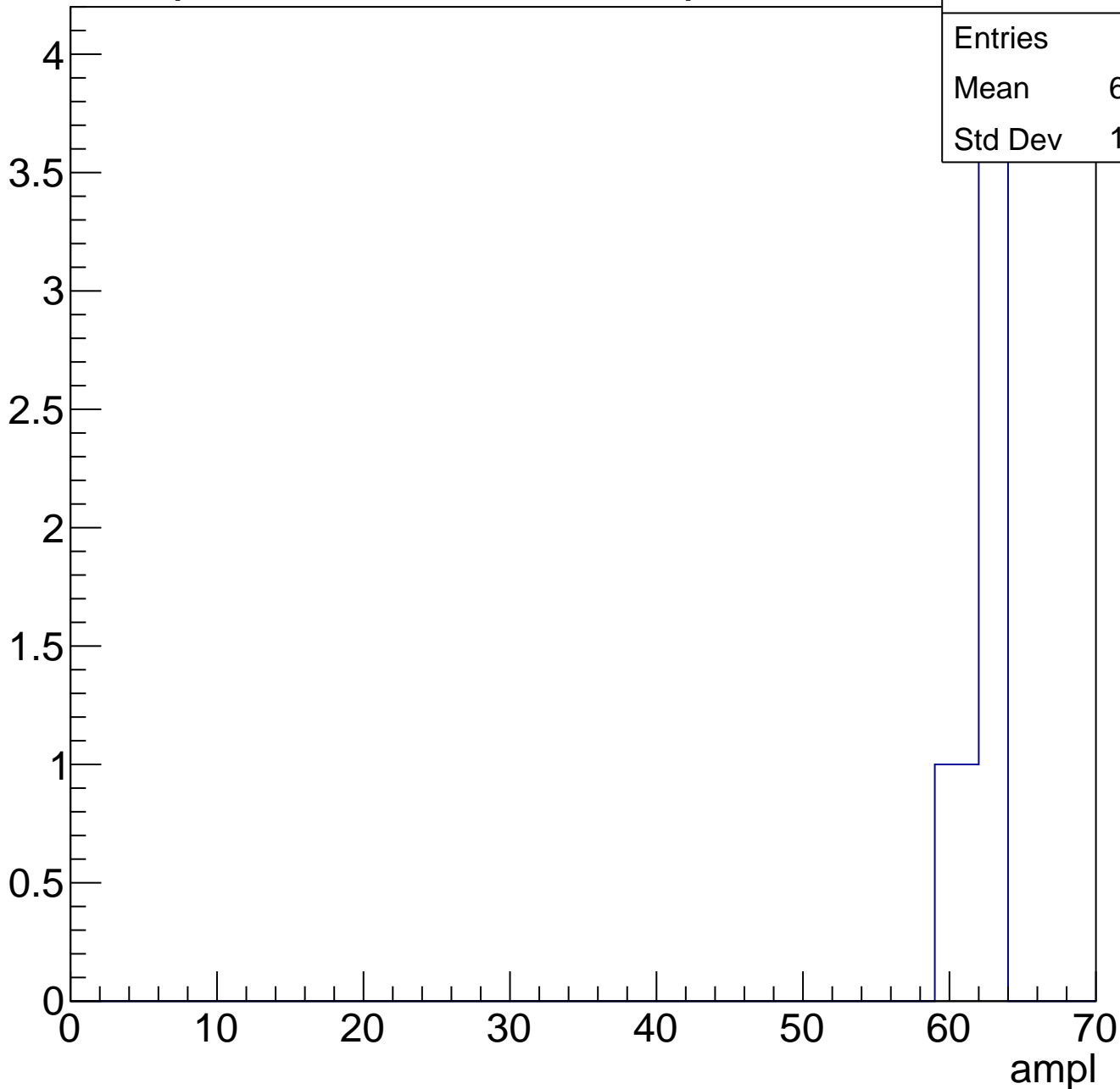
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch98, adc0

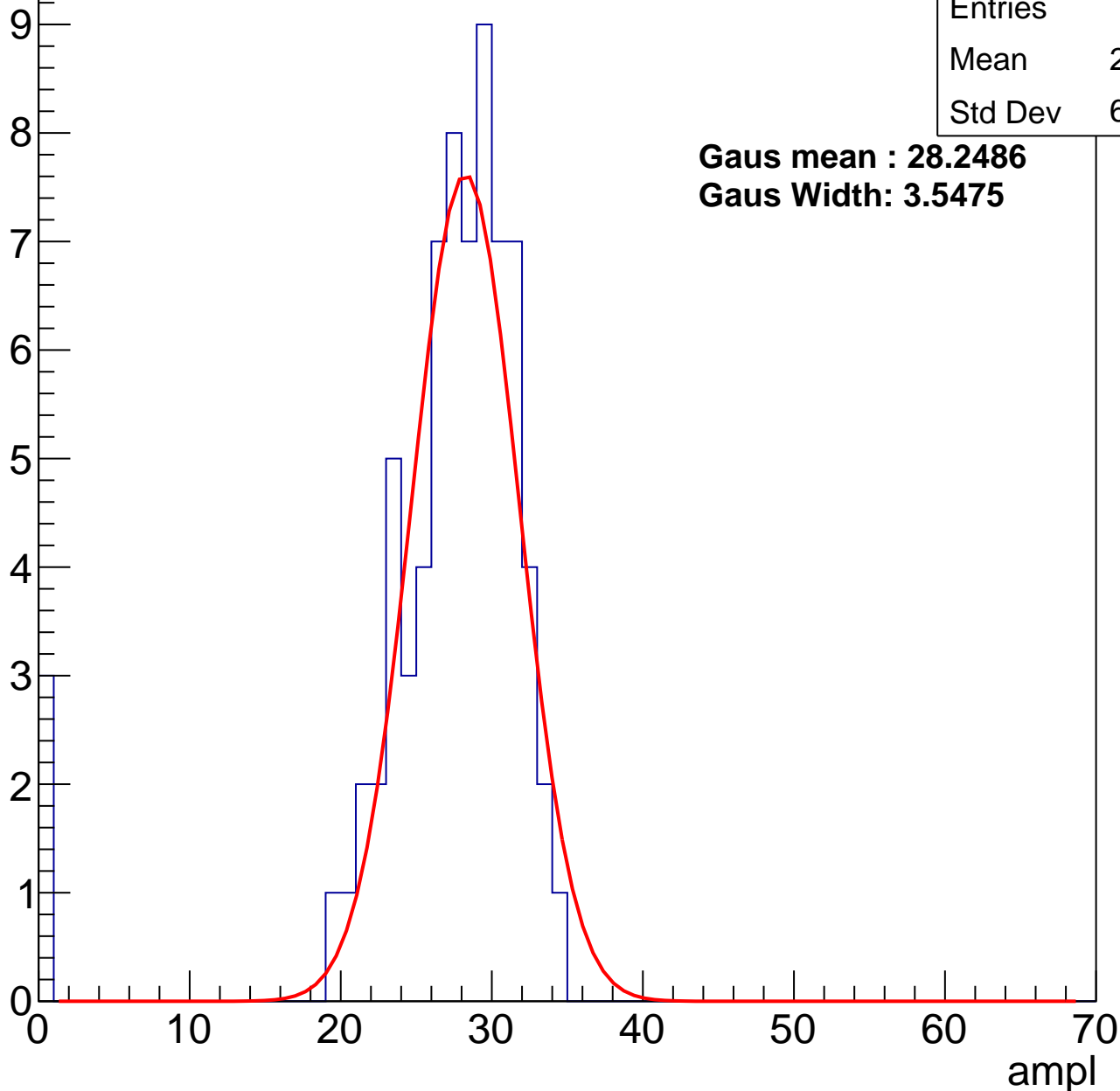
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	26.33
Std Dev	6.366

Gaus mean : 28.2486

Gaus Width: 3.5475



B1L101S, U1-ch98, adc1

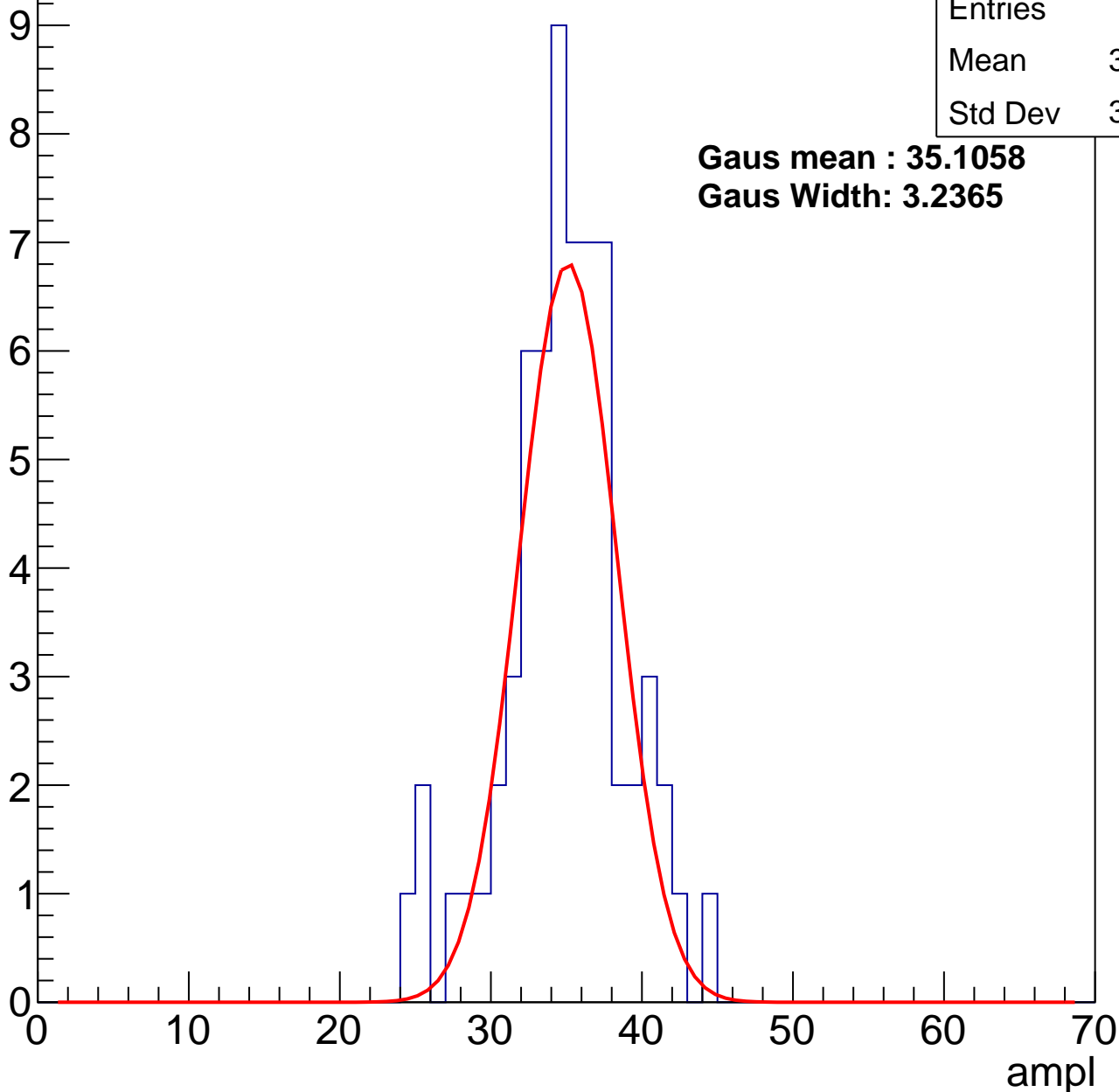
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	34.45
Std Dev	3.949

Gaus mean : 35.1058

Gaus Width: 3.2365



B1L101S, U1-ch98, adc2

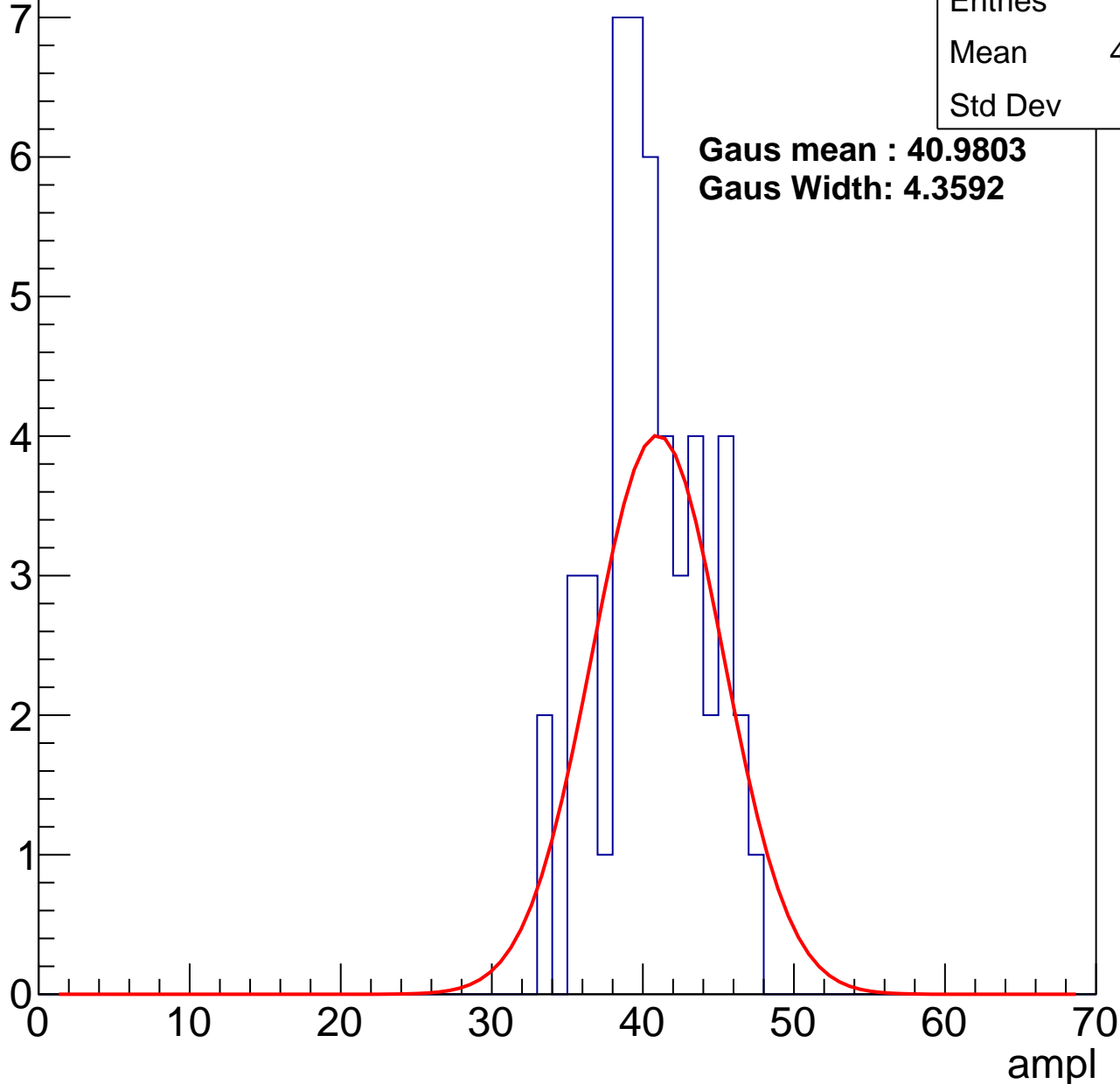
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	40.08
Std Dev	3.41

Gaus mean : 40.9803

Gaus Width: 4.3592



B1L101S, U1-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

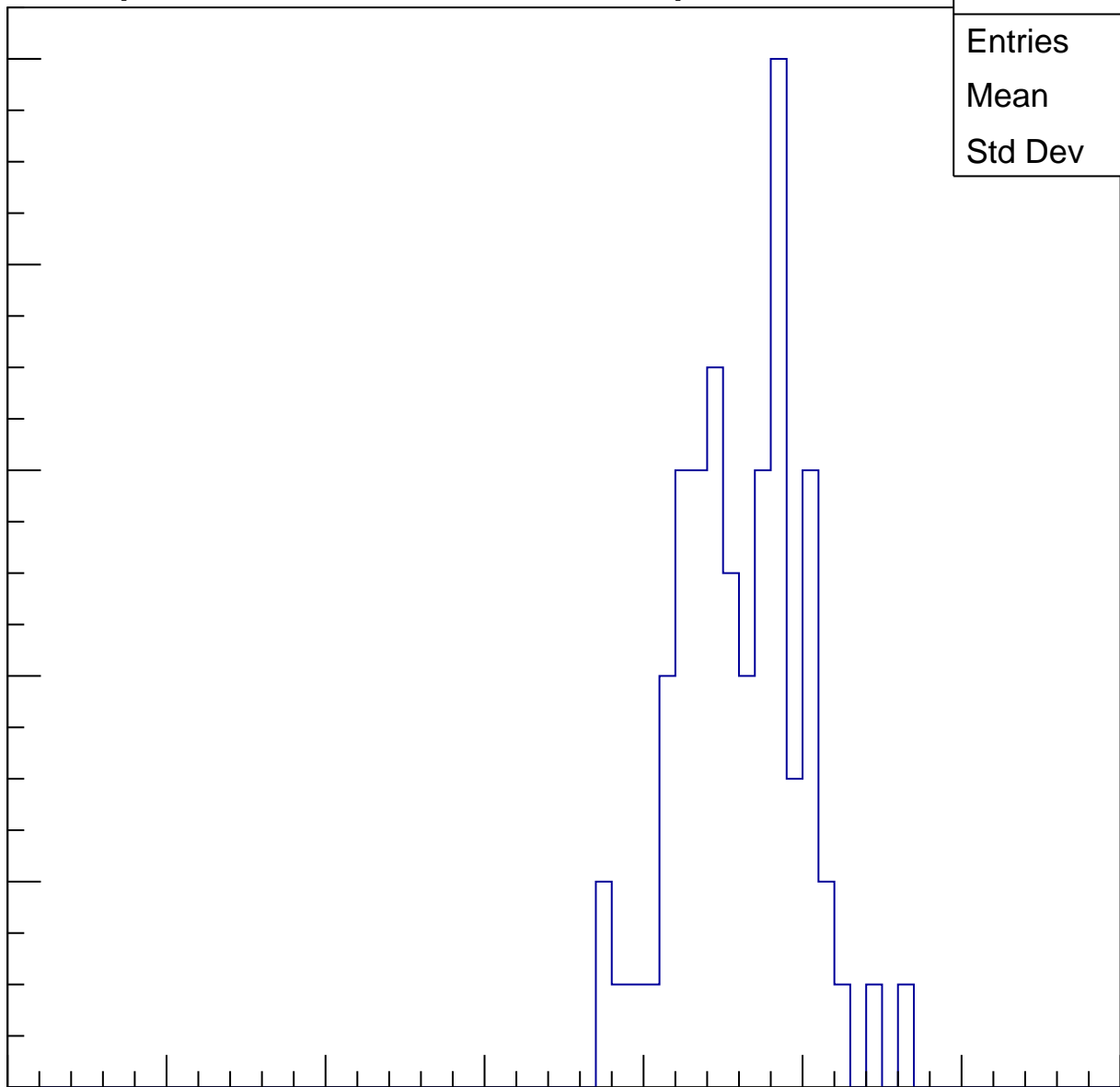
Entries	67
Mean	45.6
Std Dev	3.867

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

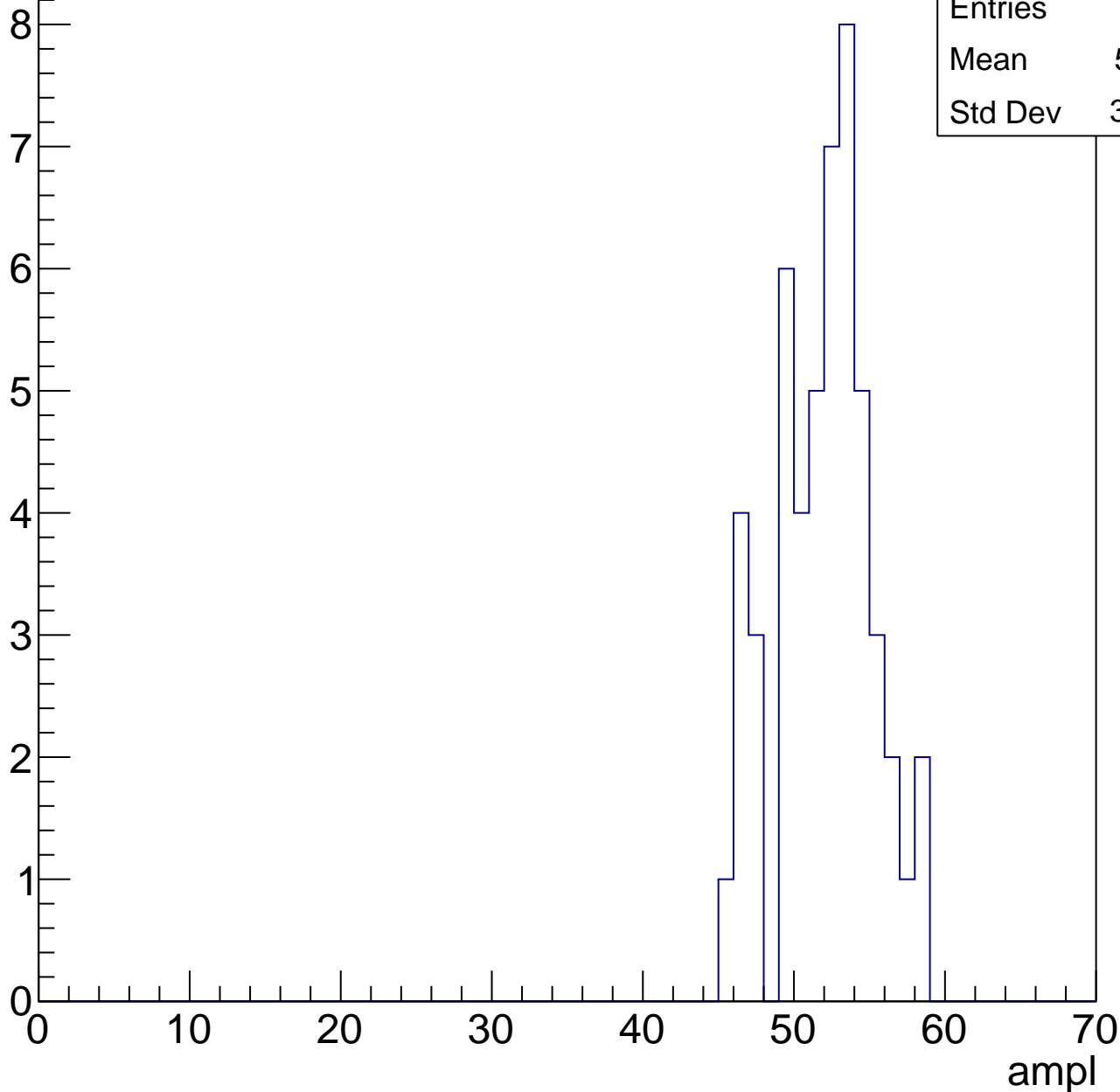


B1L101S, U1-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	51.51
Std Dev	3.177



B1L101S, U1-ch98, adc5

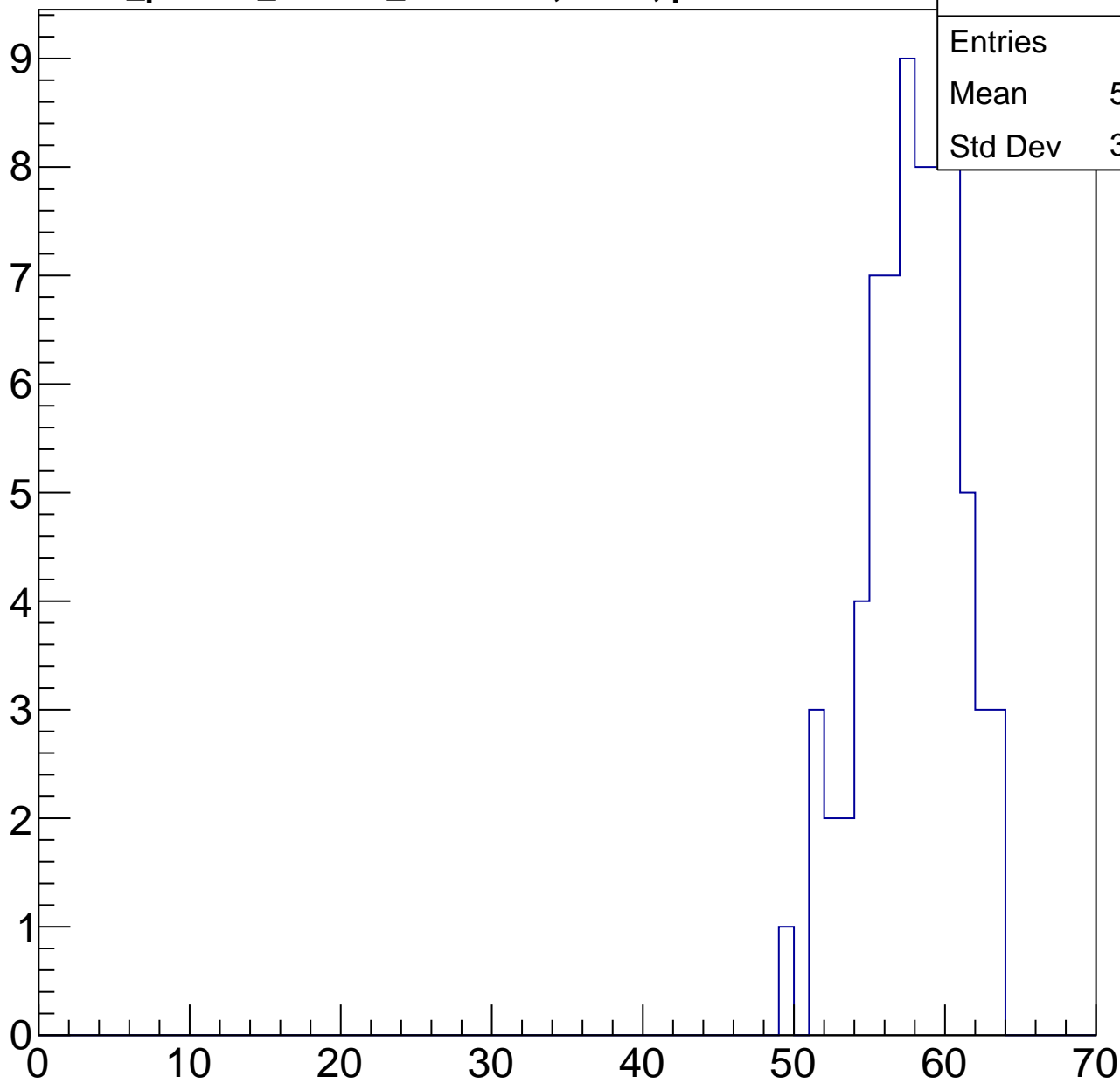
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

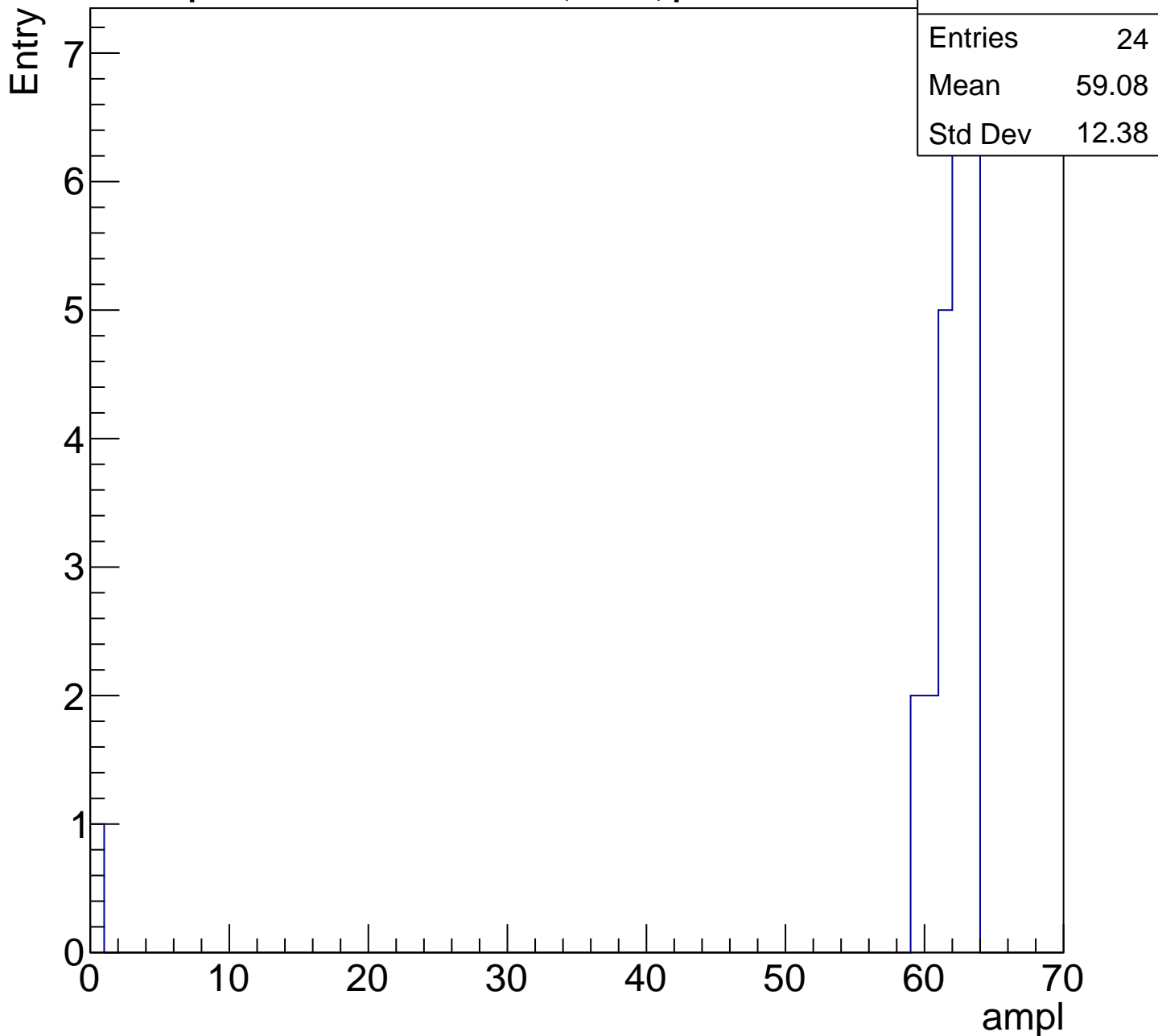
Entries	70
Mean	57.34
Std Dev	3.144

ampl



B1L101S, U1-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch99, adc0

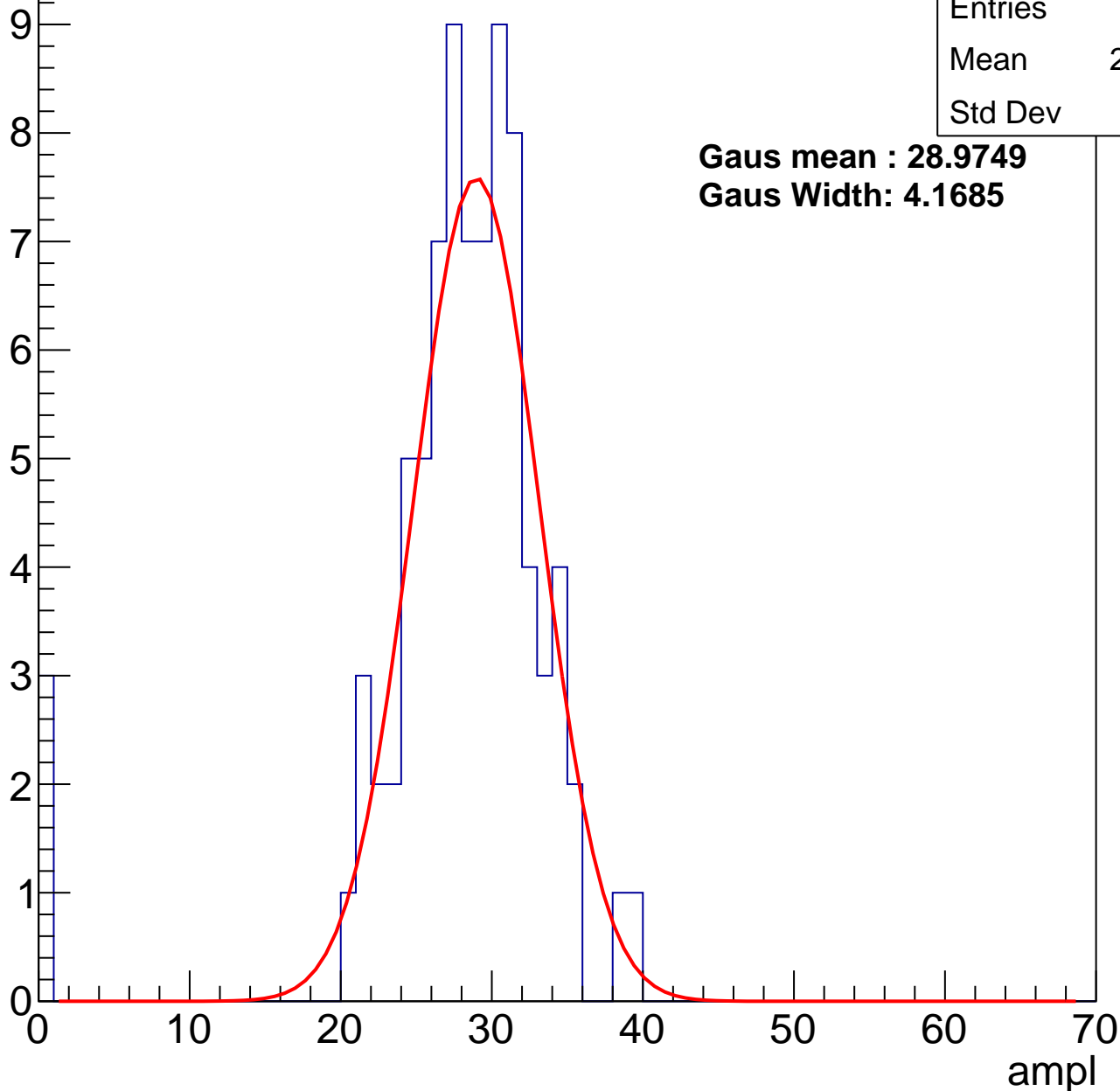
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	27.35
Std Dev	6.52

Gaus mean : 28.9749

Gaus Width: 4.1685



B1L101S, U1-ch99, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	35.72
Std Dev	3.521

Gaus mean : 36.0407

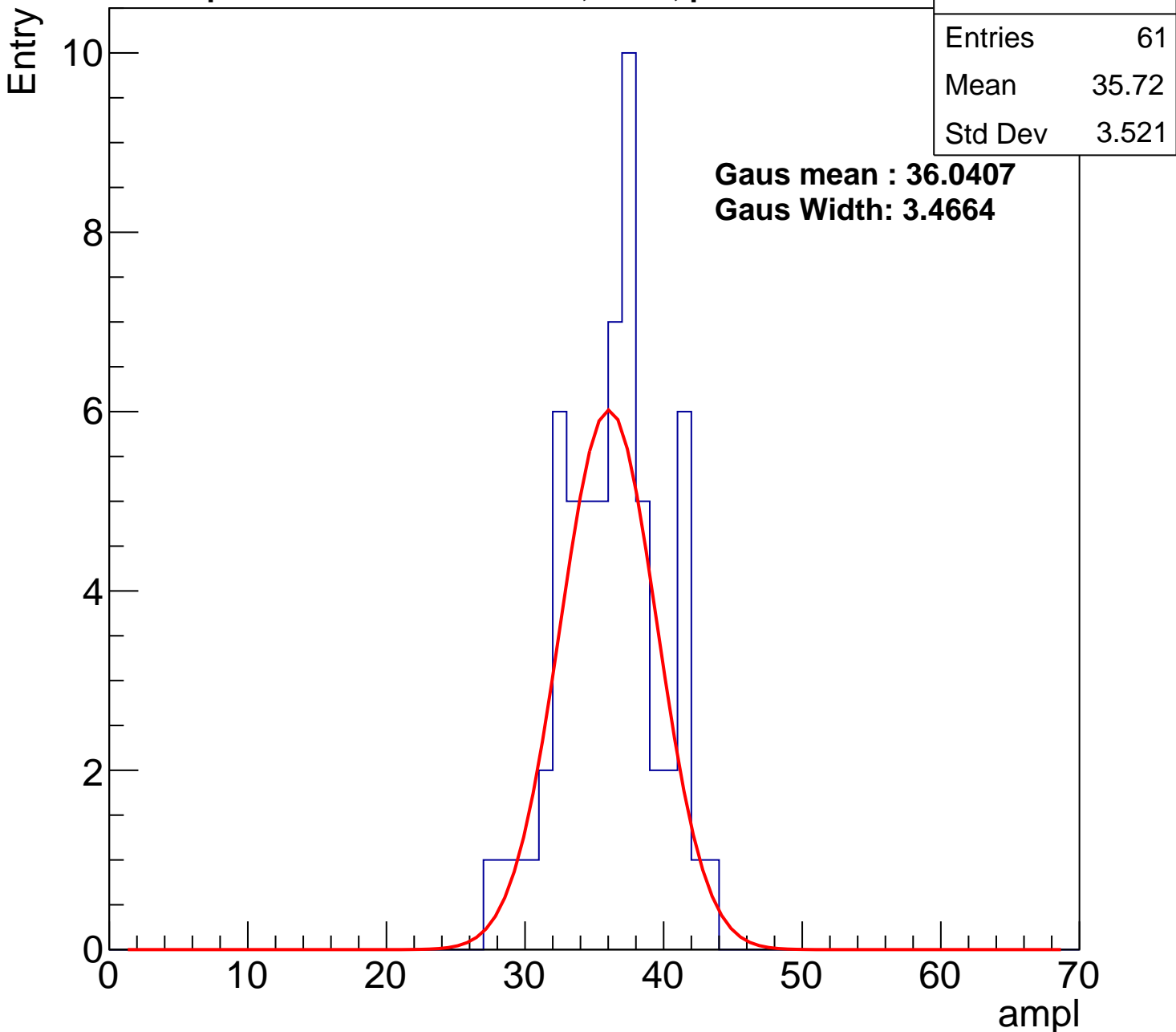
Gaus Width: 3.4664

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch99, adc2

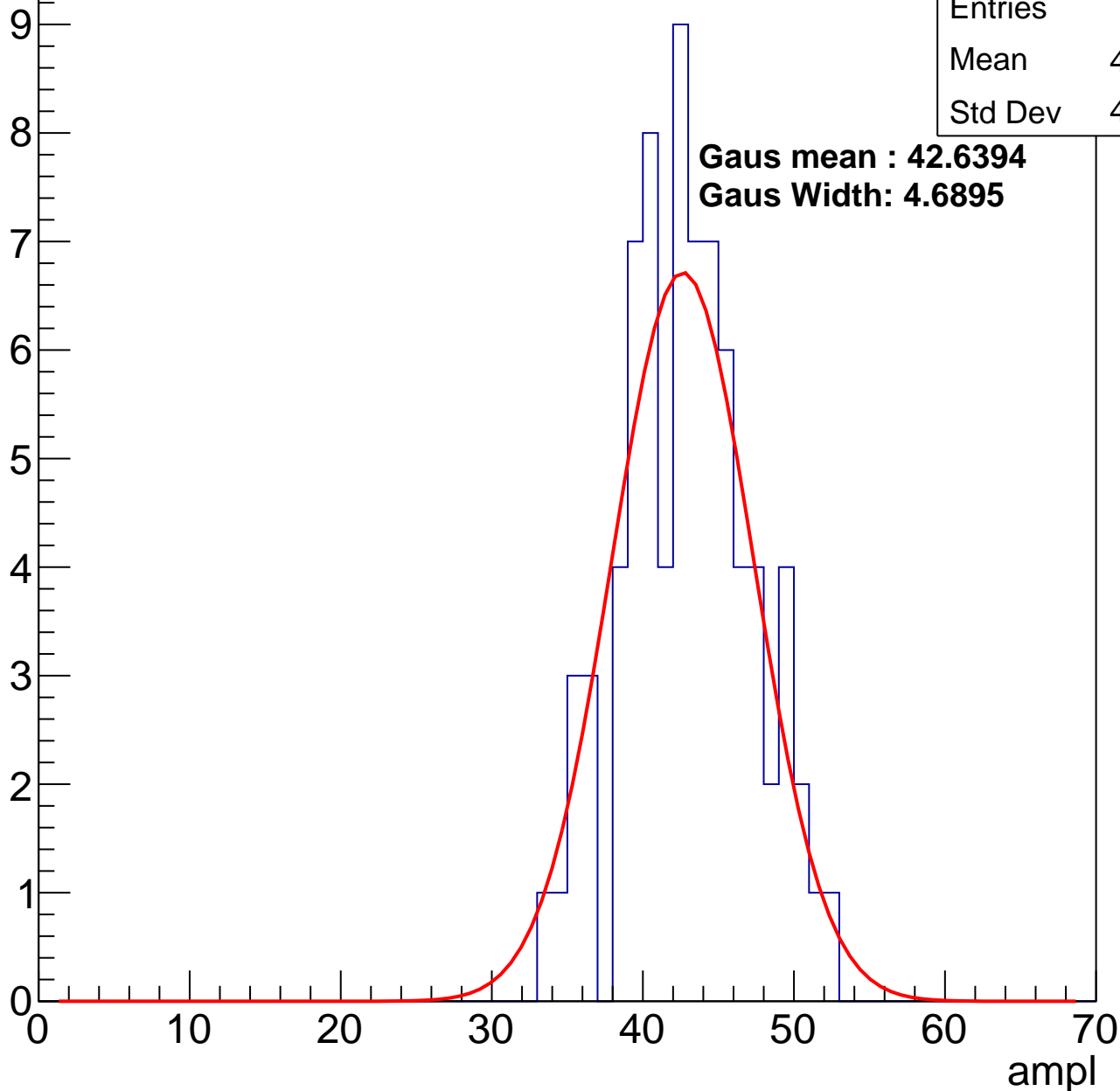
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.47
Std Dev	4.199

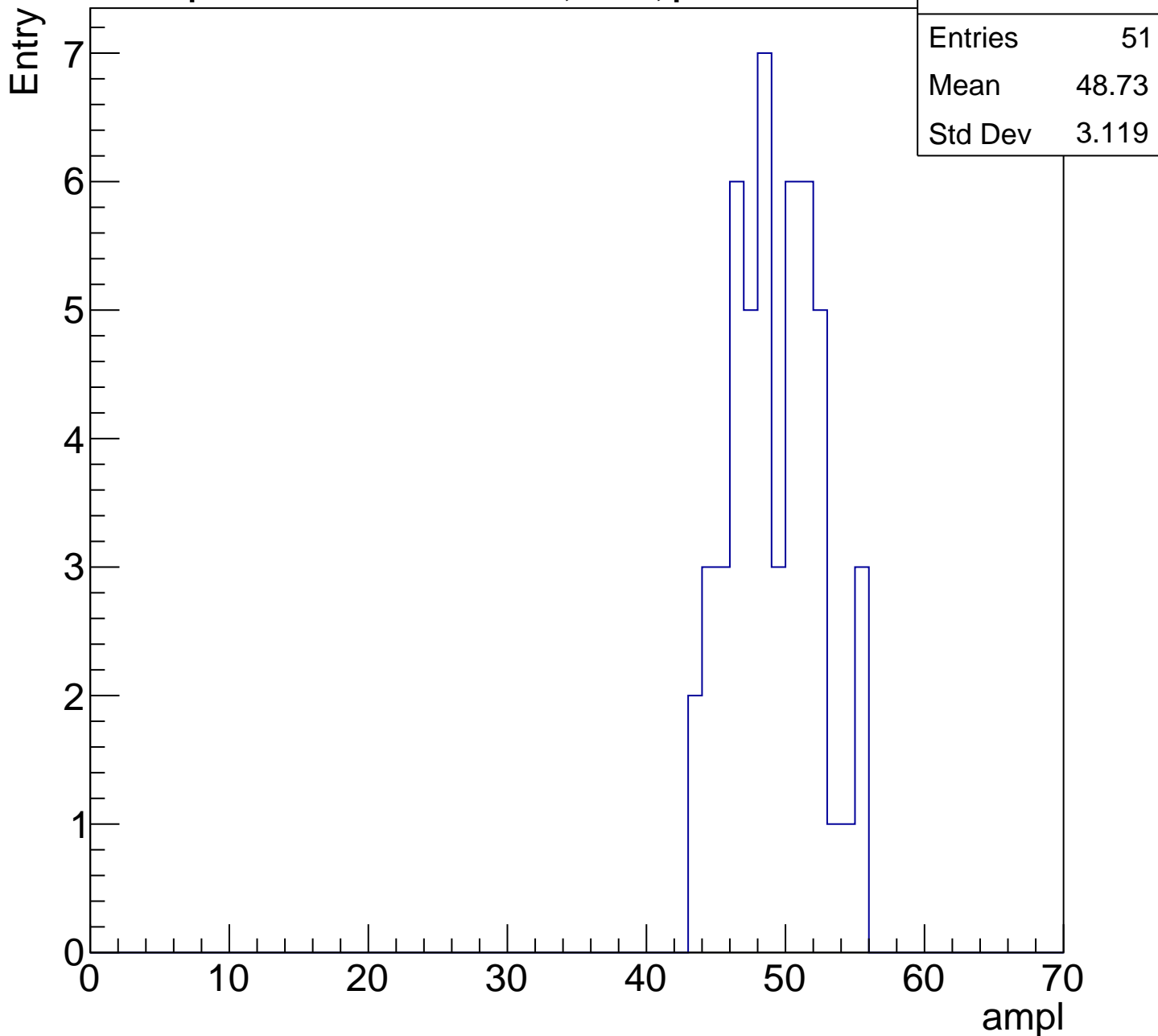
Gaus mean : 42.6394

Gaus Width: 4.6895



B1L101S, U1-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

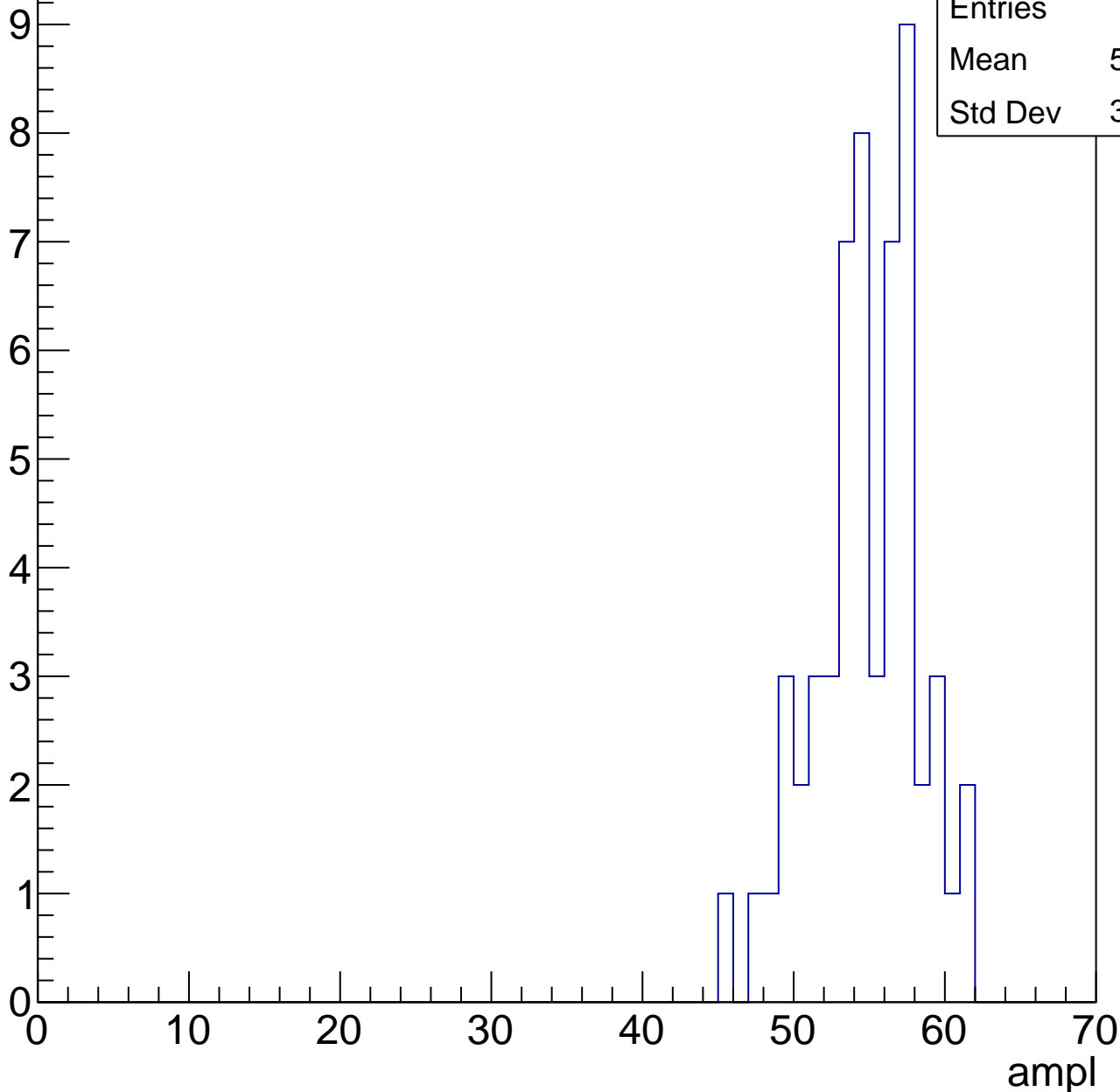


B1L101S, U1-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	54.36
Std Dev	3.446

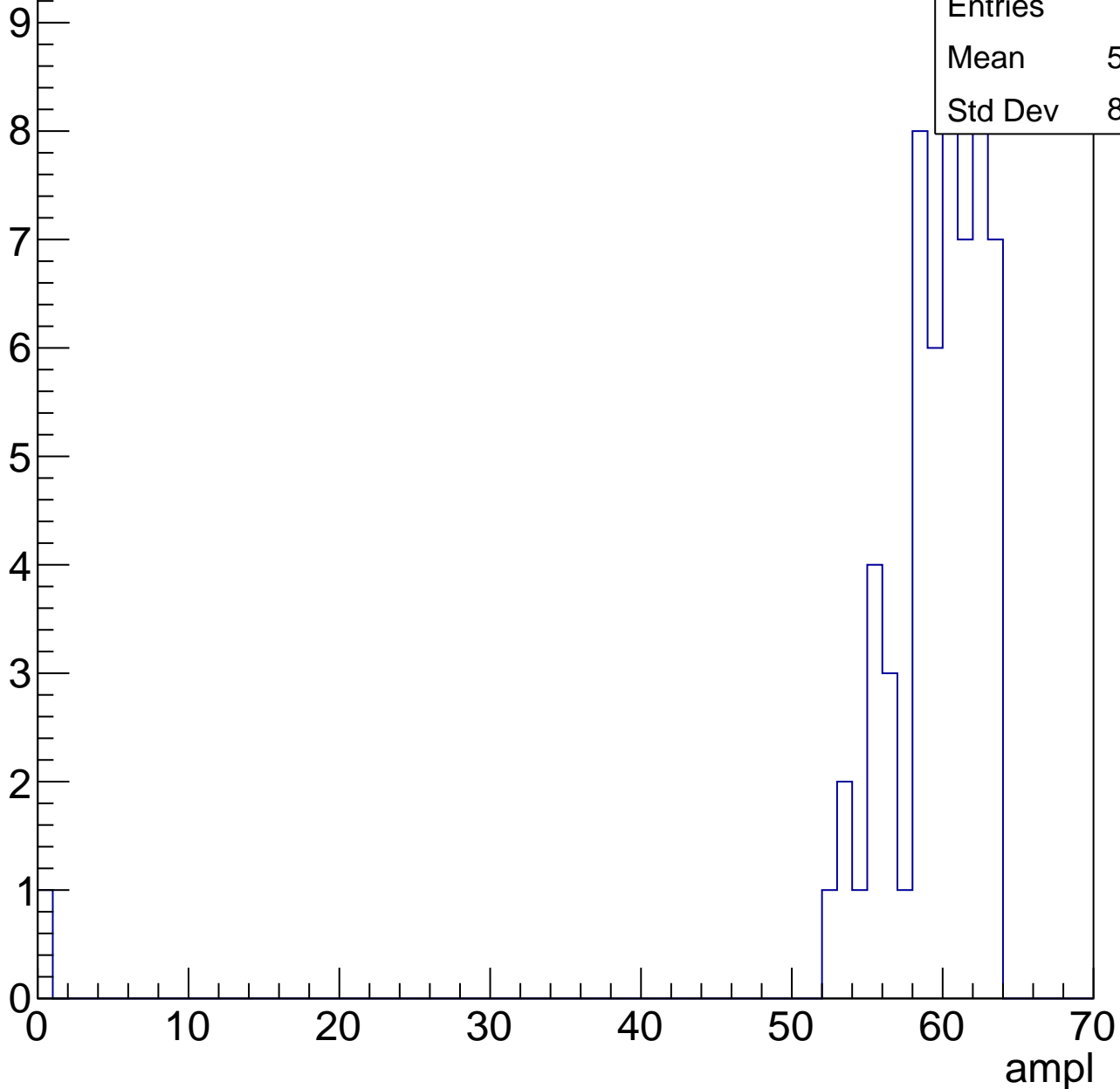


B1L101S, U1-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

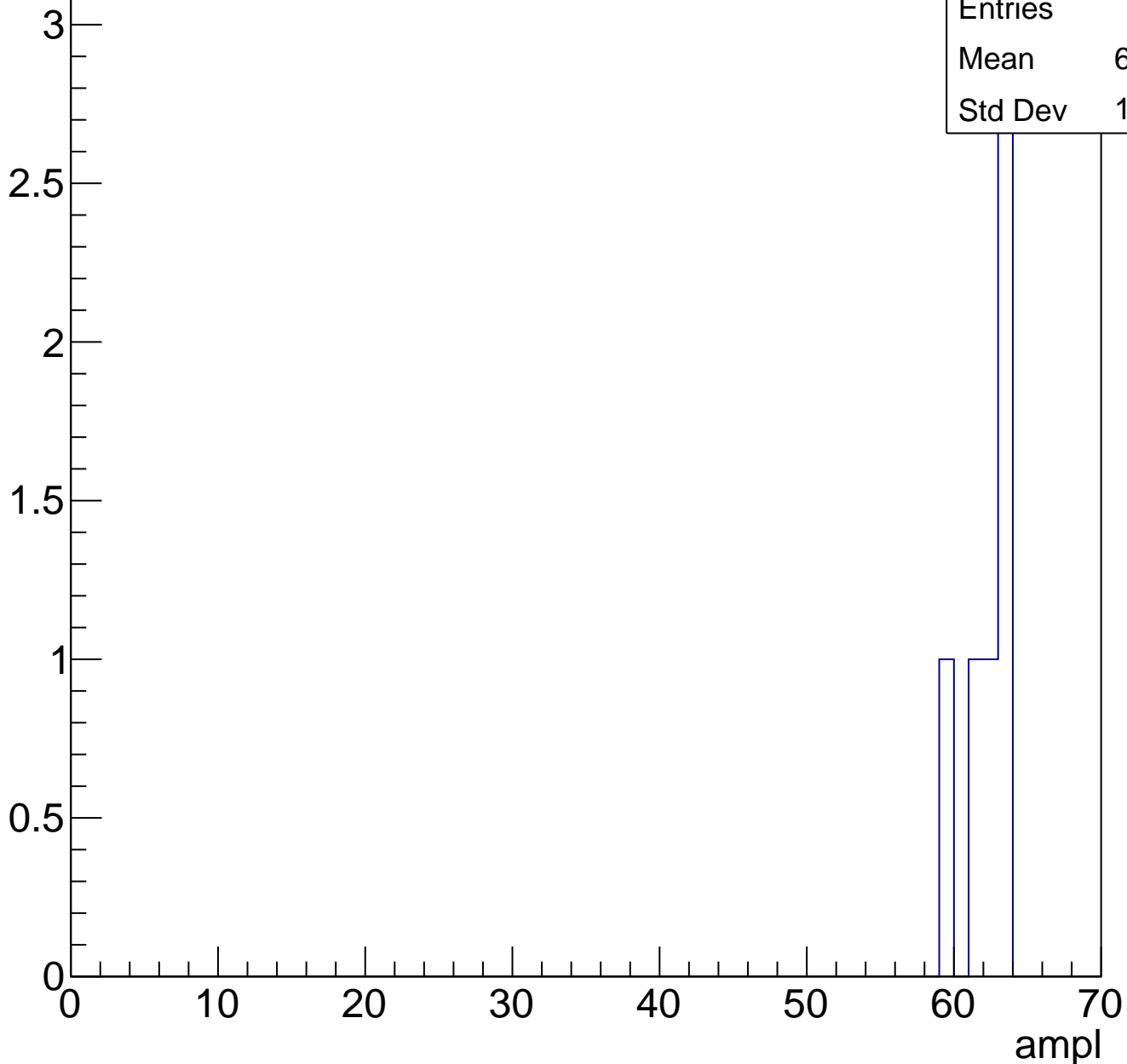
Entries	59
Mean	58.32
Std Dev	8.164



B1L101S, U1-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	61.83
Std Dev	1.462

B1L101S, U1-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch100, adc0

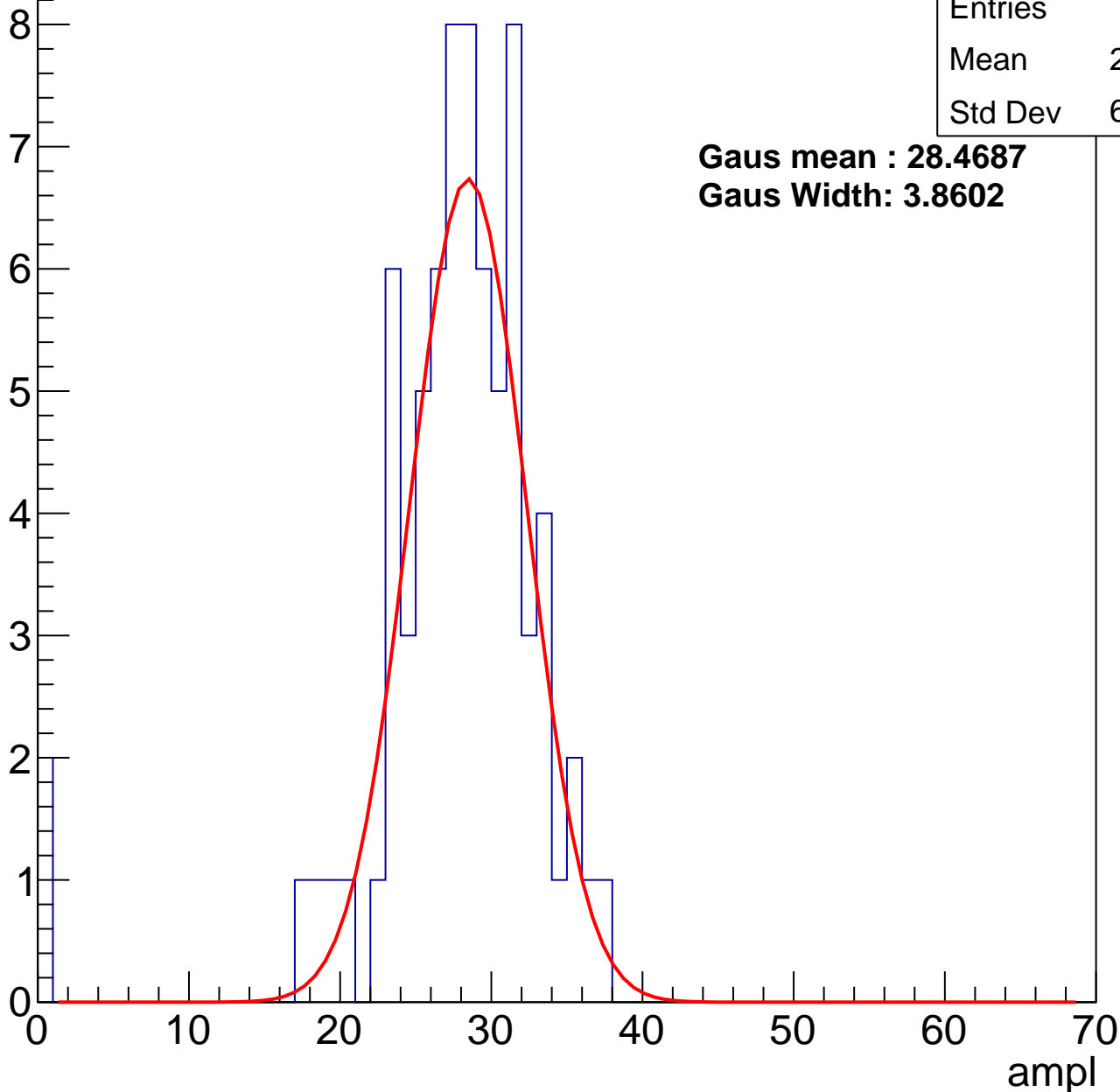
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	27.08
Std Dev	6.049

Gaus mean : 28.4687

Gaus Width: 3.8602



B1L101S, U1-ch100, adc1

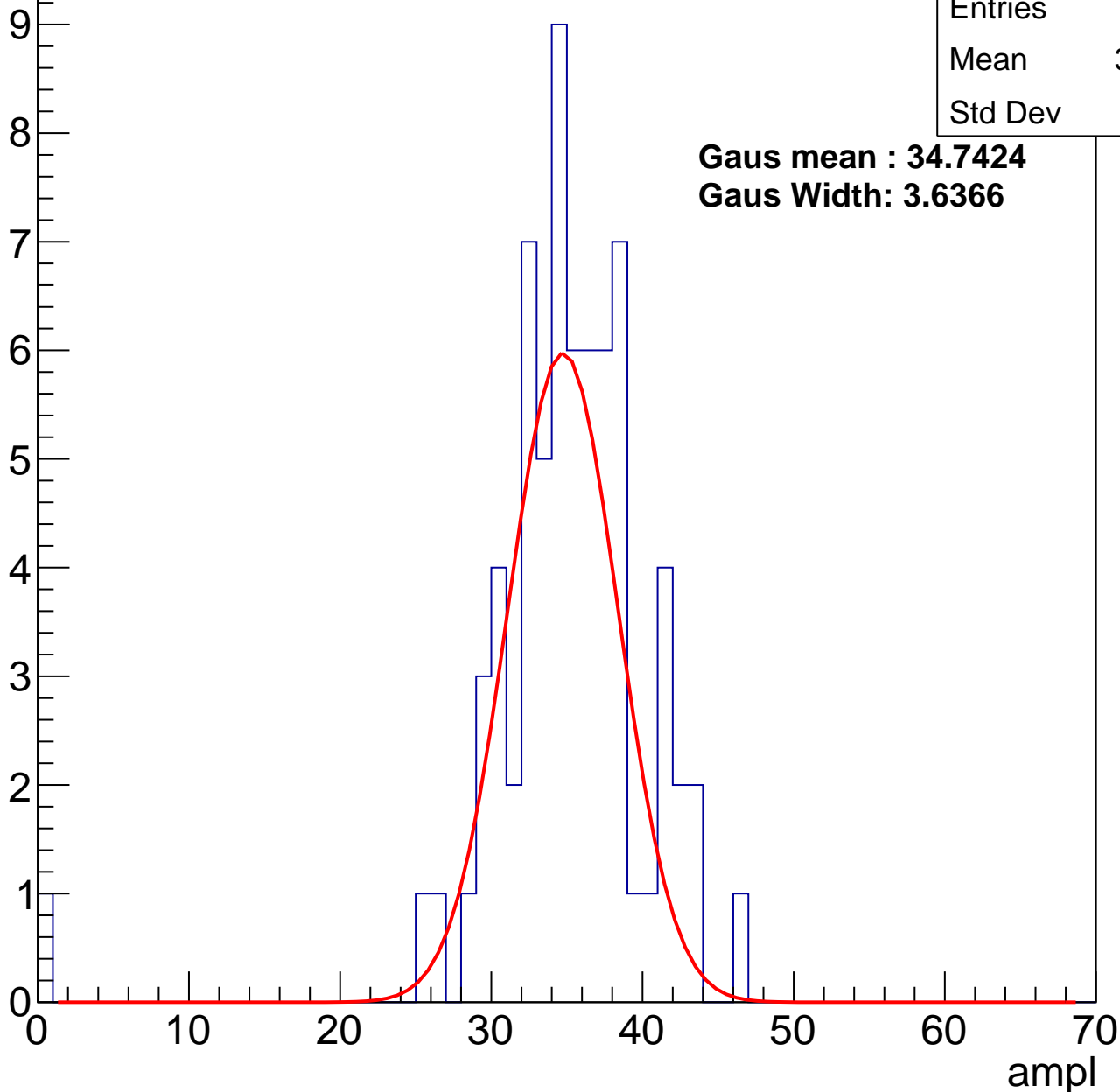
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	34.51
Std Dev	5.85

Gaus mean : 34.7424

Gaus Width: 3.6366



B1L101S, U1-ch100, adc2

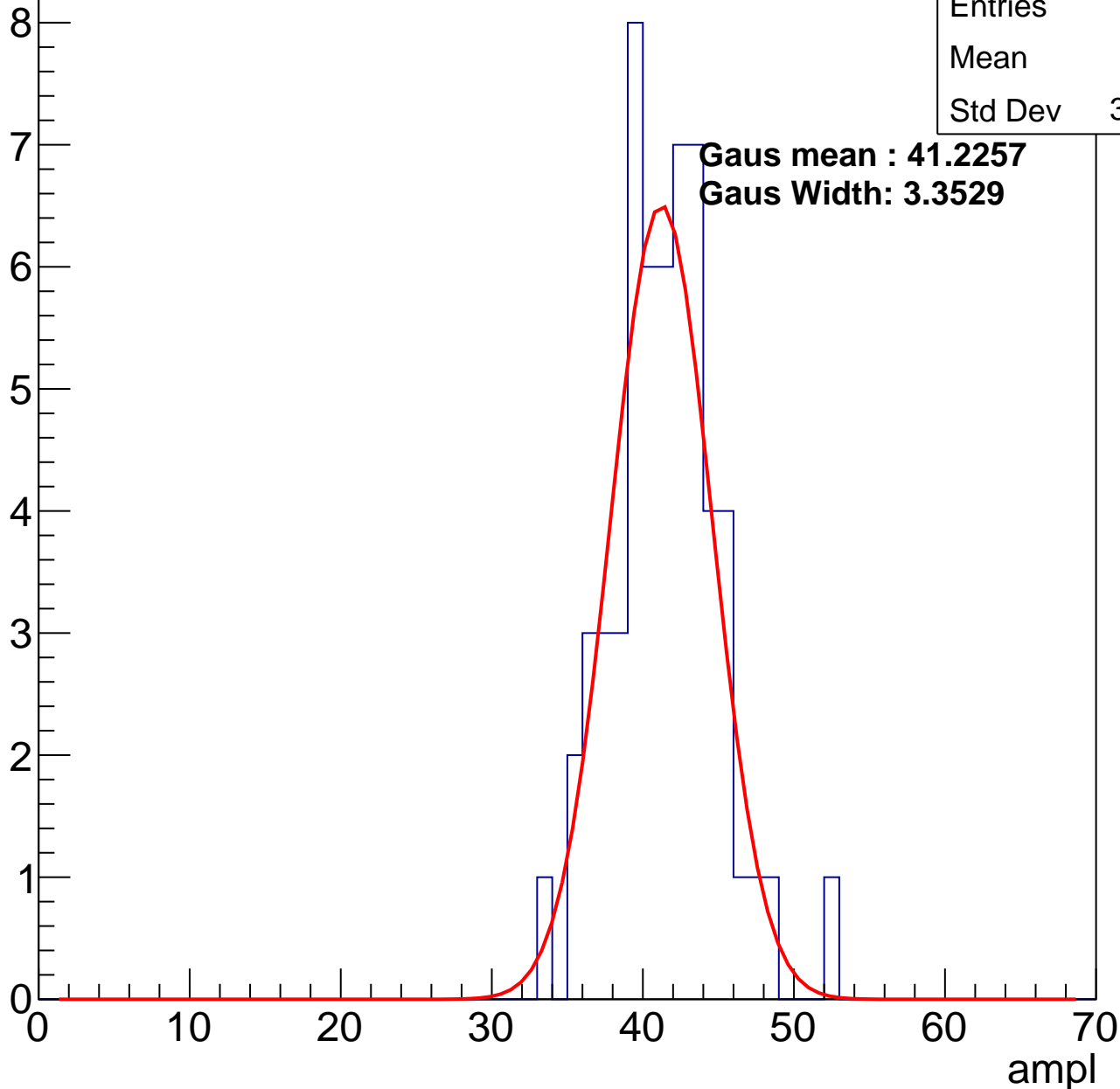
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41
Std Dev	3.449

Gaus mean : 41.2257

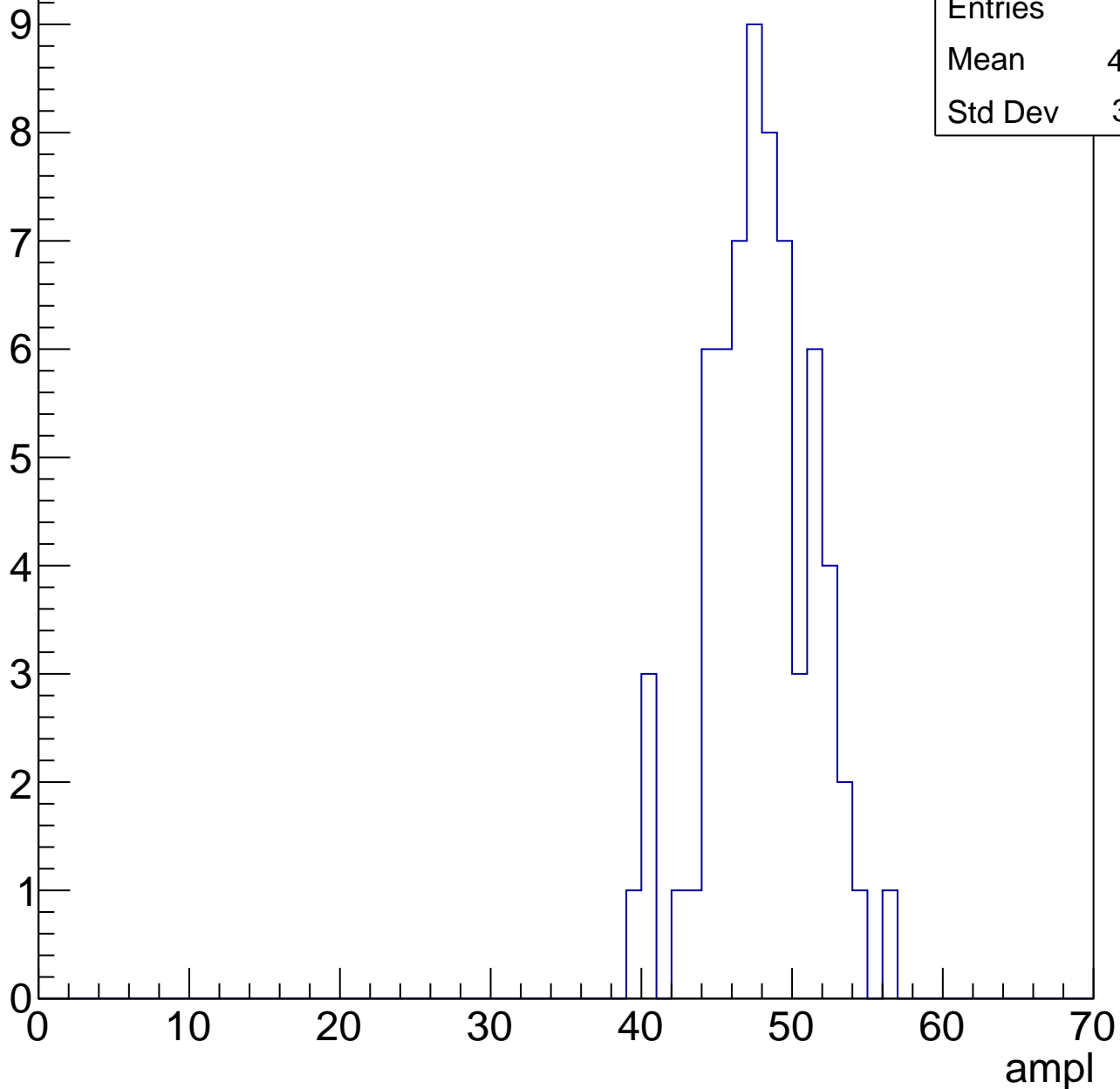
Gaus Width: 3.3529



B1L101S, U1-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

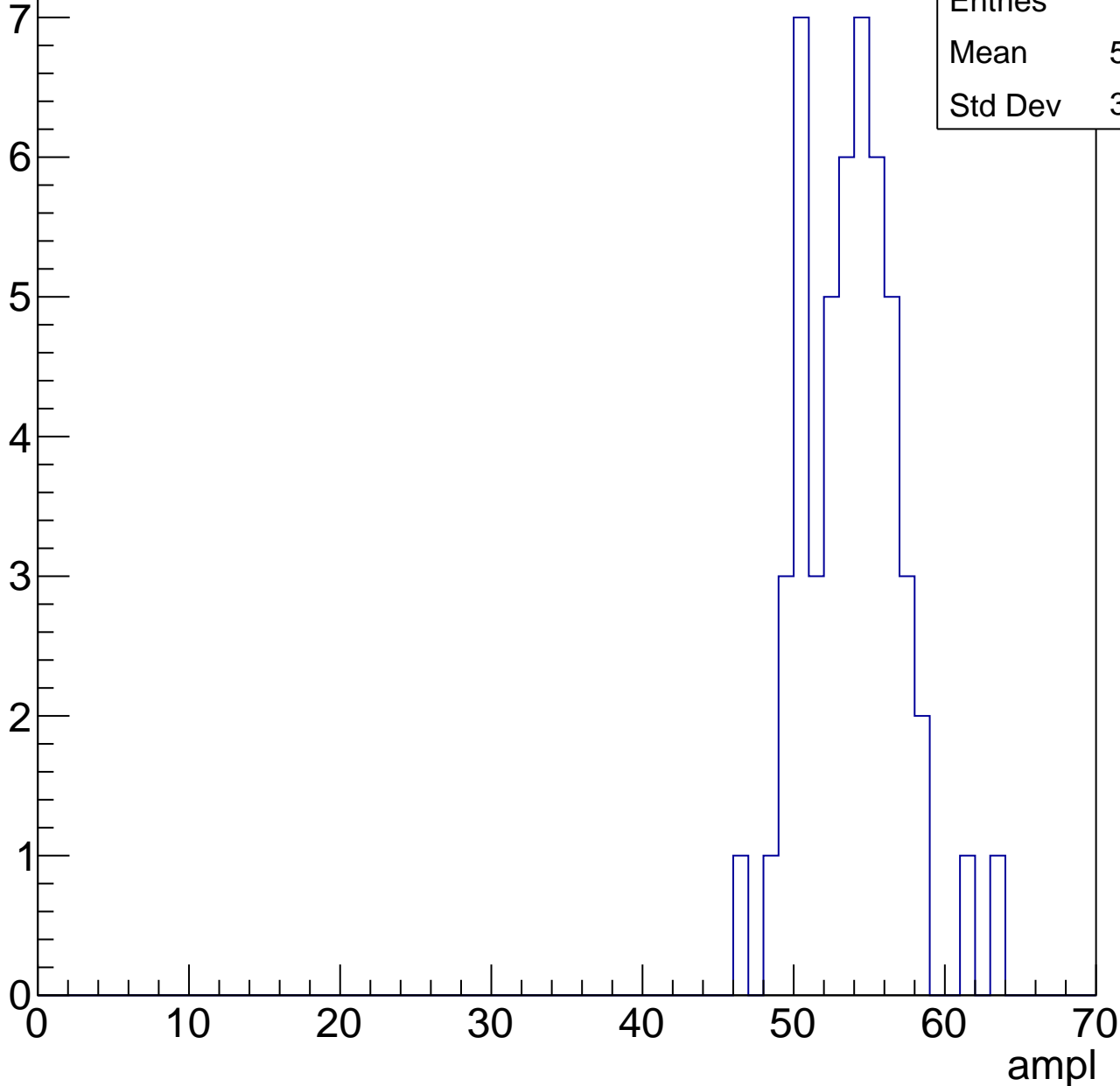


B1L101S, U1-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

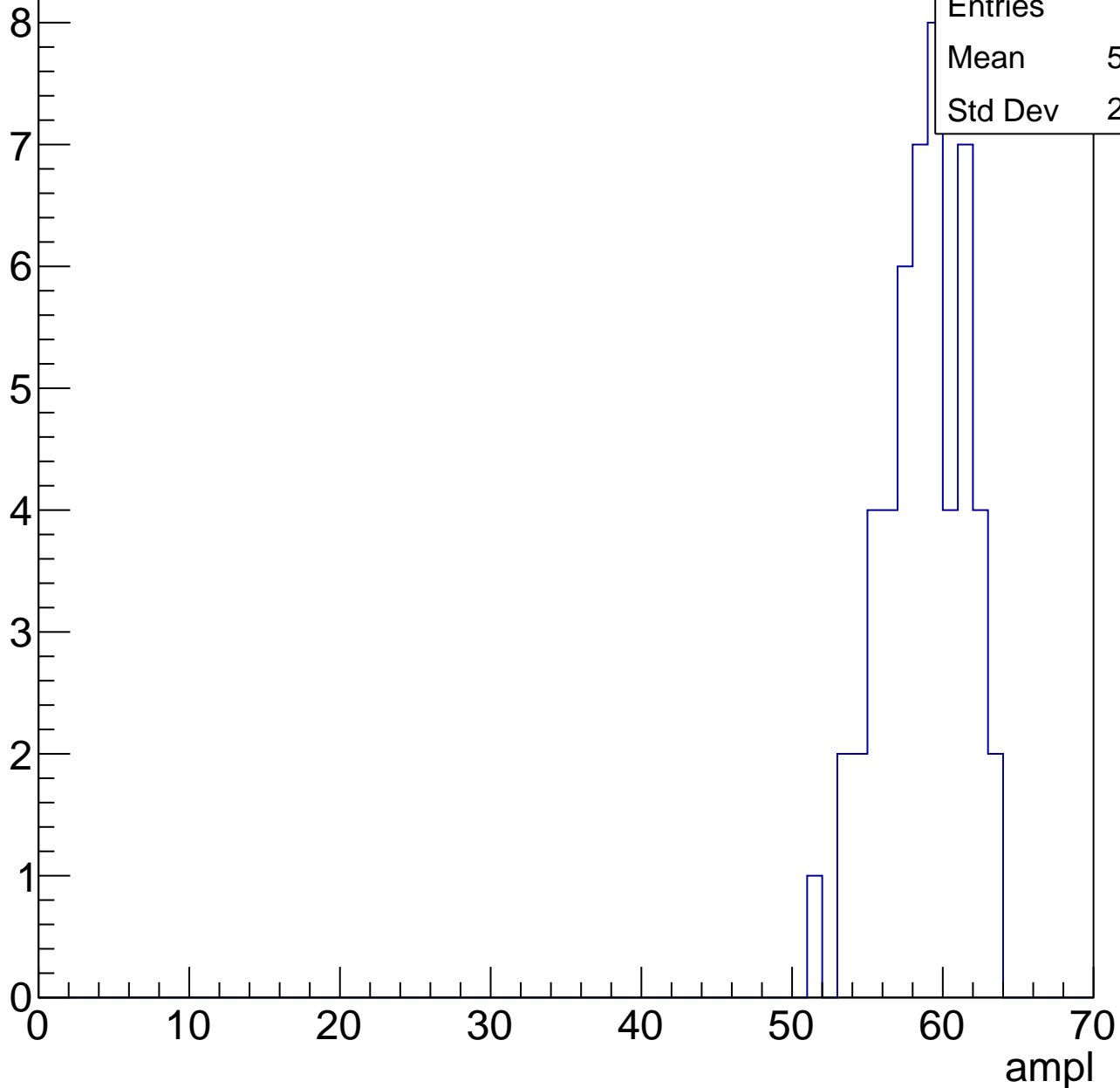
Entries	51
Mean	53.35
Std Dev	3.235



B1L101S, U1-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

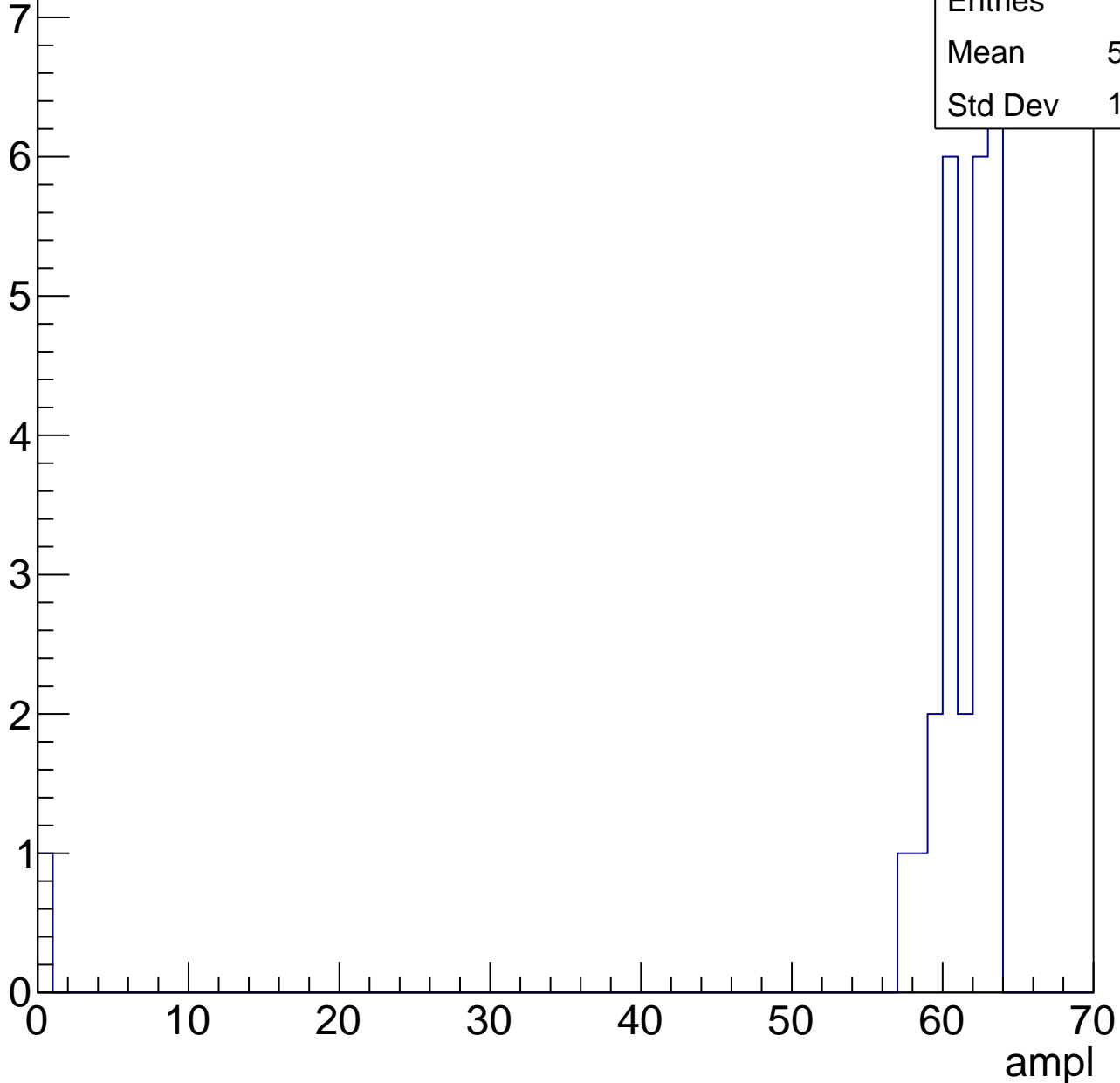


B1L101S, U1-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.77
Std Dev	11.87



B1L101S, U1-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch101, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	27.91
Std Dev	5.641

Gaus mean : 29.6661

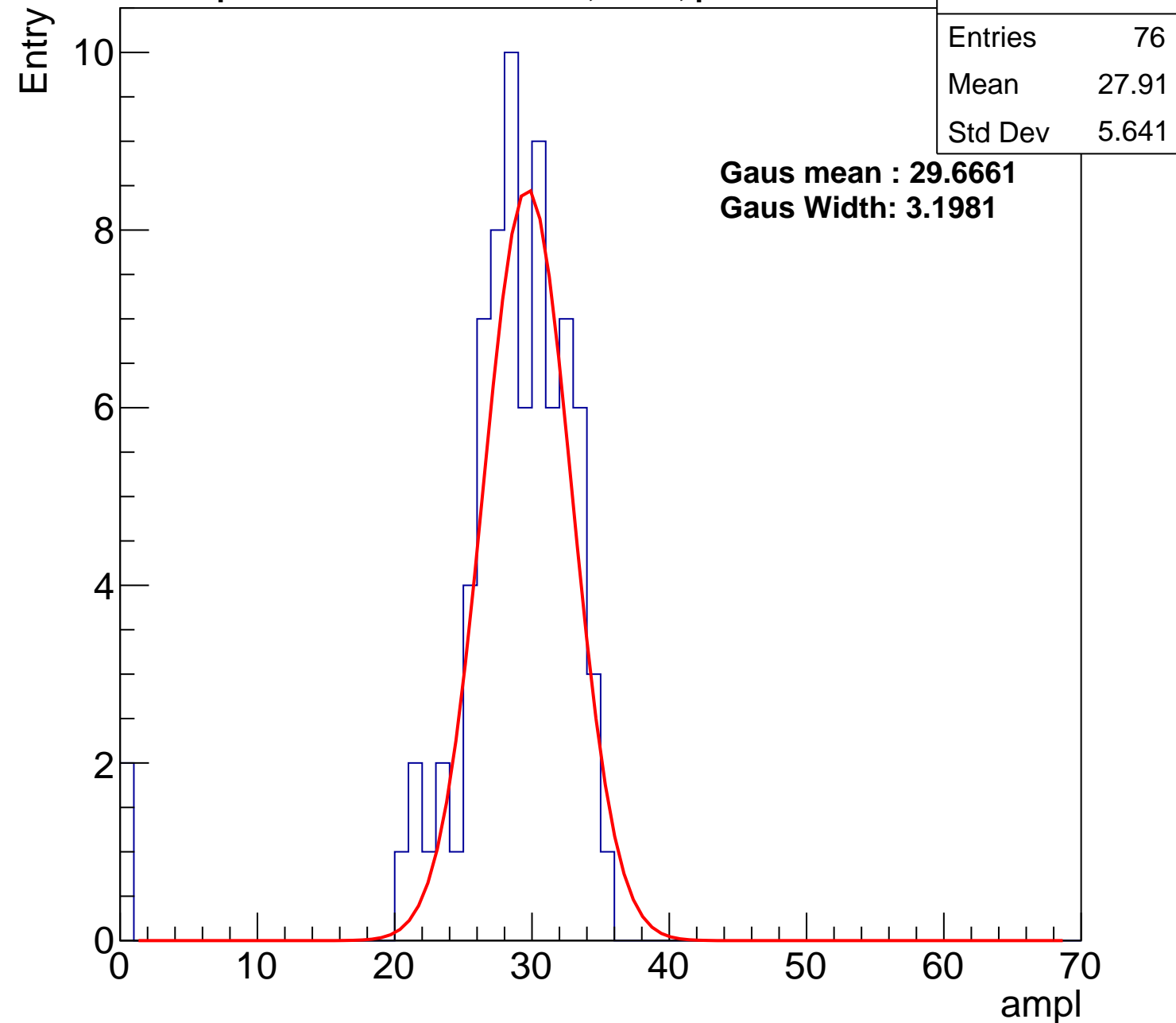
Gaus Width: 3.1981

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch101, adc1

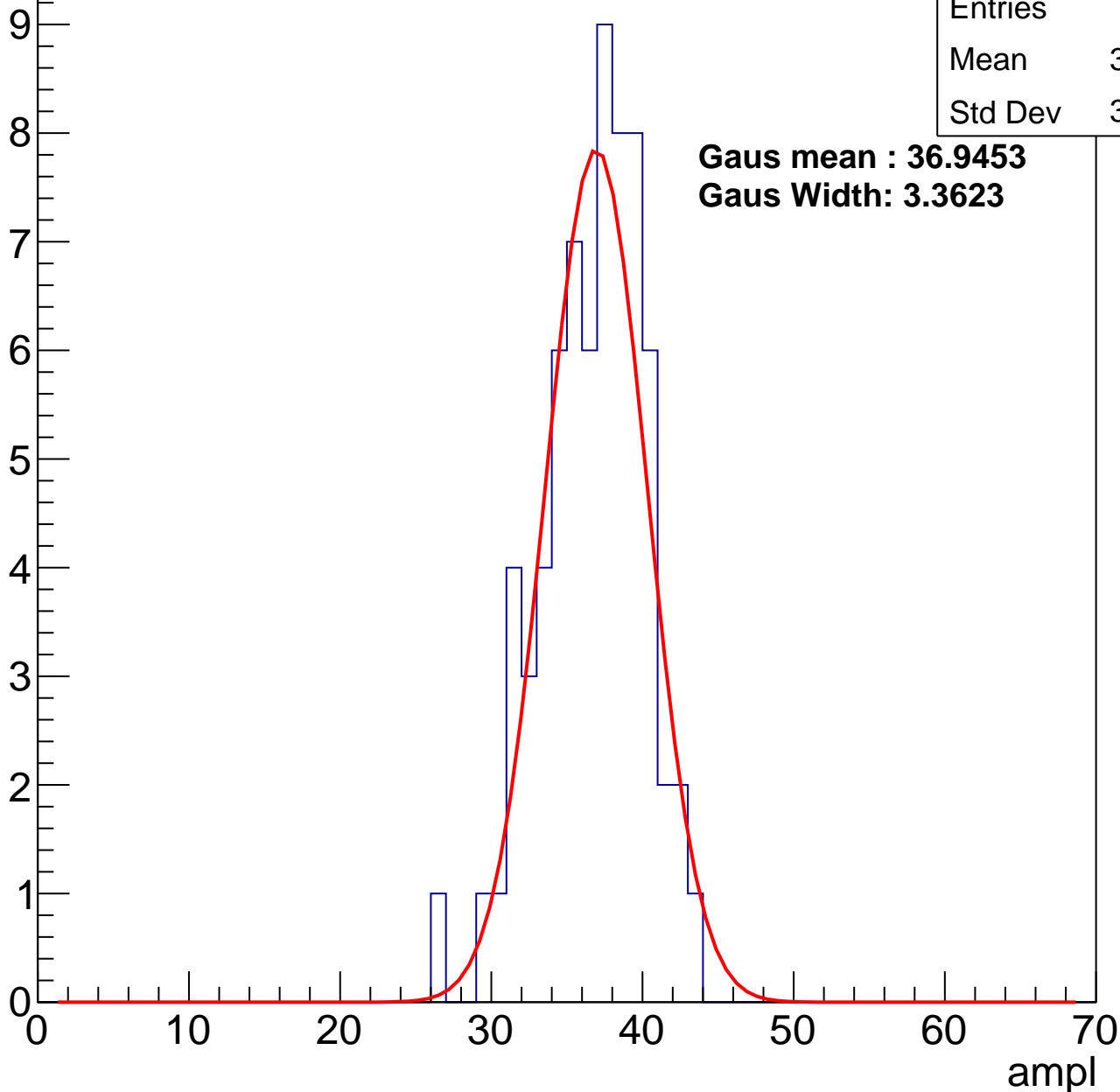
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.23
Std Dev	3.354

Gaus mean : 36.9453

Gaus Width: 3.3623



B1L101S, U1-ch101, adc2

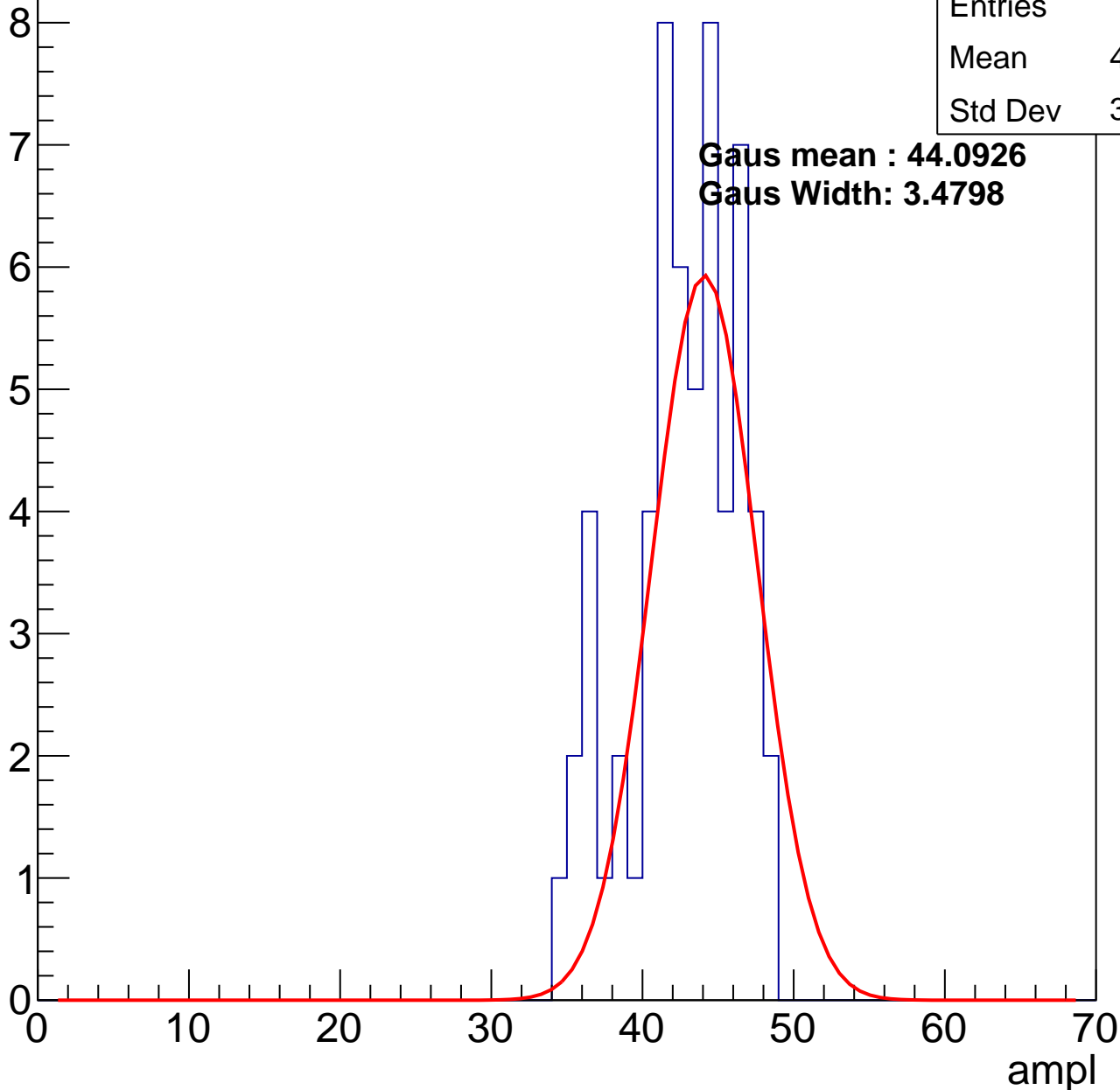
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.25
Std Dev	3.563

Gaus mean : 44.0926

Gaus Width: 3.4798

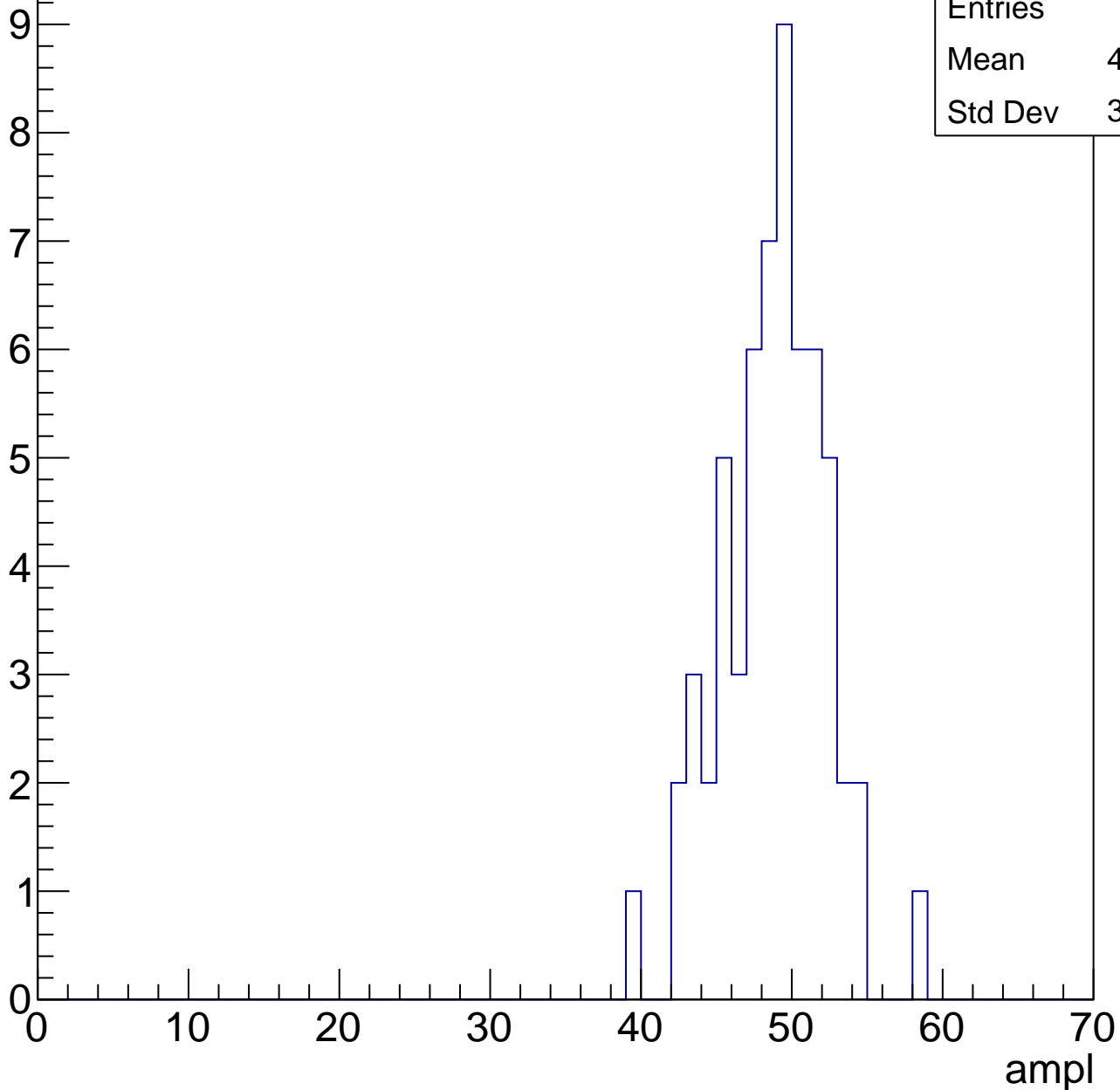


B1L101S, U1-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.33
Std Dev	3.429

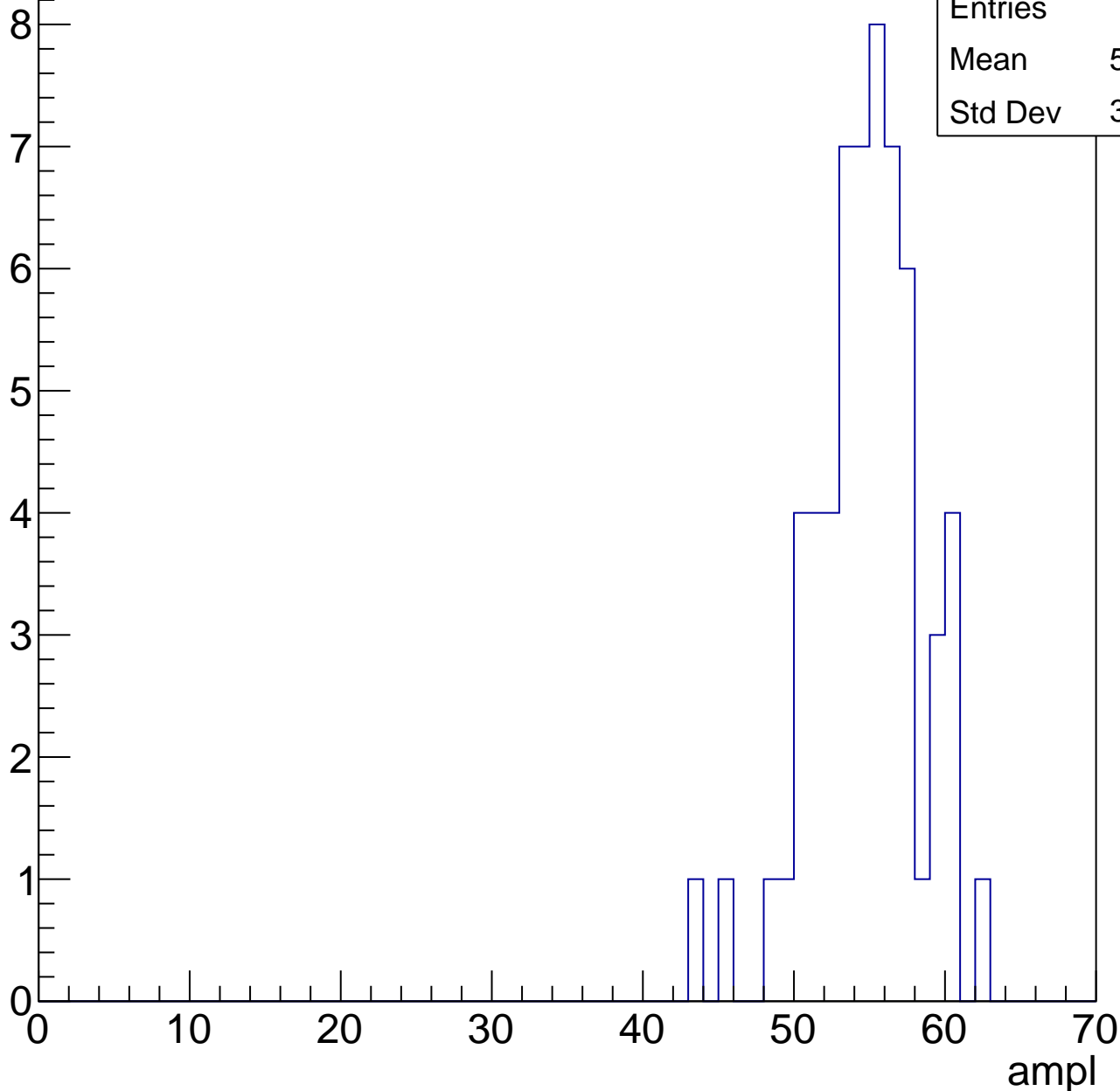


B1L101S, U1-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.28
Std Dev	3.583

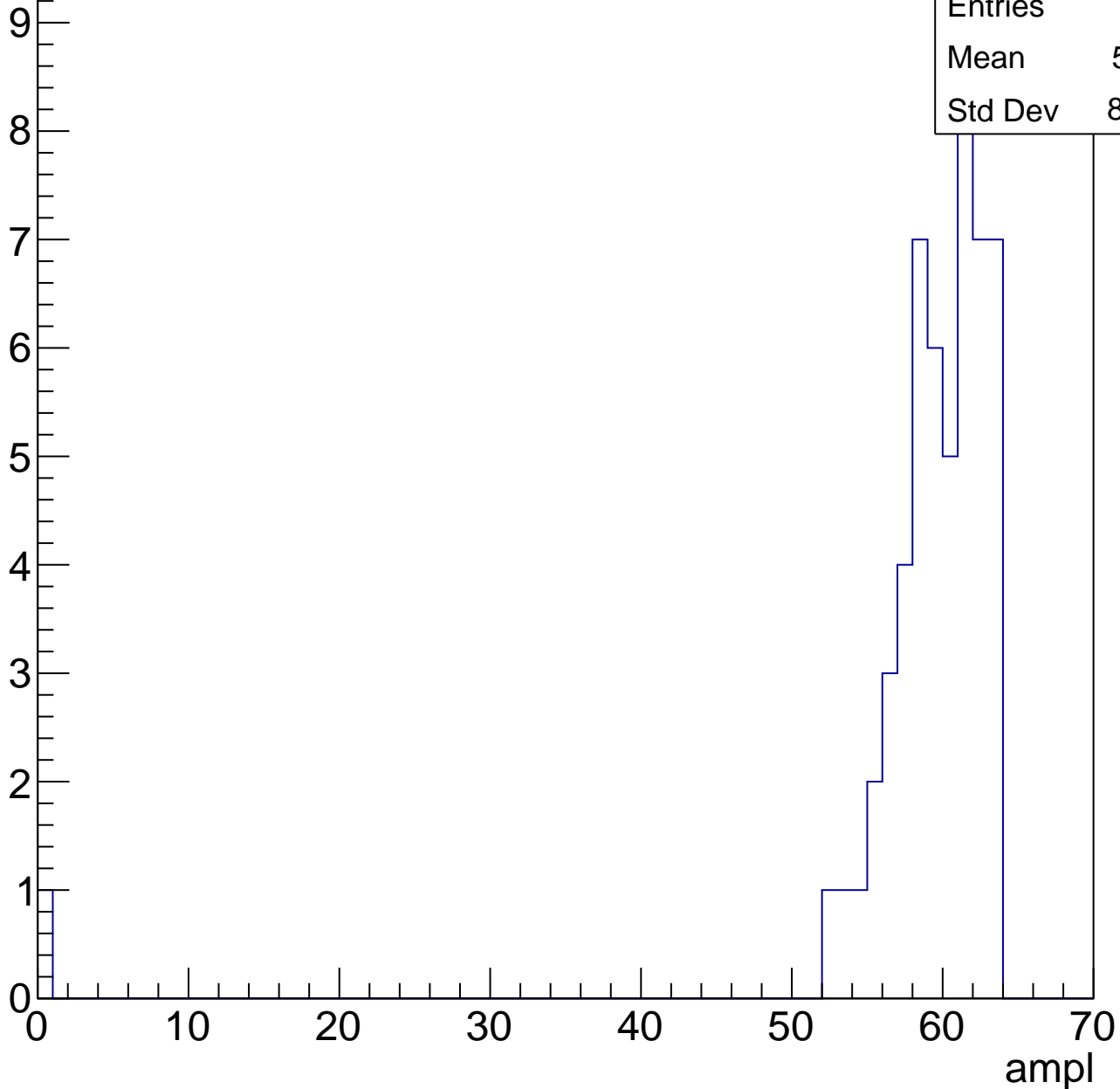


B1L101S, U1-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

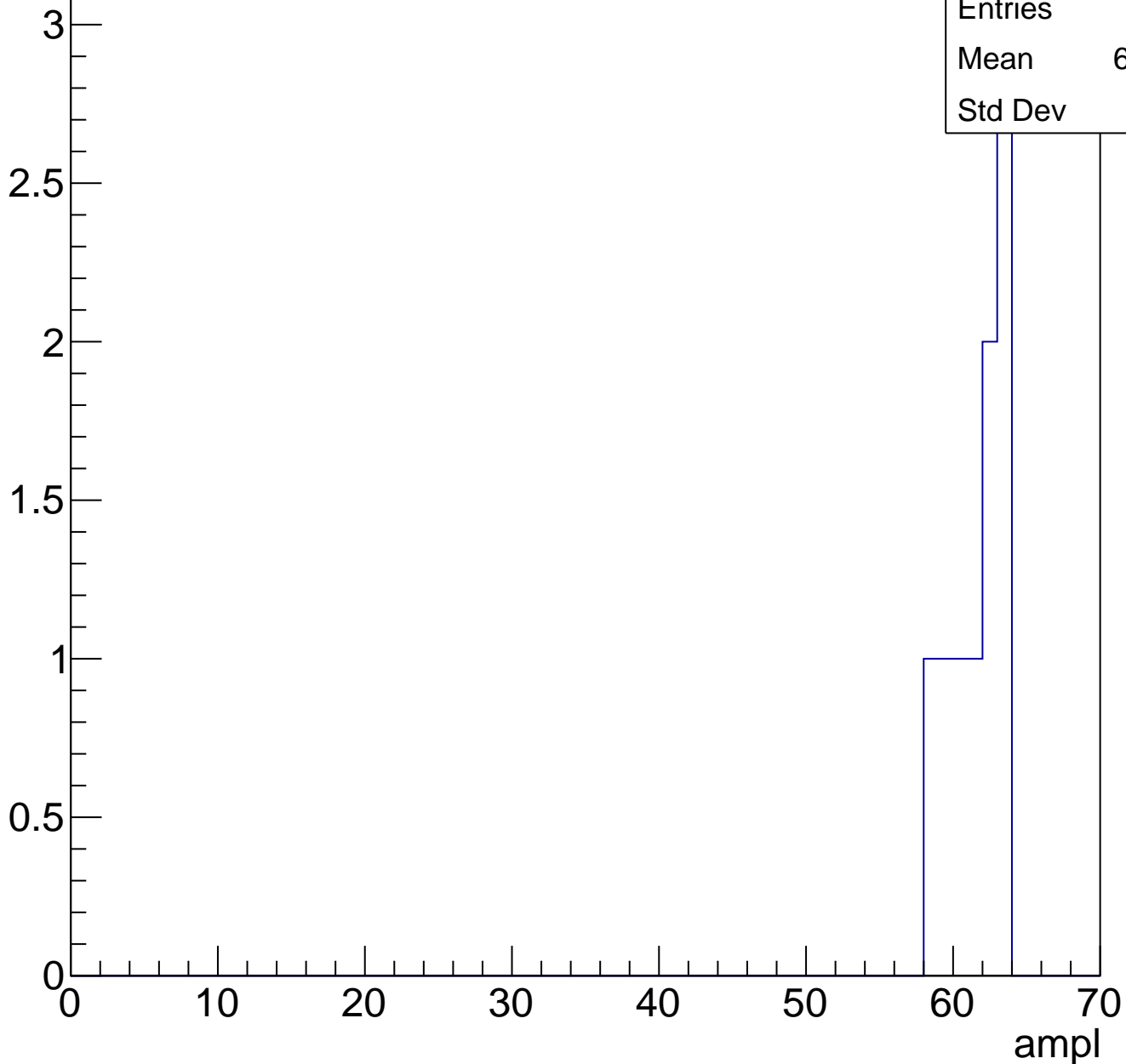
Entries	54
Mean	58.31
Std Dev	8.459



B1L101S, U1-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch102, adc0

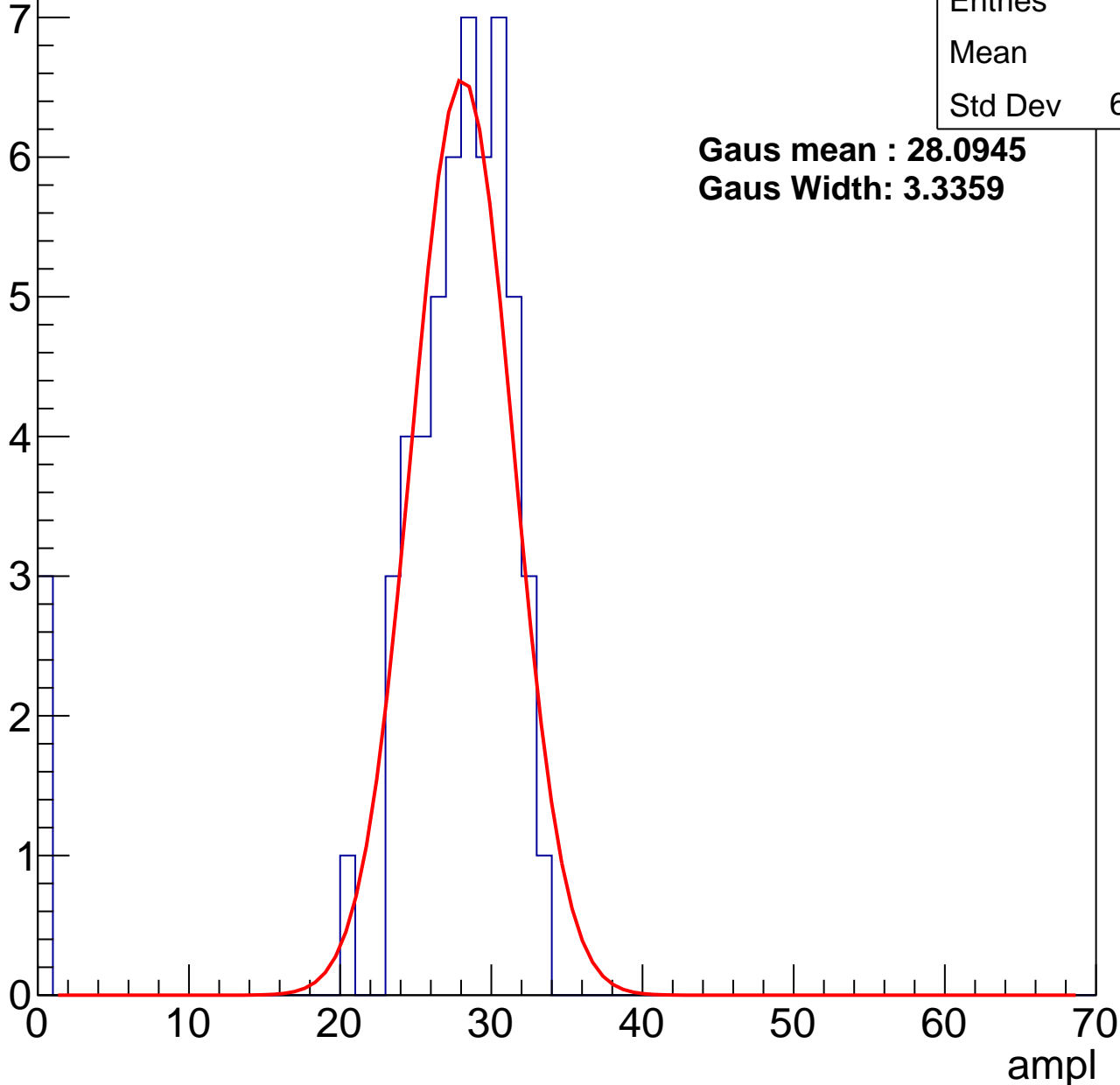
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	26.2
Std Dev	6.863

Gaus mean : 28.0945

Gaus Width: 3.3359



B1L101S, U1-ch102, adc1

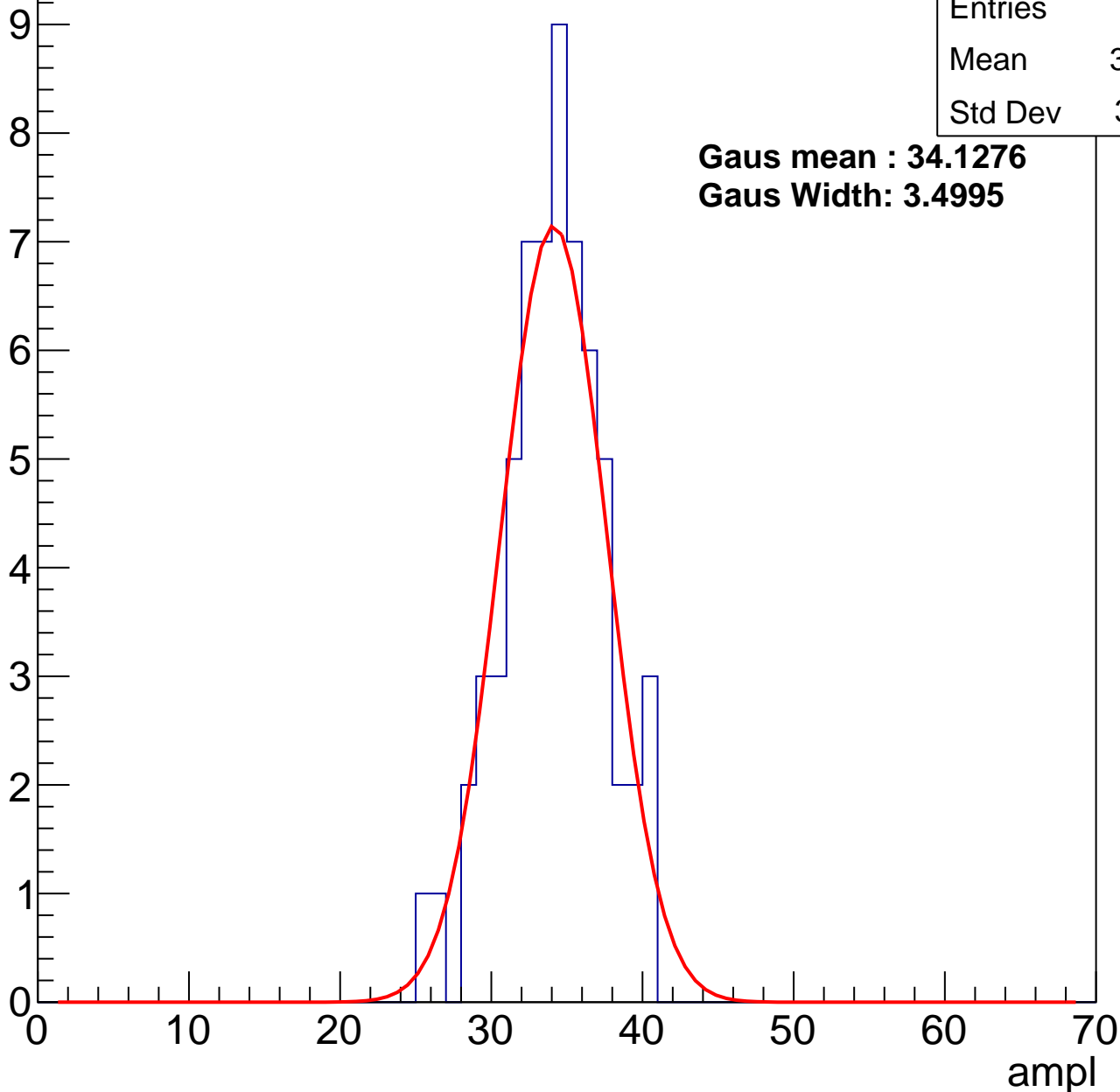
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	33.65
Std Dev	3.281

Gaus mean : 34.1276

Gaus Width: 3.4995



B1L101S, U1-ch102, adc2

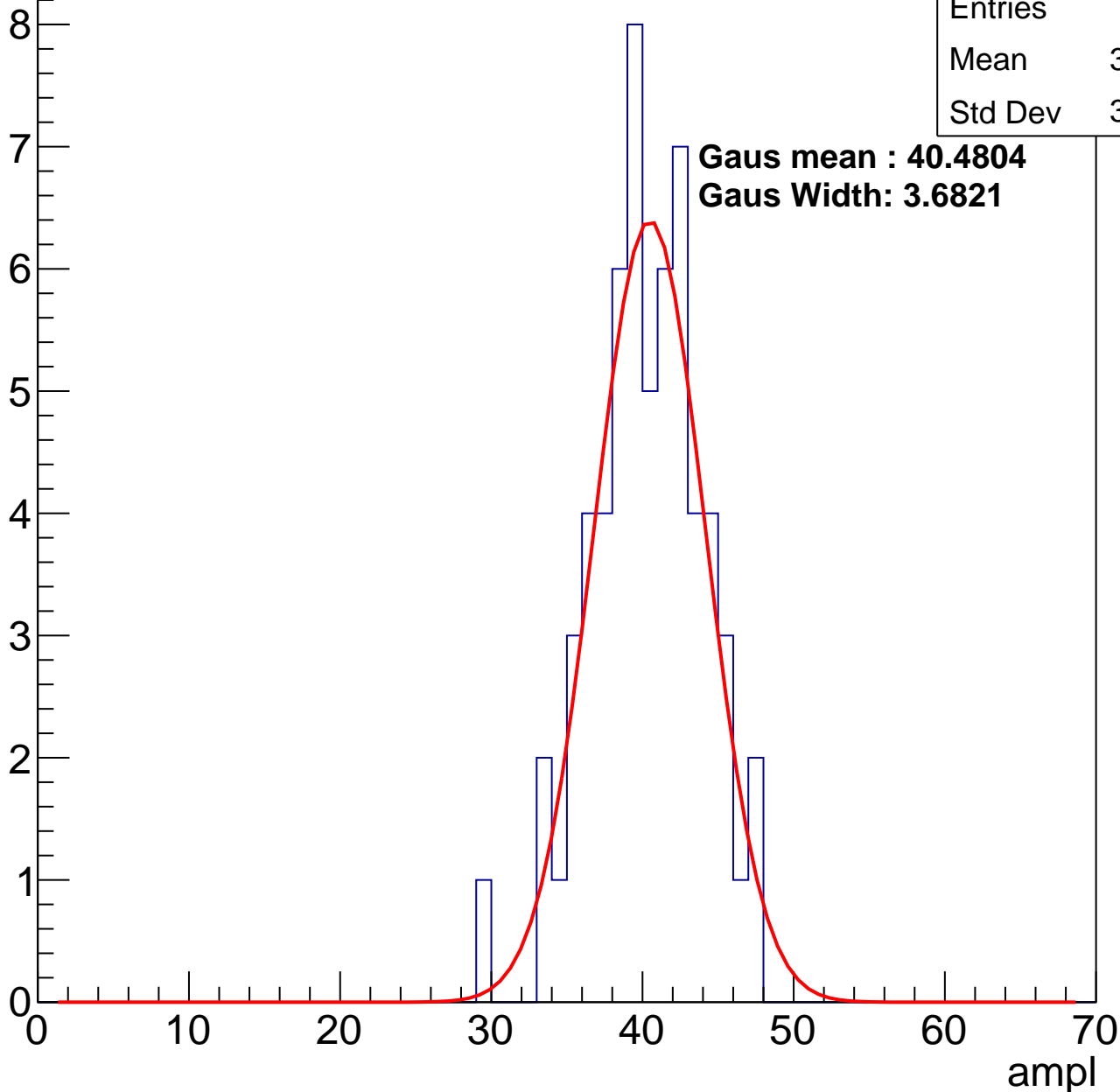
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	39.82
Std Dev	3.628

Gaus mean : 40.4804

Gaus Width: 3.6821

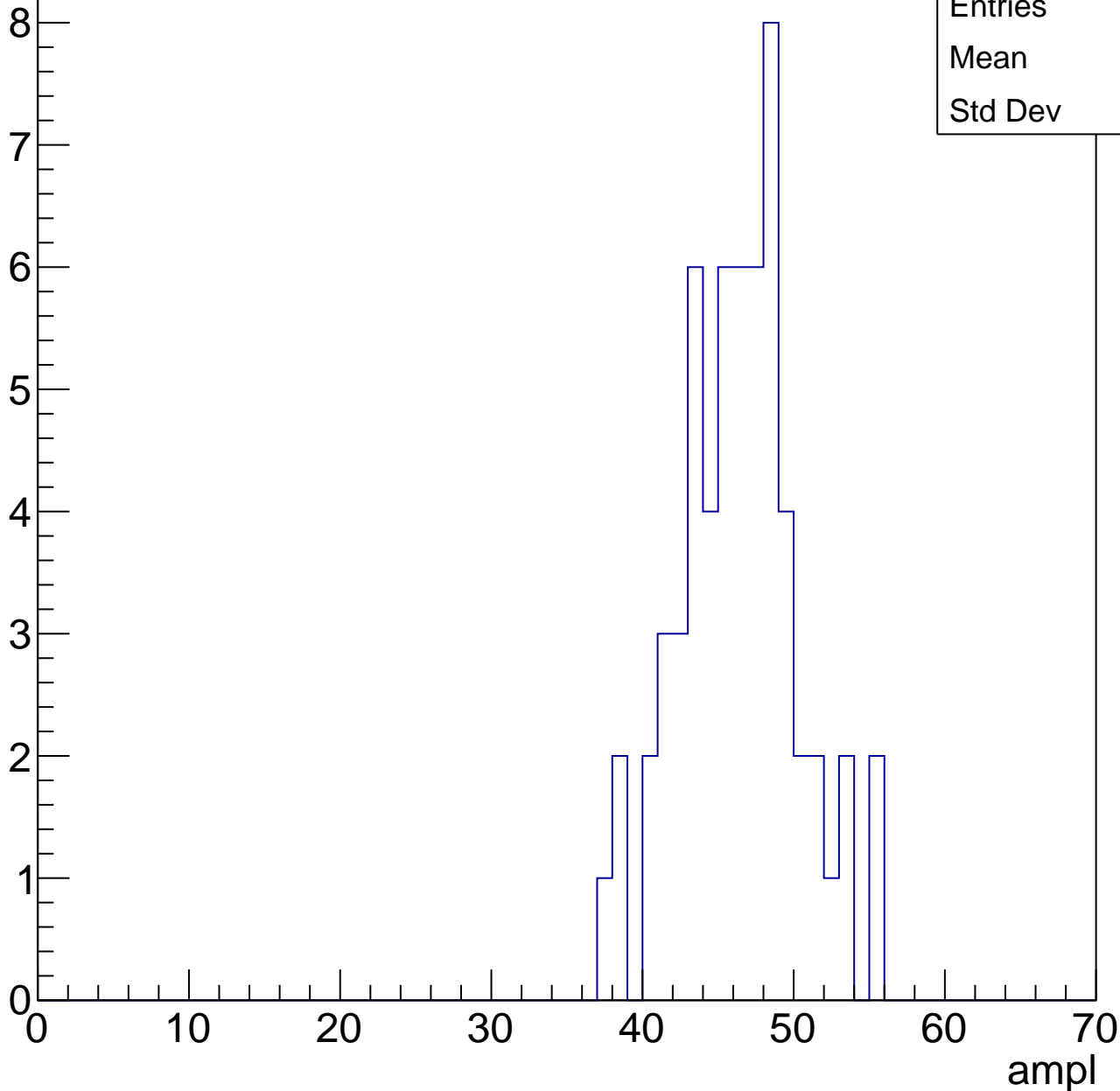


B1L101S, U1-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	45.9
Std Dev	3.94

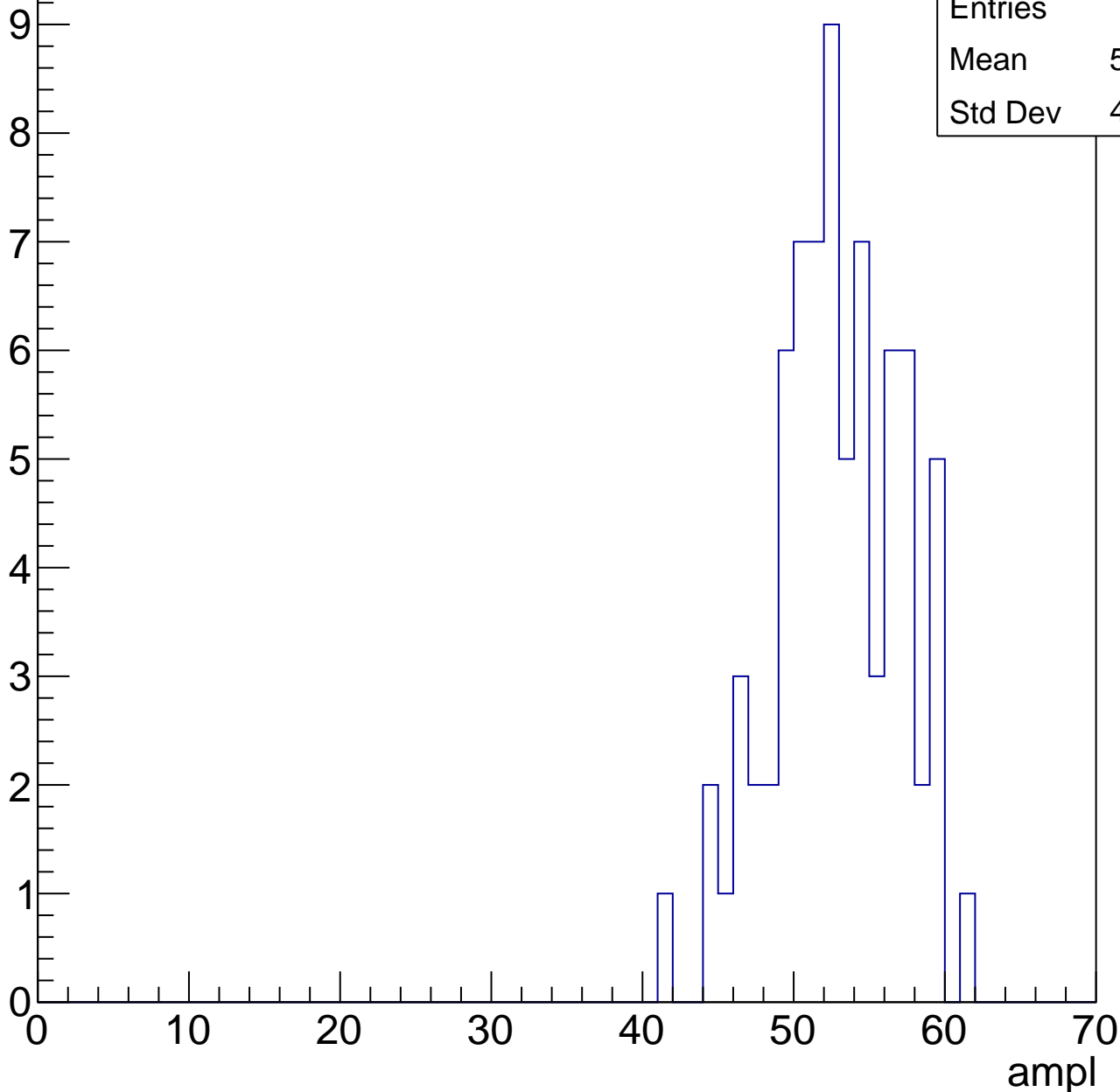


B1L101S, U1-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

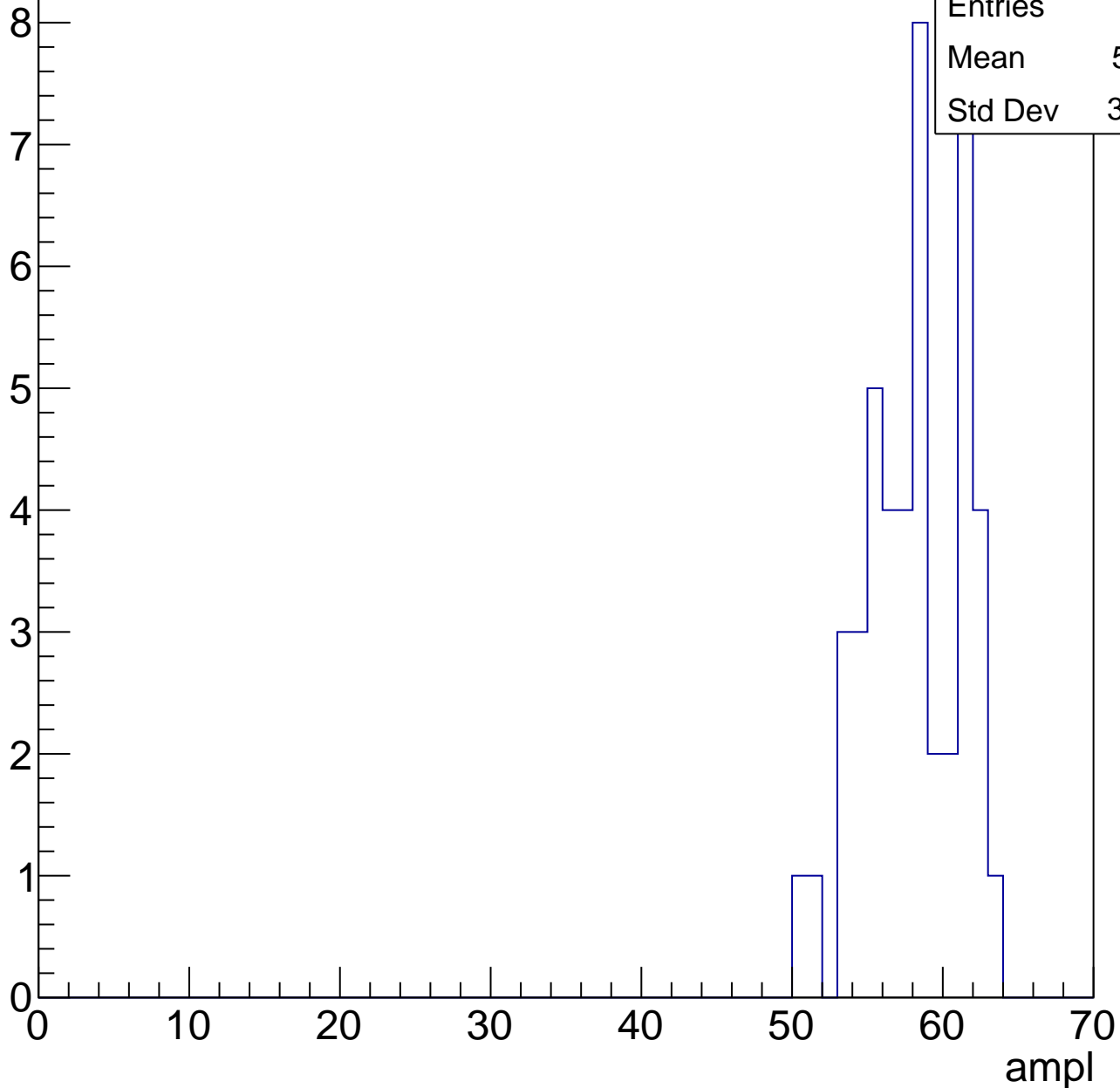
Entries	75
Mean	52.39
Std Dev	4.115



B1L101S, U1-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

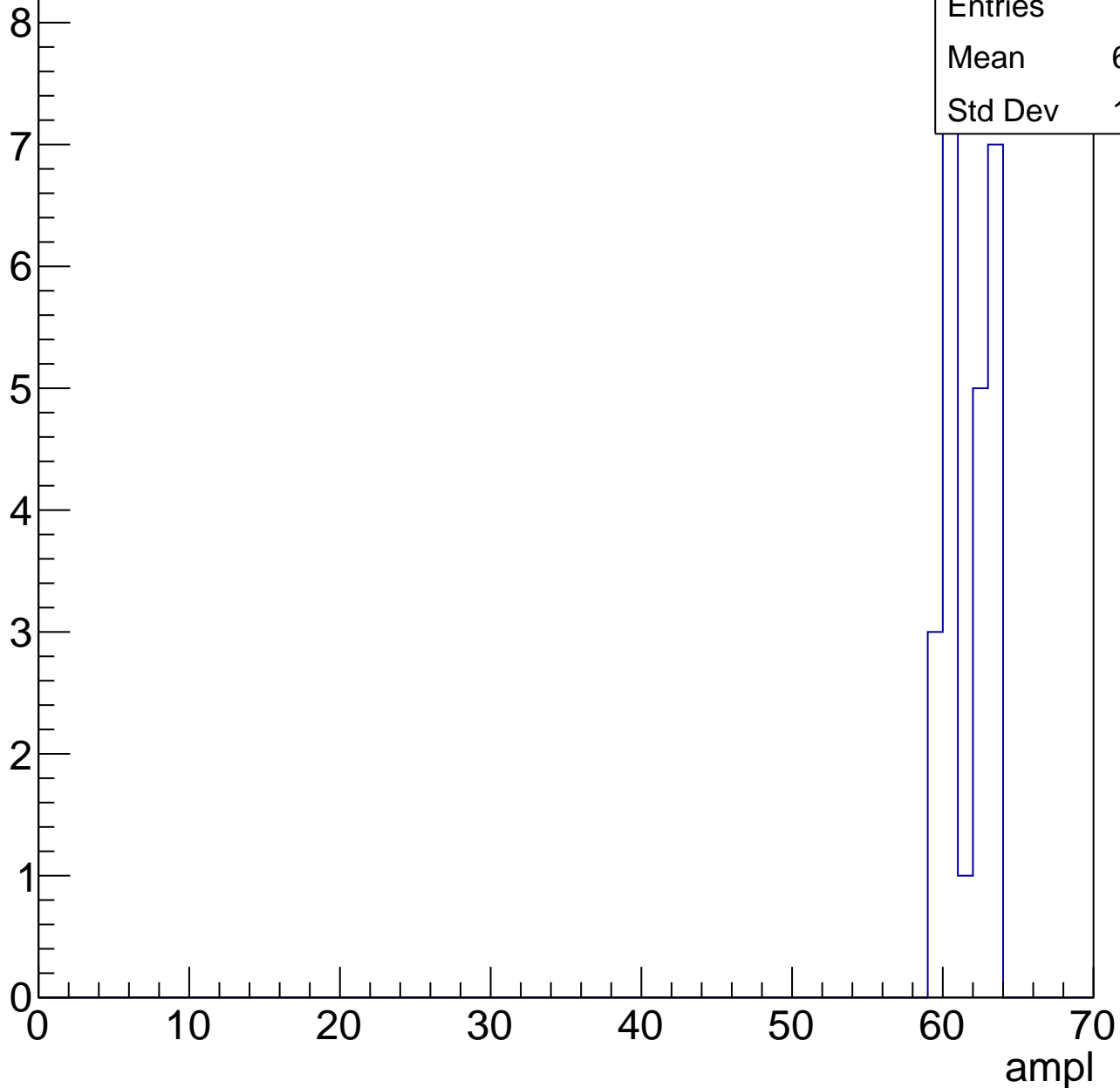
Entry



B1L101S, U1-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

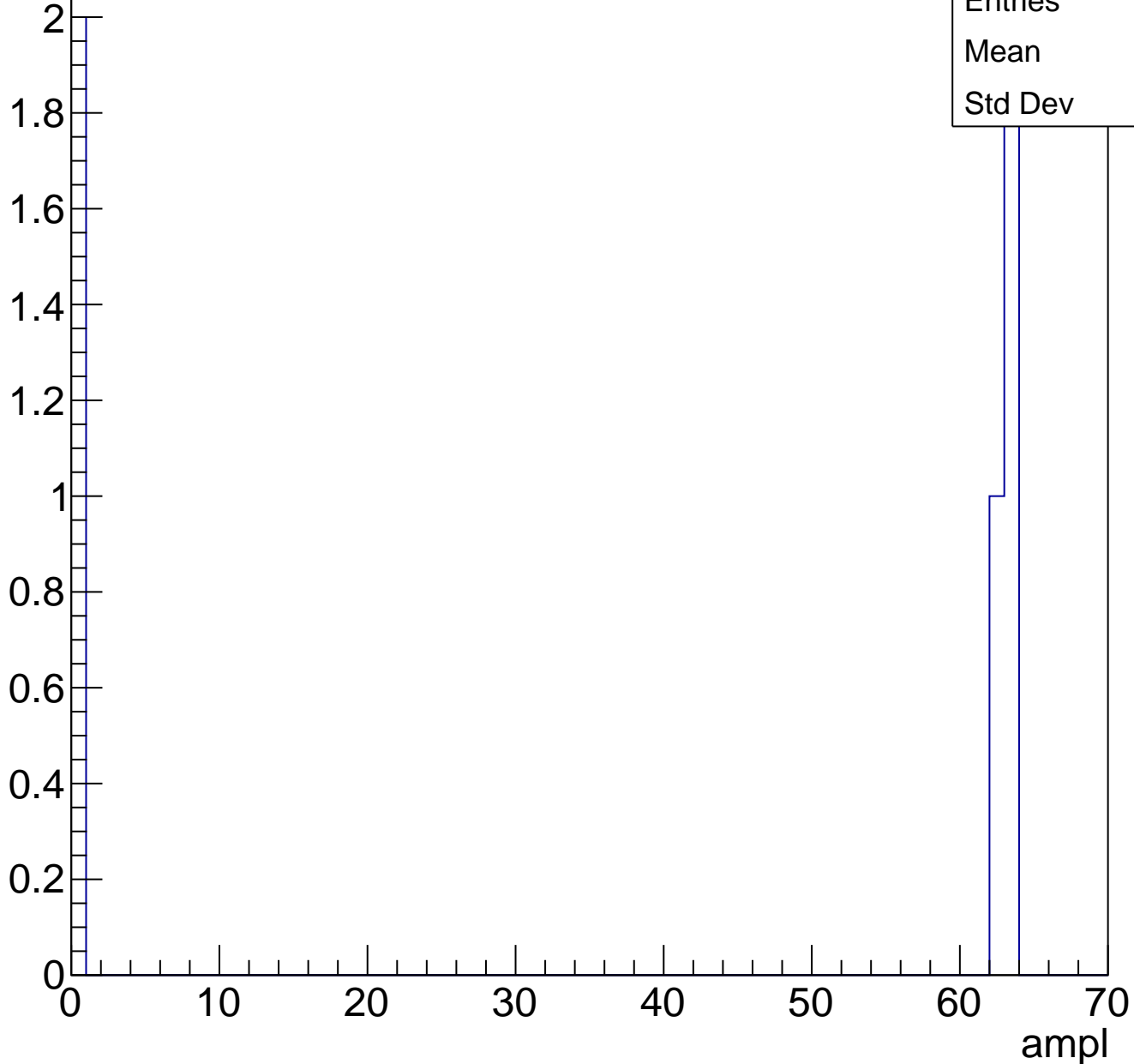
Entry



B1L101S, U1-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch103, adc0

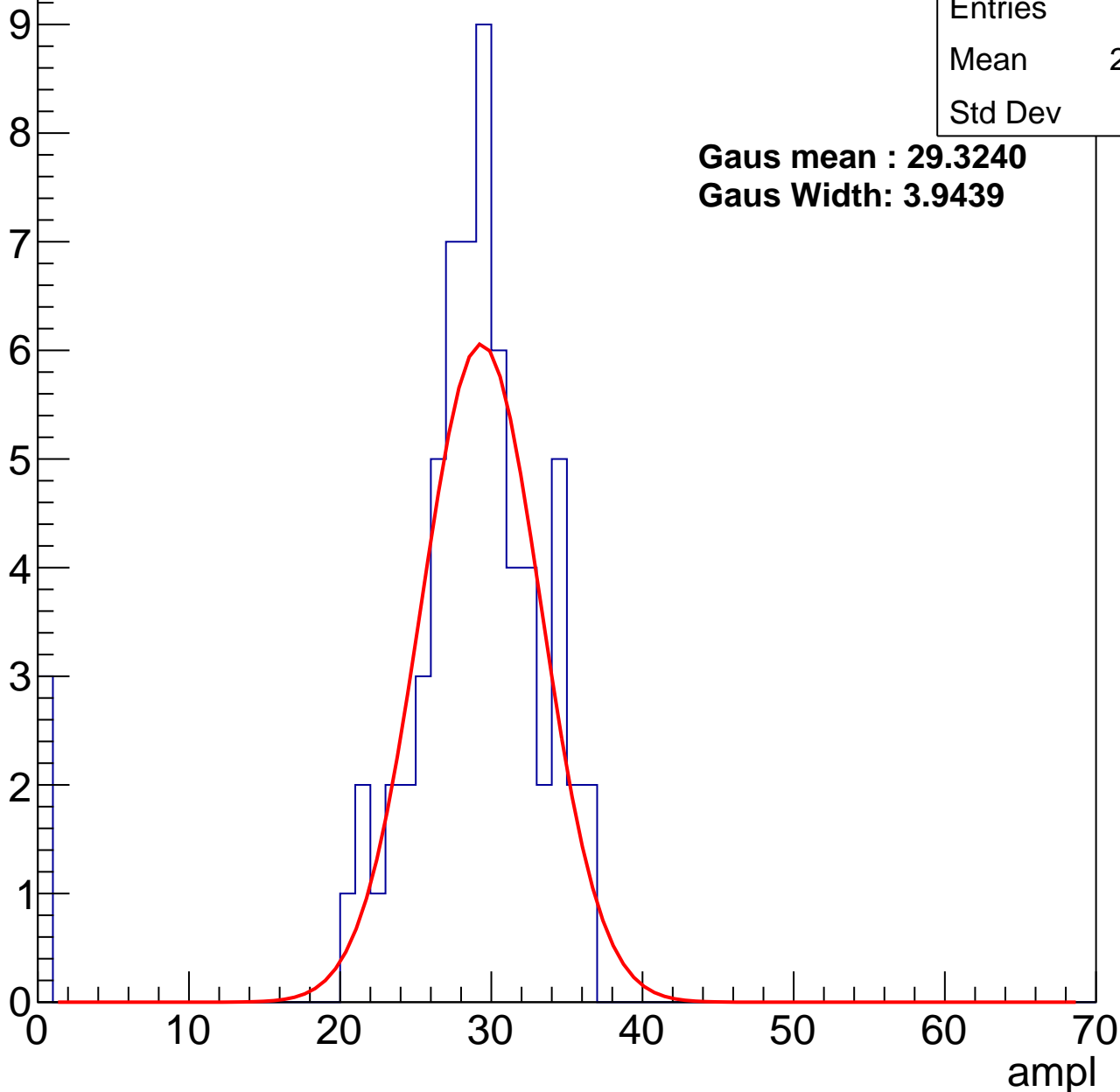
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	27.45
Std Dev	6.97

Gaus mean : 29.3240

Gaus Width: 3.9439



B1L101S, U1-ch103, adc1

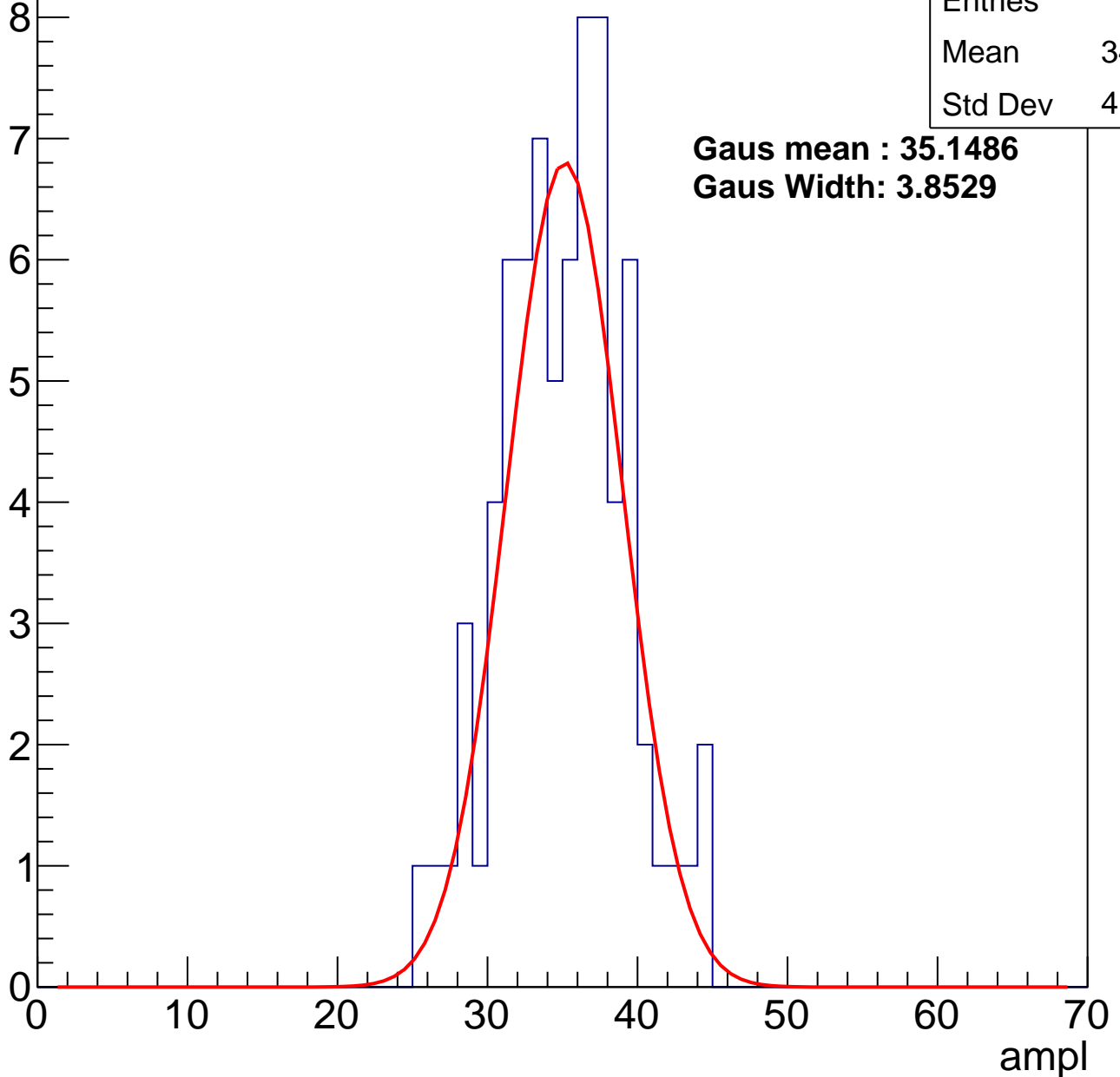
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.65
Std Dev	4.085

Gaus mean : 35.1486

Gaus Width: 3.8529

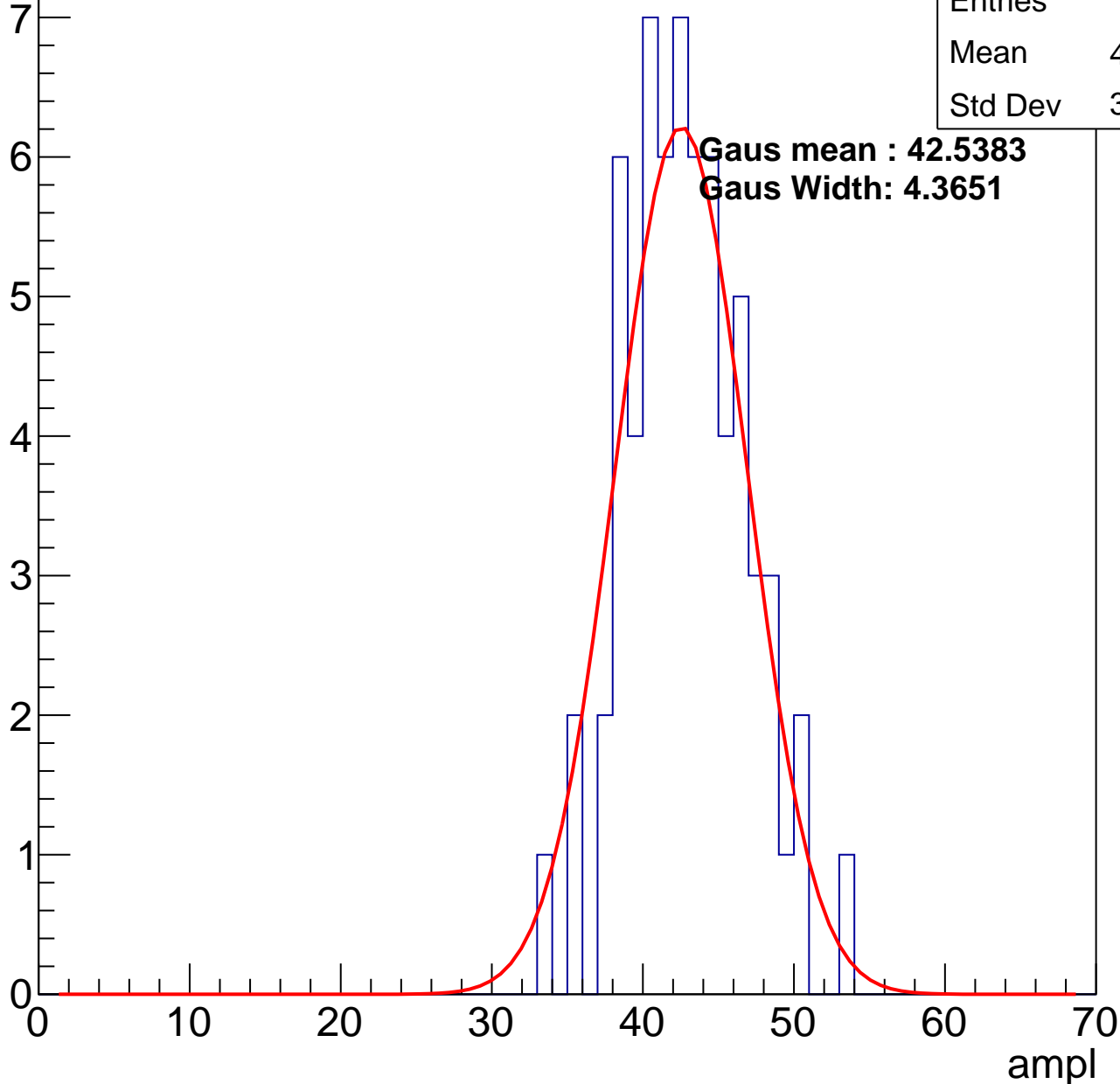


B1L101S, U1-ch103, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.42
Std Dev	3.912

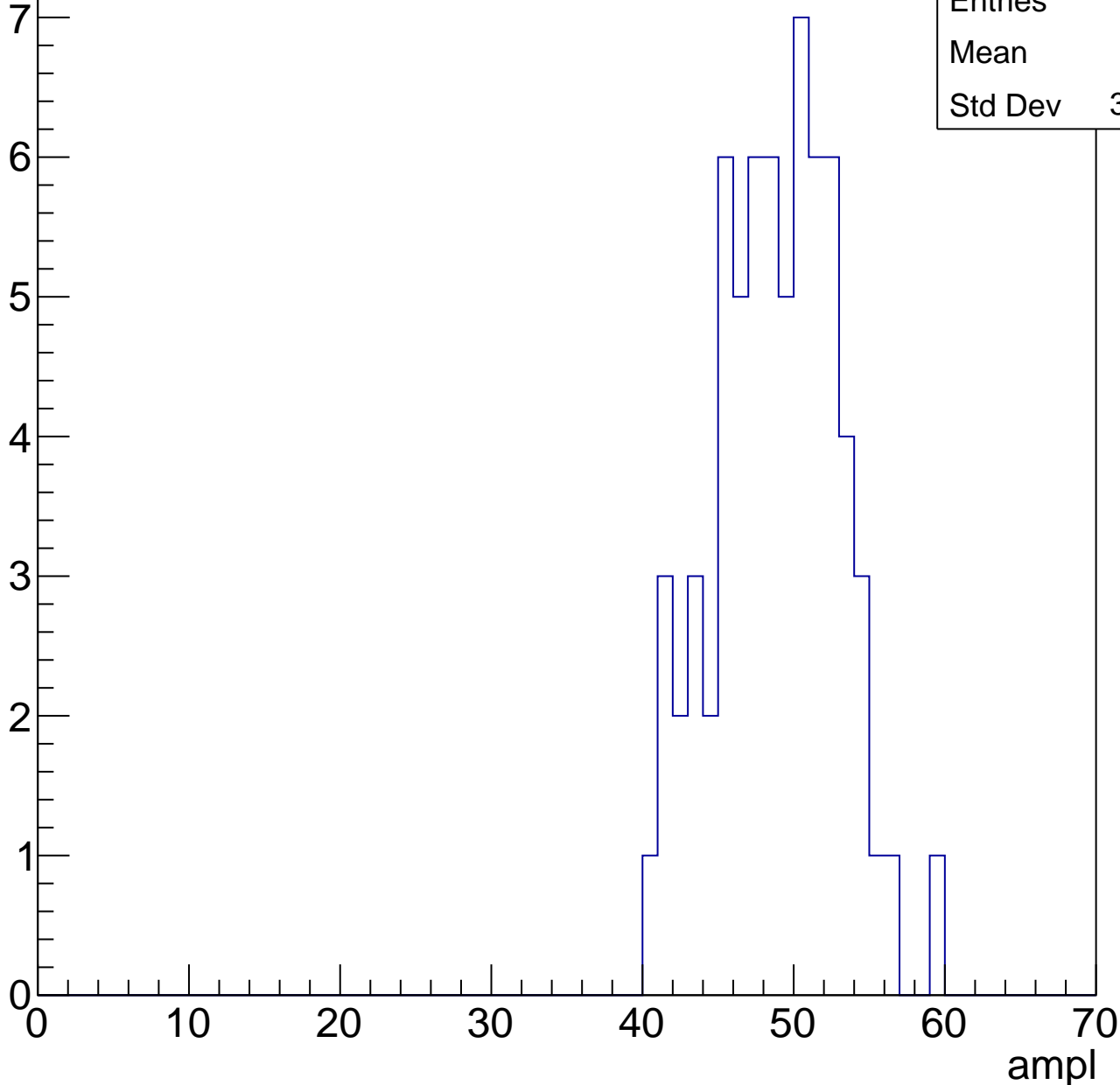


B1L101S, U1-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.4
Std Dev	3.993

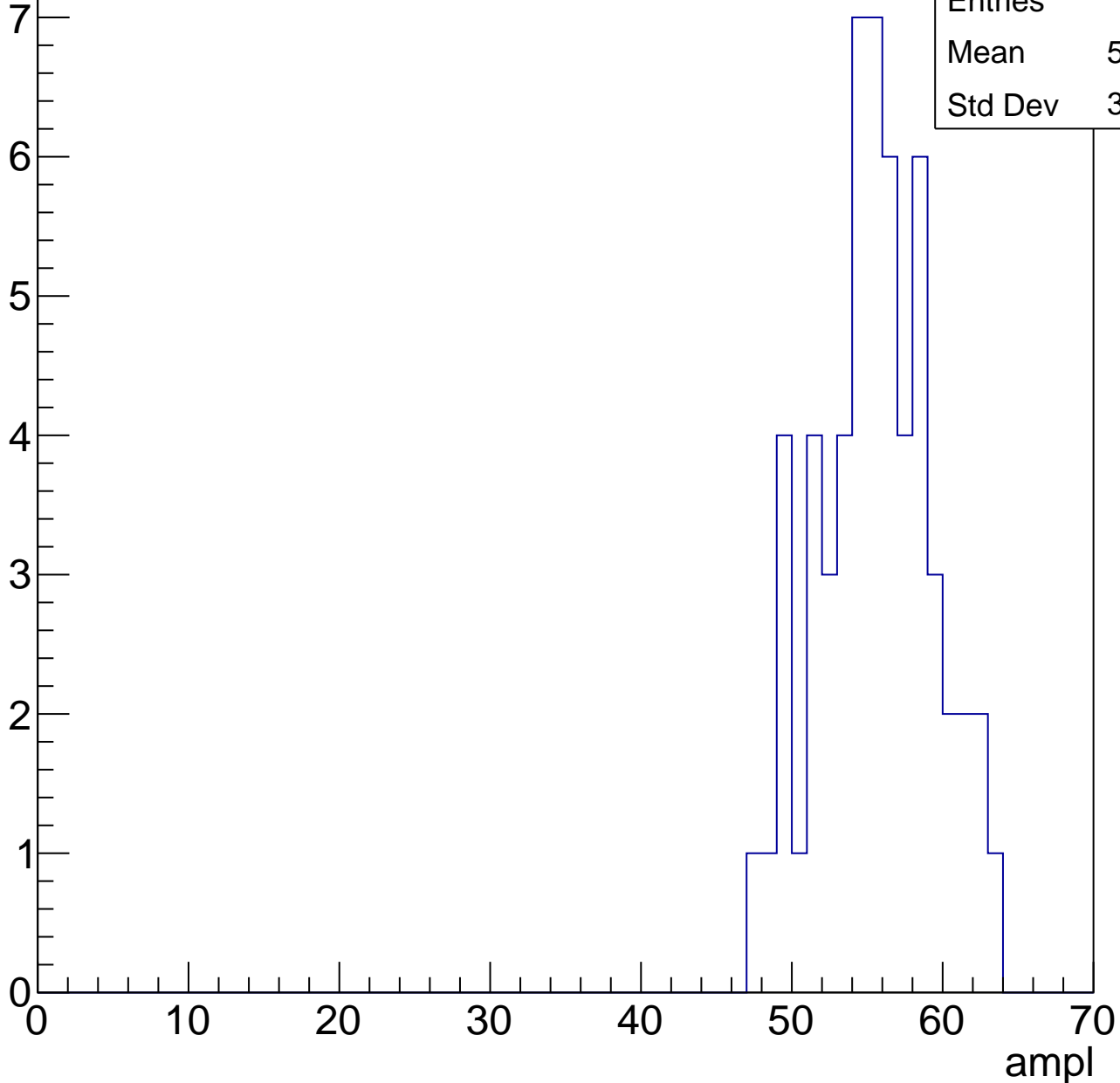


B1L101S, U1-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.07
Std Dev	3.723

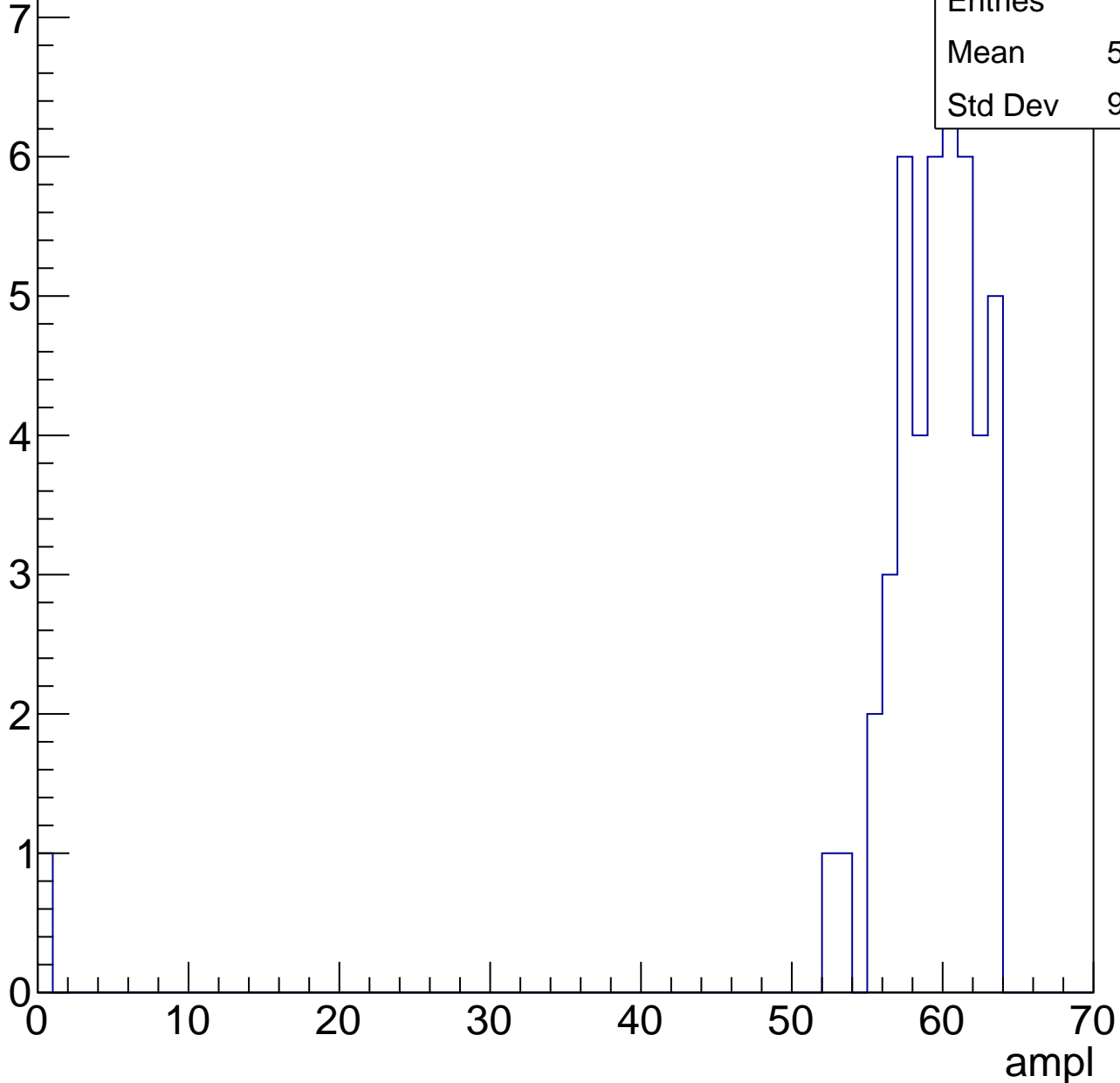


B1L101S, U1-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

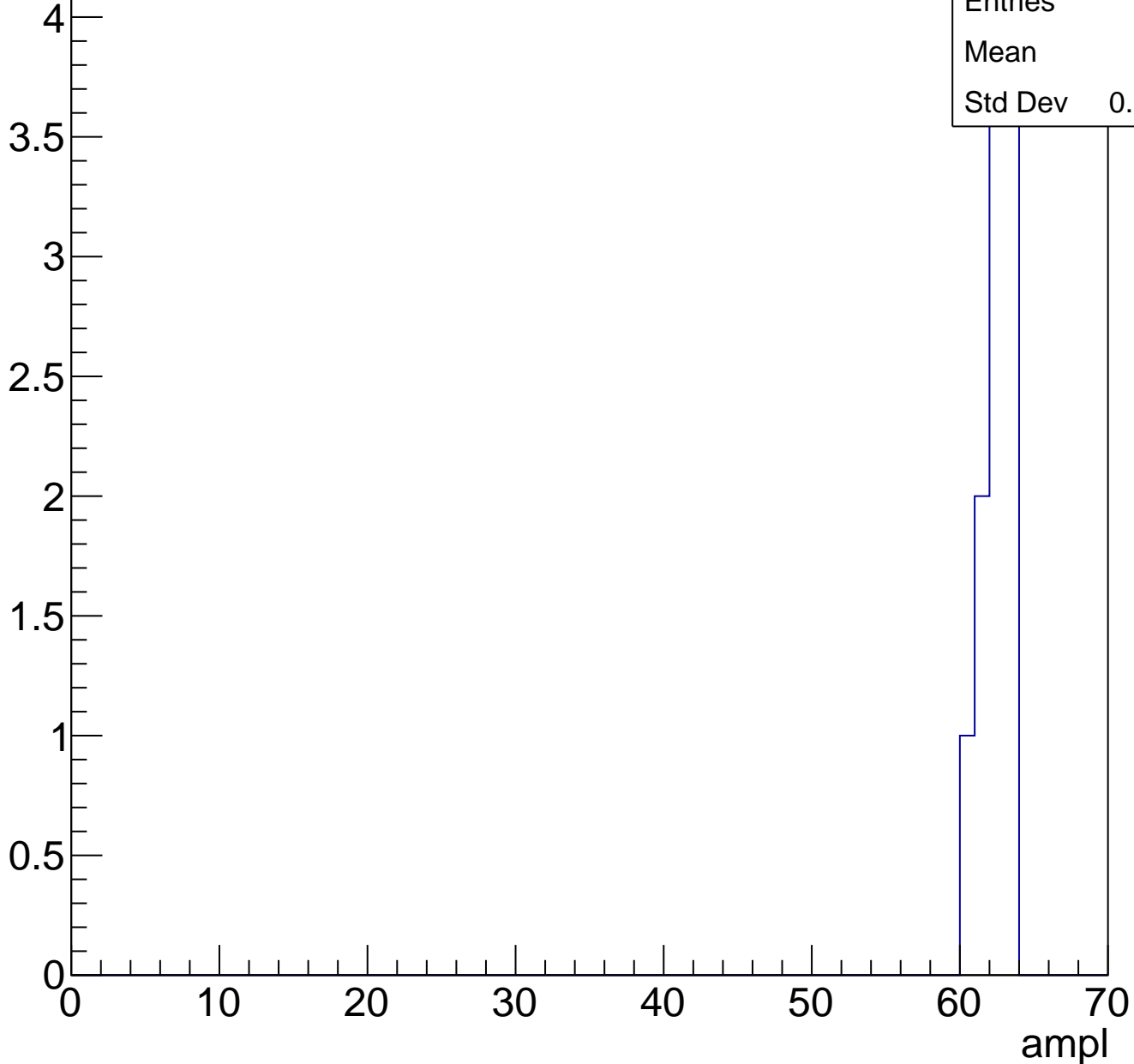
Entries	46
Mean	57.83
Std Dev	9.013



B1L101S, U1-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch104, adc0

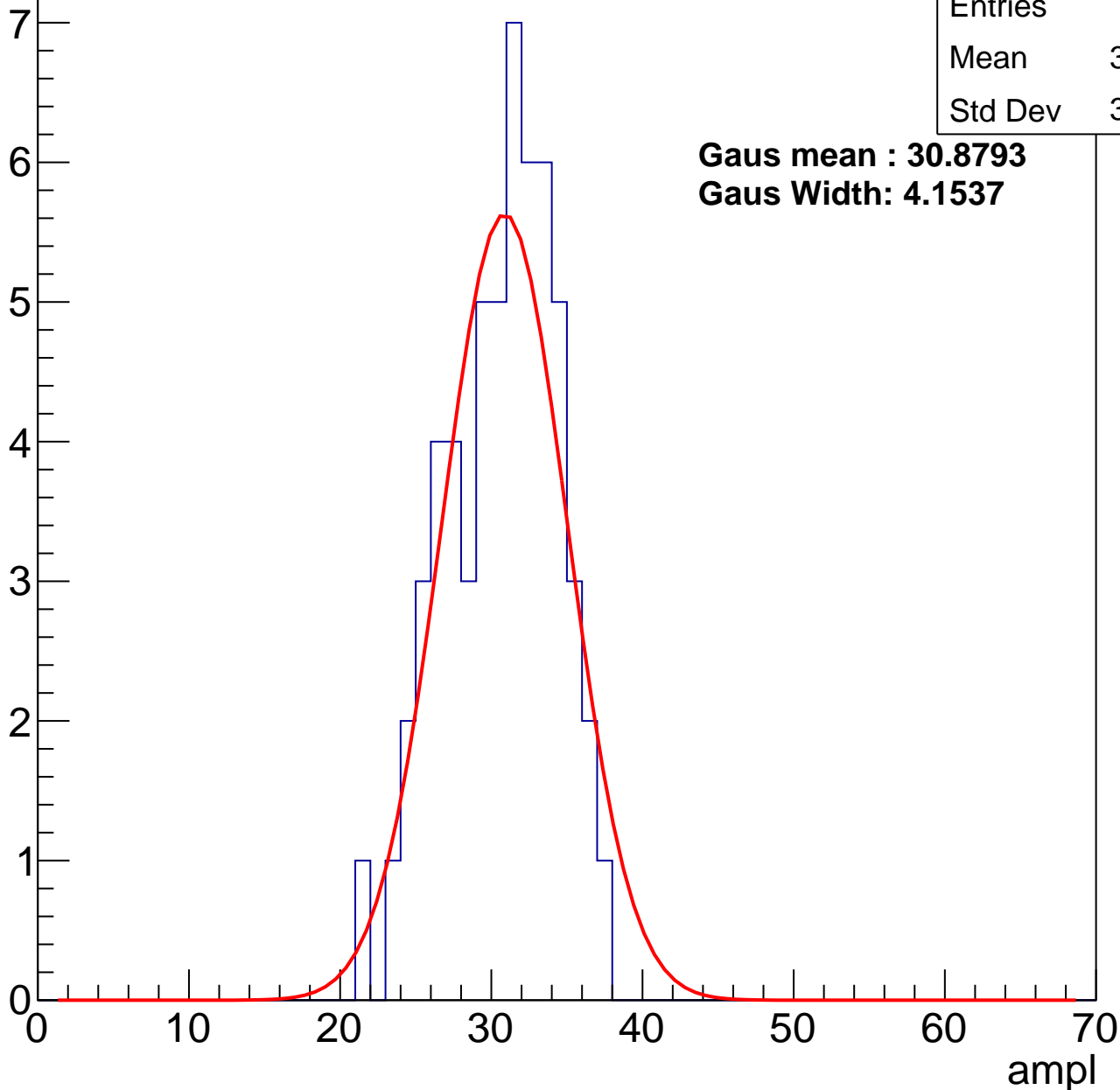
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	30.16
Std Dev	3.605

Gaus mean : 30.8793

Gaus Width: 4.1537



B1L101S, U1-ch104, adc1

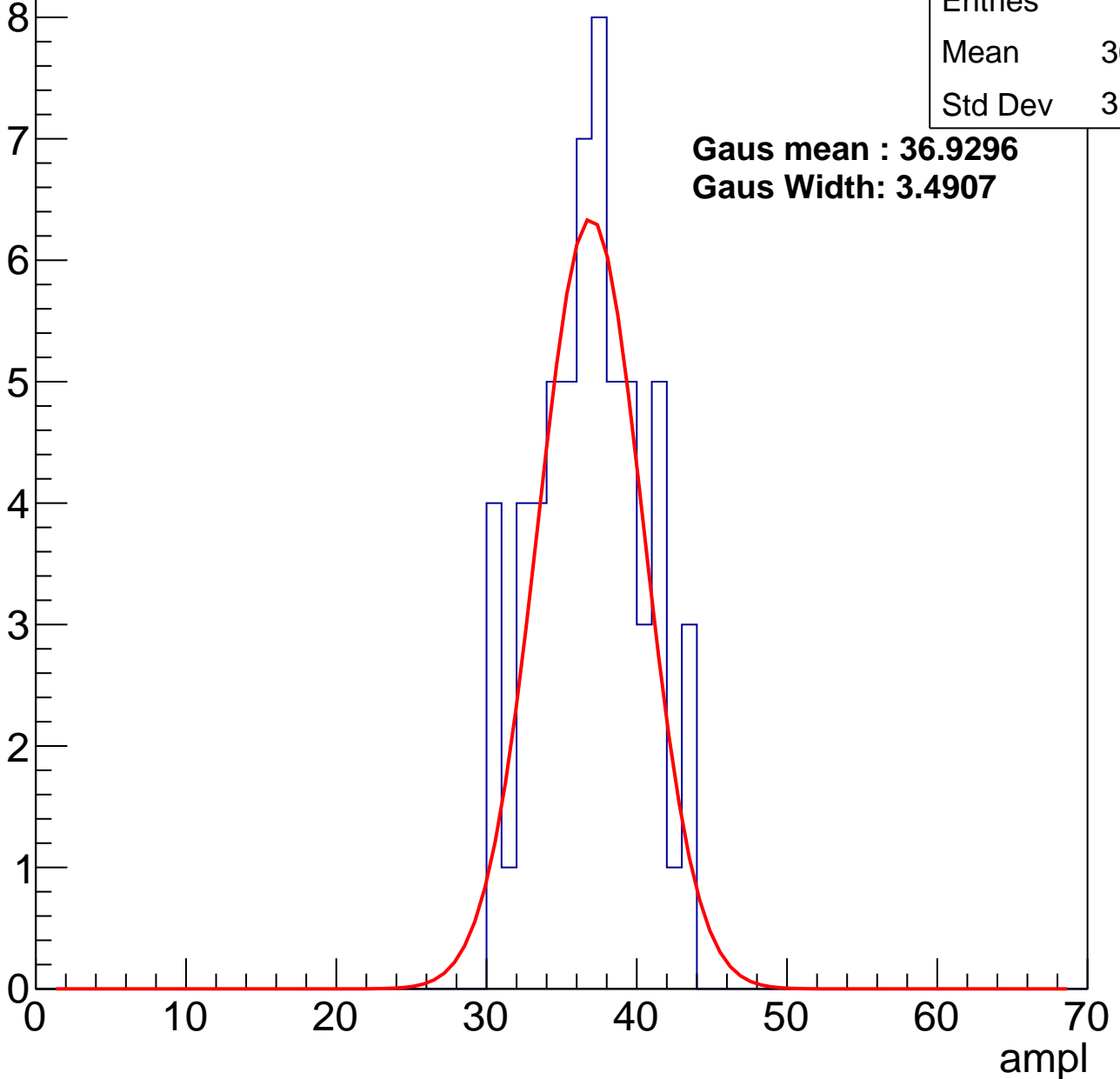
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.42
Std Dev	3.446

Gaus mean : 36.9296

Gaus Width: 3.4907



B1L101S, U1-ch104, adc2

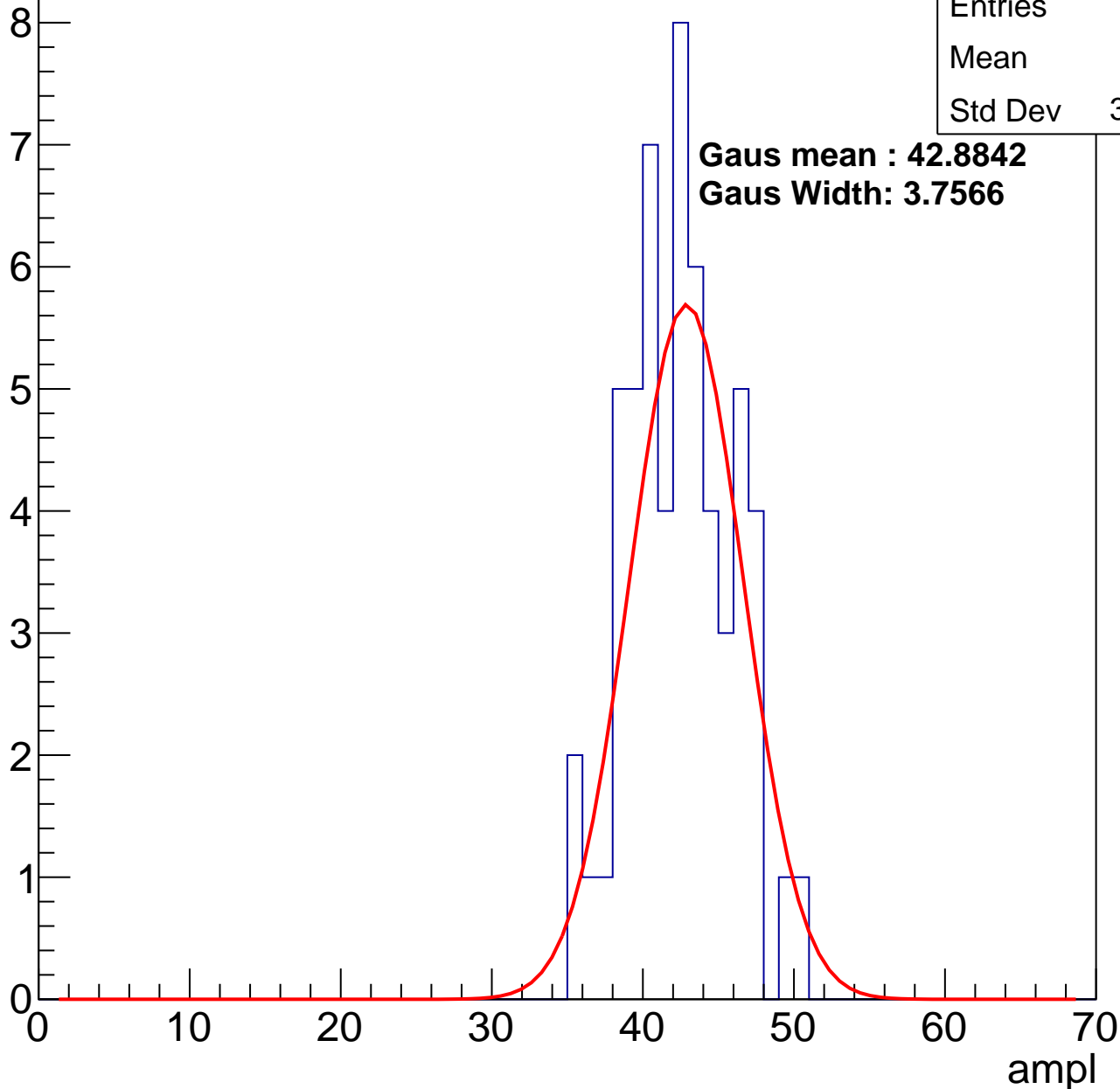
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42
Std Dev	3.398

Gaus mean : 42.8842

Gaus Width: 3.7566

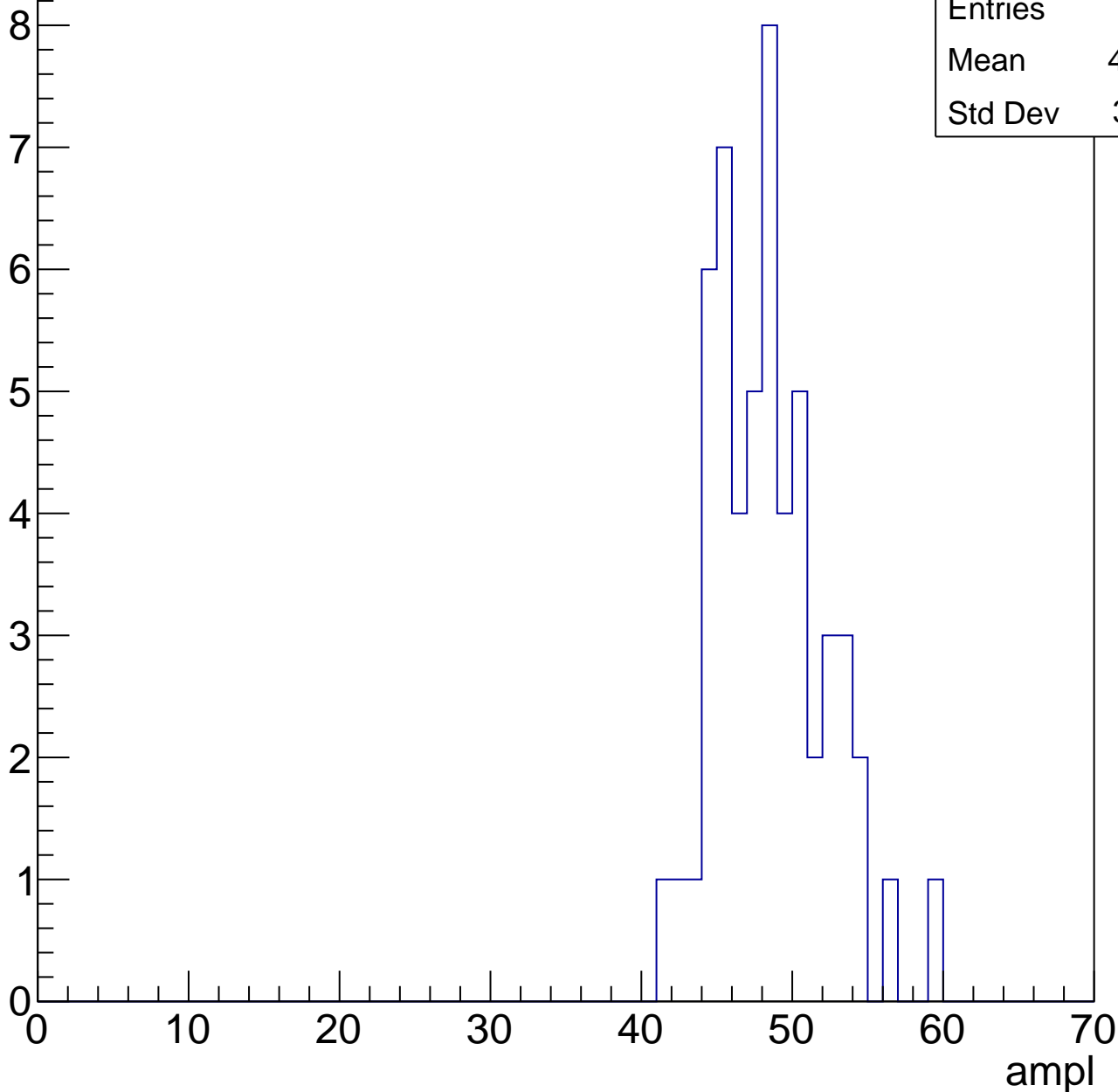


B1L101S, U1-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	48.04
Std Dev	3.631

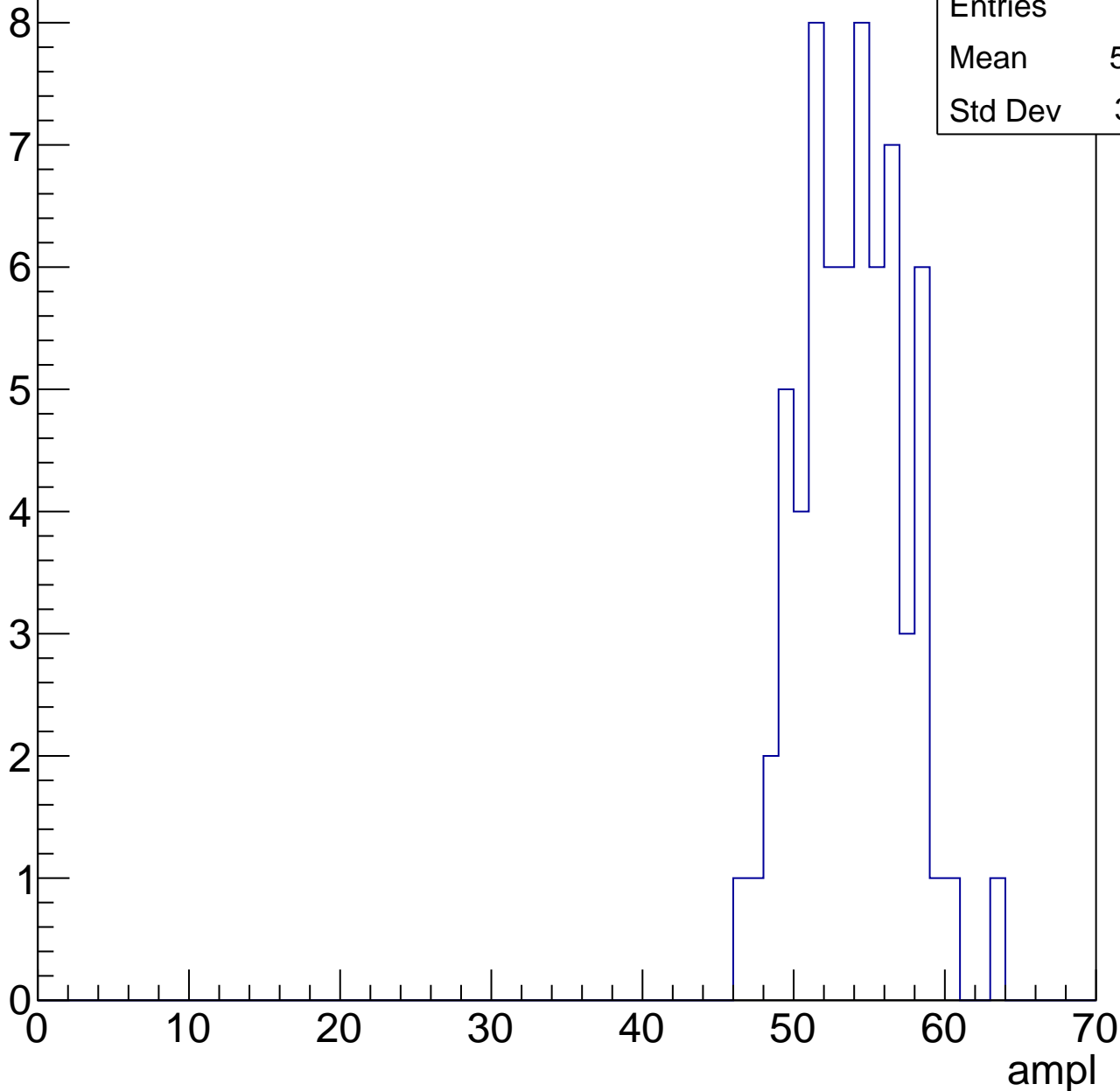


B1L101S, U1-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

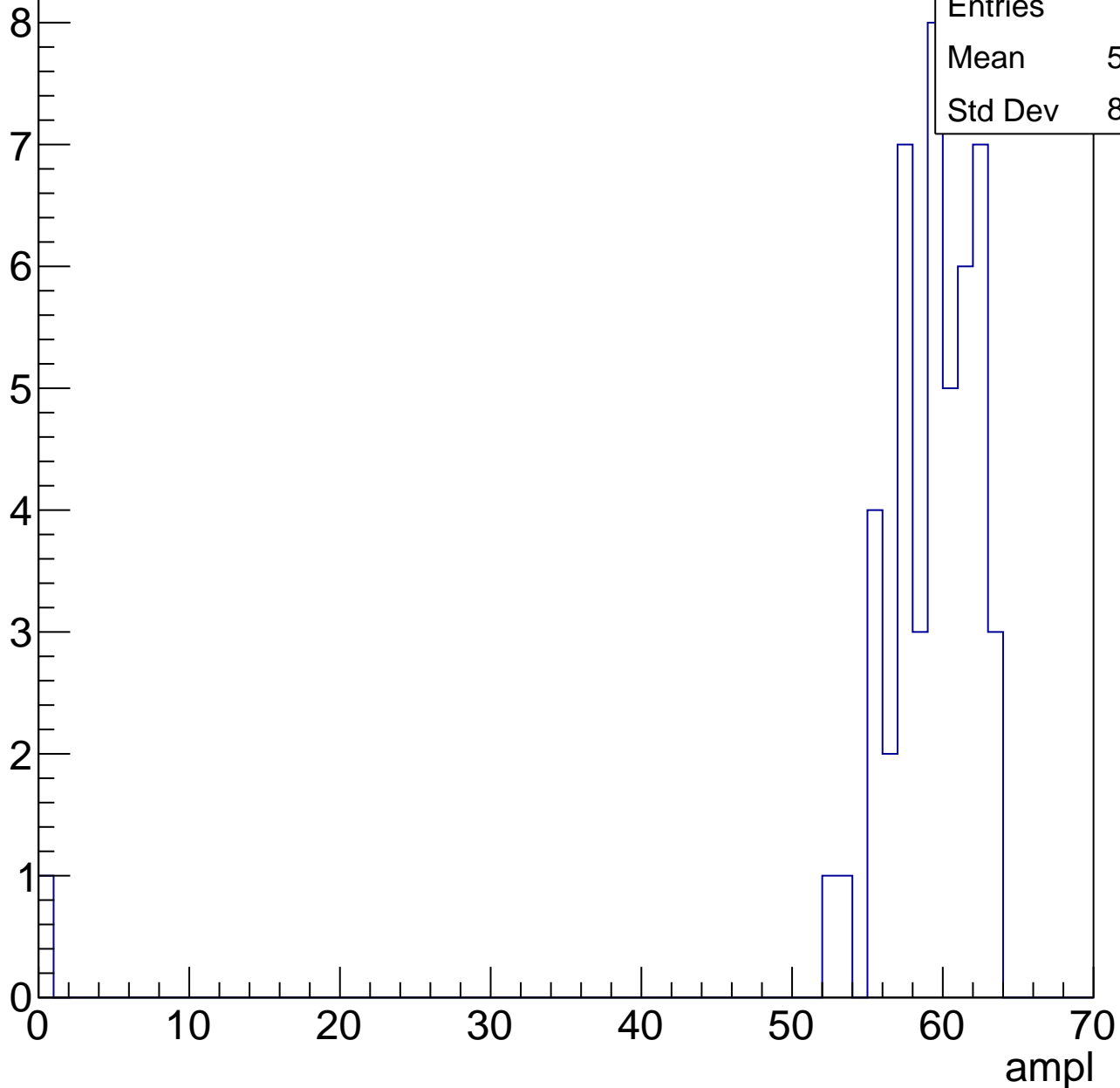
Entries	66
Mean	53.44
Std Dev	3.381



B1L101S, U1-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

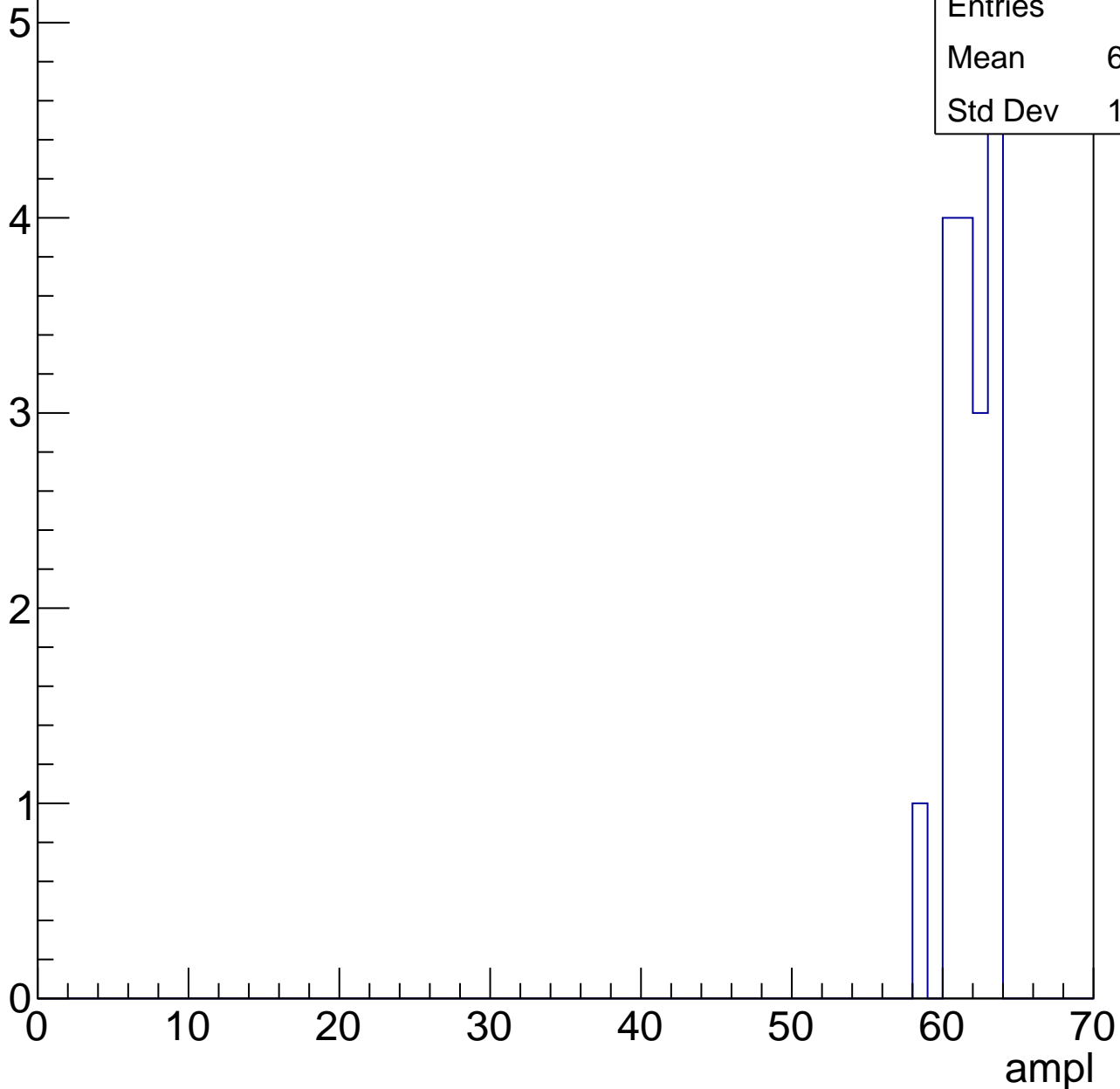


B1L101S, U1-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61.35
Std Dev	1.412



B1L101S, U1-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch105, adc0

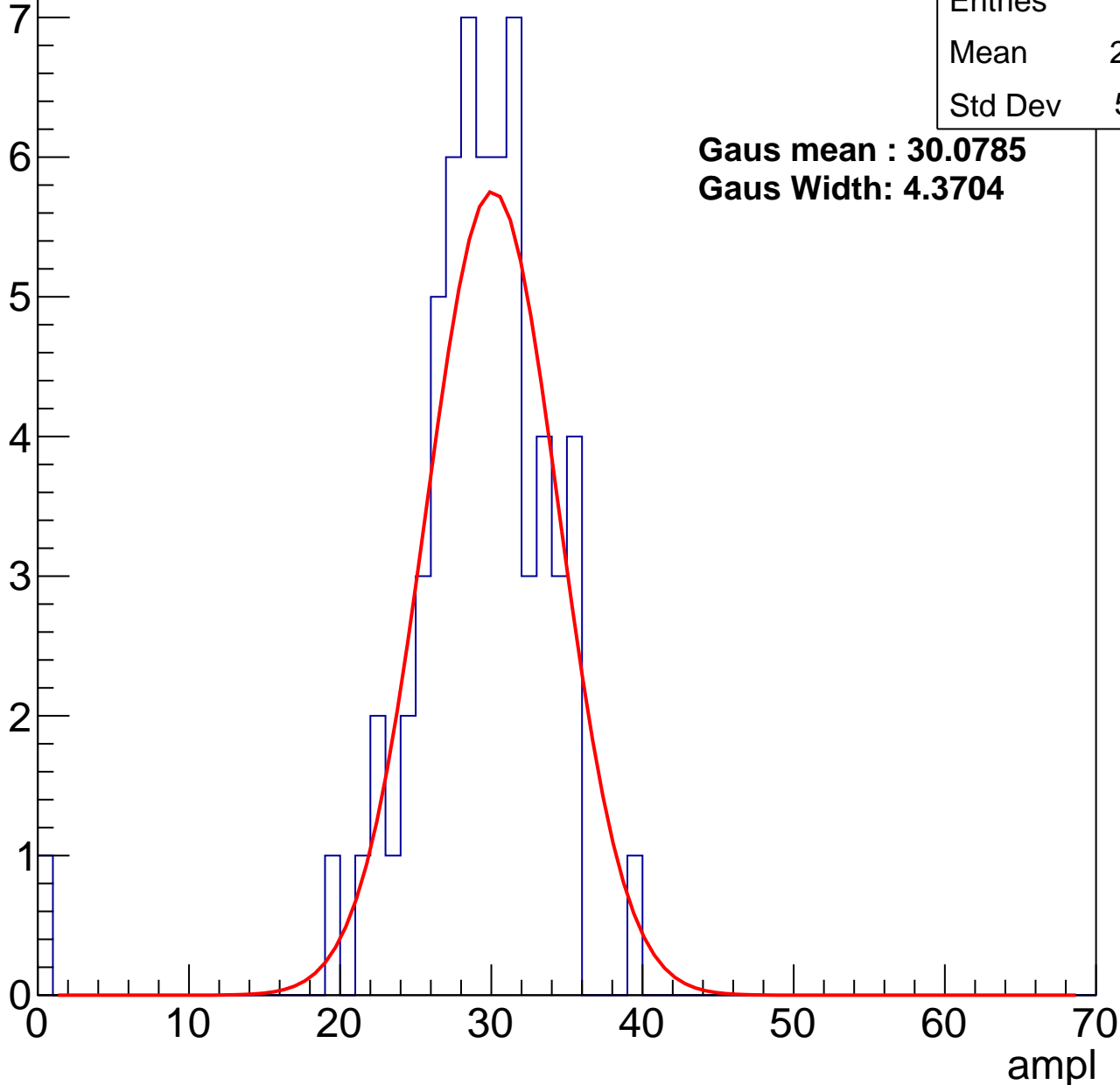
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.54
Std Dev	5.261

Gaus mean : 30.0785

Gaus Width: 4.3704



B1L101S, U1-ch105, adc1

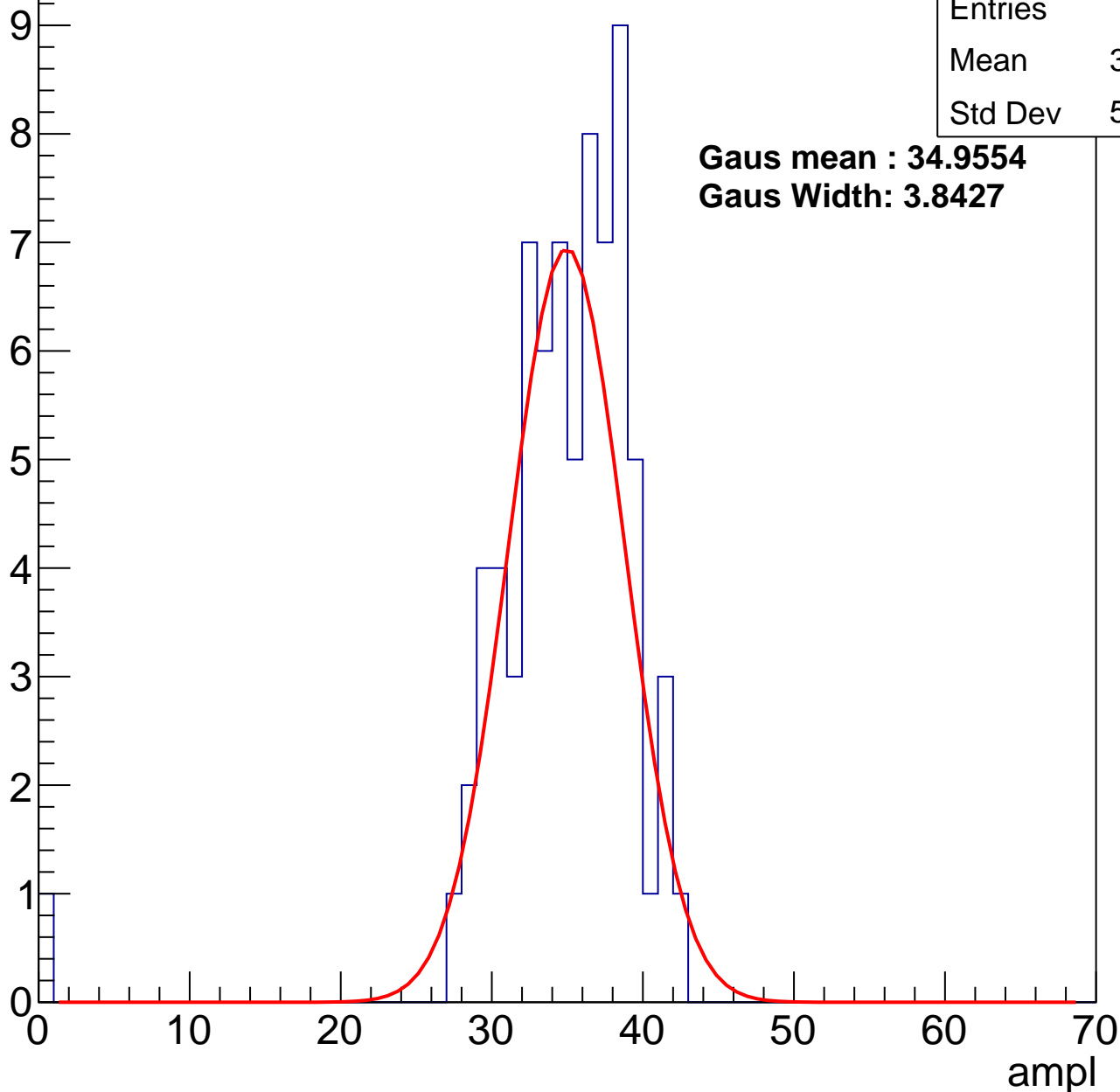
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.27
Std Dev	5.343

Gaus mean : 34.9554

Gaus Width: 3.8427



B1L101S, U1-ch105, adc2

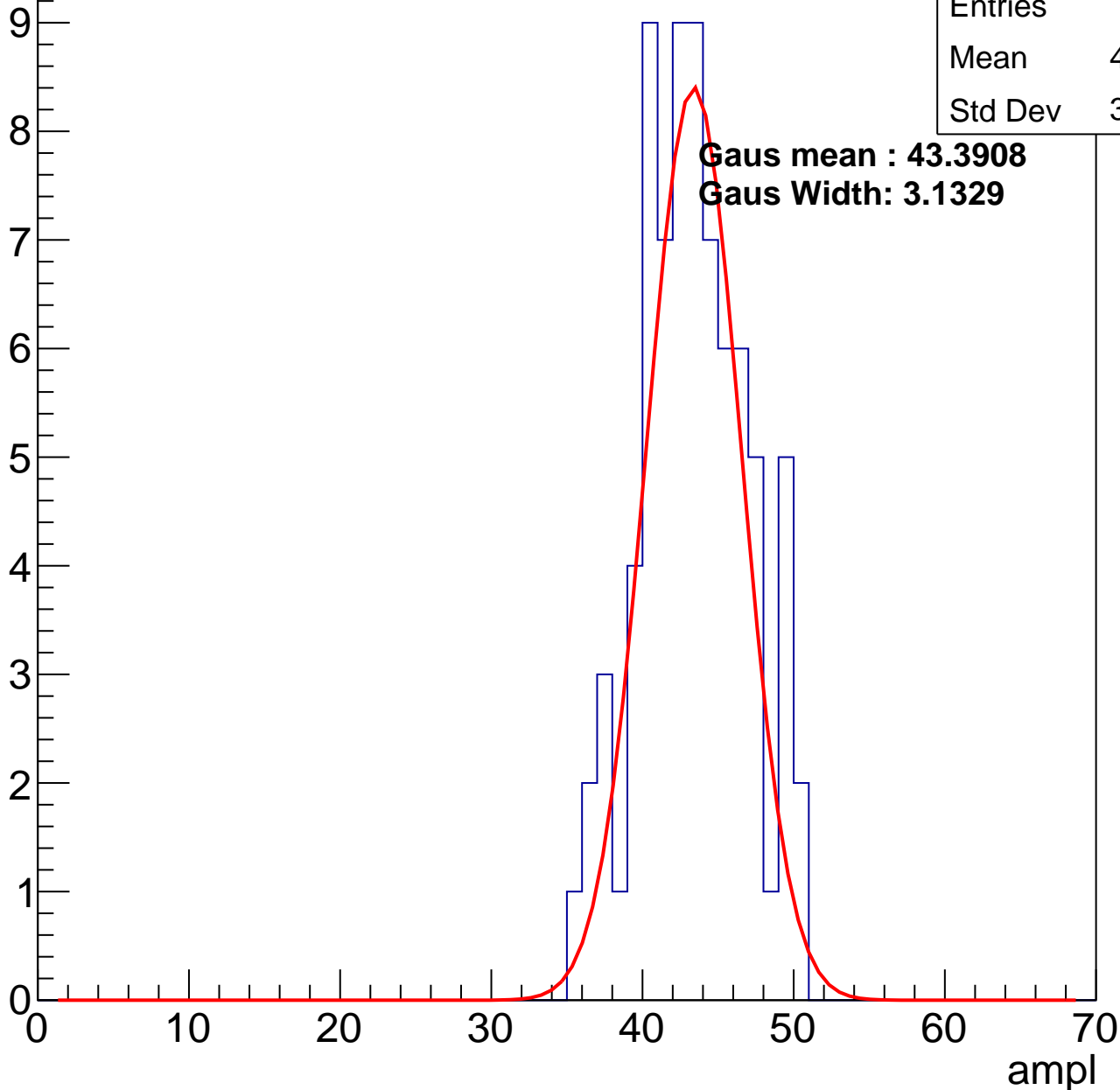
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	42.94
Std Dev	3.503

Gaus mean : 43.3908

Gaus Width: 3.1329

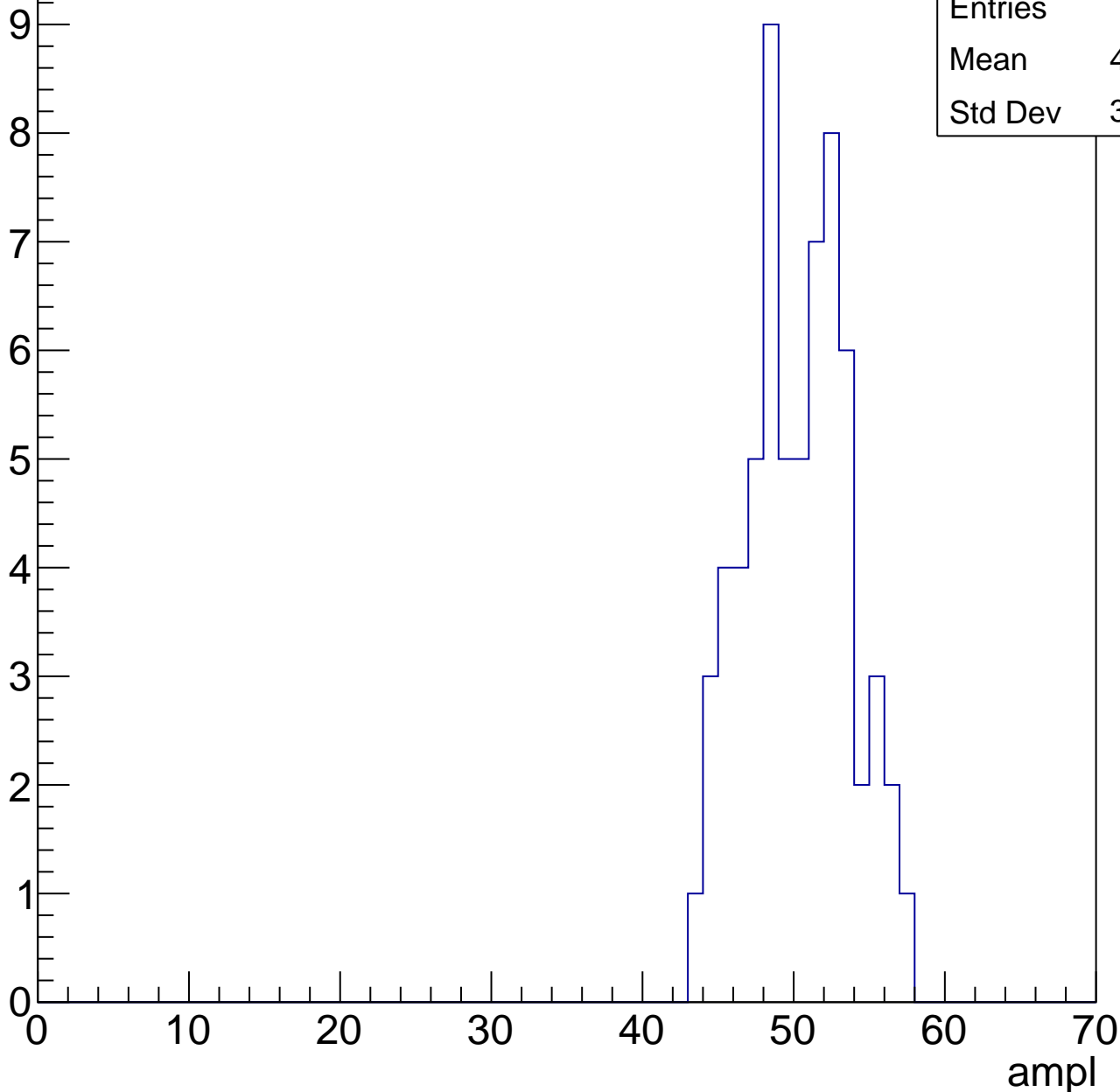


B1L101S, U1-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.75
Std Dev	3.338

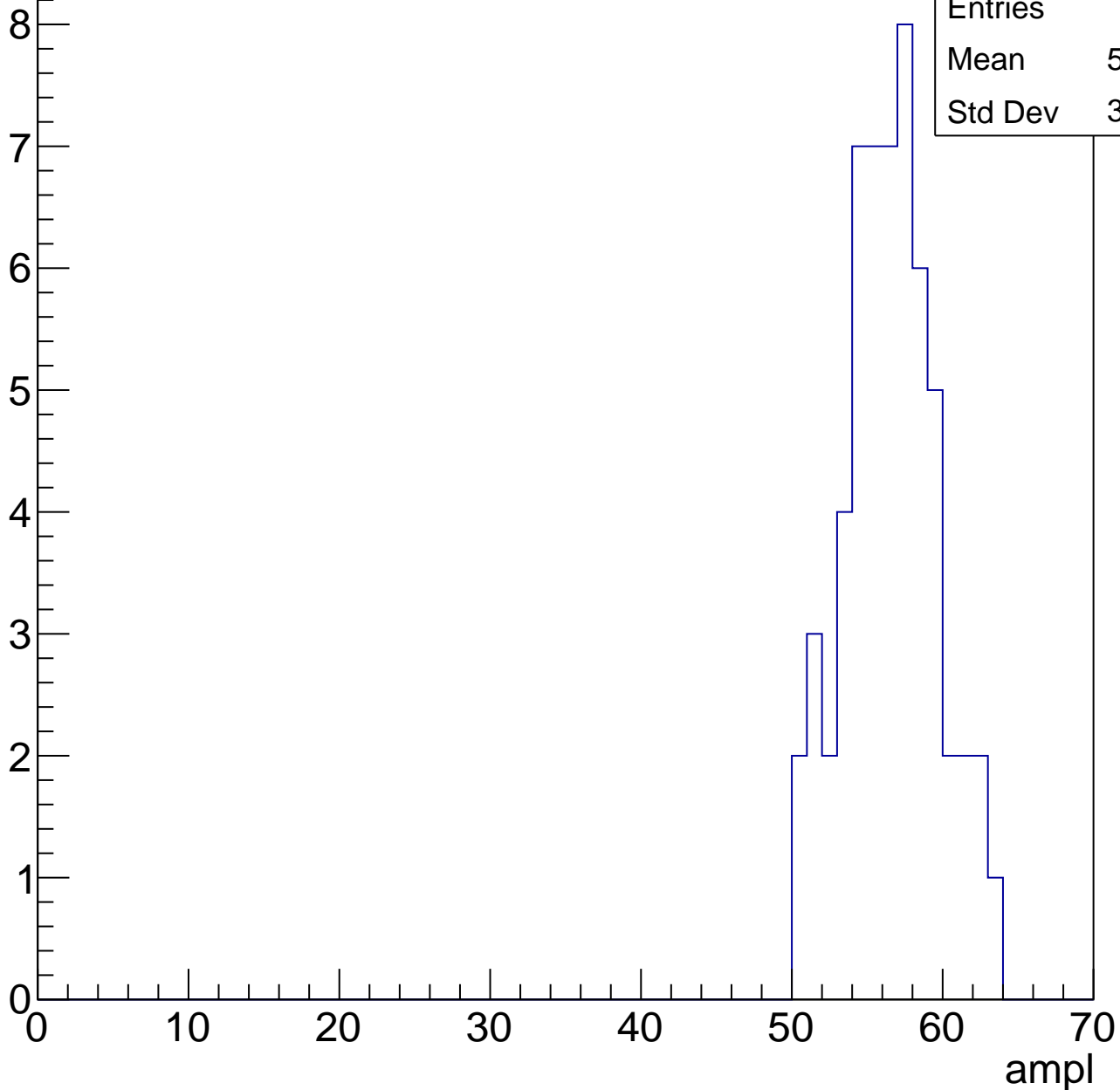


B1L101S, U1-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

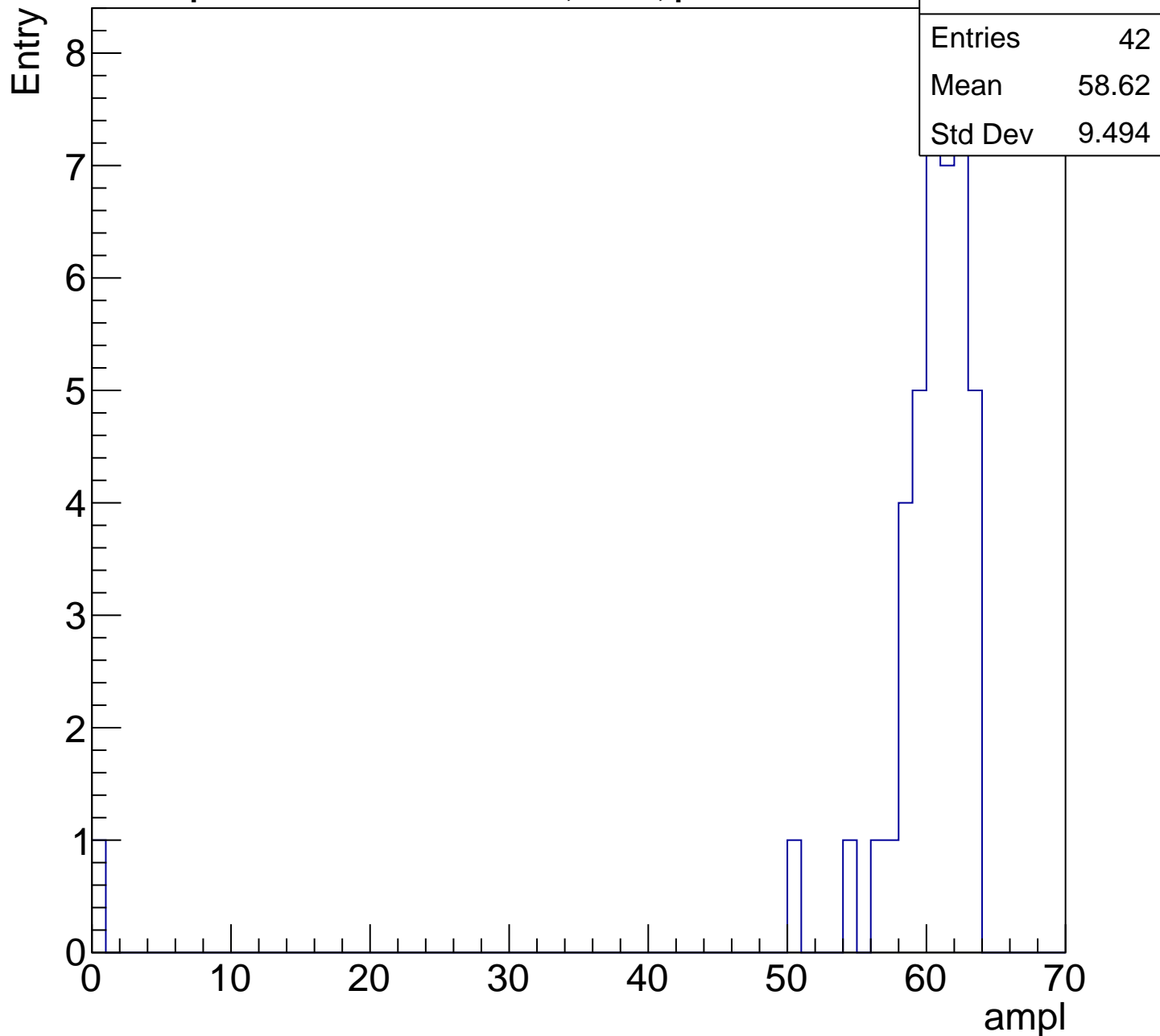
Entry

Entries	58
Mean	56.07
Std Dev	3.022



B1L101S, U1-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U1-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch106, adc0

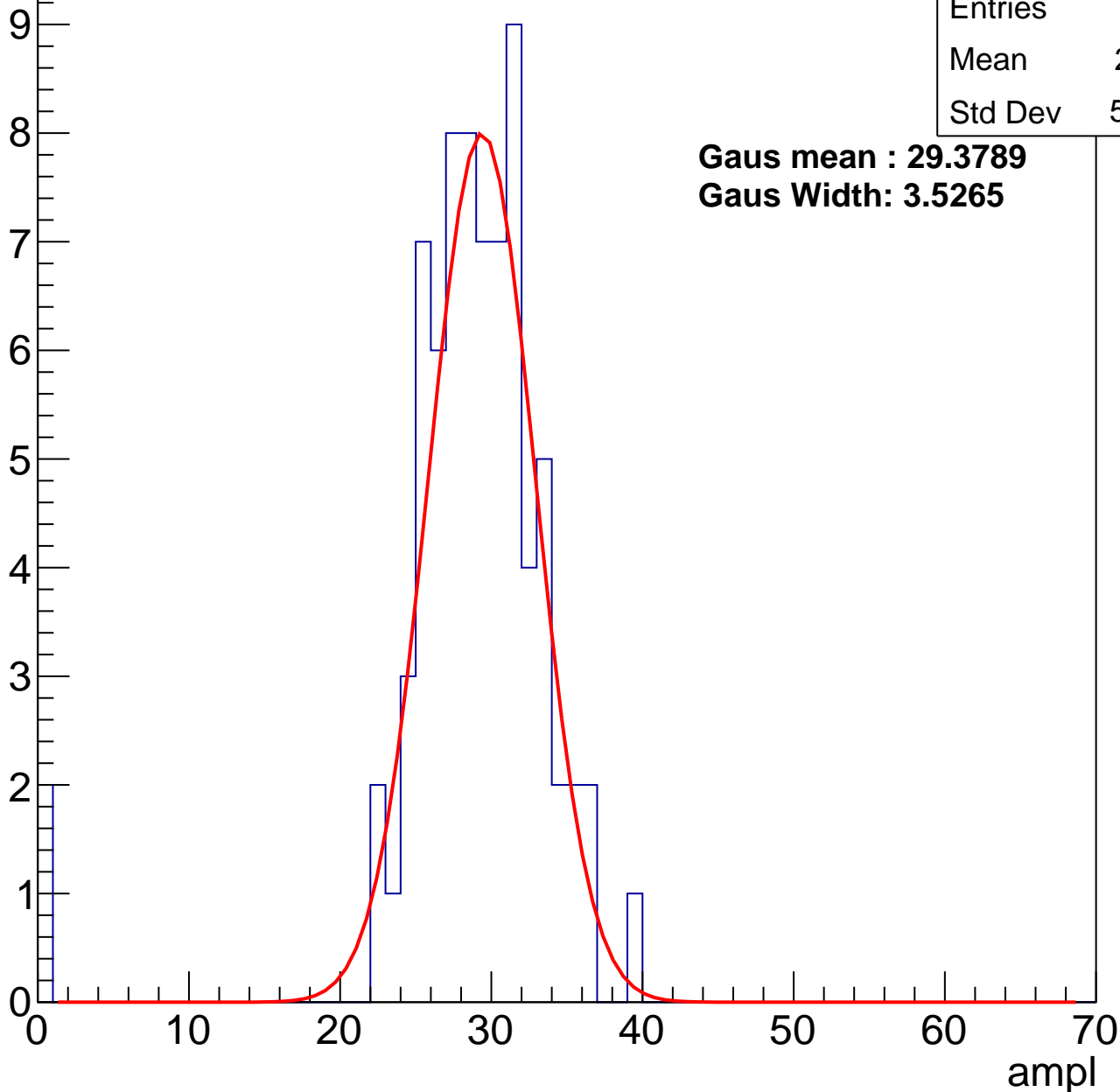
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.21
Std Dev	5.768

Gaus mean : 29.3789

Gaus Width: 3.5265



B1L101S, U1-ch106, adc1

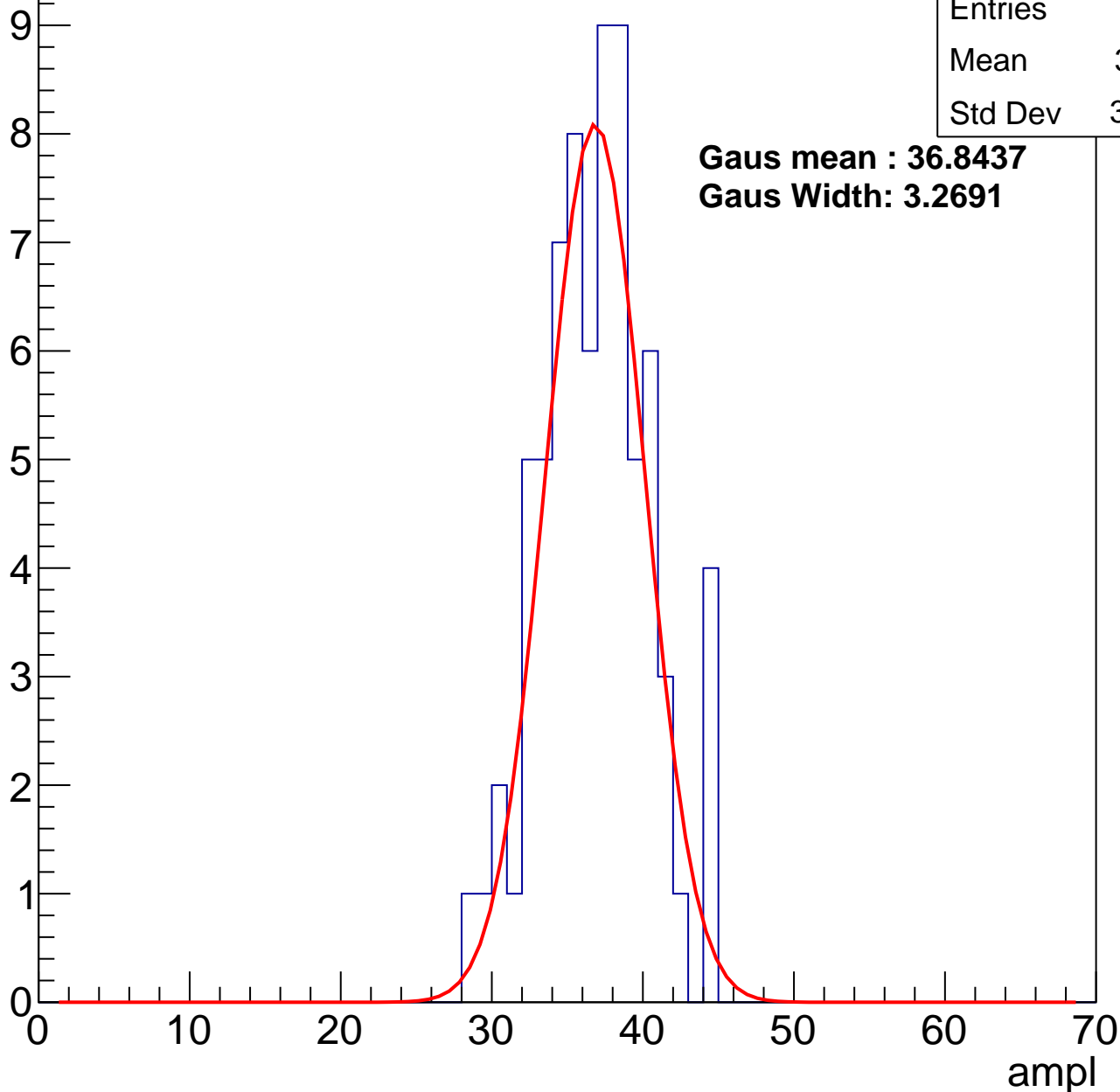
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.41
Std Dev	3.534

Gaus mean : 36.8437

Gaus Width: 3.2691



B1L101S, U1-ch106, adc2

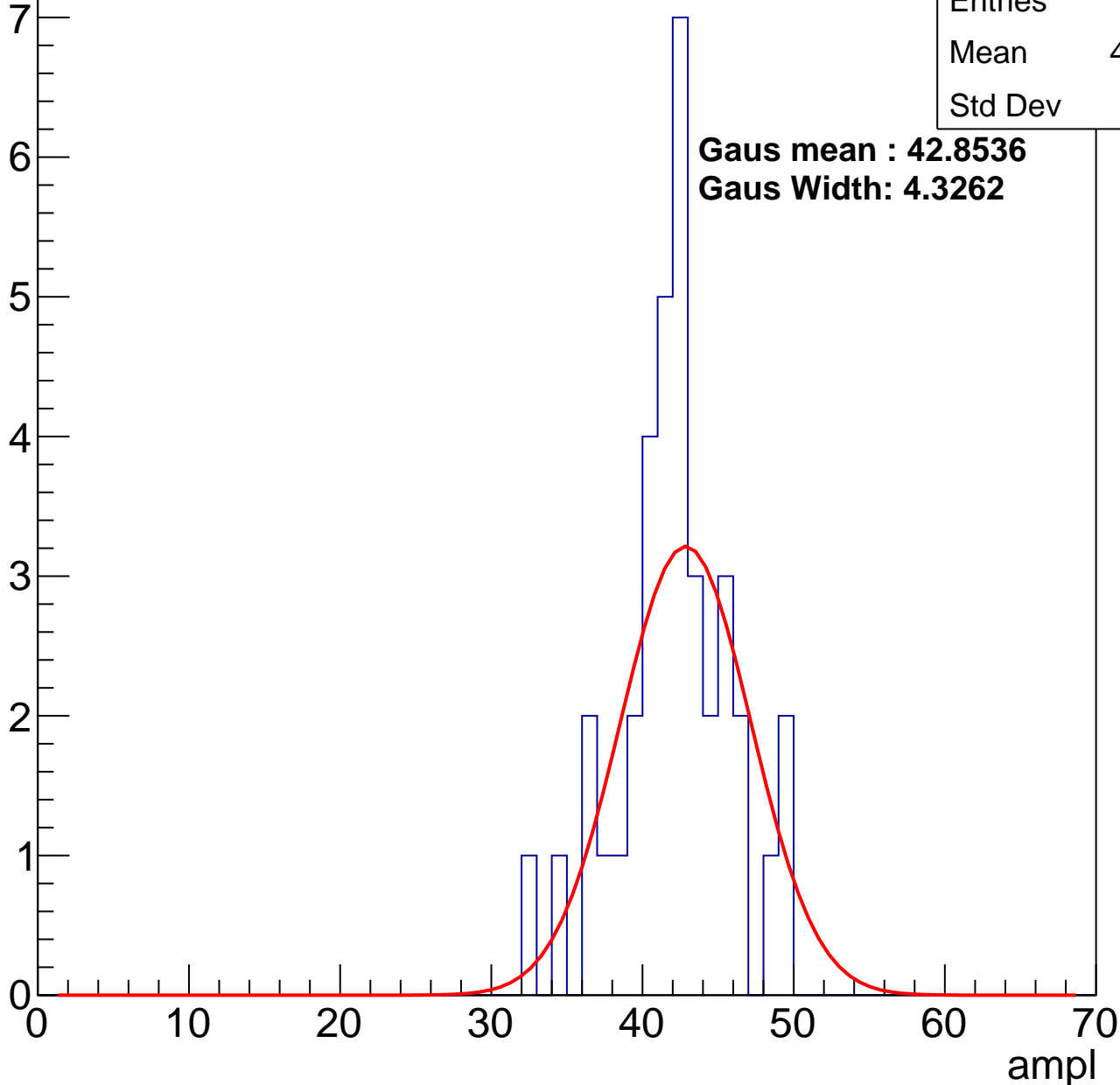
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	41.62
Std Dev	3.73

Gaus mean : 42.8536

Gaus Width: 4.3262

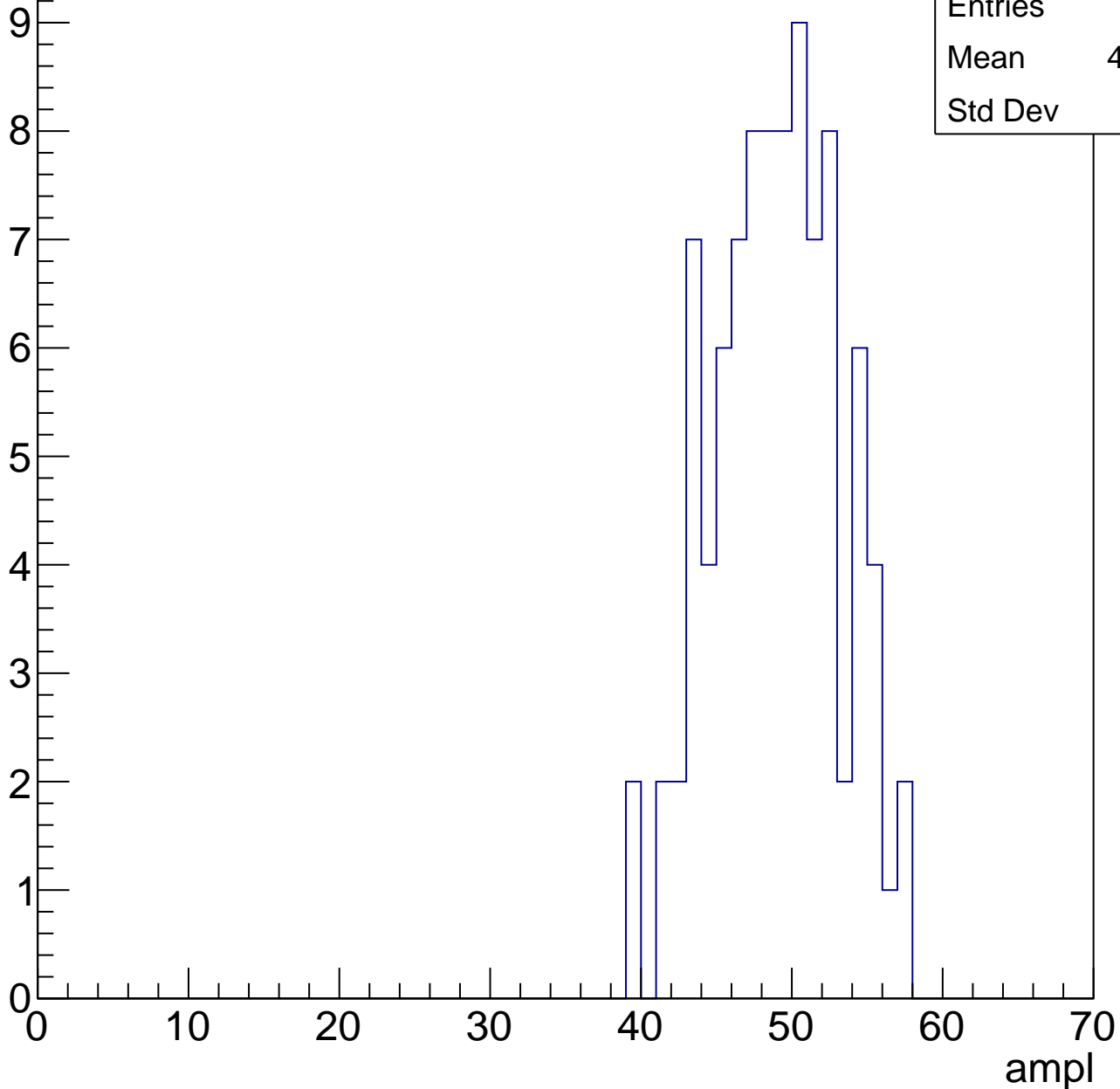


B1L101S, U1-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	48.47
Std Dev	4.1

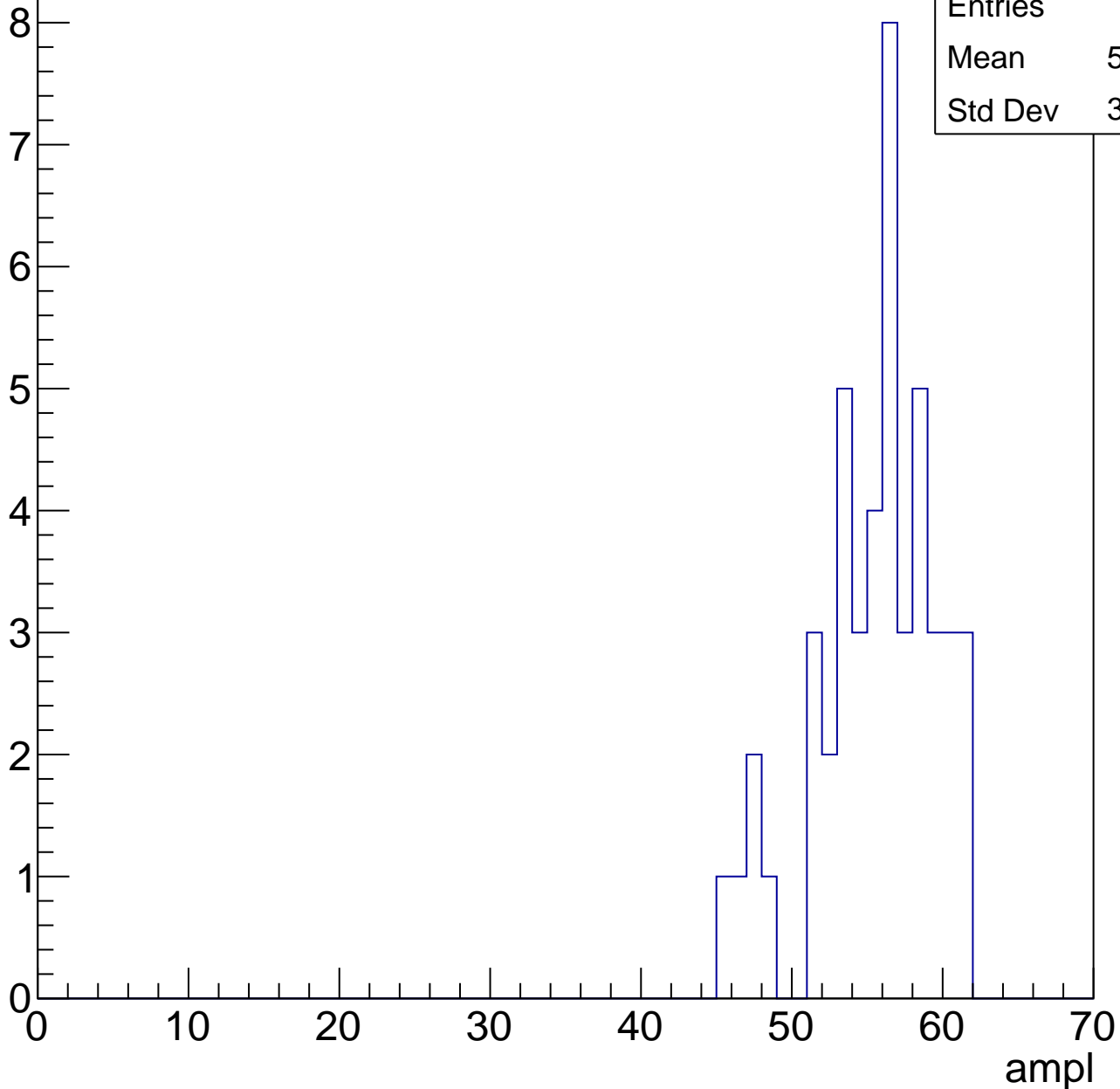


B1L101S, U1-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	55.02
Std Dev	3.976

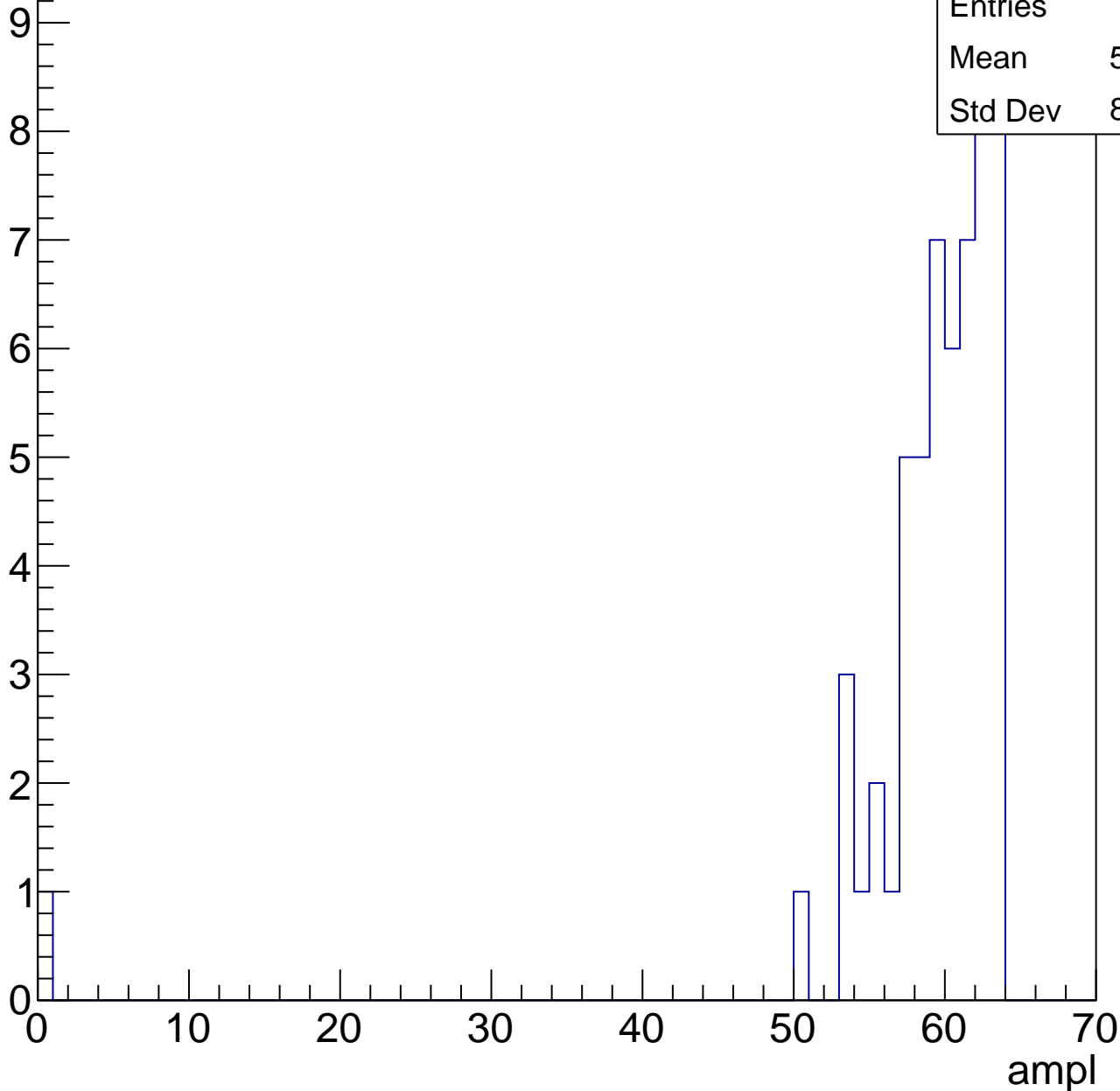


B1L101S, U1-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

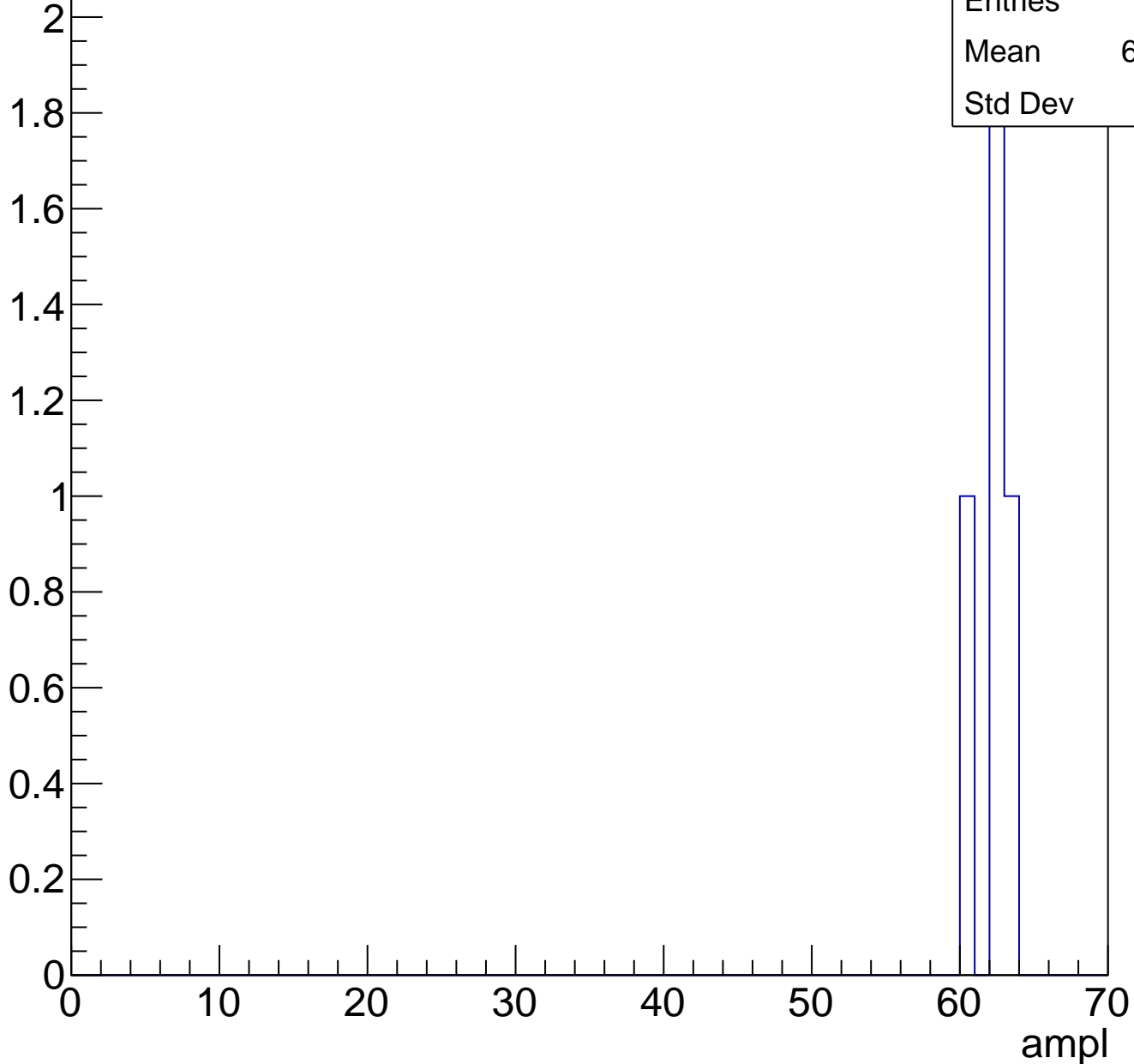
Entries	56
Mean	58.34
Std Dev	8.437



B1L101S, U1-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch107, adc0

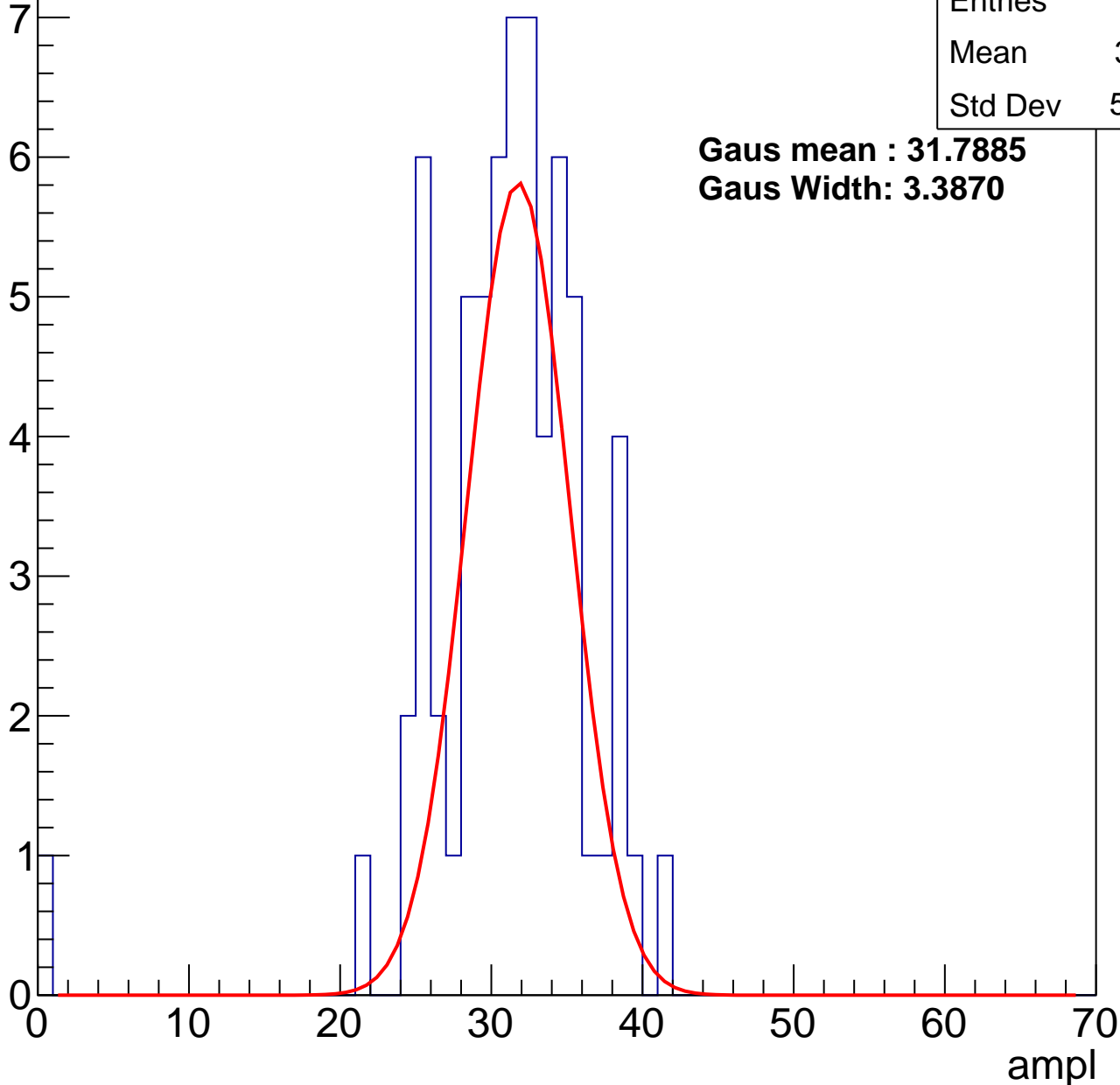
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.61
Std Dev	5.614

Gaus mean : 31.7885

Gaus Width: 3.3870



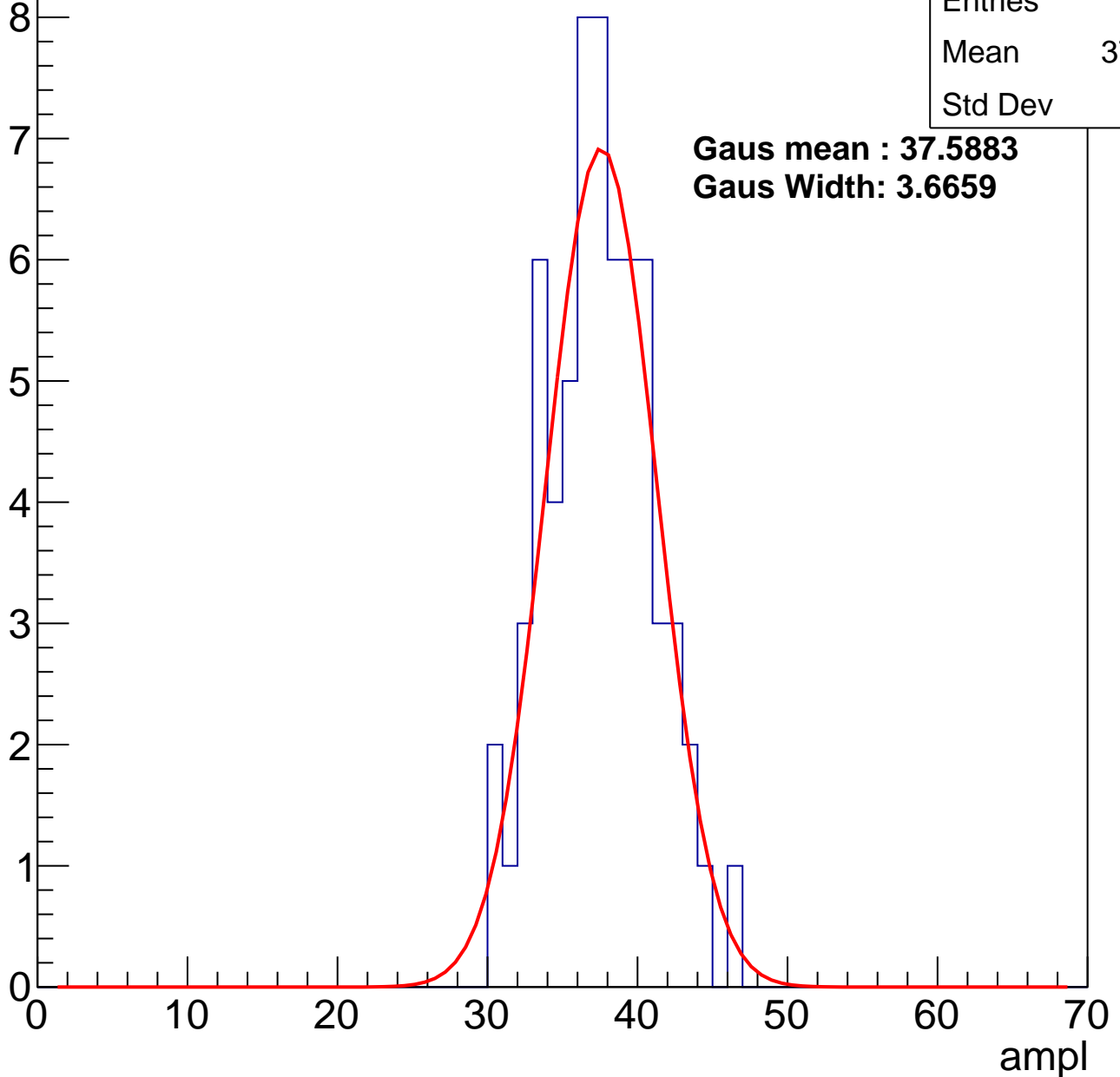
B1L101S, U1-ch107, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.03
Std Dev	3.46

Gaus mean : 37.5883
Gaus Width: 3.6659



B1L101S, U1-ch107, adc2

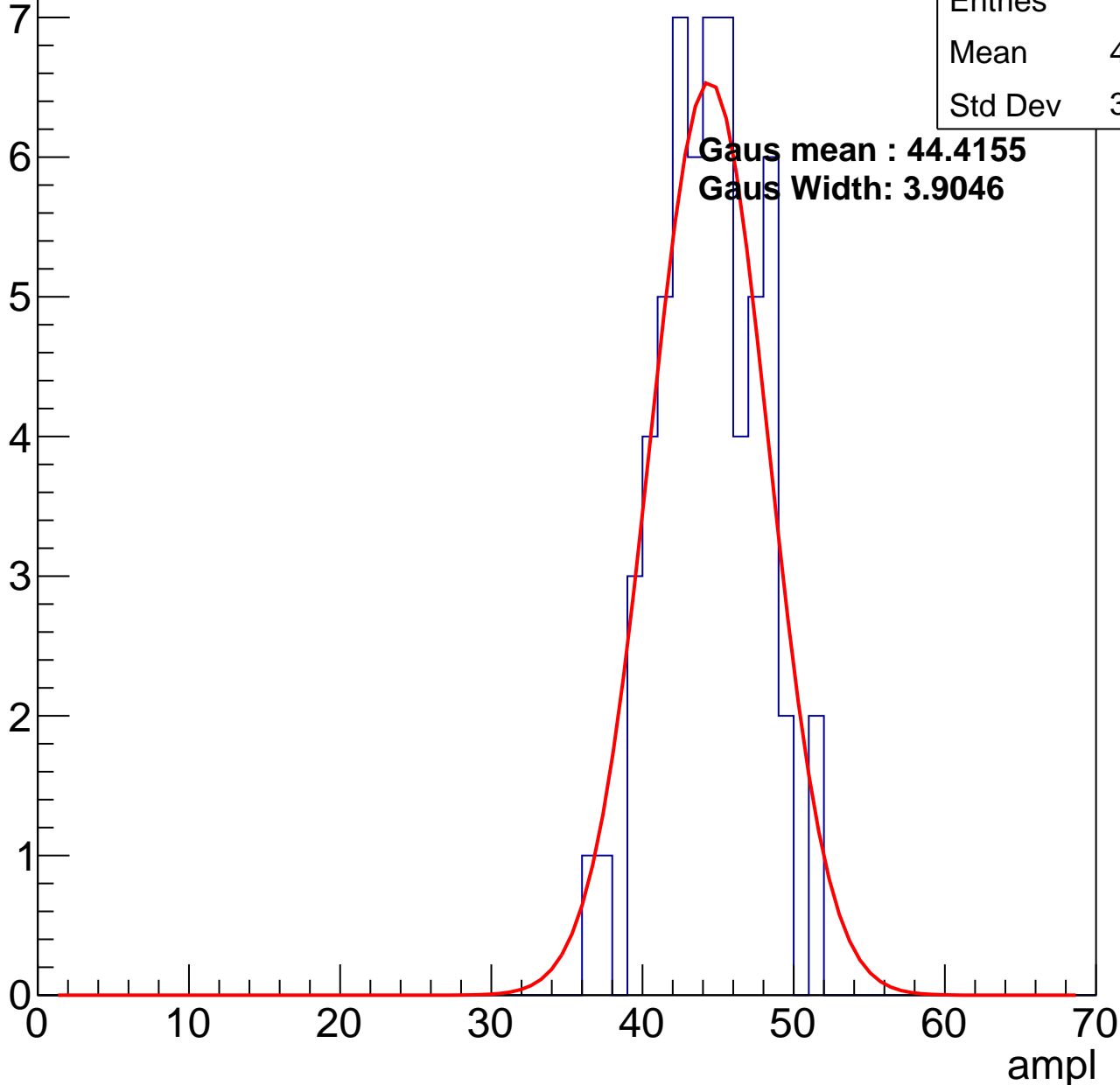
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.95
Std Dev	3.273

Gaus mean : 44.4155

Gaus Width: 3.9046



B1L101S, U1-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

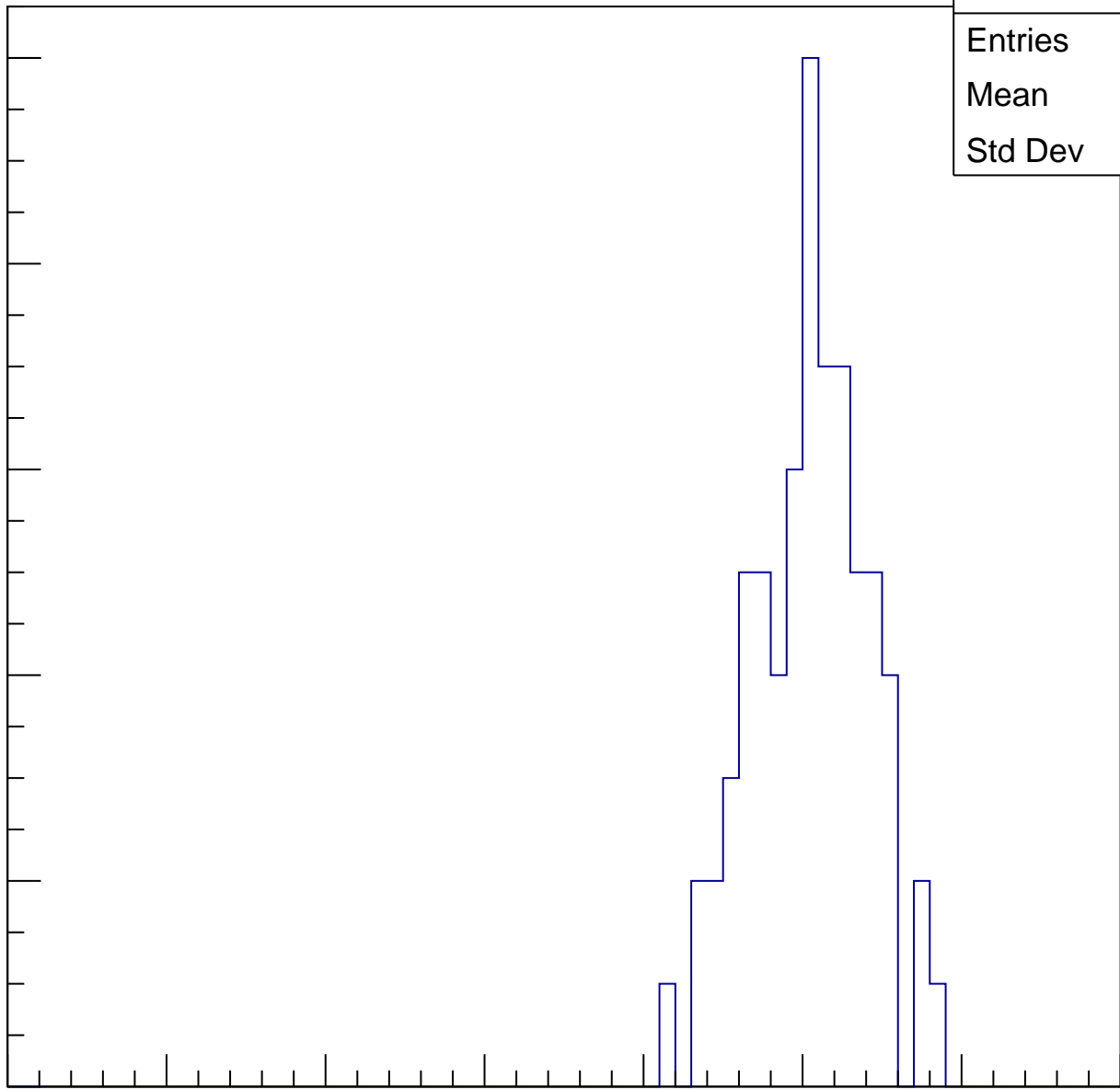
Entries	69
Mean	49.99
Std Dev	3.602

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

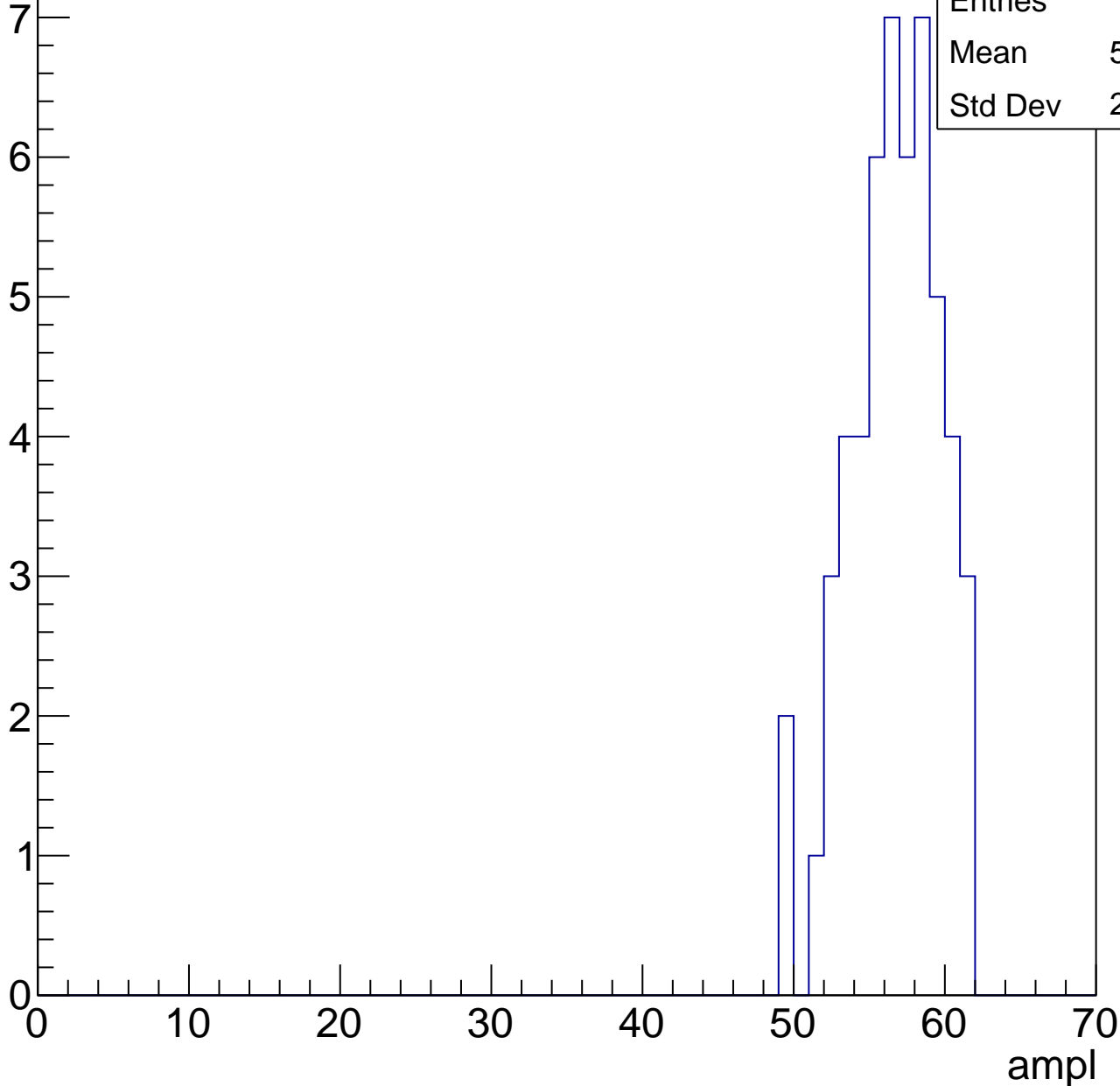


B1L101S, U1-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.17
Std Dev	2.927

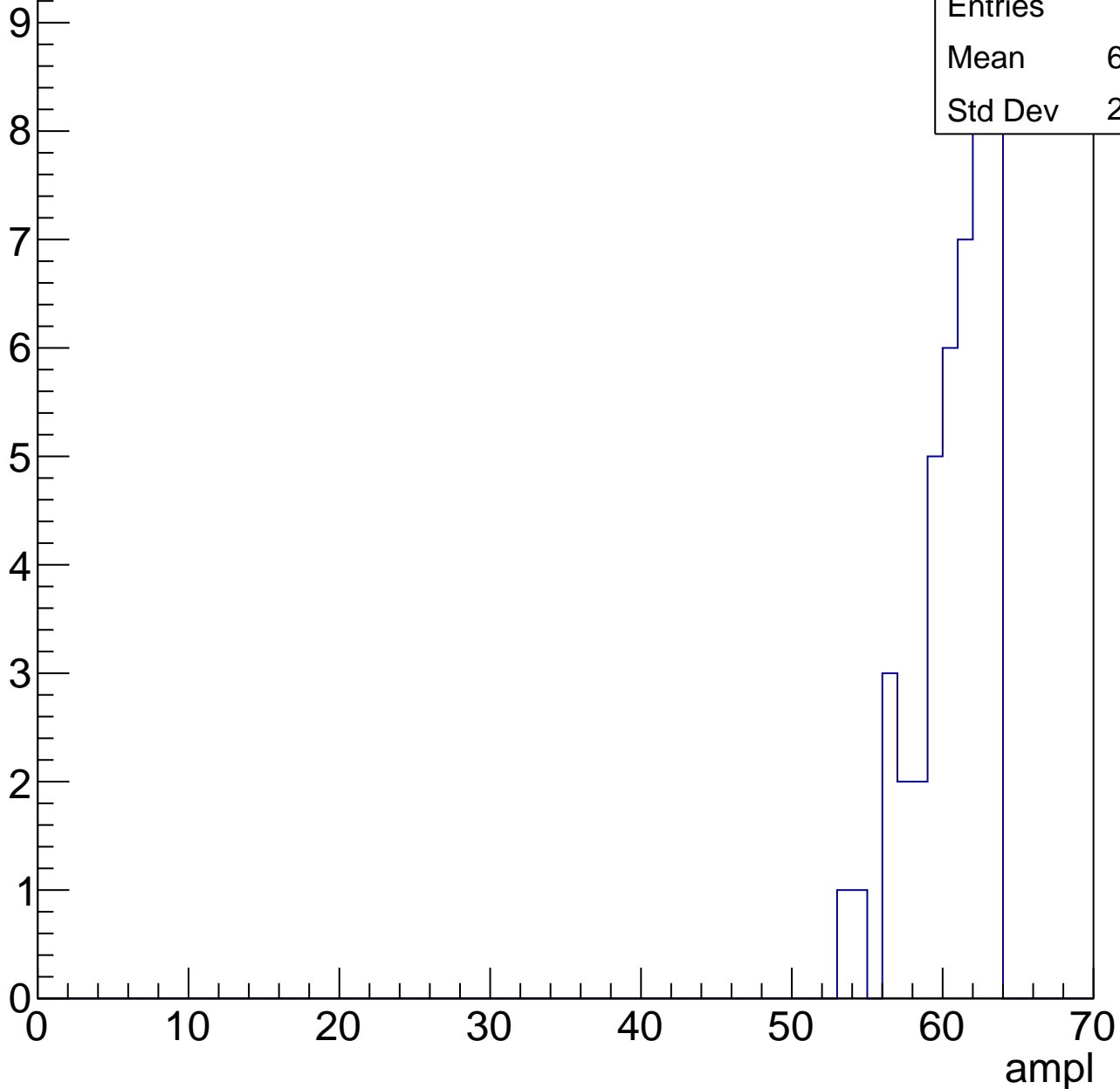


B1L101S, U1-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	60.27
Std Dev	2.516



B1L101S, U1-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch108, adc0

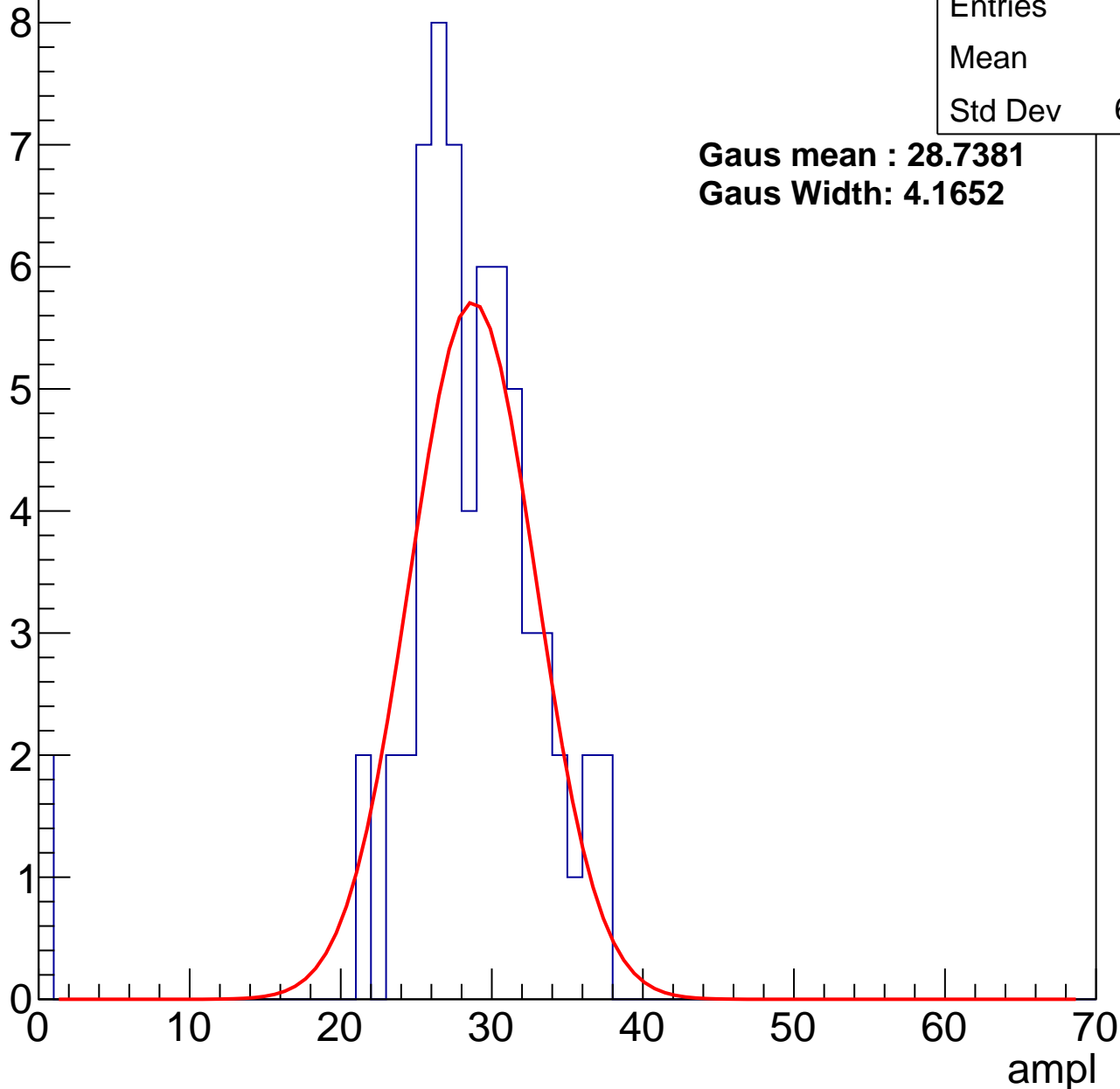
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.7
Std Dev	6.191

Gaus mean : 28.7381

Gaus Width: 4.1652



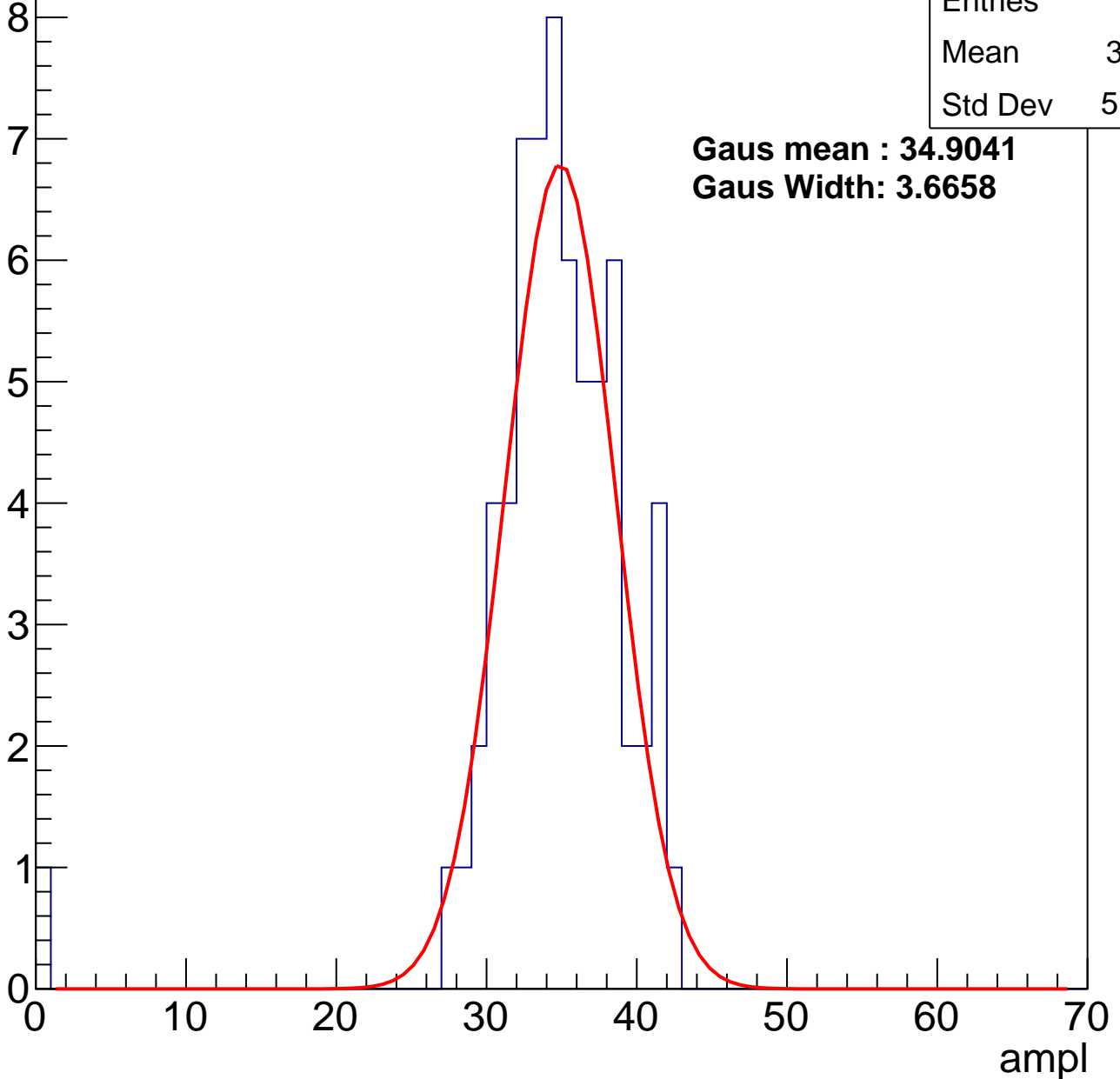
B1L101S, U1-ch108, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.11
Std Dev	5.469

Gaus mean : 34.9041
Gaus Width: 3.6658



B1L101S, U1-ch108, adc2

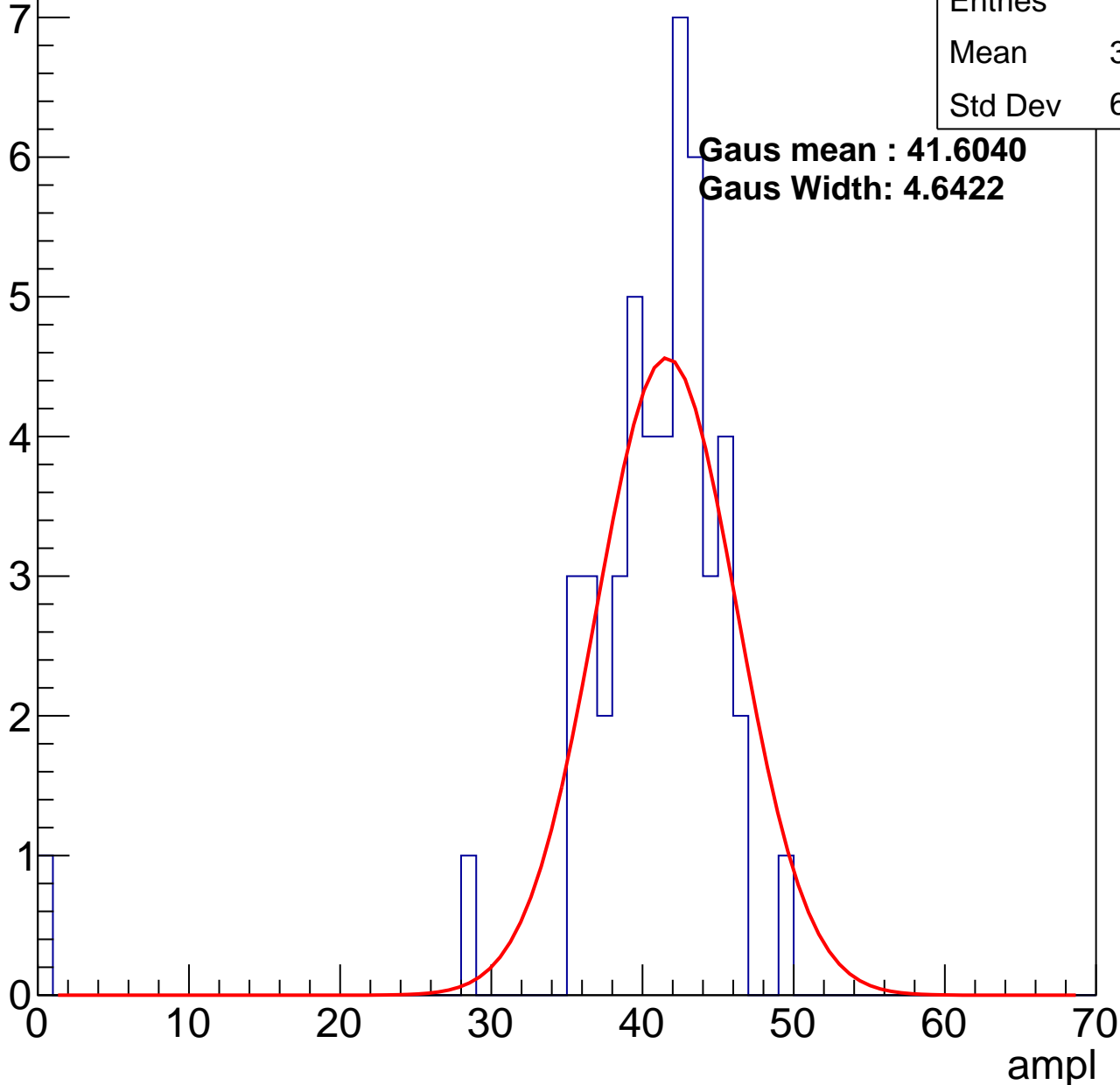
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	39.86
Std Dev	6.833

Gaus mean : 41.6040

Gaus Width: 4.6422

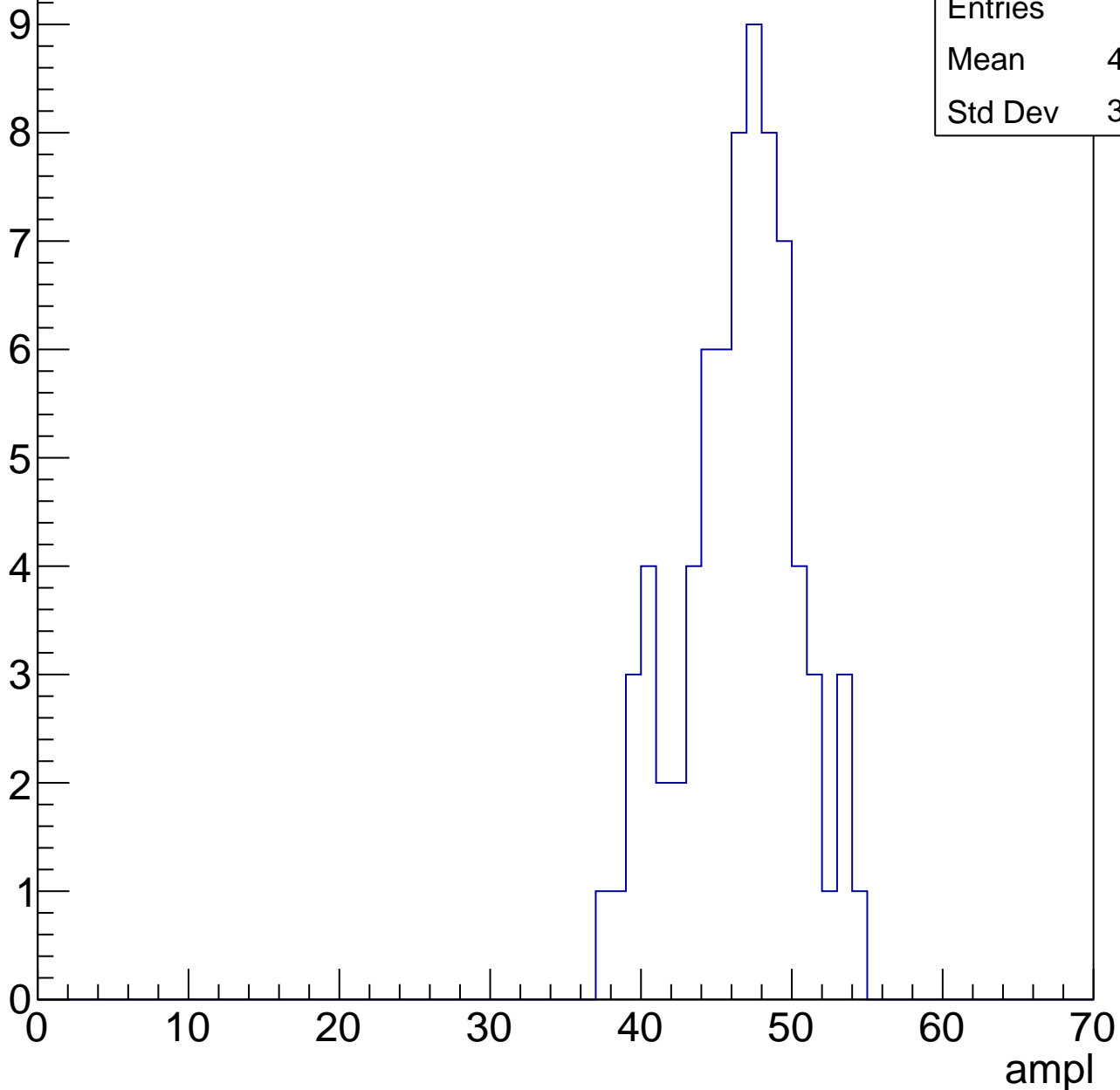


B1L101S, U1-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	46.03
Std Dev	3.836

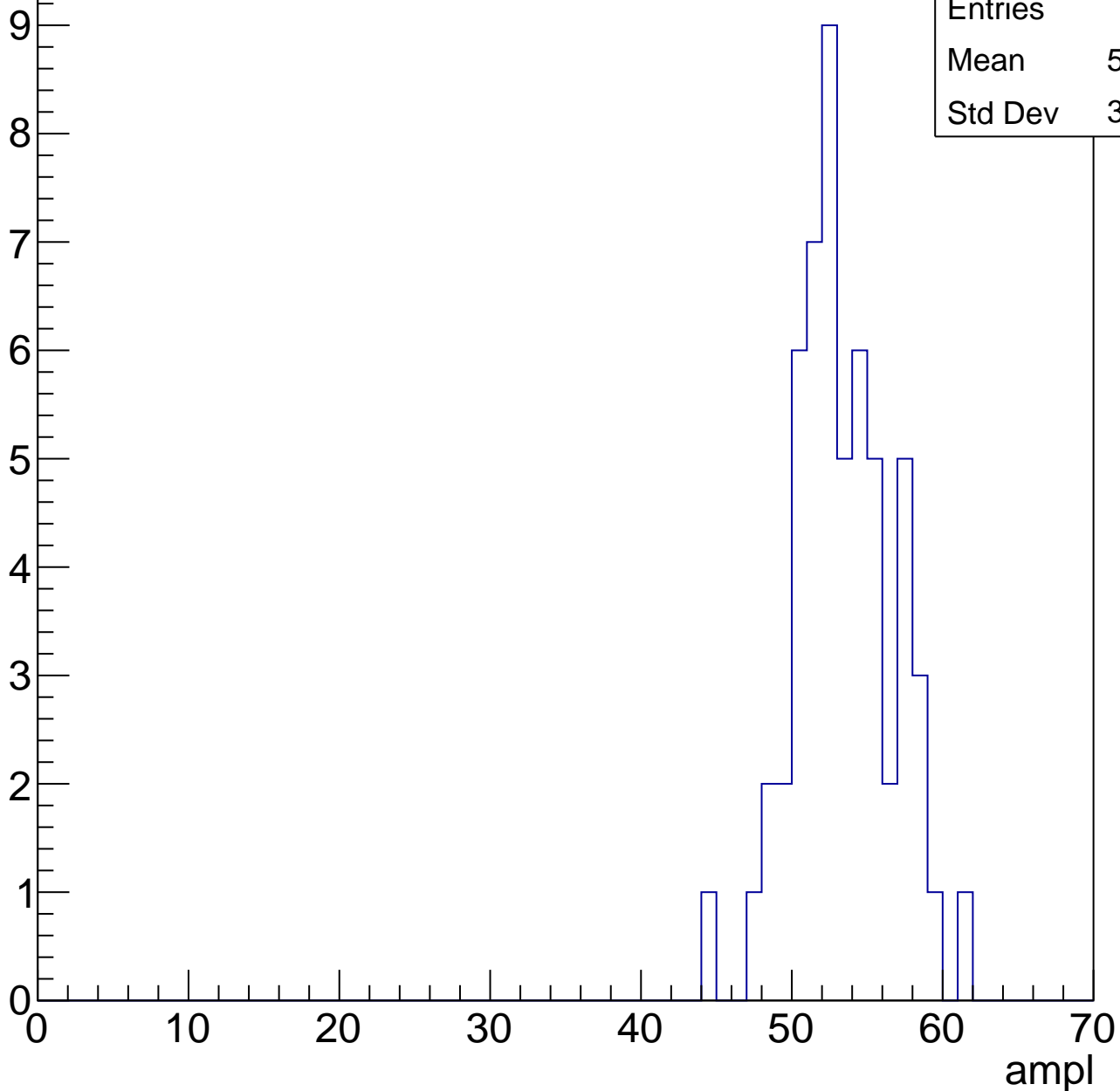


B1L101S, U1-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

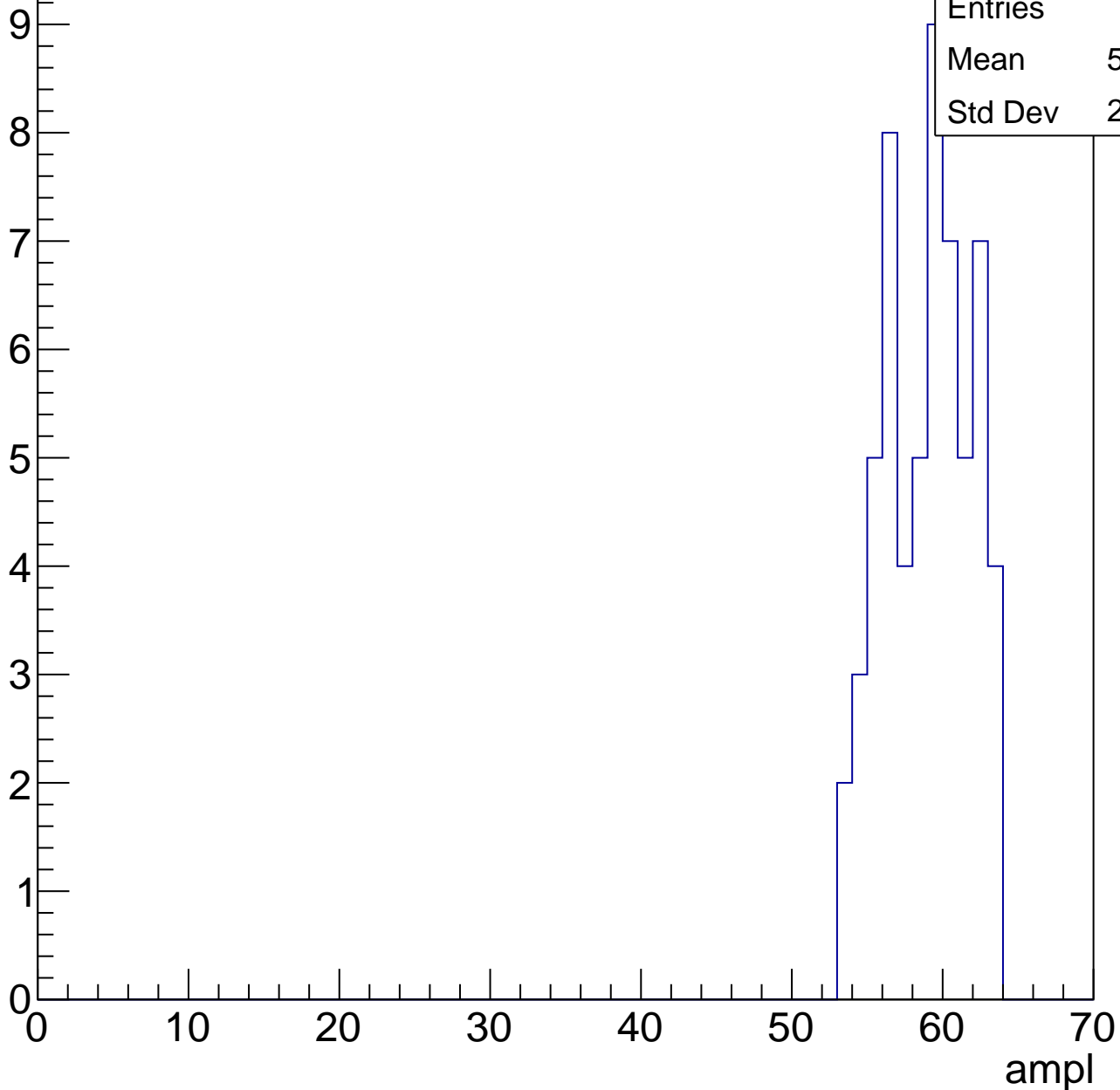
Entries	56
Mean	52.95
Std Dev	3.243



B1L101S, U1-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

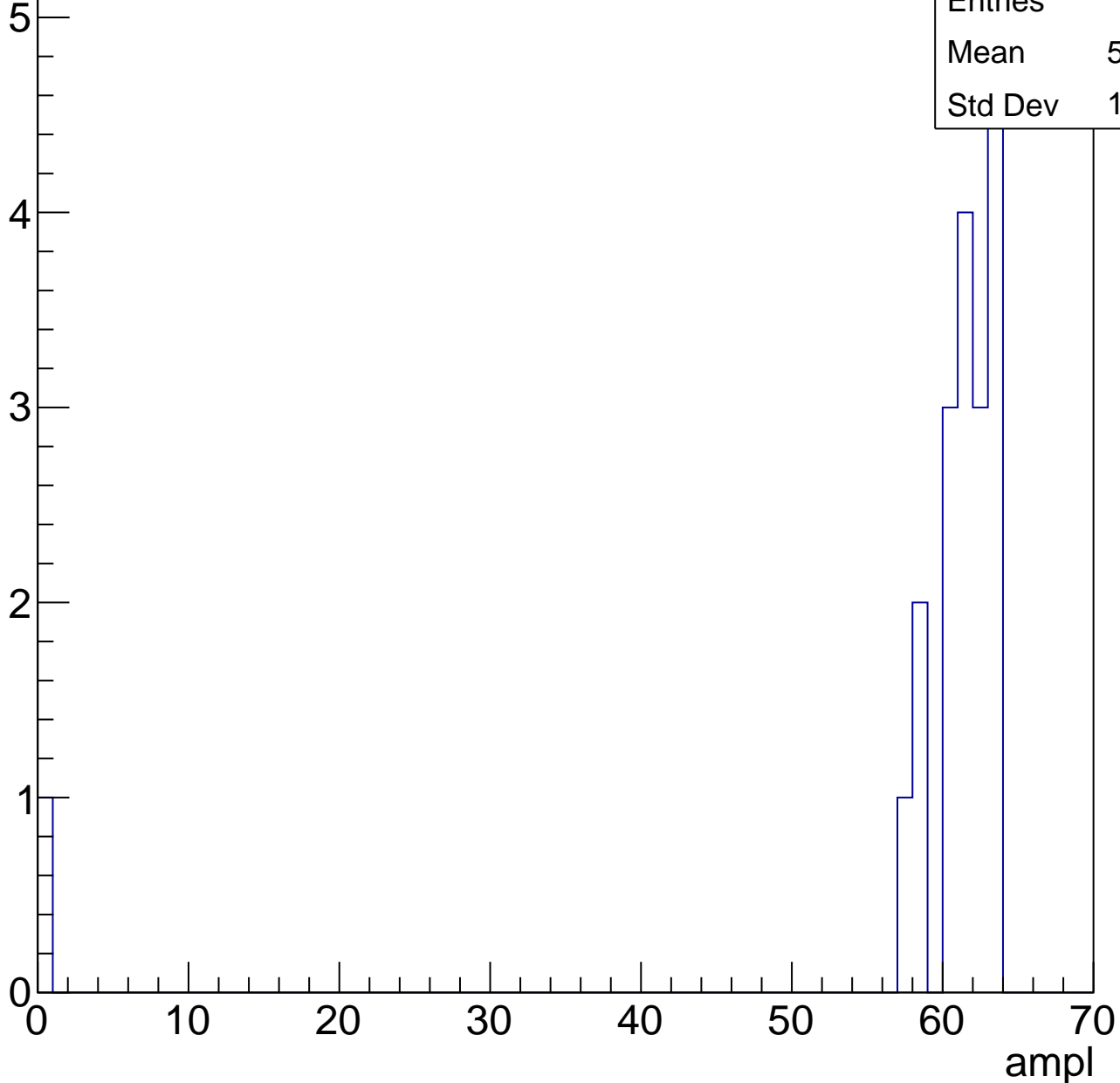


B1L101S, U1-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	57.79
Std Dev	13.74



B1L101S, U1-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch109, adc0

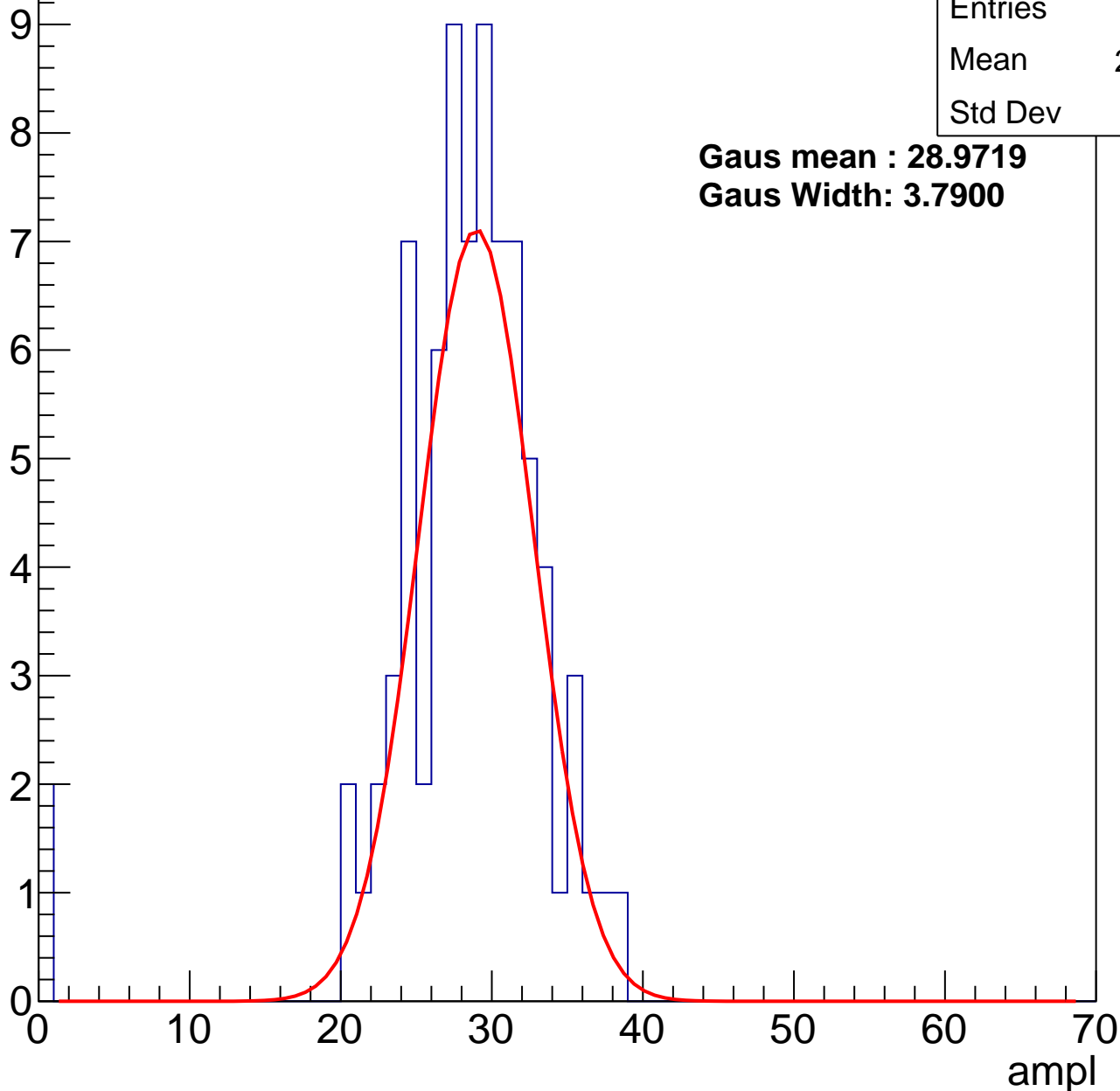
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	27.71
Std Dev	5.87

Gaus mean : 28.9719

Gaus Width: 3.7900



B1L101S, U1-ch109, adc1

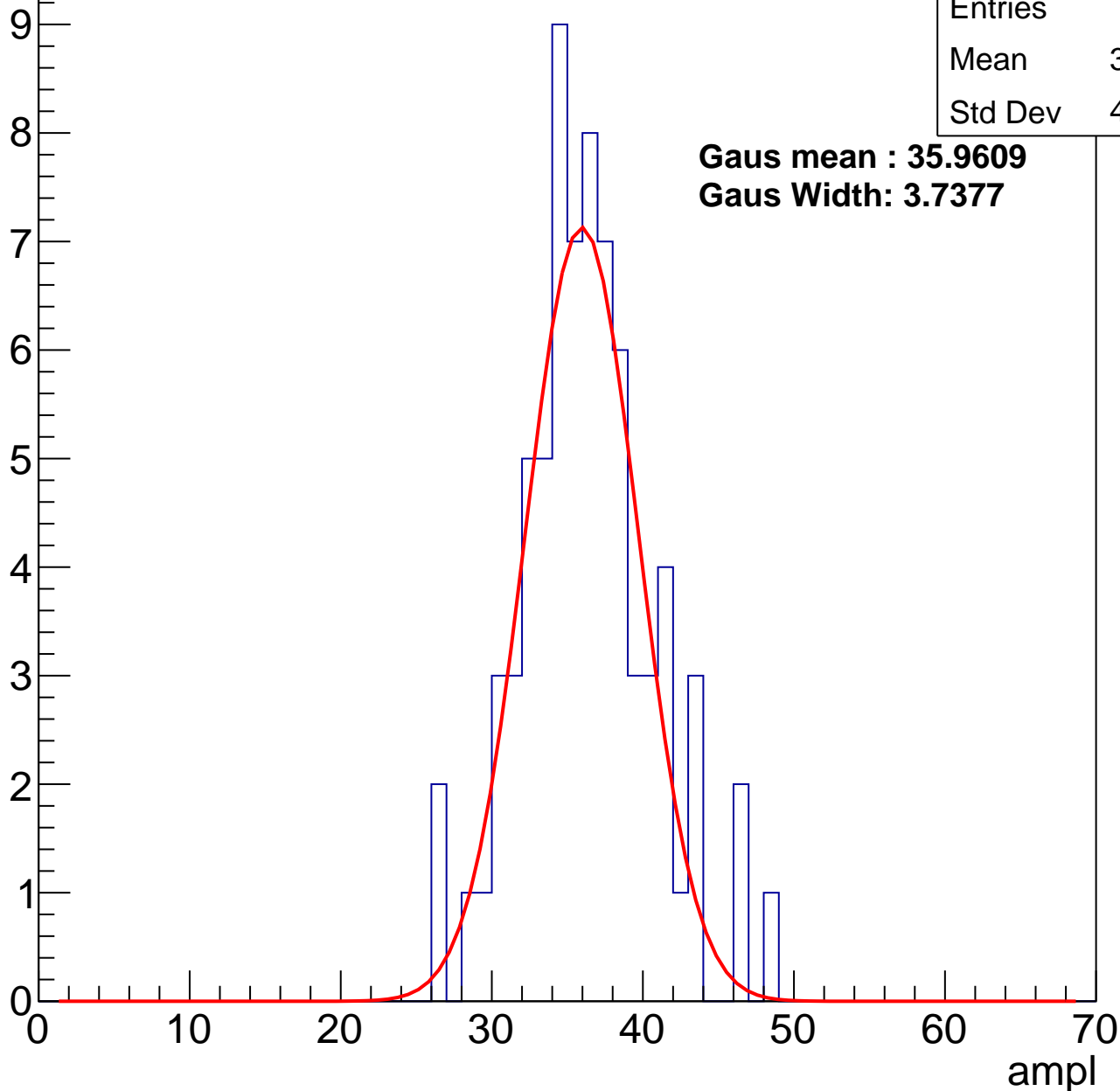
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.88
Std Dev	4.343

Gaus mean : 35.9609

Gaus Width: 3.7377



B1L101S, U1-ch109, adc2

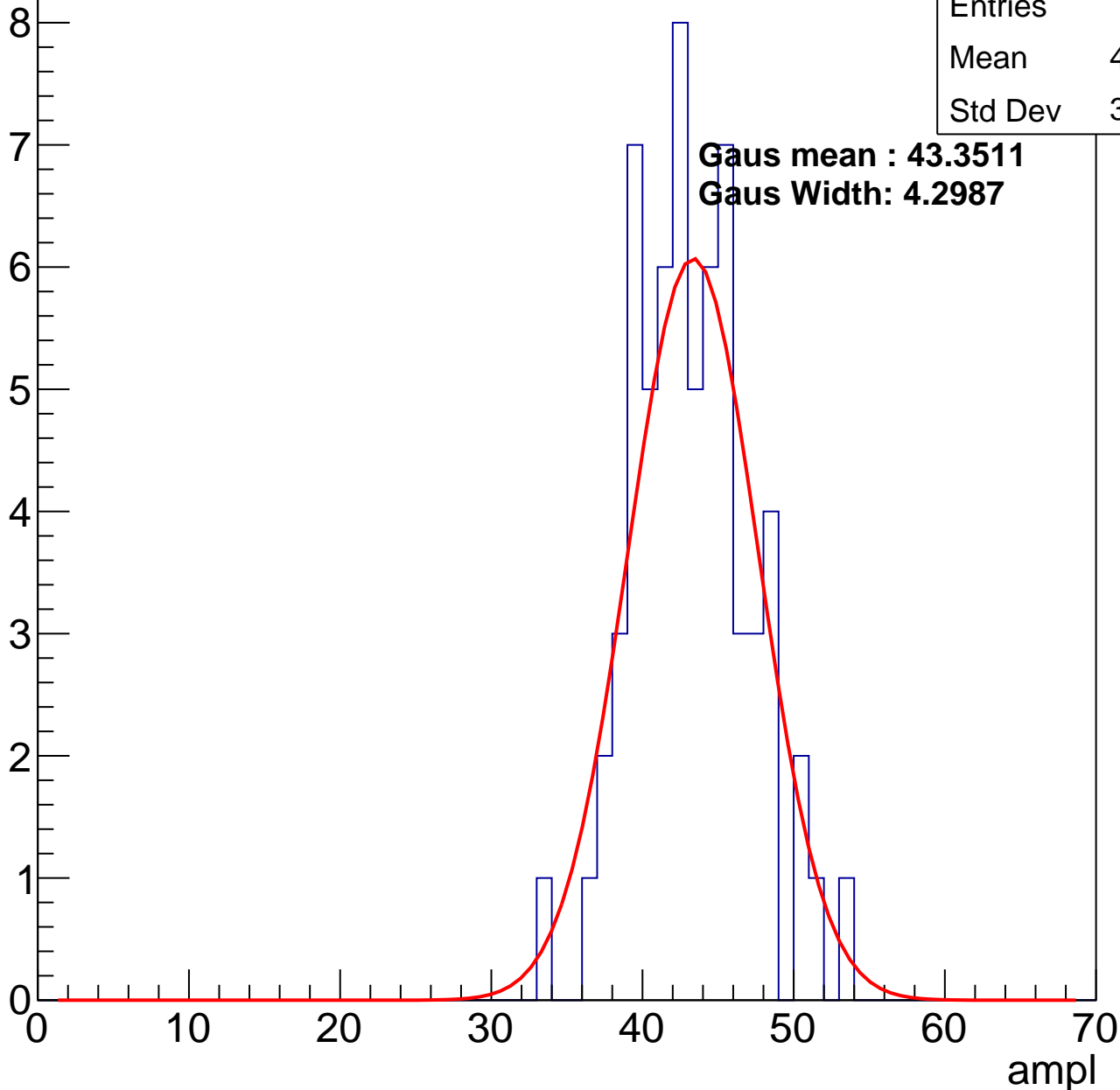
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.78
Std Dev	3.829

Gaus mean : 43.3511

Gaus Width: 4.2987

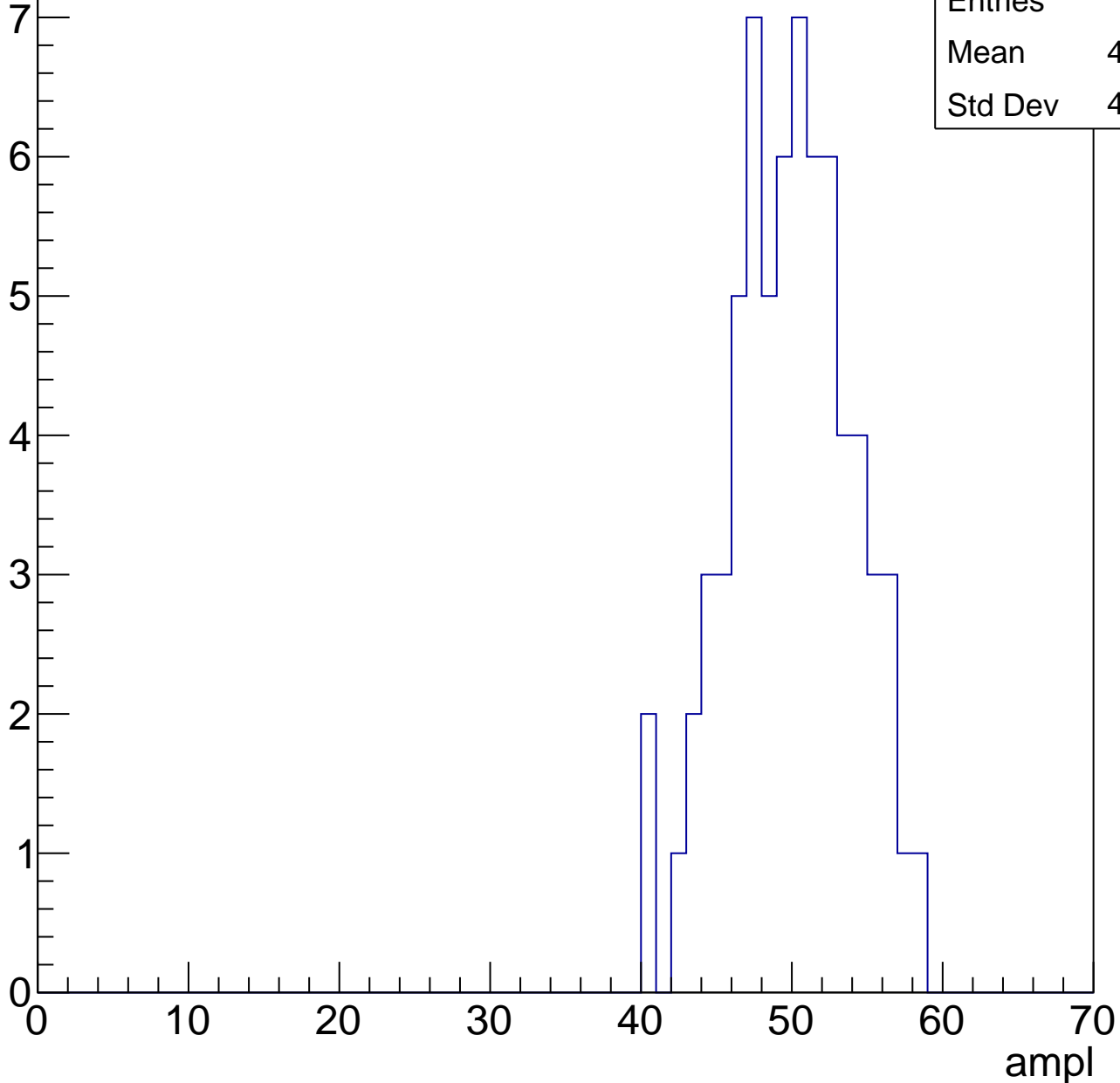


B1L101S, U1-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.45
Std Dev	4.027

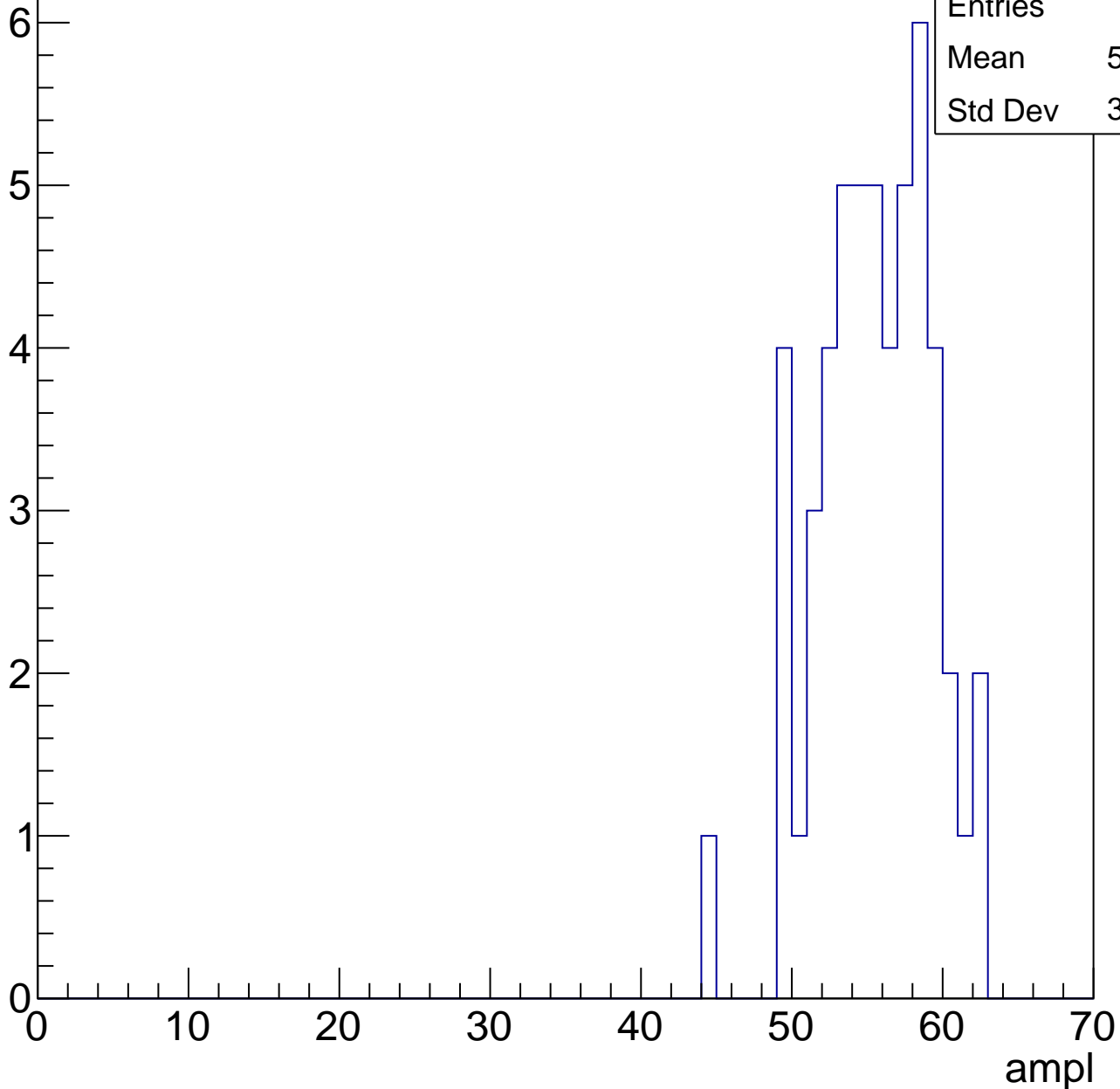


B1L101S, U1-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.98
Std Dev	3.734



B1L101S, U1-ch109, adc5

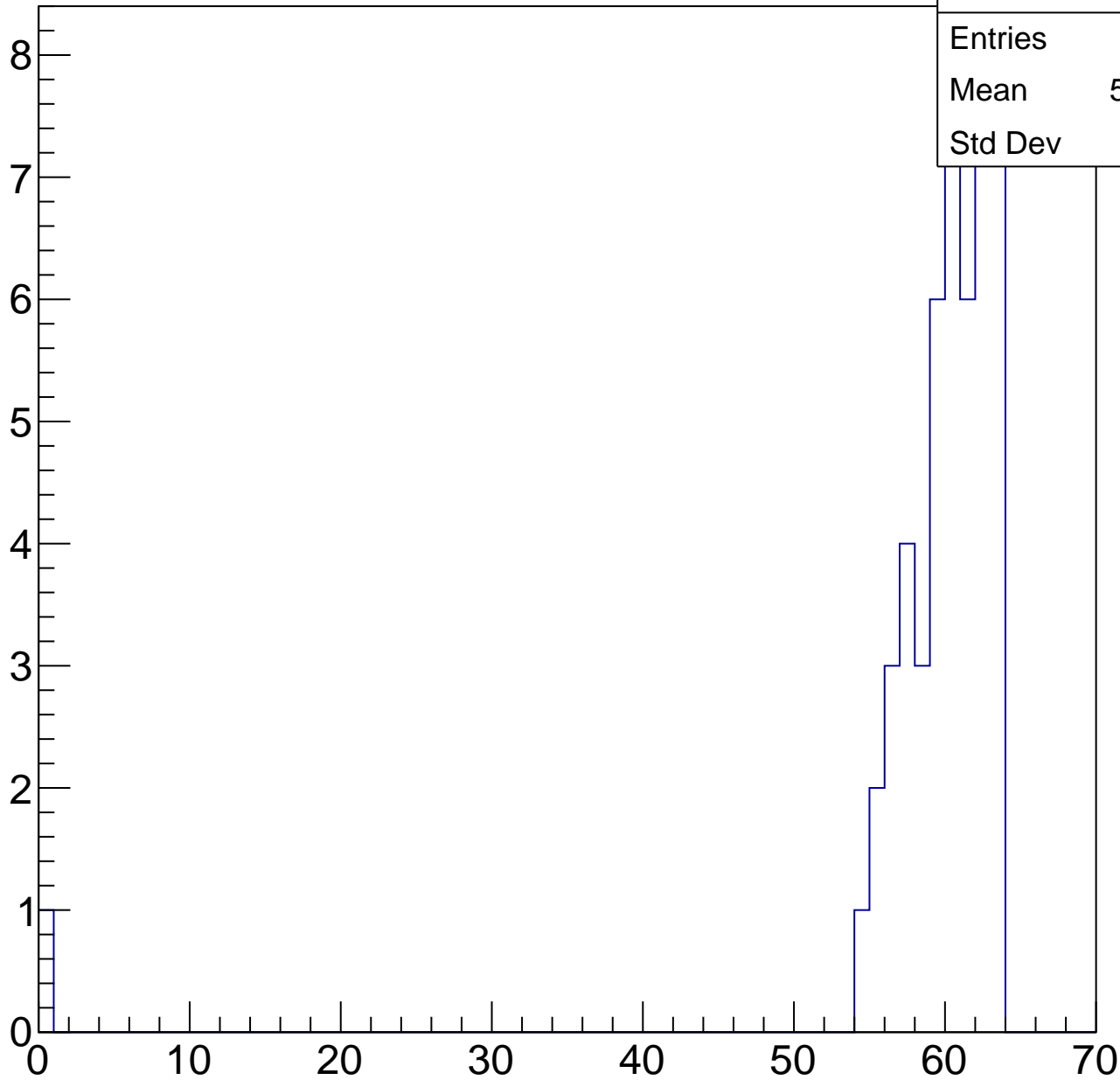
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.68
Std Dev	8.73

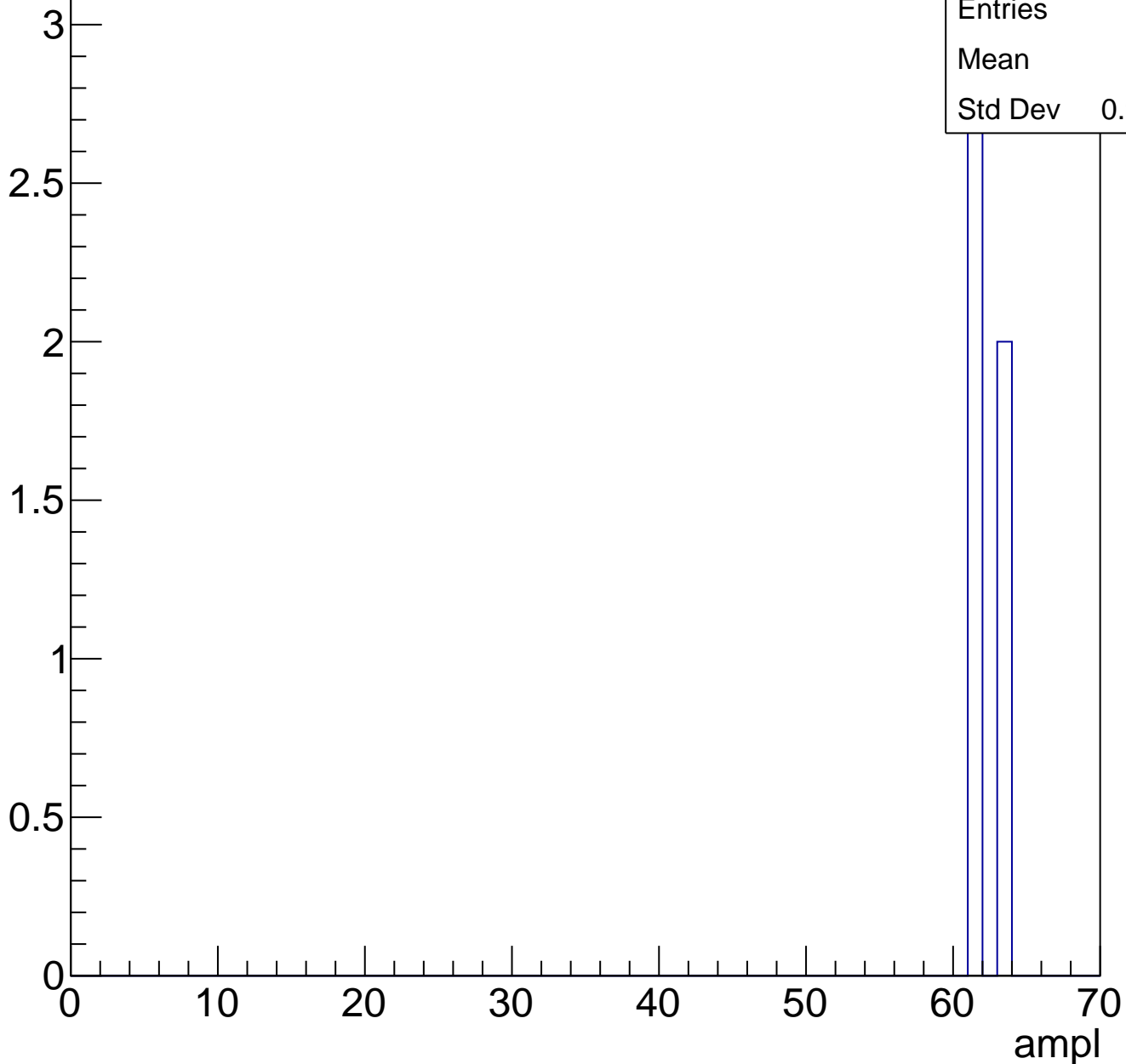
ampl



B1L101S, U1-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch110, adc0

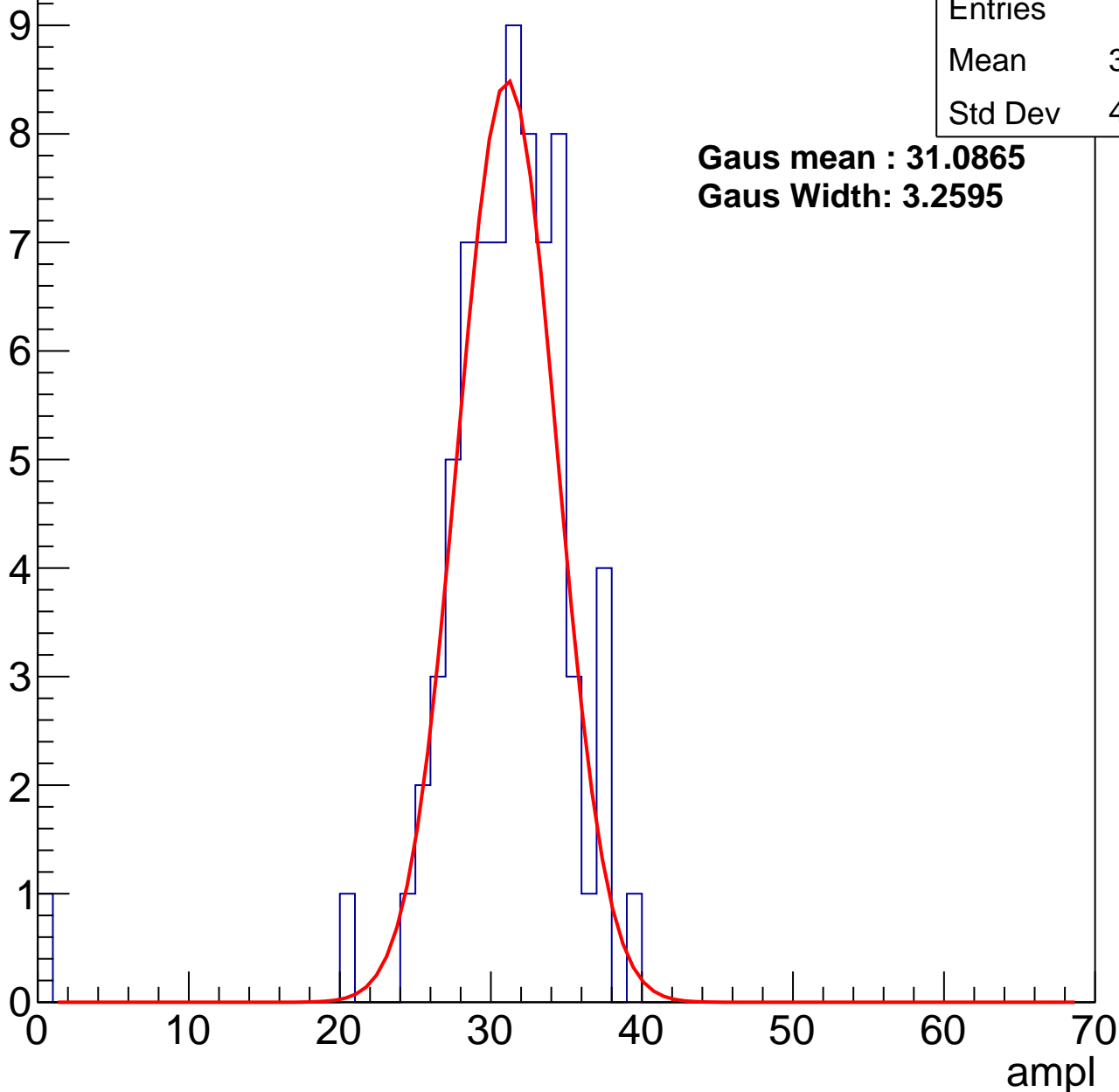
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.43
Std Dev	4.927

Gaus mean : 31.0865

Gaus Width: 3.2595



B1L101S, U1-ch110, adc1

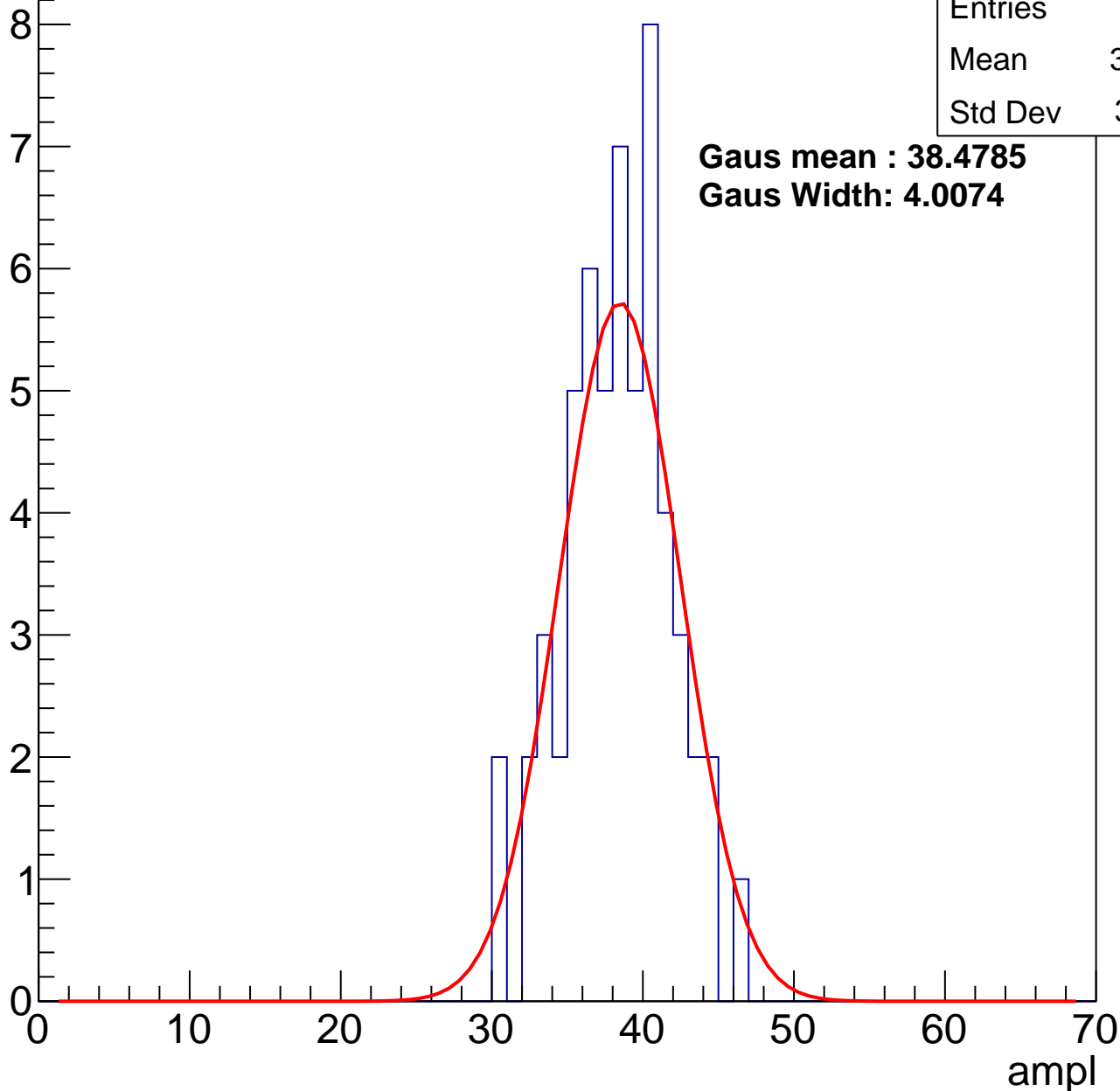
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.86
Std Dev	3.471

Gaus mean : 38.4785

Gaus Width: 4.0074



B1L101S, U1-ch110, adc2

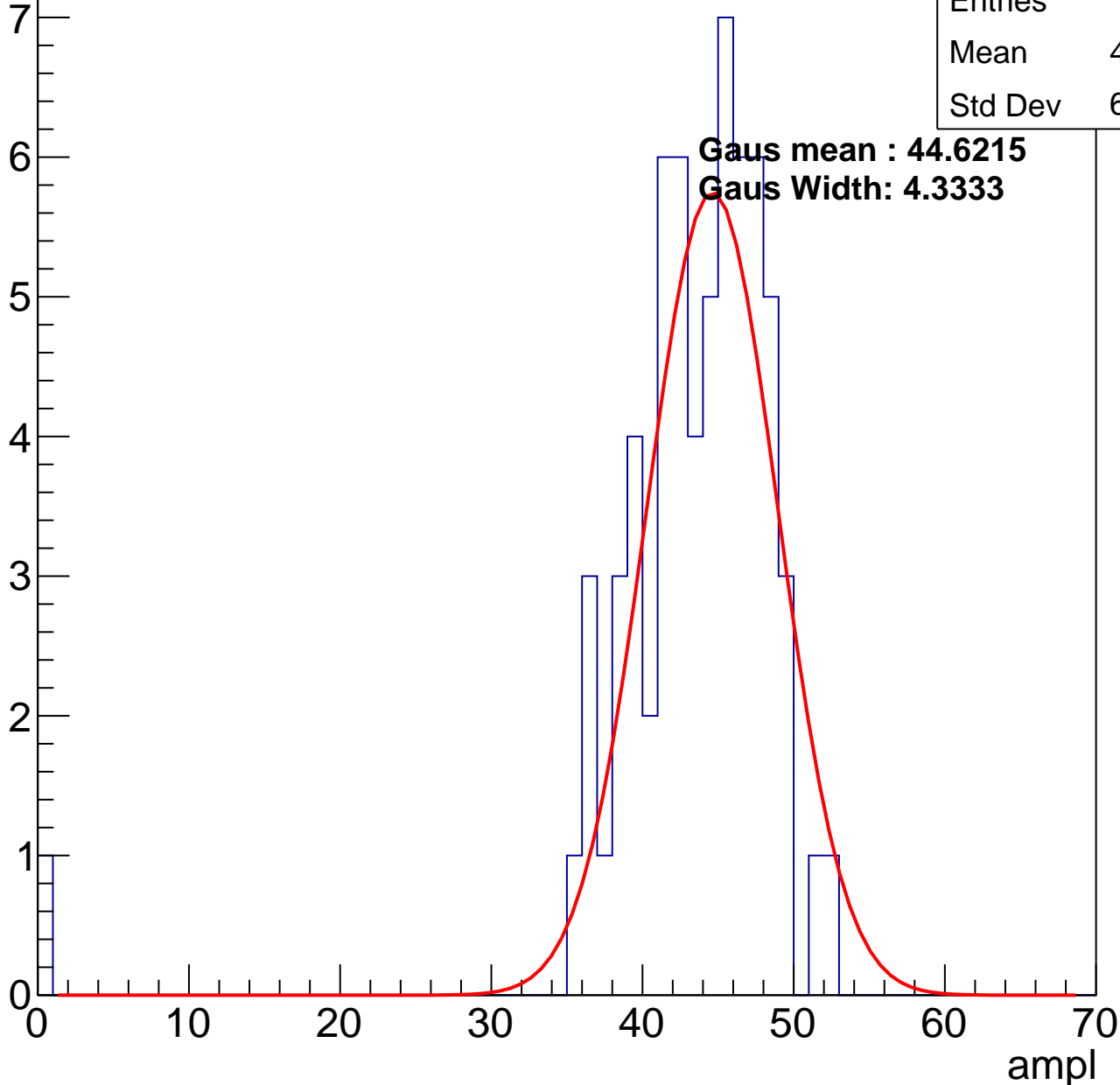
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.82
Std Dev	6.612

Gaus mean : 44.6215

Gaus Width: 4.3333

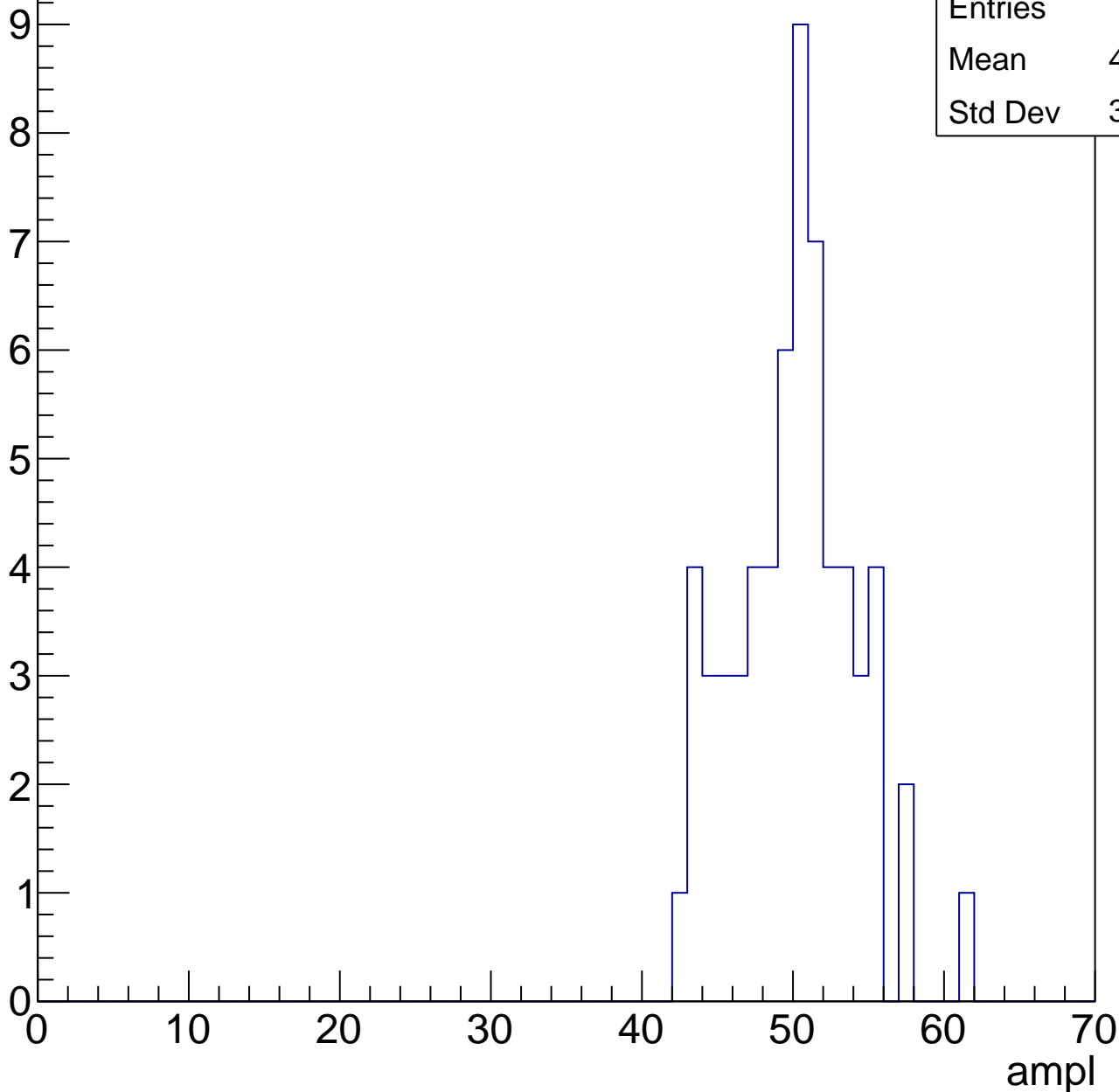


B1L101S, U1-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.63
Std Dev	3.948

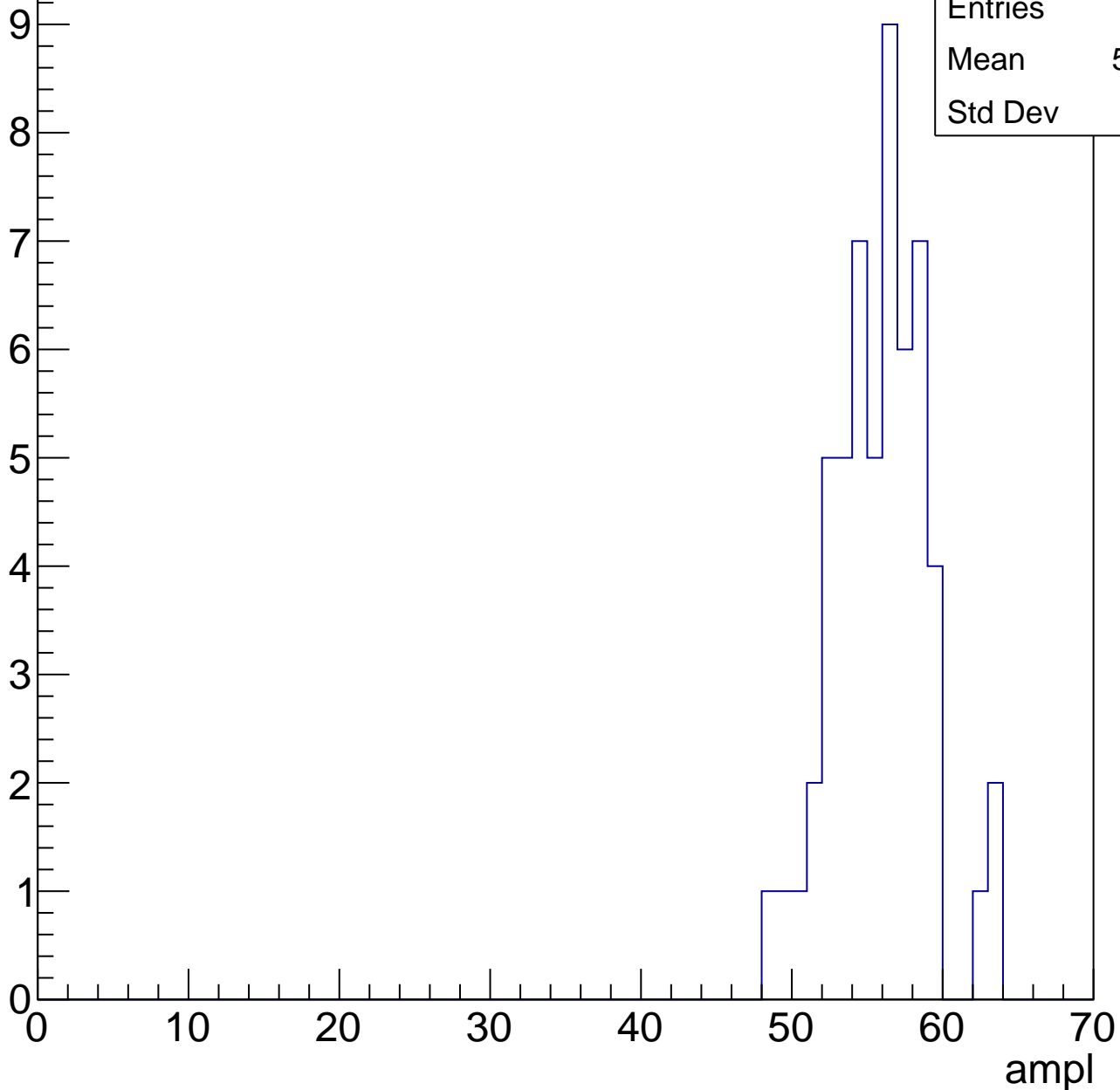


B1L101S, U1-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

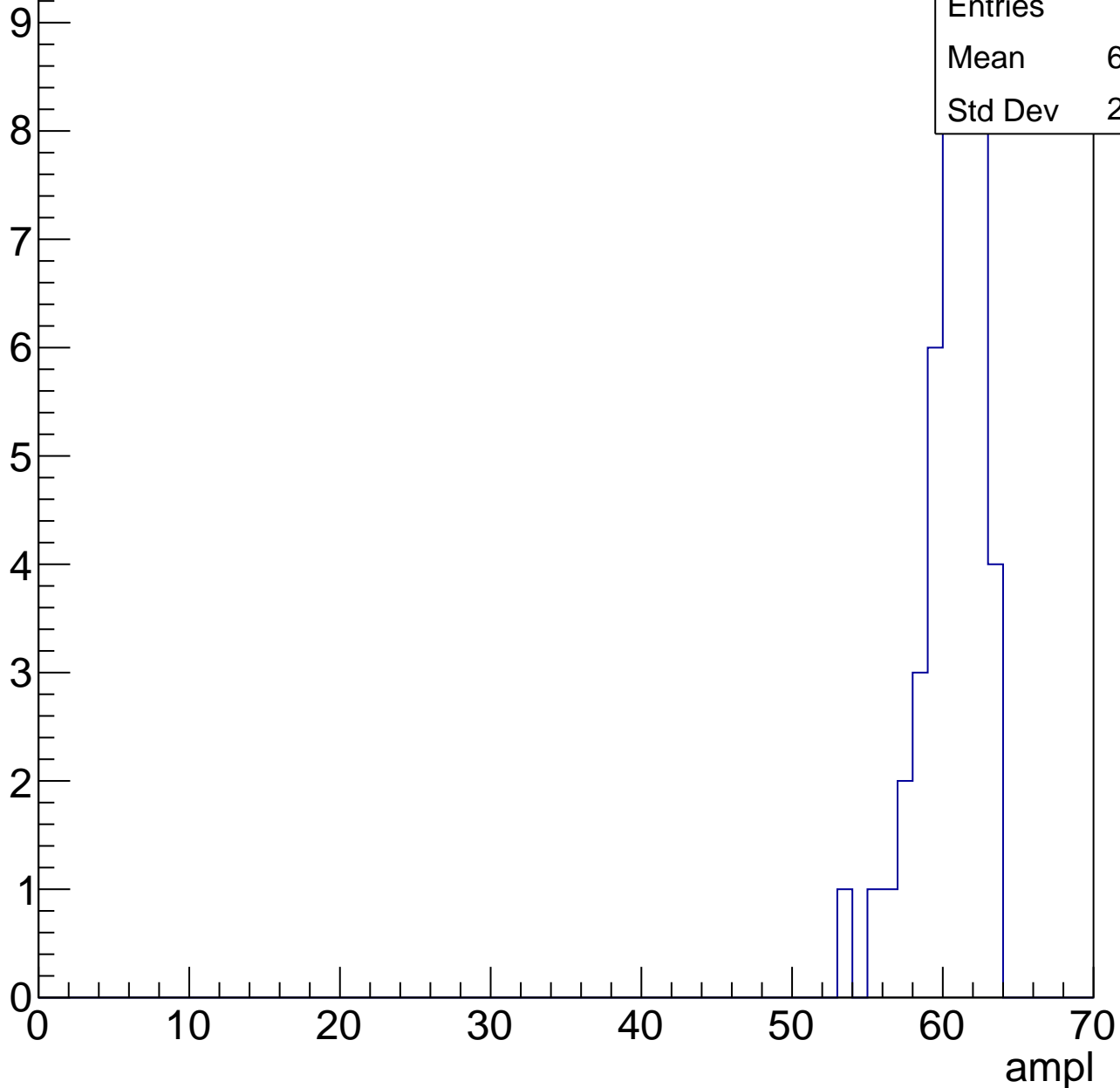
Entries	56
Mean	55.41
Std Dev	3.11



B1L101S, U1-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

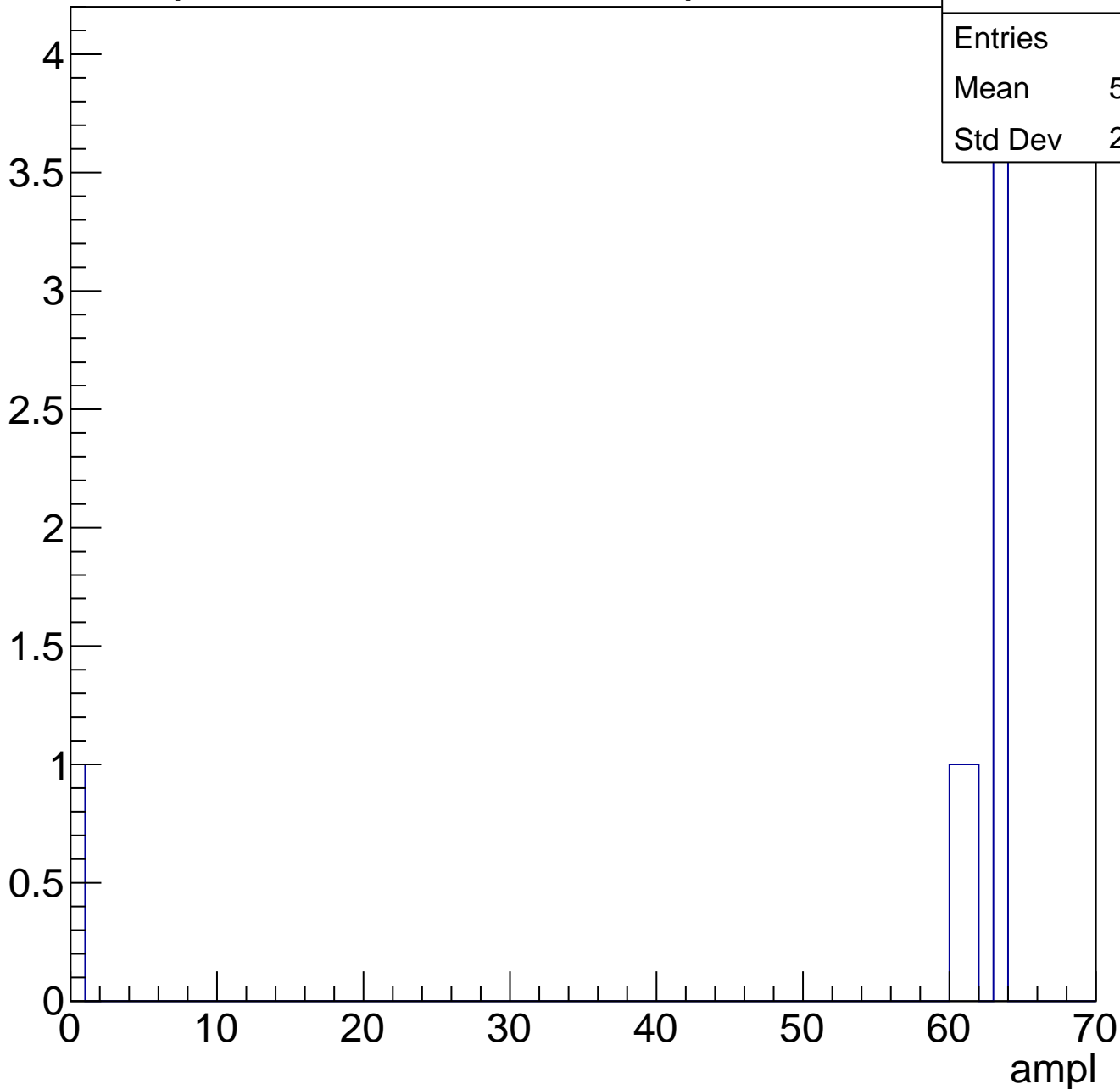


Entries	43
Mean	60.05
Std Dev	2.167

B1L101S, U1-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch111, adc0

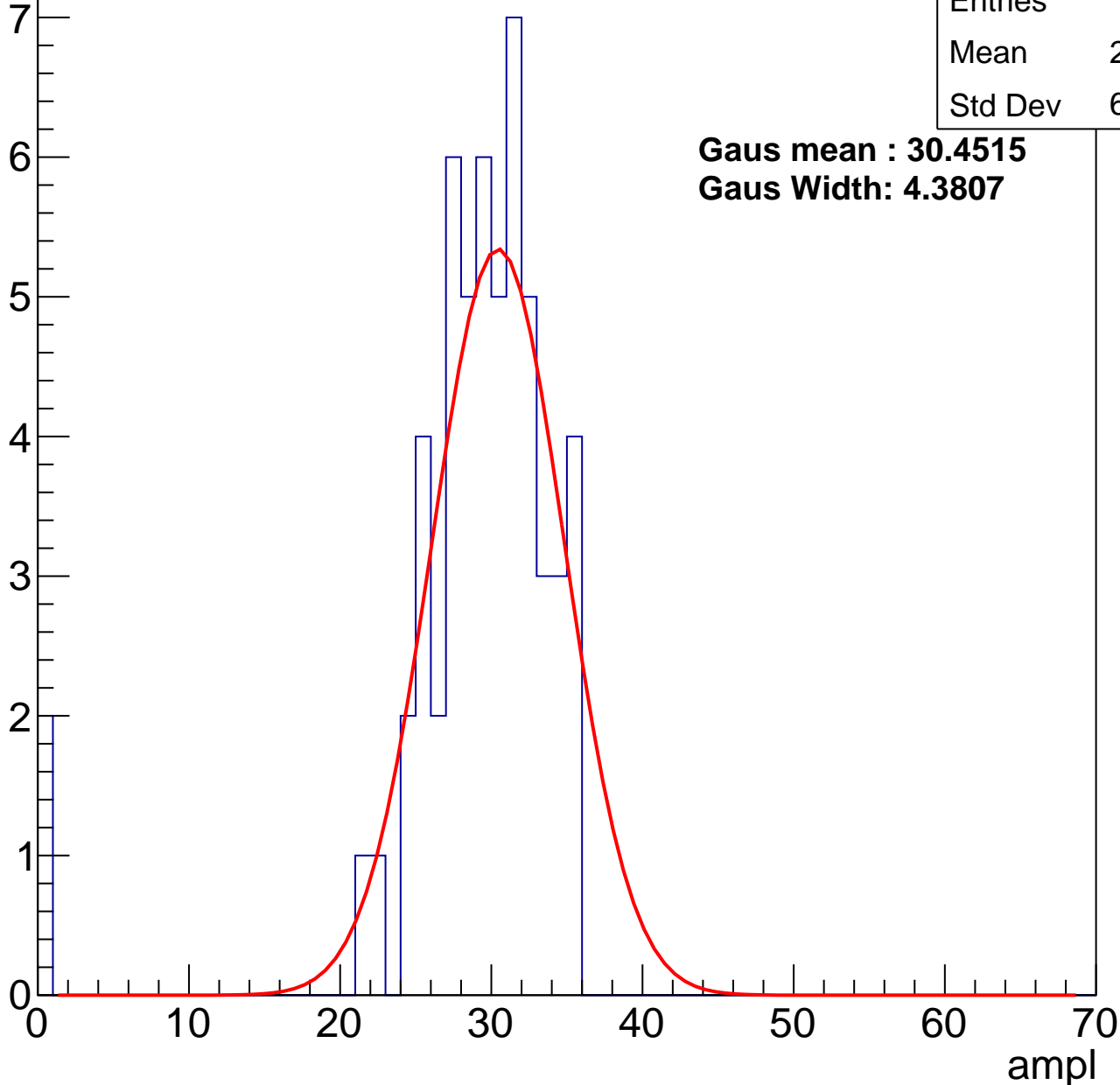
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.34
Std Dev	6.376

Gaus mean : 30.4515

Gaus Width: 4.3807



B1L101S, U1-ch111, adc1

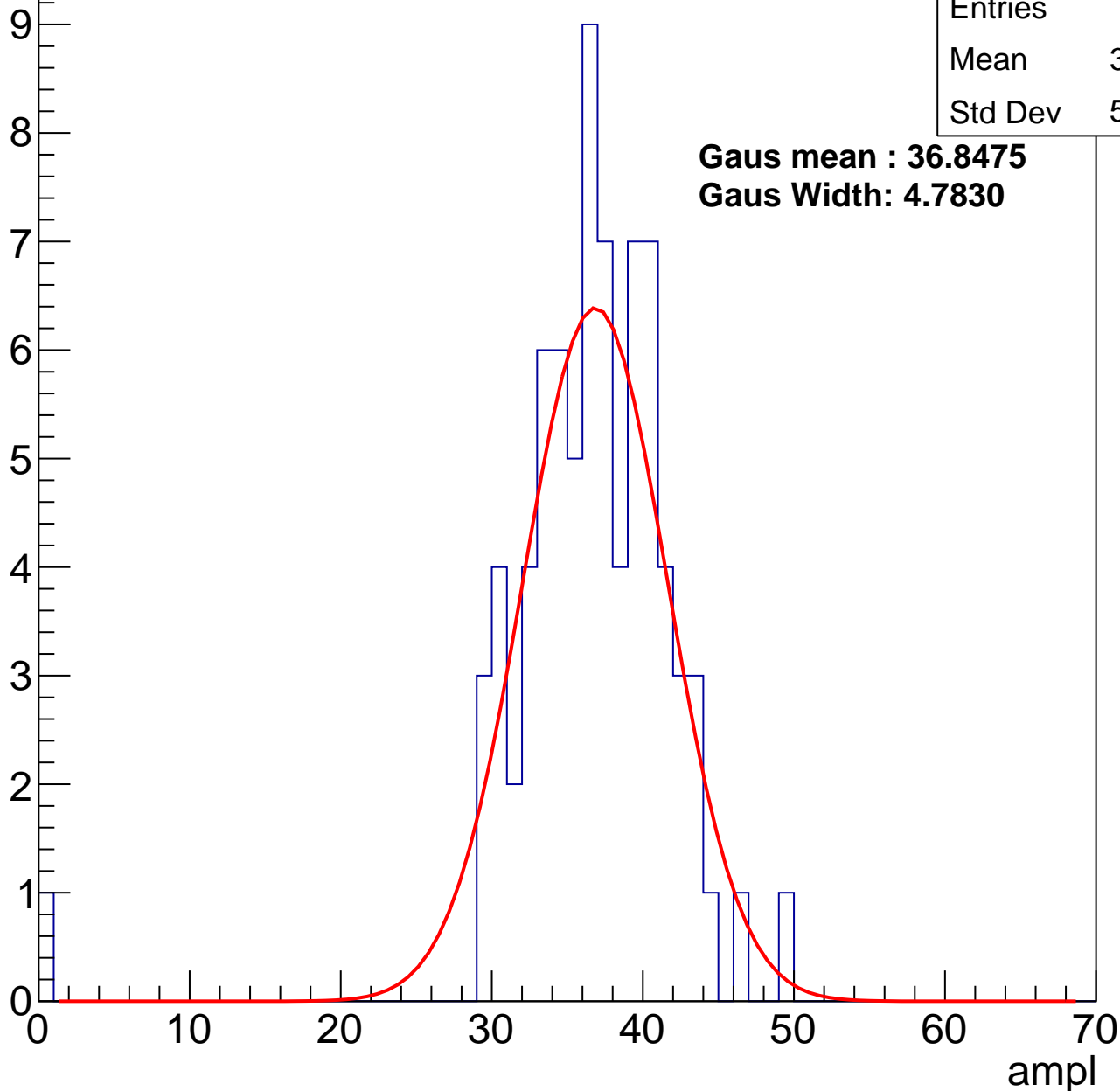
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.15
Std Dev	5.849

Gaus mean : 36.8475

Gaus Width: 4.7830



B1L101S, U1-ch111, adc2

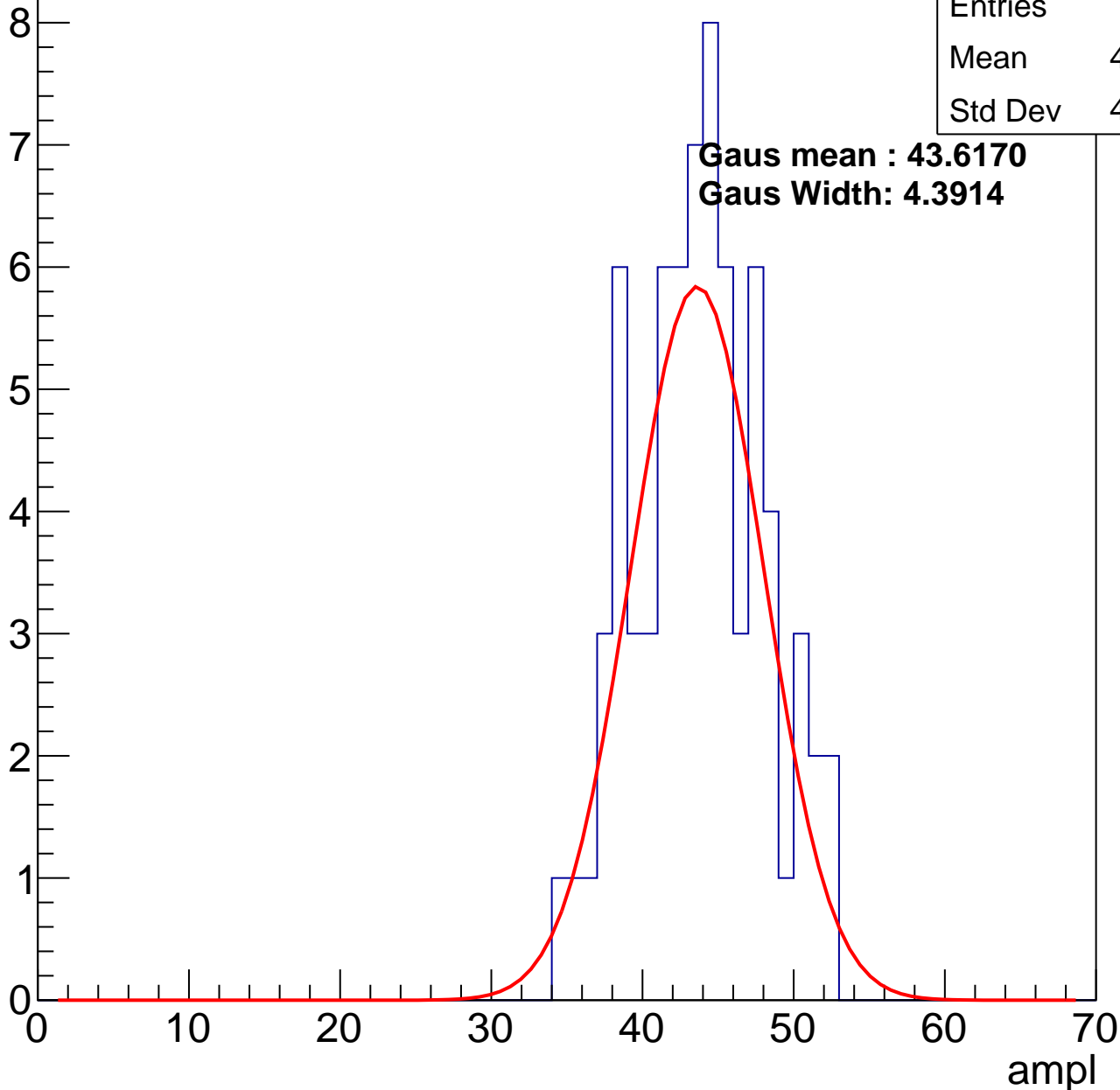
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.32
Std Dev	4.213

Gaus mean : 43.6170

Gaus Width: 4.3914

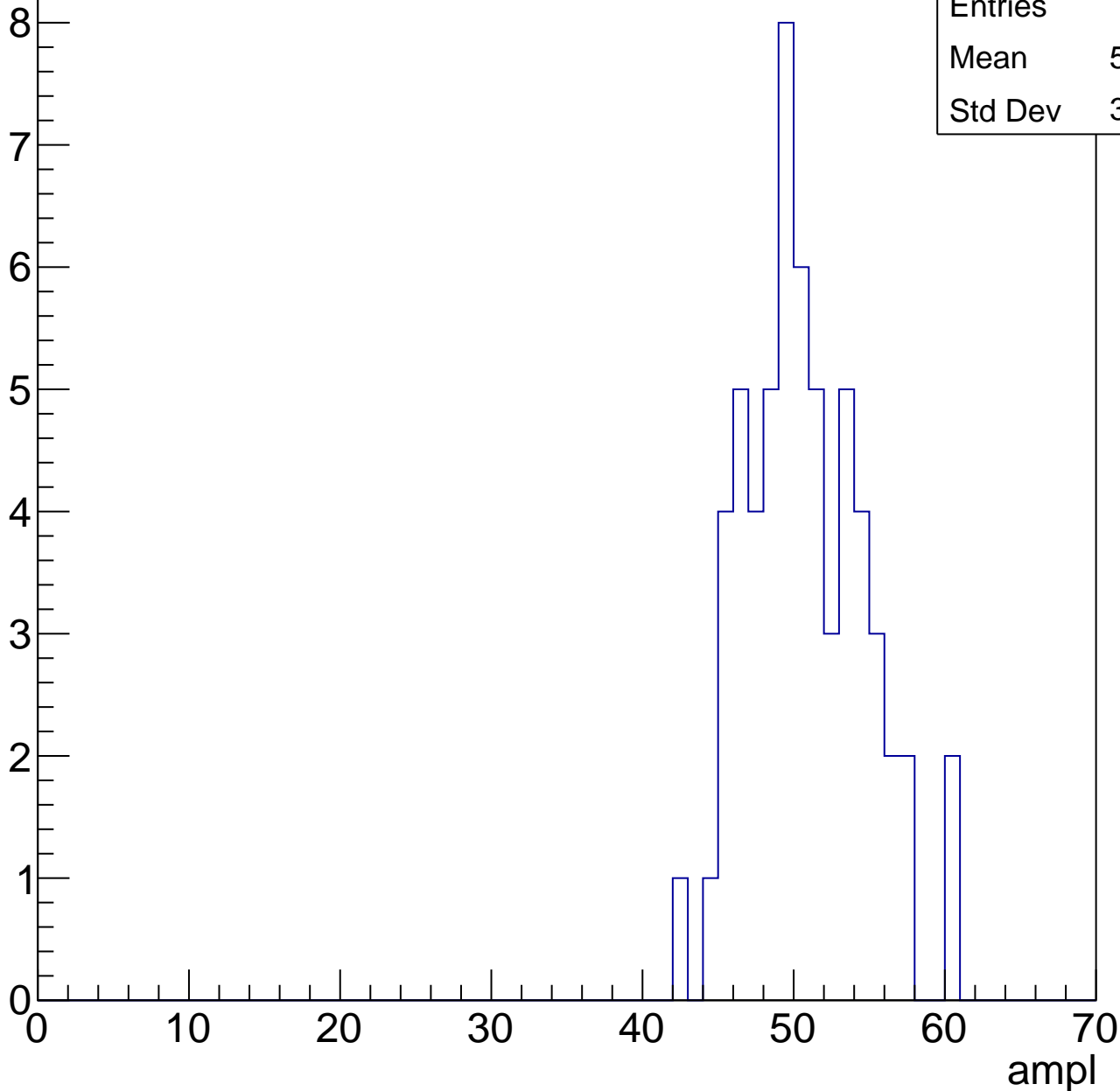


B1L101S, U1-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.32
Std Dev	3.892

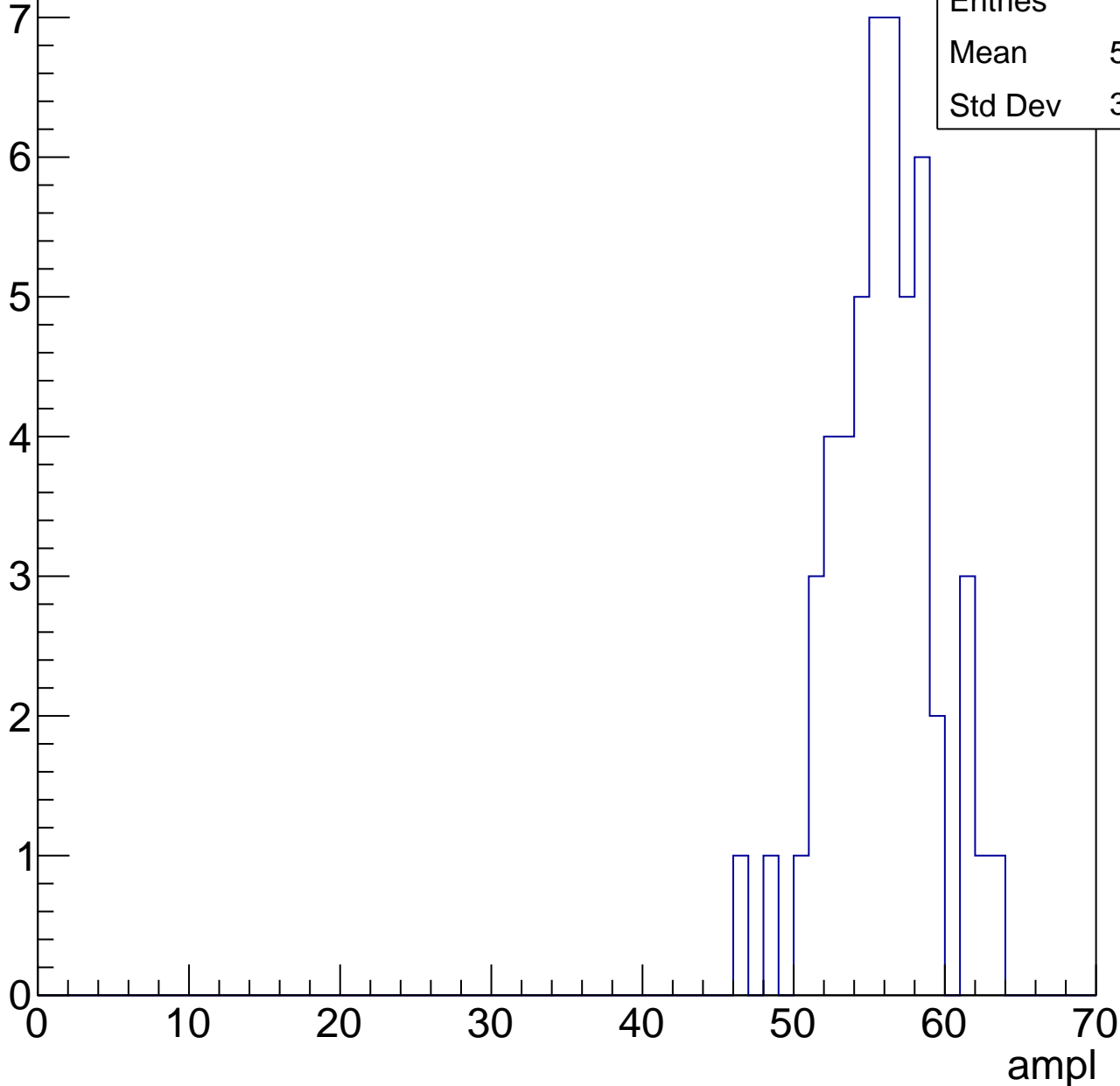


B1L101S, U1-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.35
Std Dev	3.394

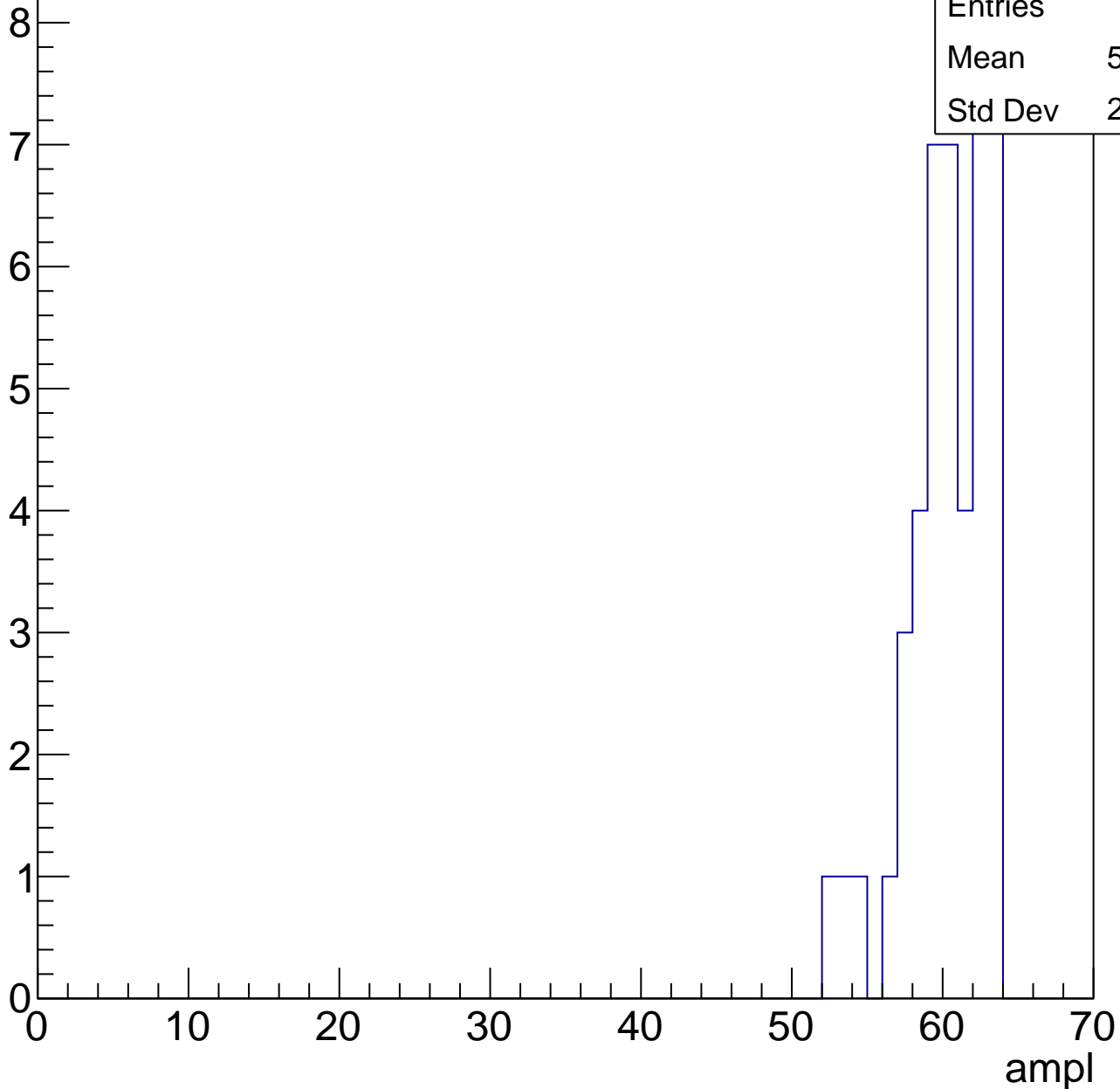


B1L101S, U1-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

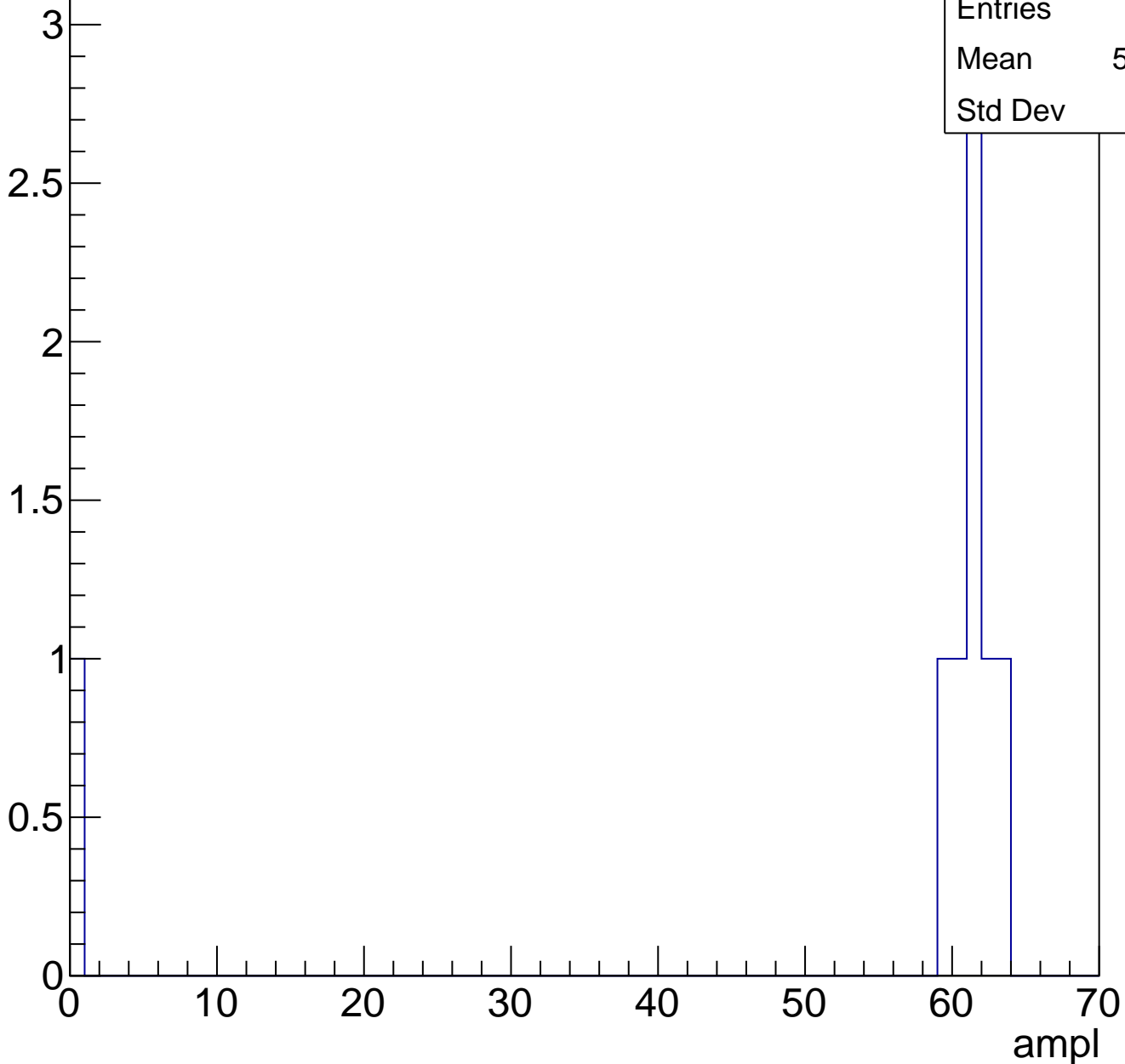
Entries	45
Mean	59.89
Std Dev	2.677



B1L101S, U1-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch112, adc0

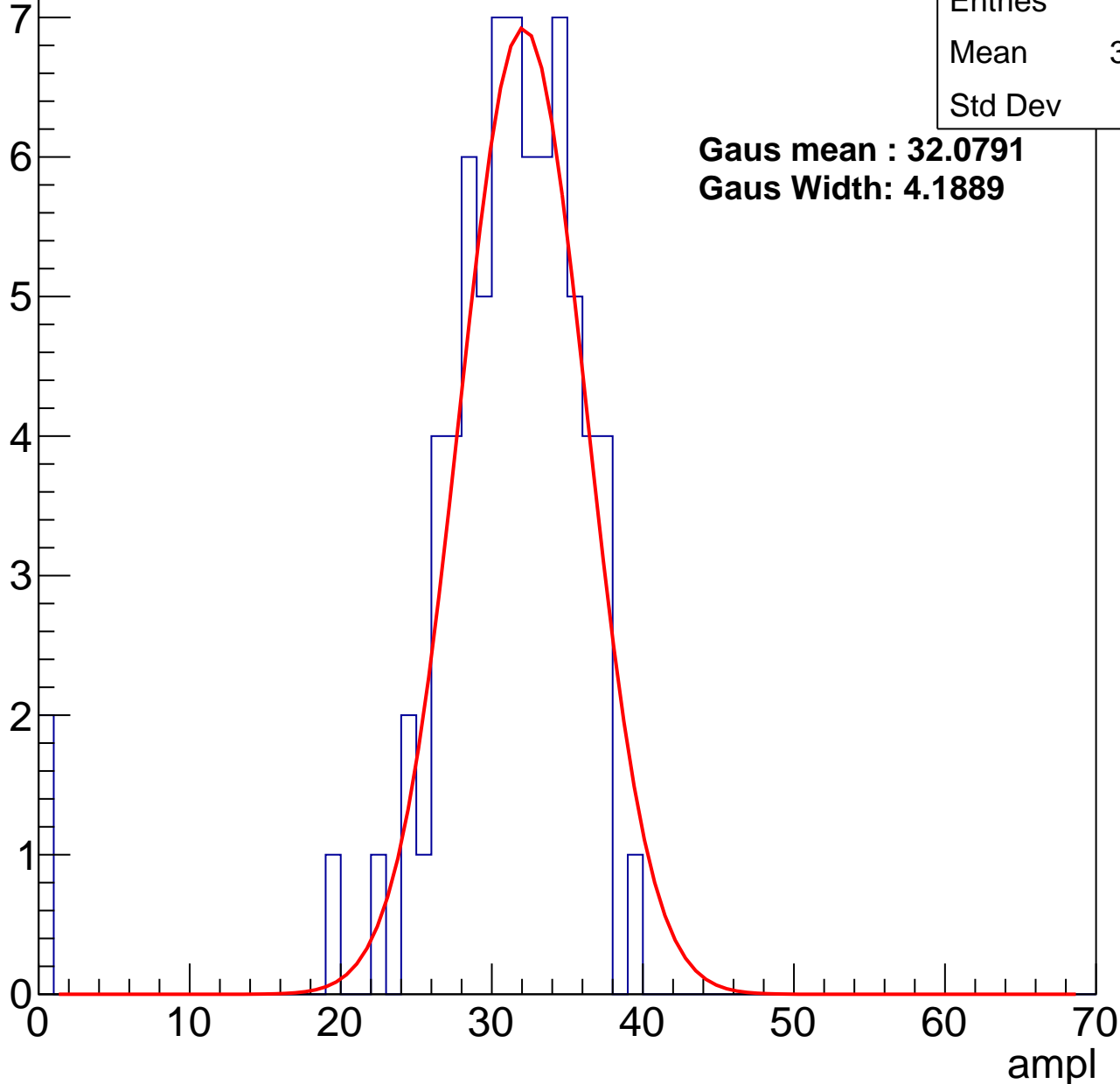
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.14
Std Dev	6.36

Gaus mean : 32.0791

Gaus Width: 4.1889



B1L101S, U1-ch112, adc1

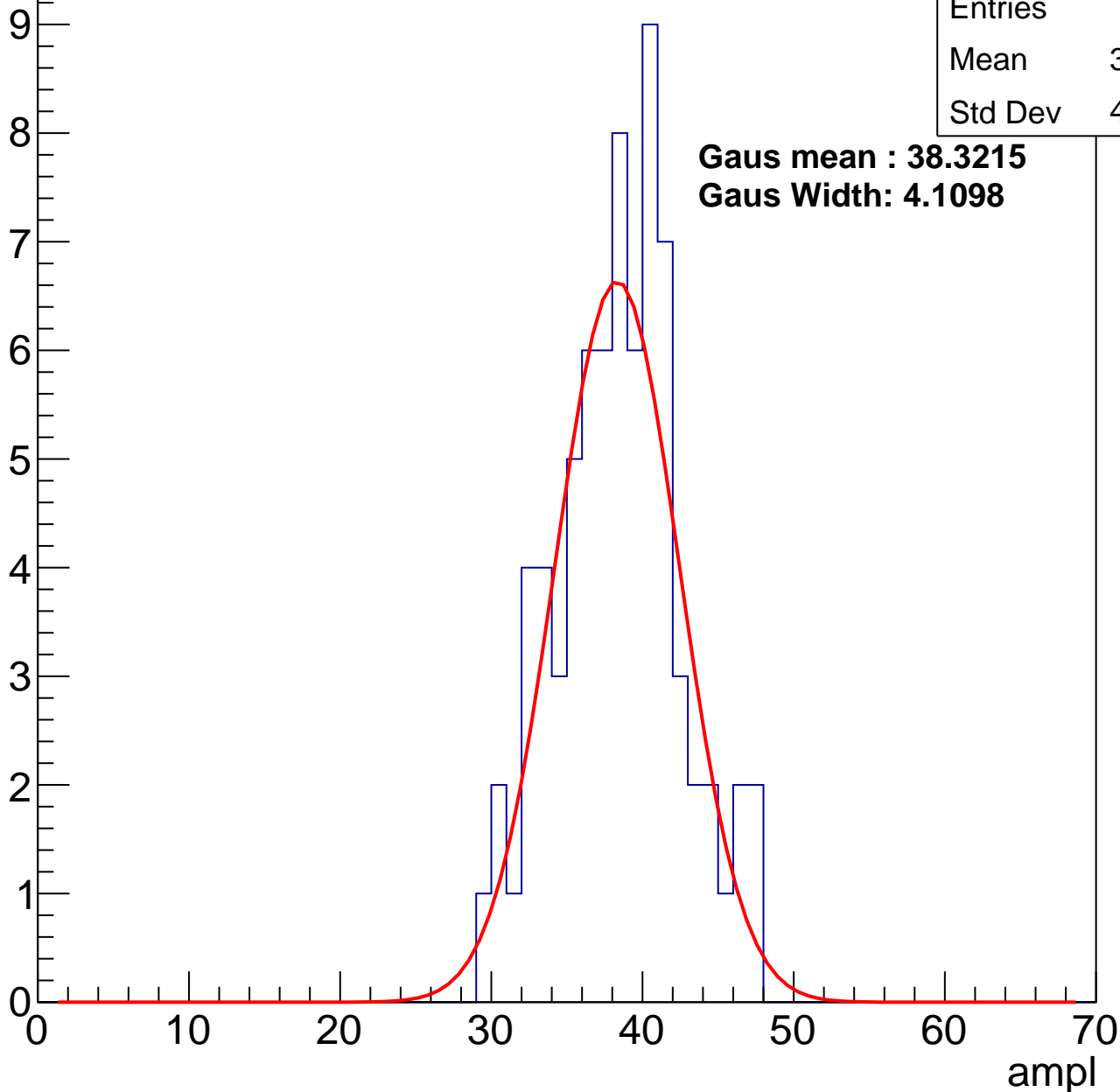
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.99
Std Dev	4.092

Gaus mean : 38.3215

Gaus Width: 4.1098



B1L101S, U1-ch112, adc2

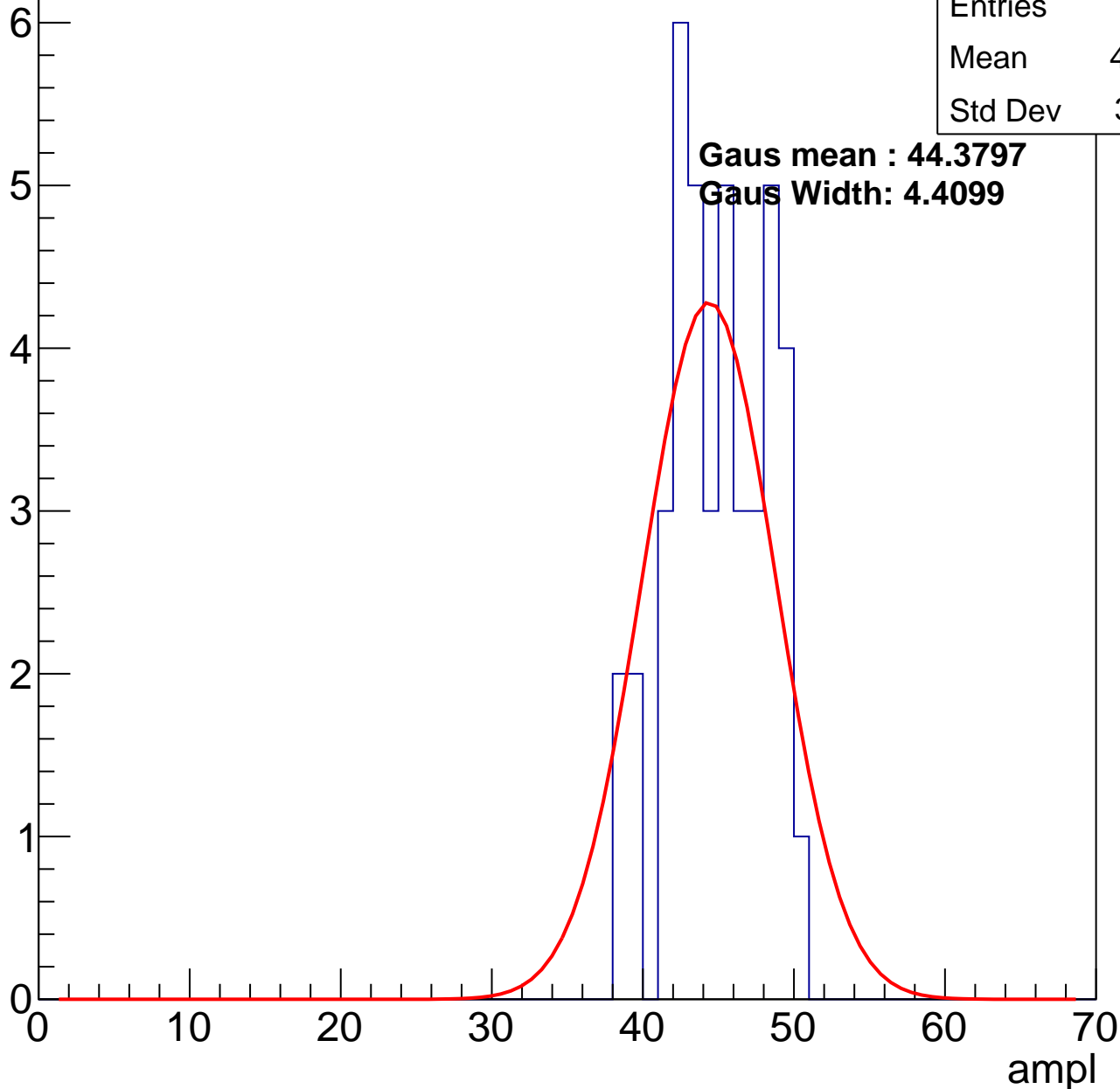
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	44.43
Std Dev	3.201

Gaus mean : 44.3797

Gaus Width: 4.4099

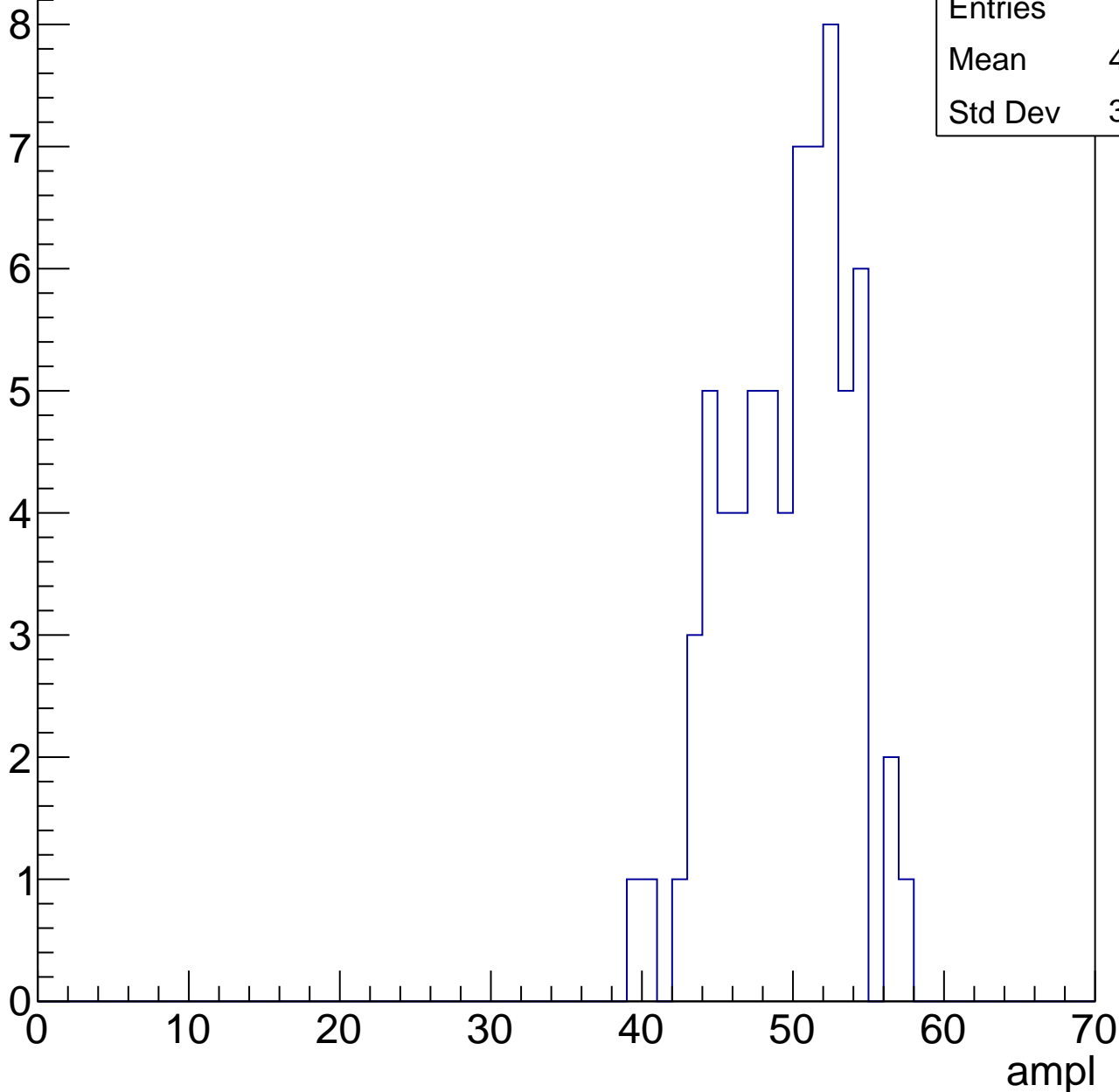


B1L101S, U1-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.07
Std Dev	3.968

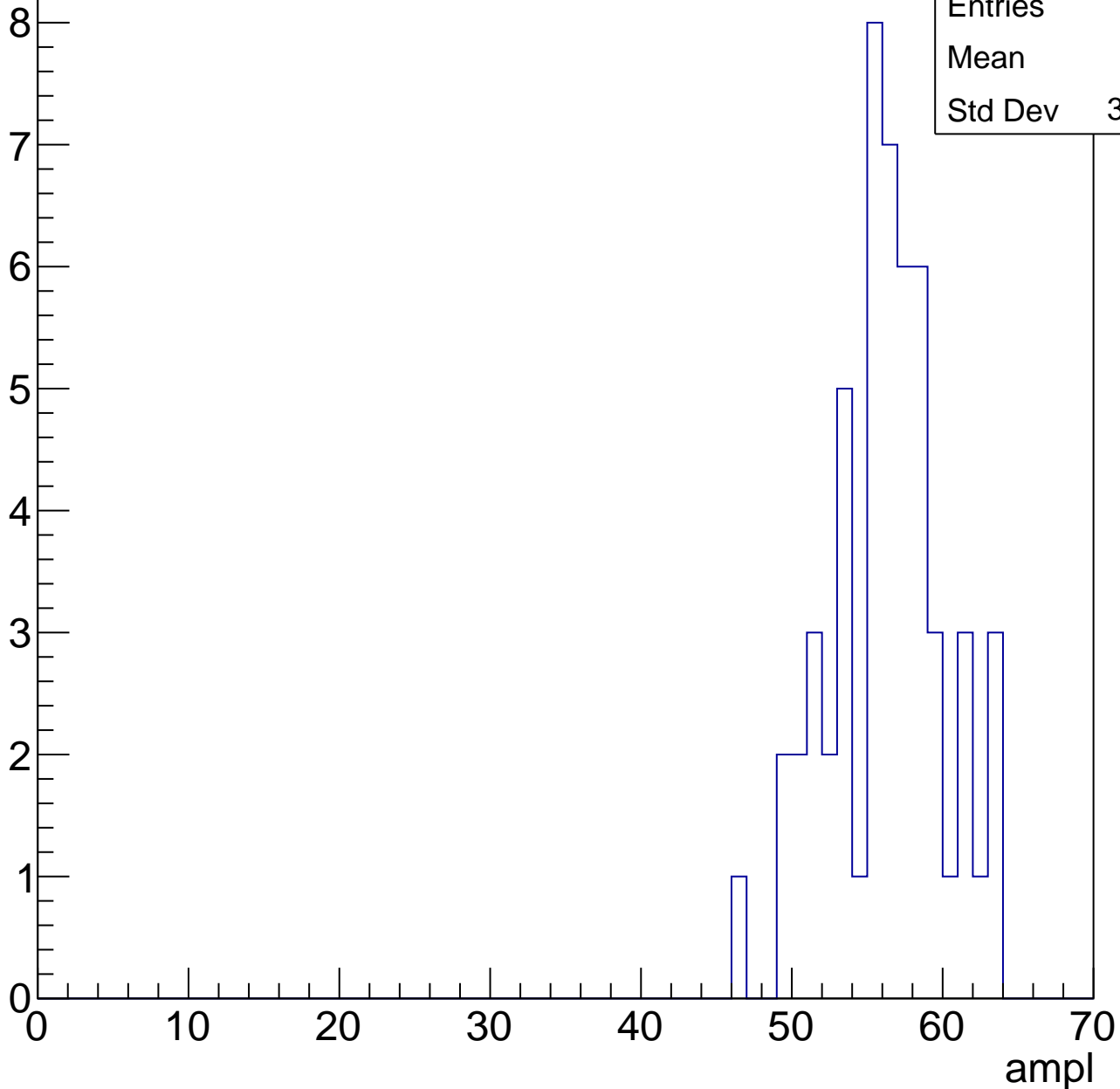


B1L101S, U1-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.8
Std Dev	3.758

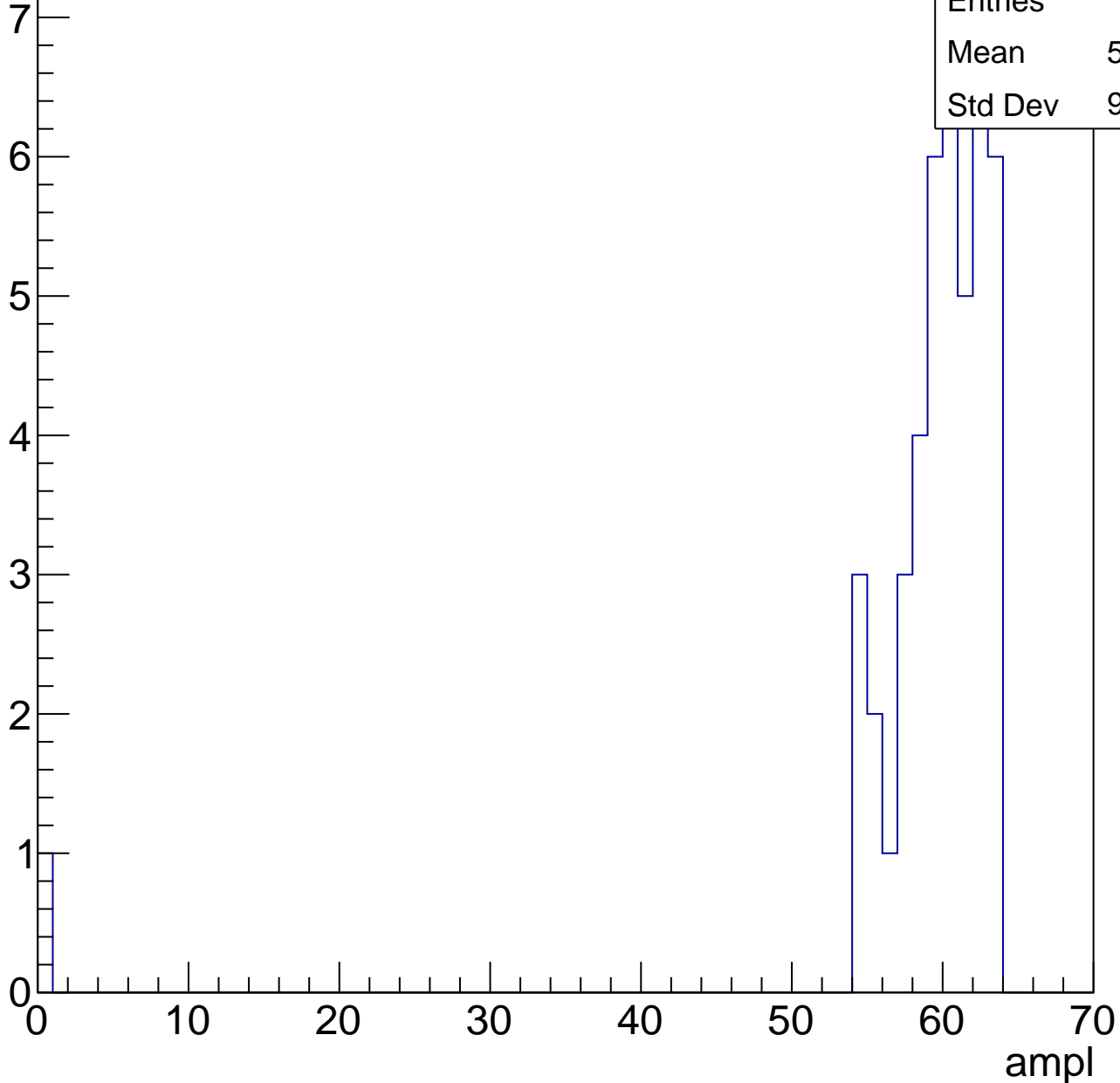


B1L101S, U1-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.27
Std Dev	9.159



B1L101S, U1-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B1L101S, U1-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U1-ch113, adc0

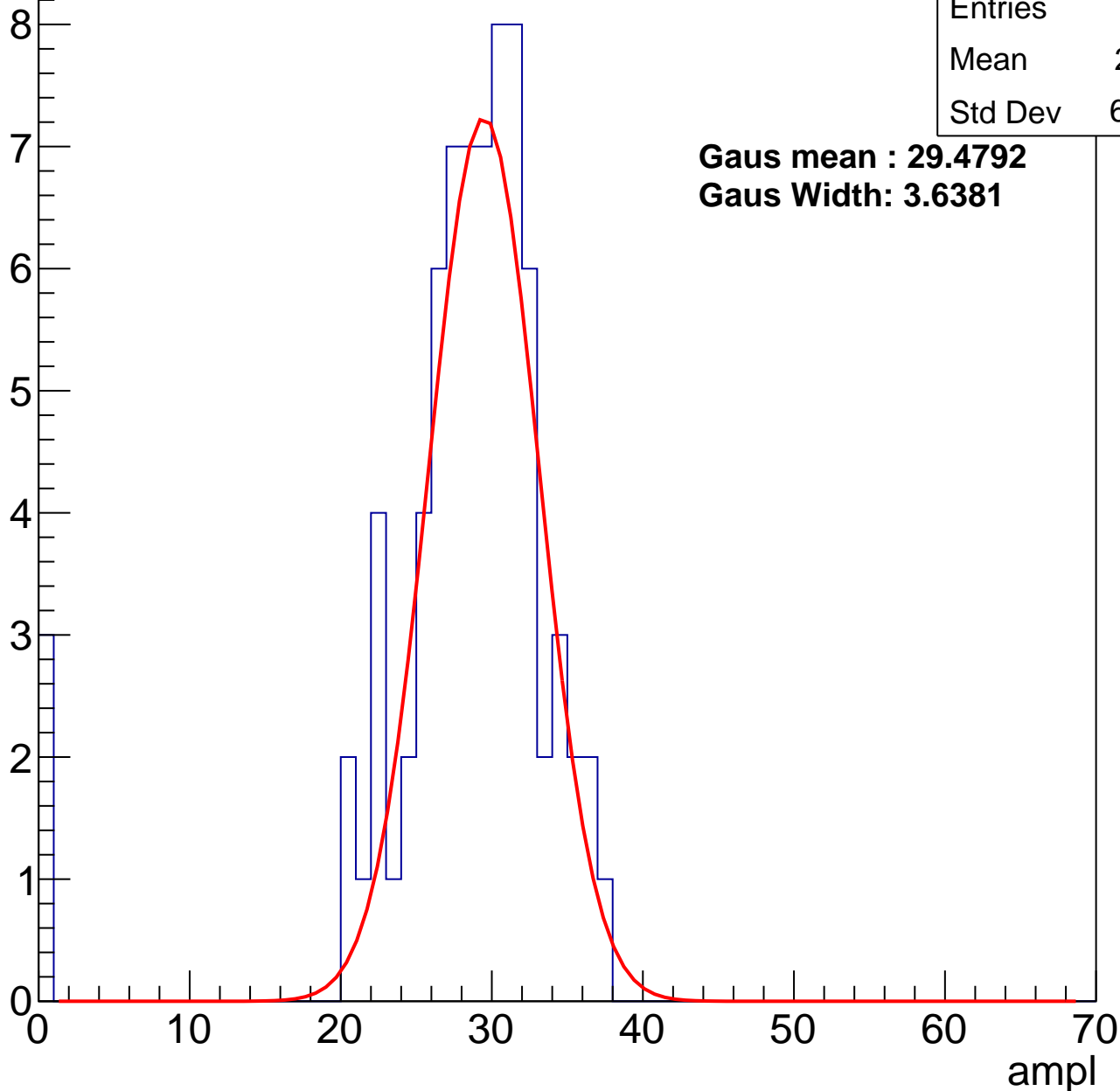
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	27.51
Std Dev	6.739

Gaus mean : 29.4792

Gaus Width: 3.6381



B1L101S, U1-ch113, adc1

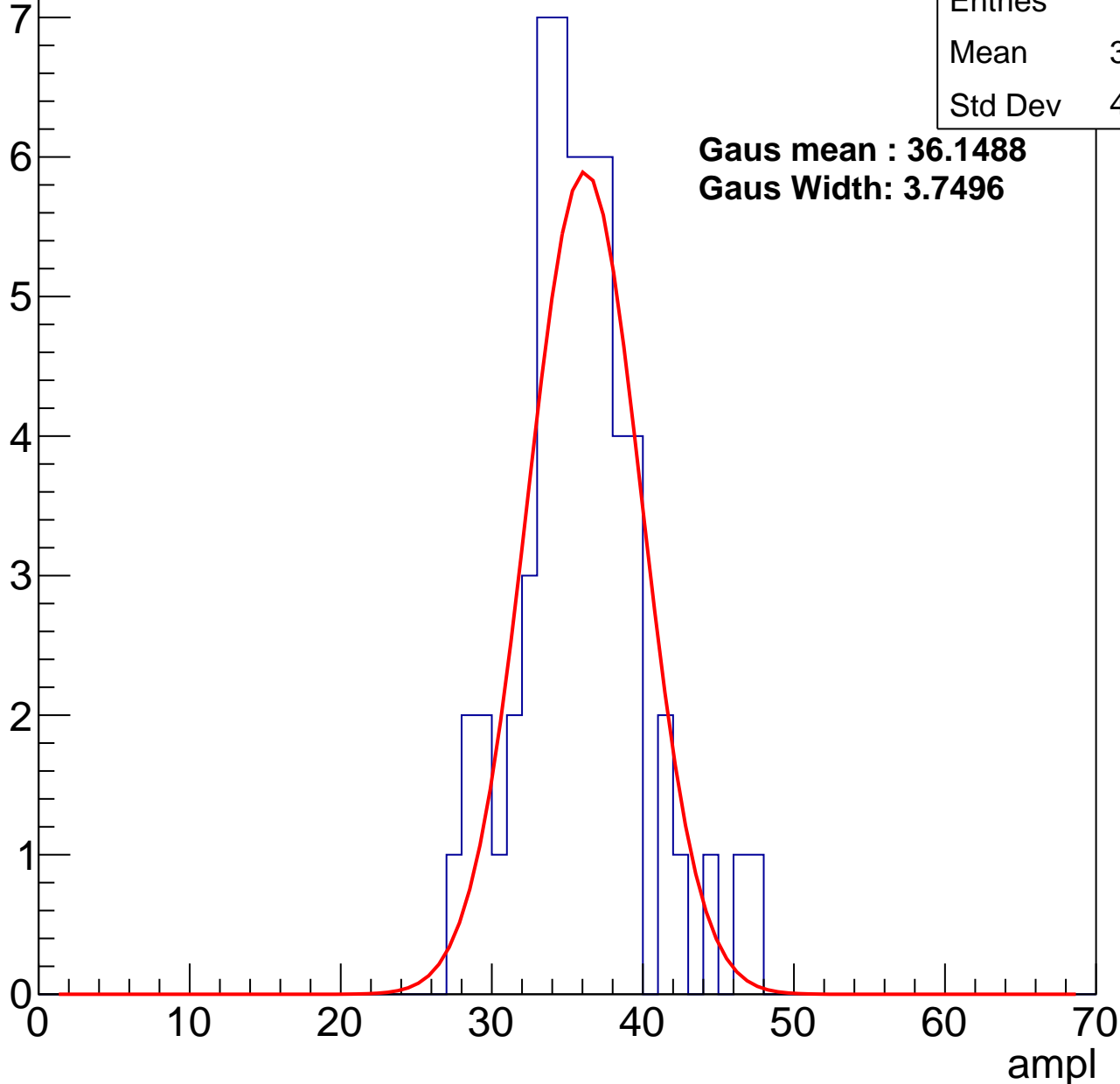
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.35
Std Dev	4.067

Gaus mean : 36.1488

Gaus Width: 3.7496



B1L101S, U1-ch113, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	41.38
Std Dev	3.698

Gaus mean : 41.4408

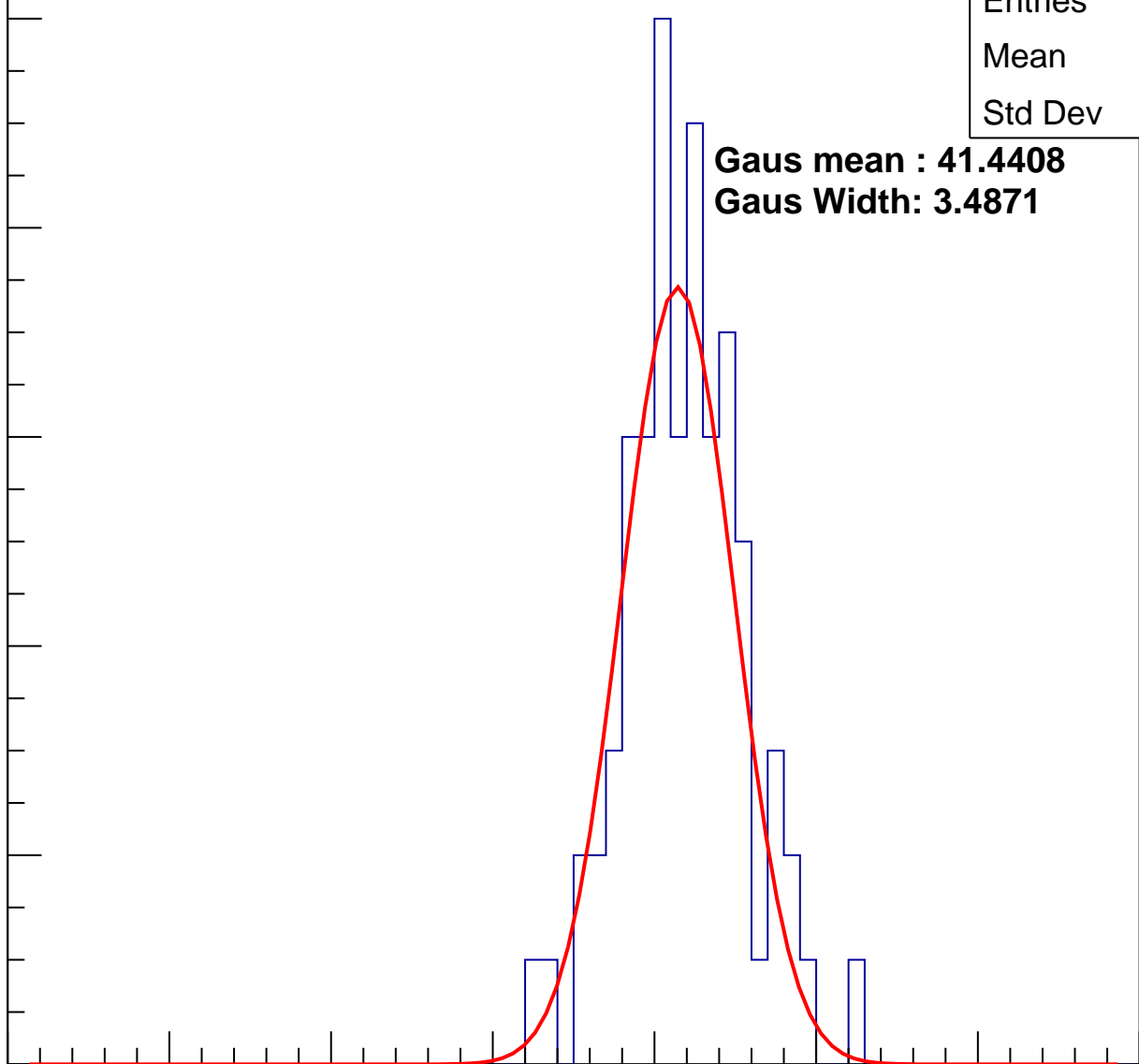
Gaus Width: 3.4871

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

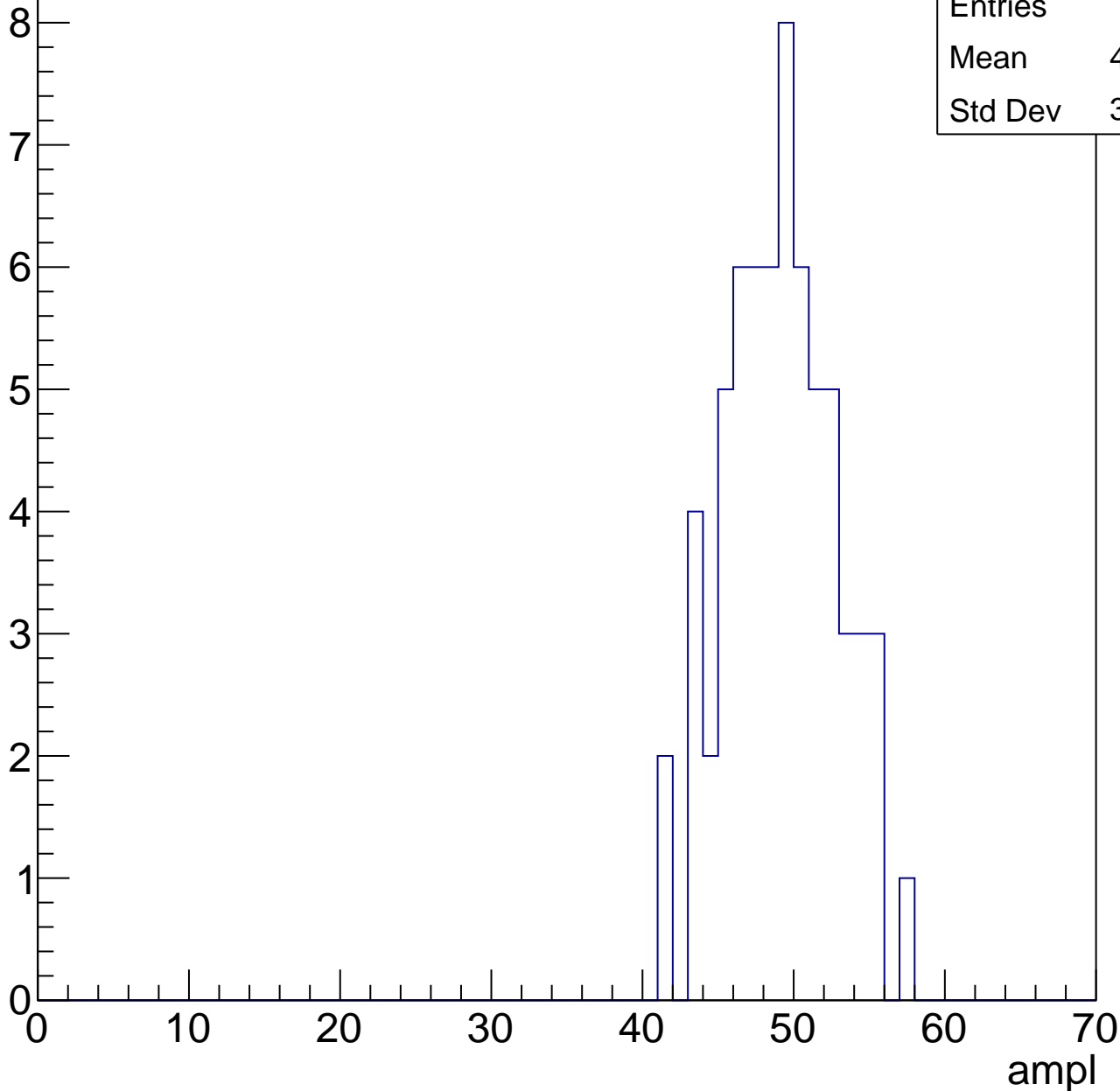


B1L101S, U1-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.66
Std Dev	3.605

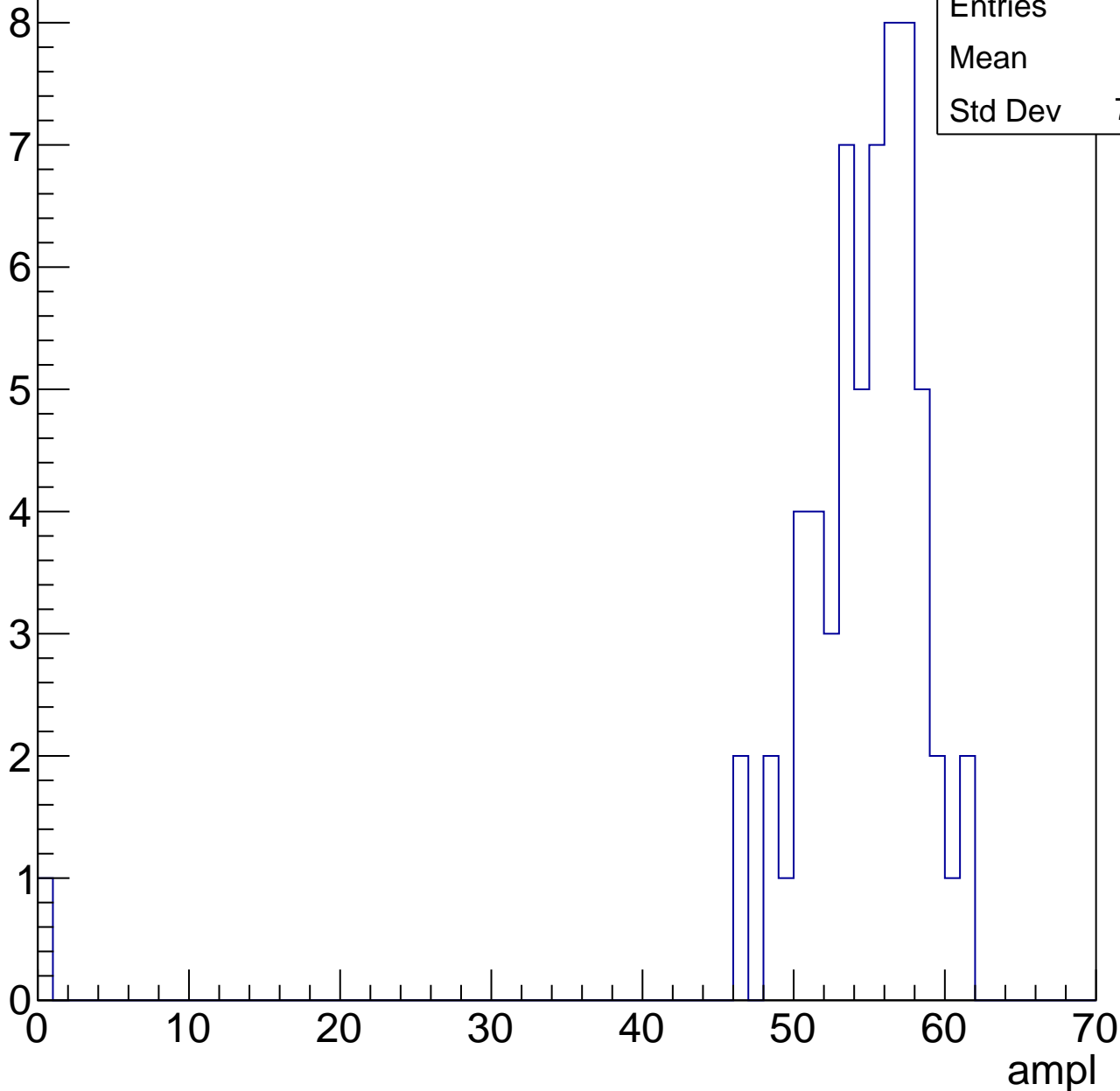


B1L101S, U1-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	53.5
Std Dev	7.641

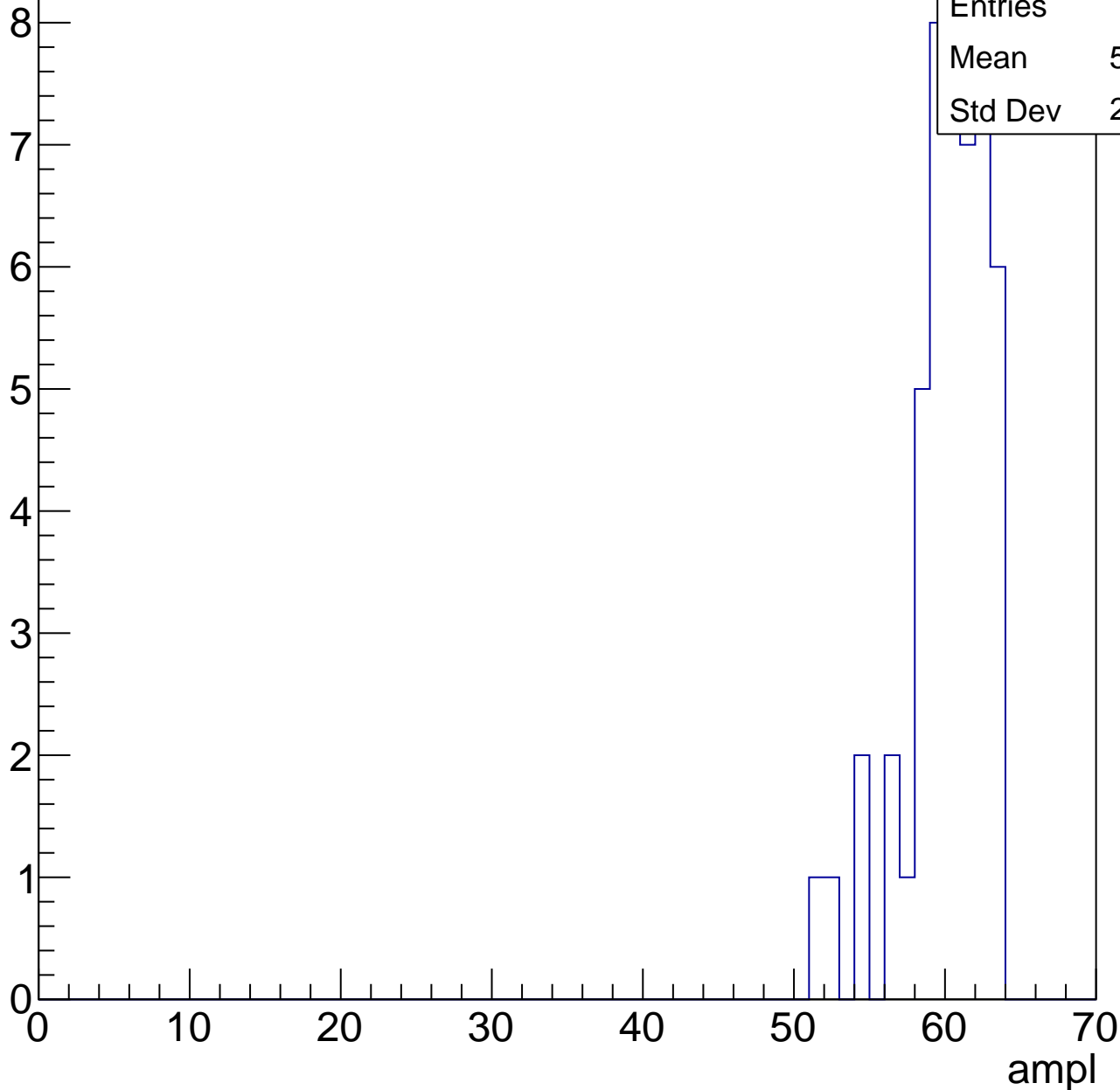


B1L101S, U1-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.65
Std Dev	2.759



B1L101S, U1-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U1-ch114, adc0

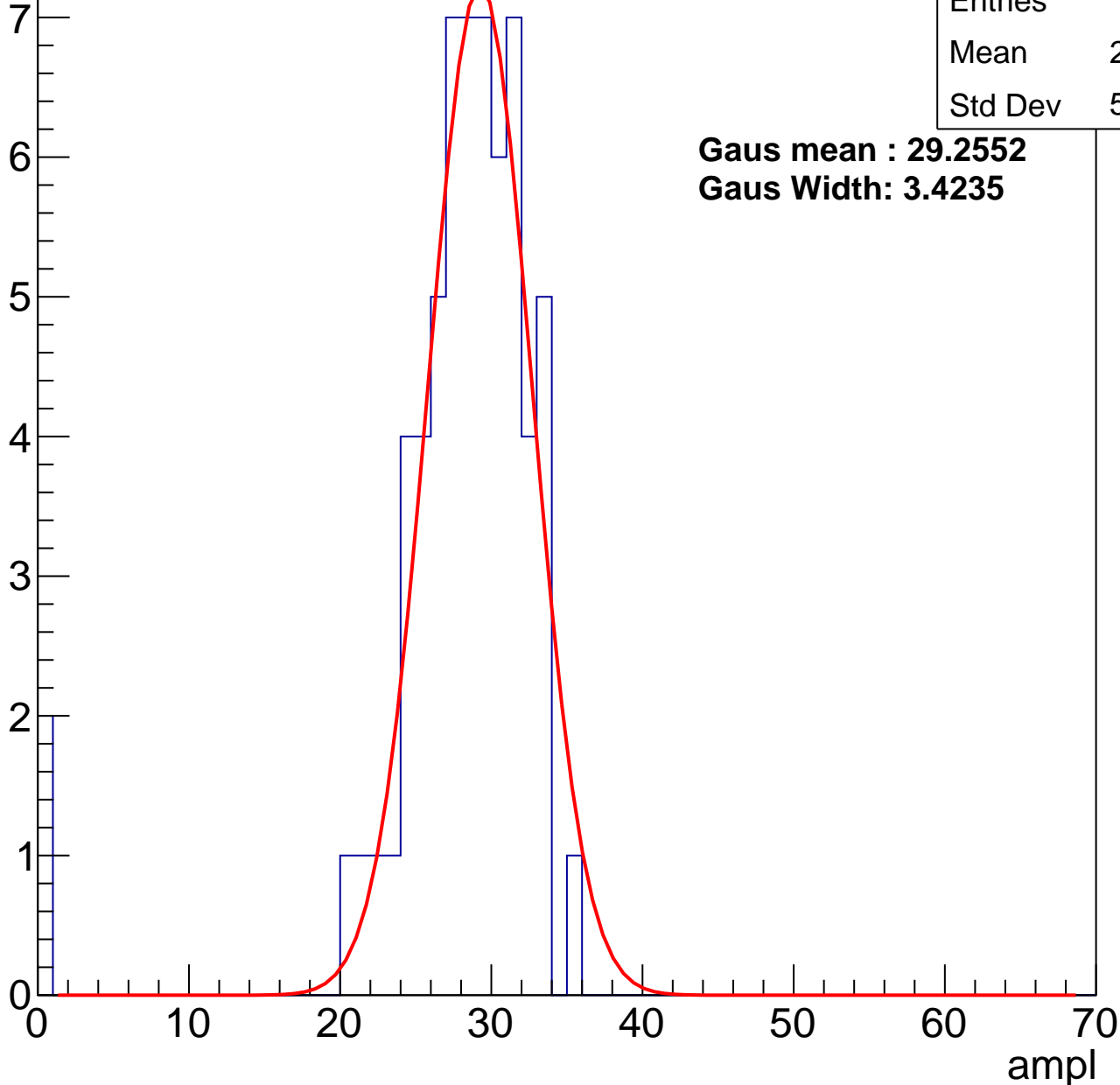
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.38
Std Dev	5.878

Gaus mean : 29.2552

Gaus Width: 3.4235



B1L101S, U1-ch114, adc1

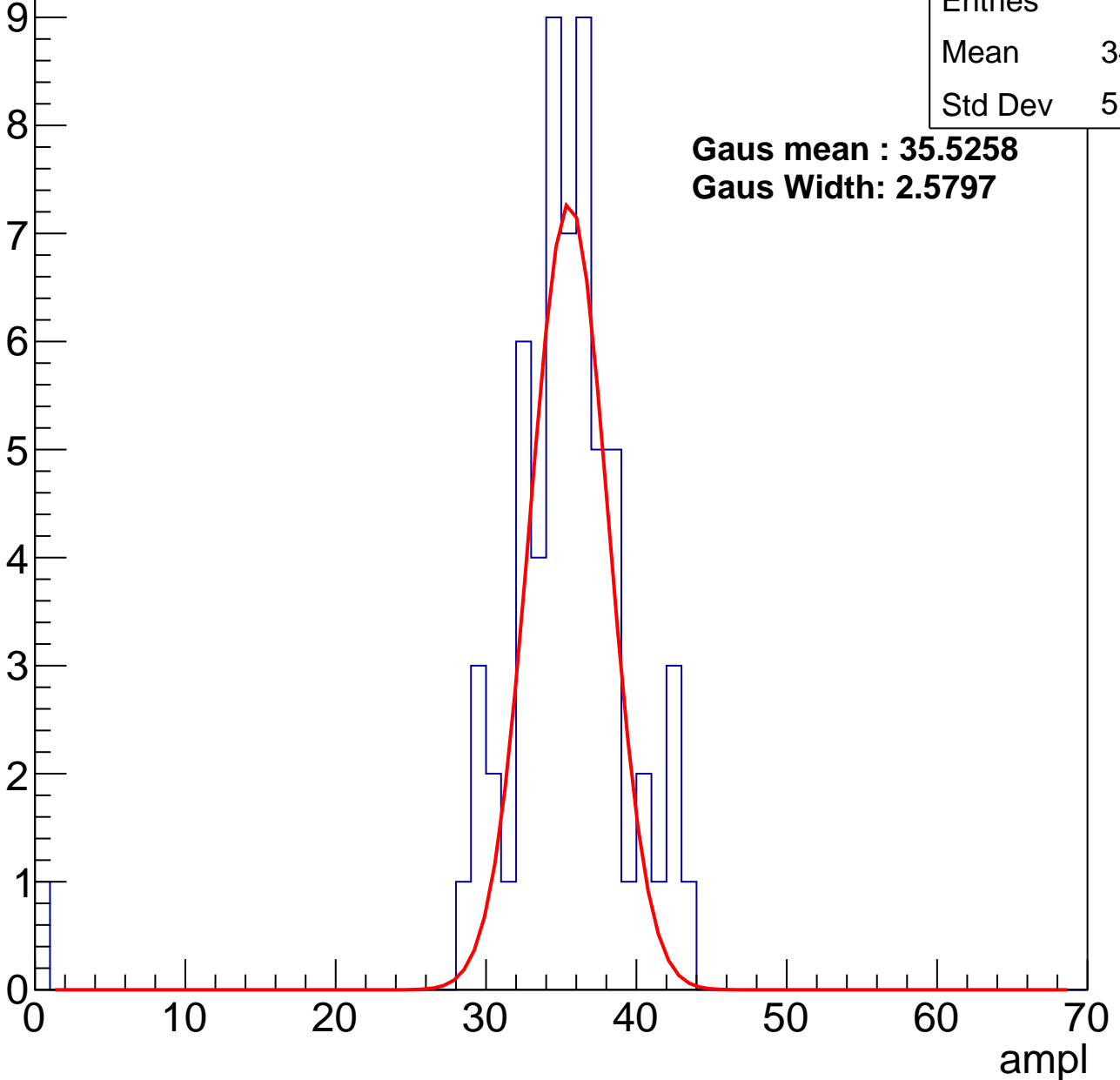
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	34.57
Std Dev	5.585

Gaus mean : 35.5258

Gaus Width: 2.5797



B1L101S, U1-ch114, adc2

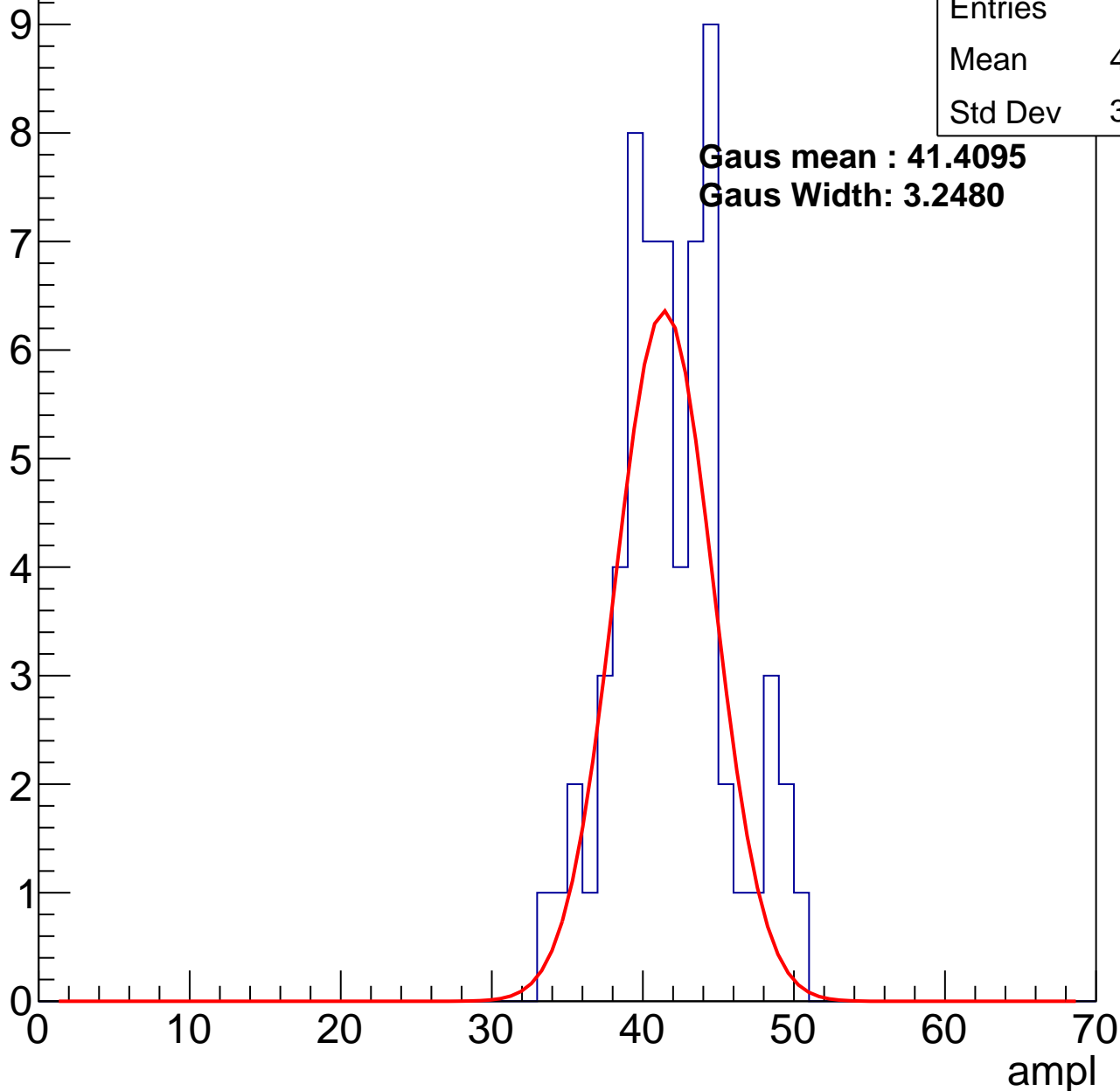
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	41.48
Std Dev	3.708

Gaus mean : 41.4095

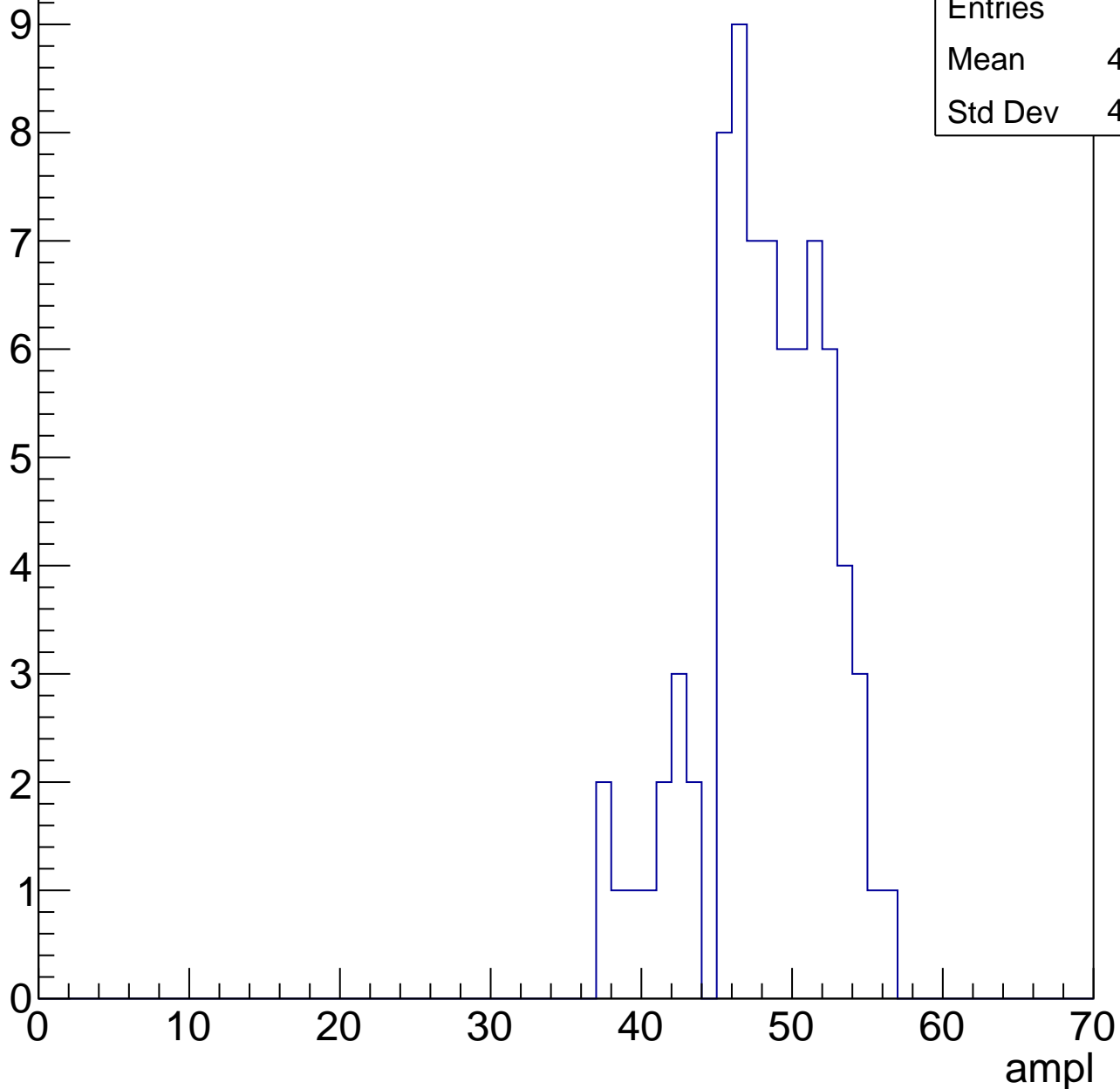
Gaus Width: 3.2480



B1L101S, U1-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



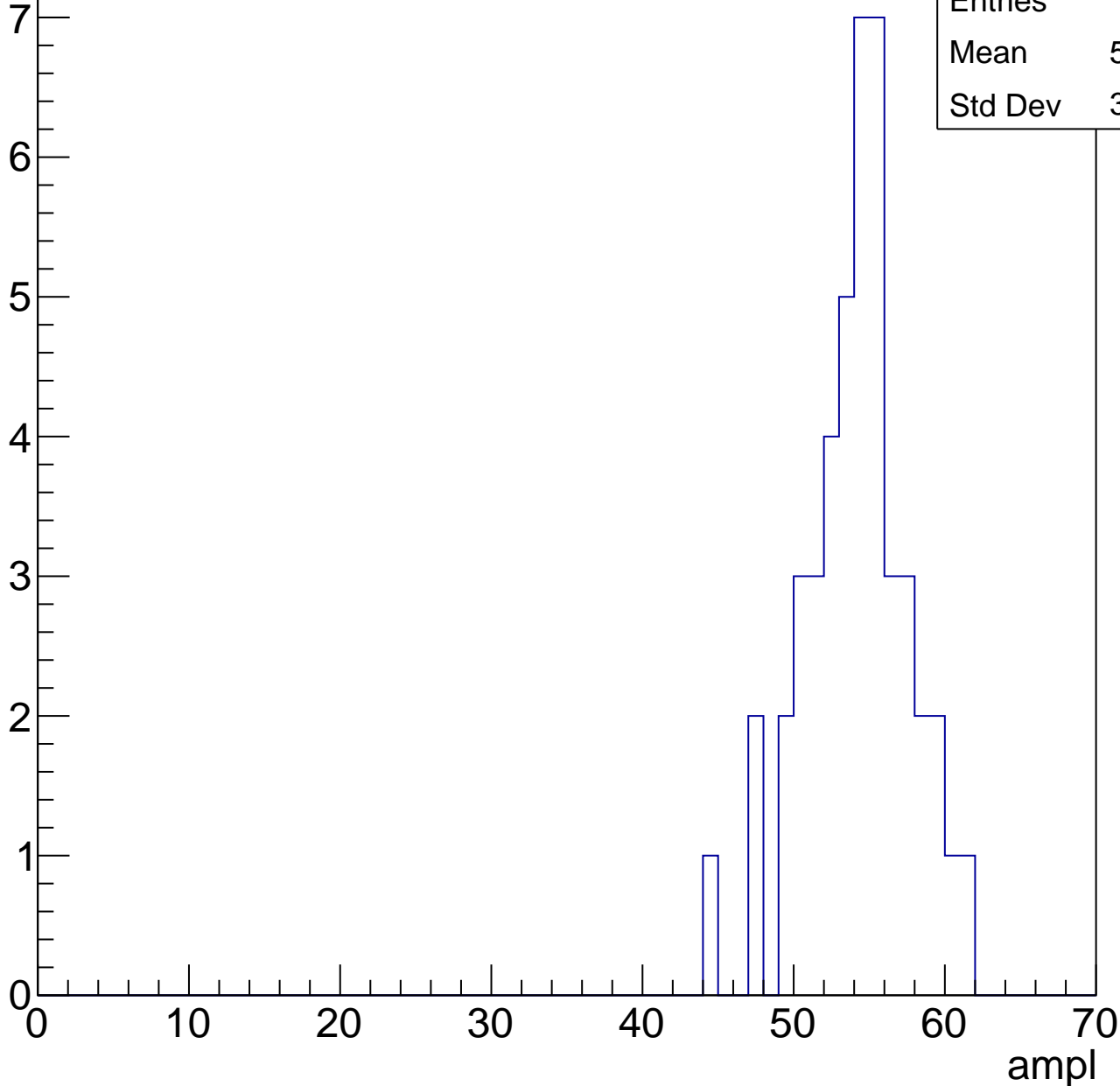
Entries	77
Mean	47.69
Std Dev	4.203

B1L101S, U1-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

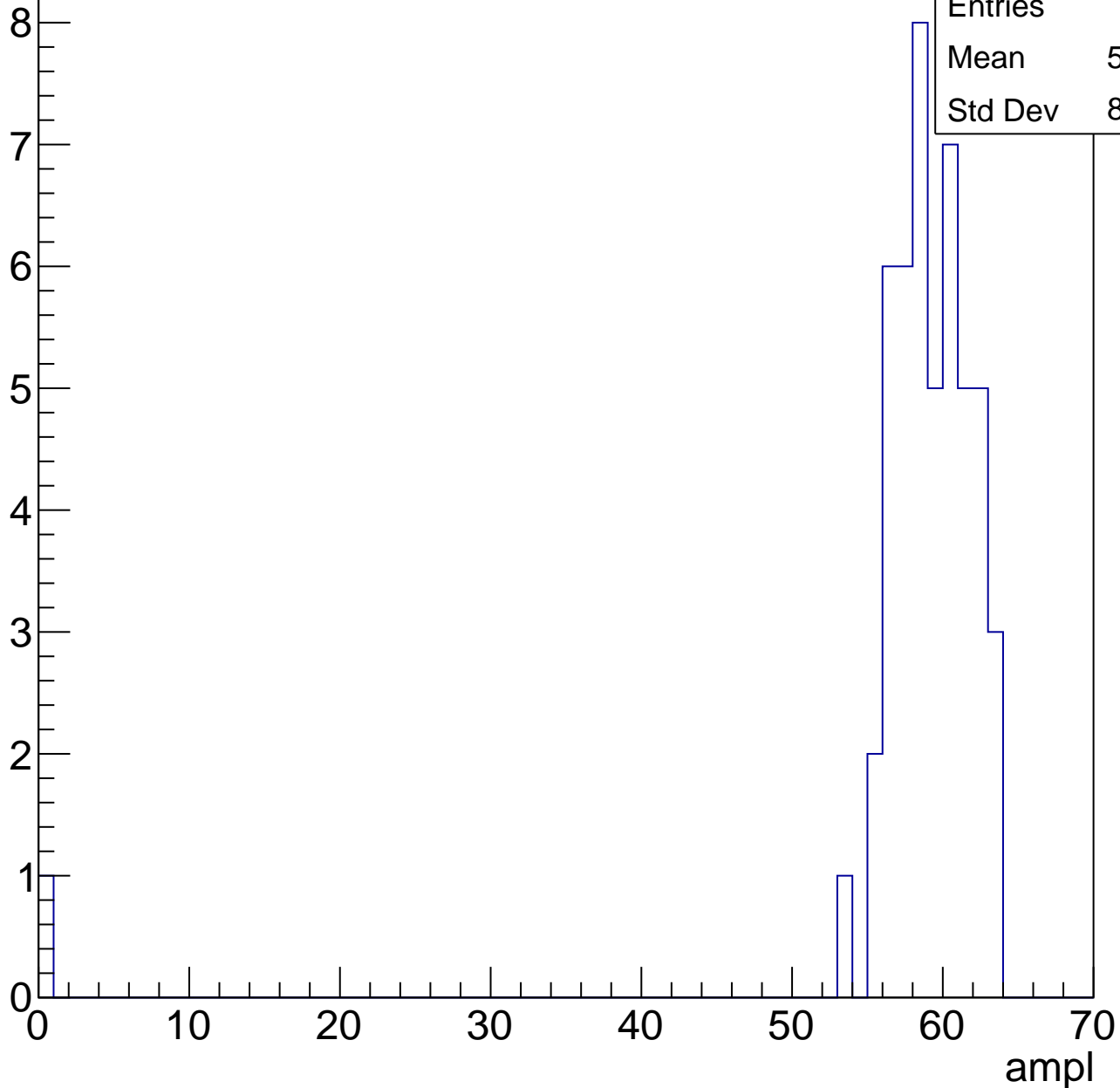
Entries	46
Mean	53.67
Std Dev	3.464



B1L101S, U1-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

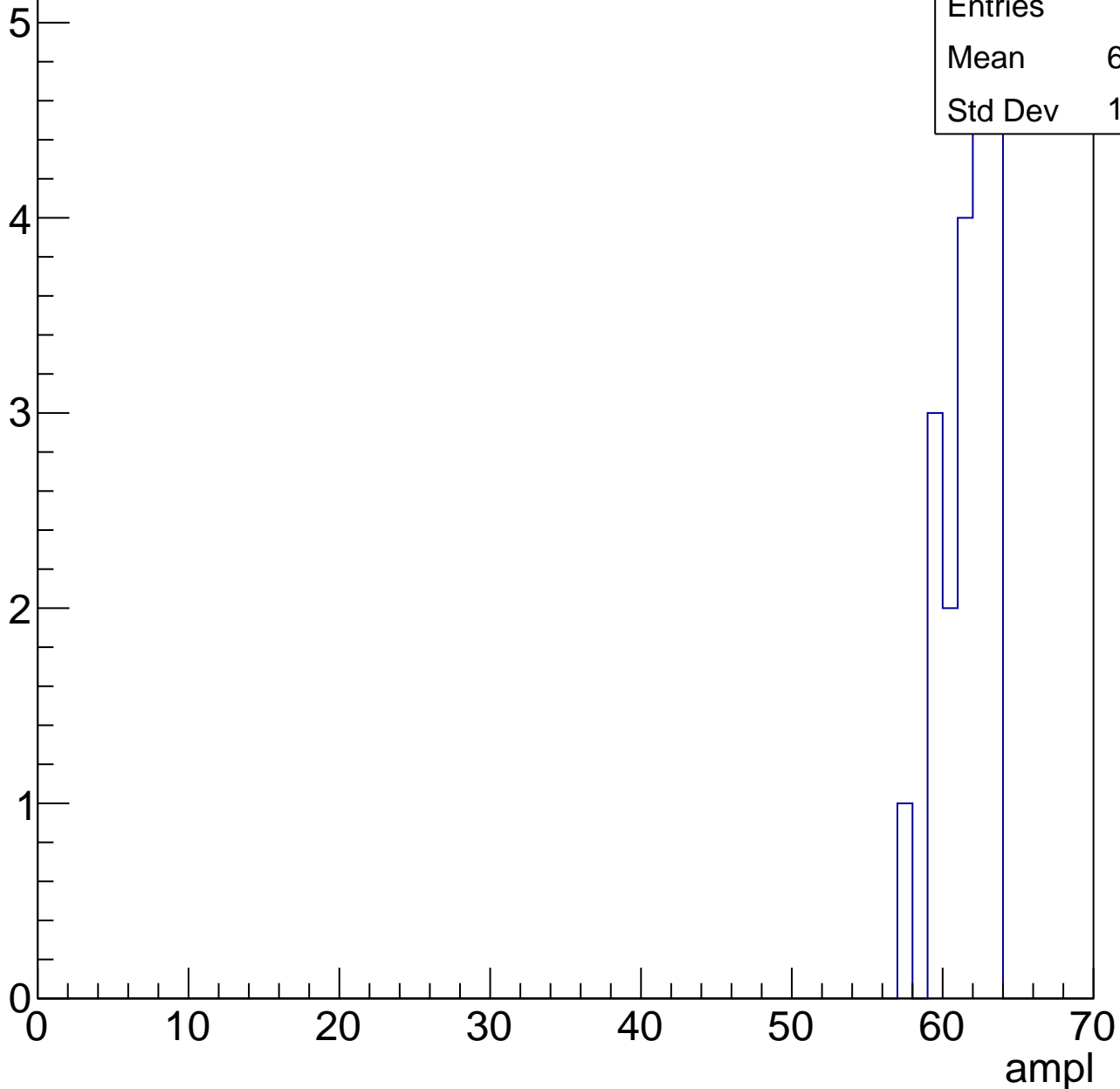


B1L101S, U1-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	61.15
Std Dev	1.652



B1L101S, U1-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch115, adc0

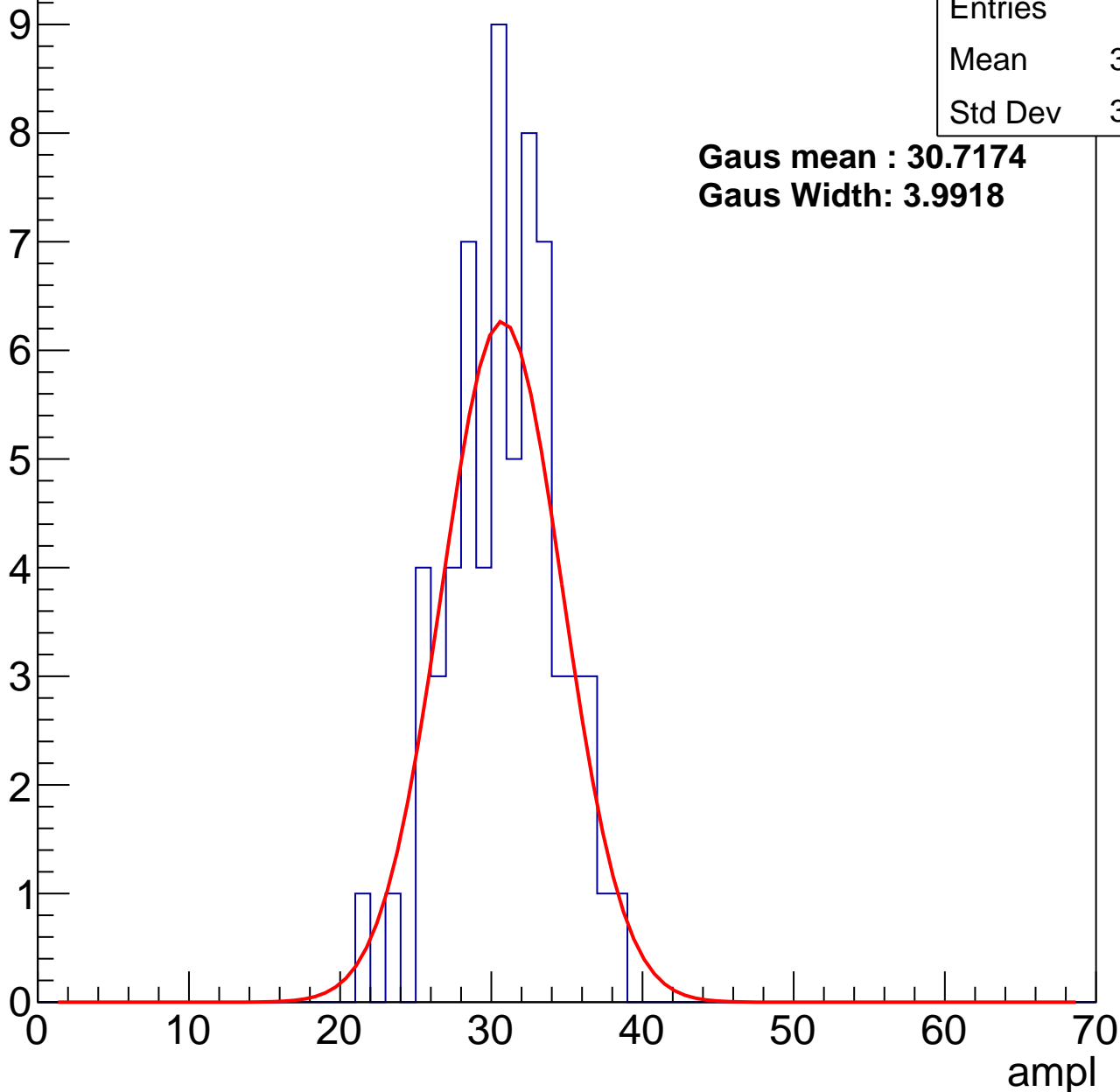
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.38
Std Dev	3.493

Gaus mean : 30.7174

Gaus Width: 3.9918



B1L101S, U1-ch115, adc1

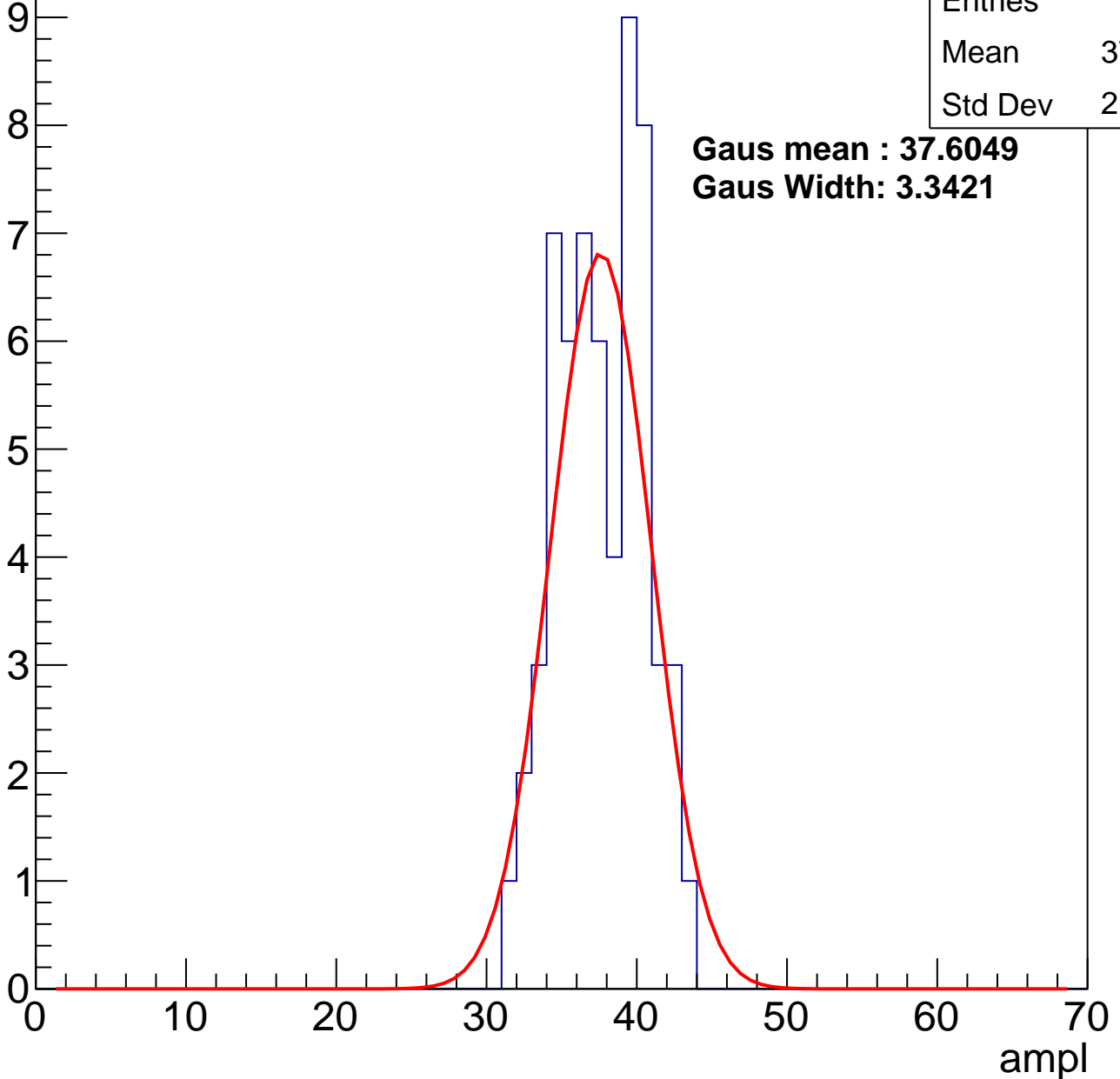
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.18
Std Dev	2.878

Gaus mean : 37.6049

Gaus Width: 3.3421



B1L101S, U1-ch115, adc2

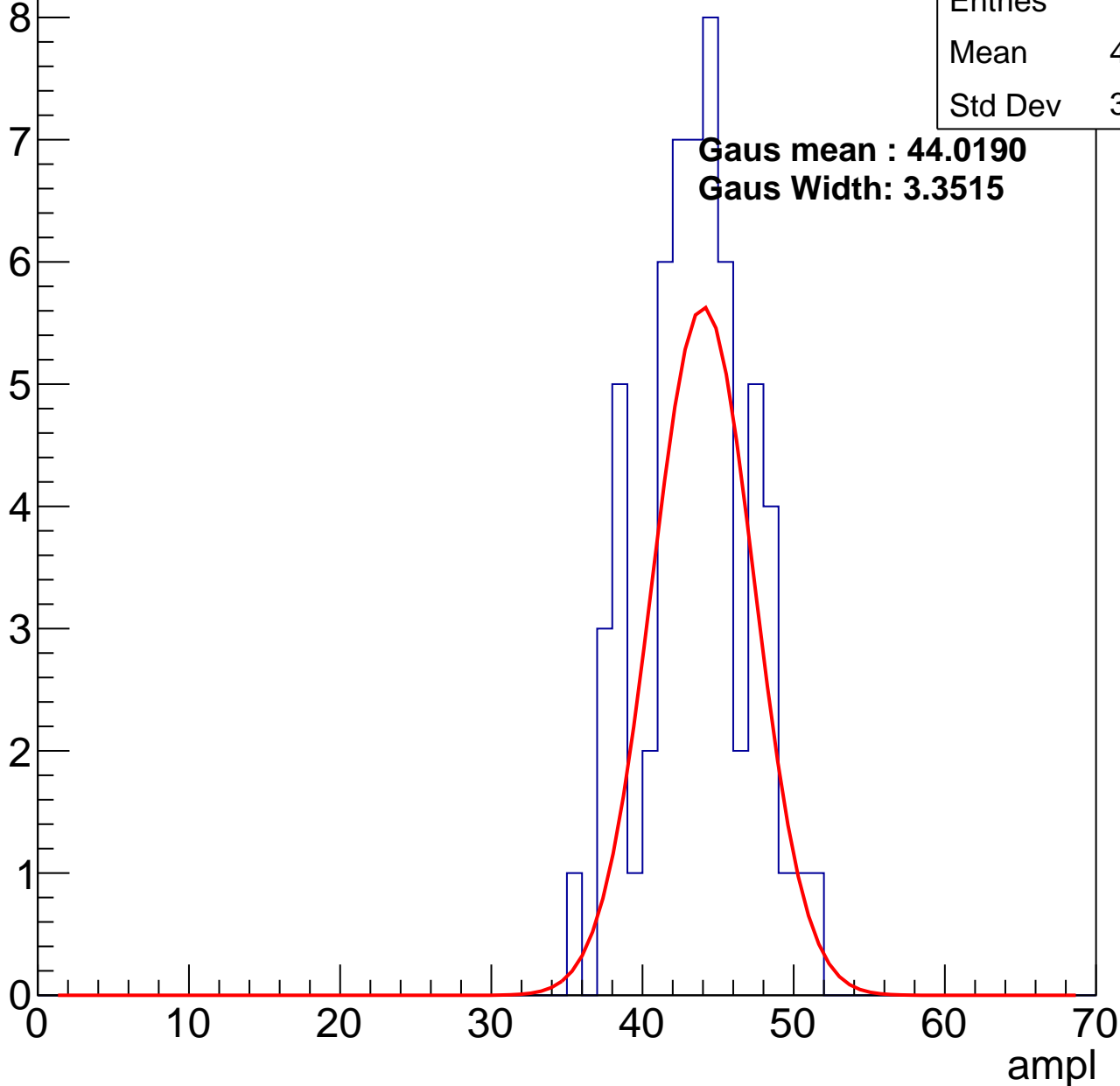
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.12
Std Dev	3.512

Gaus mean : 44.0190

Gaus Width: 3.3515

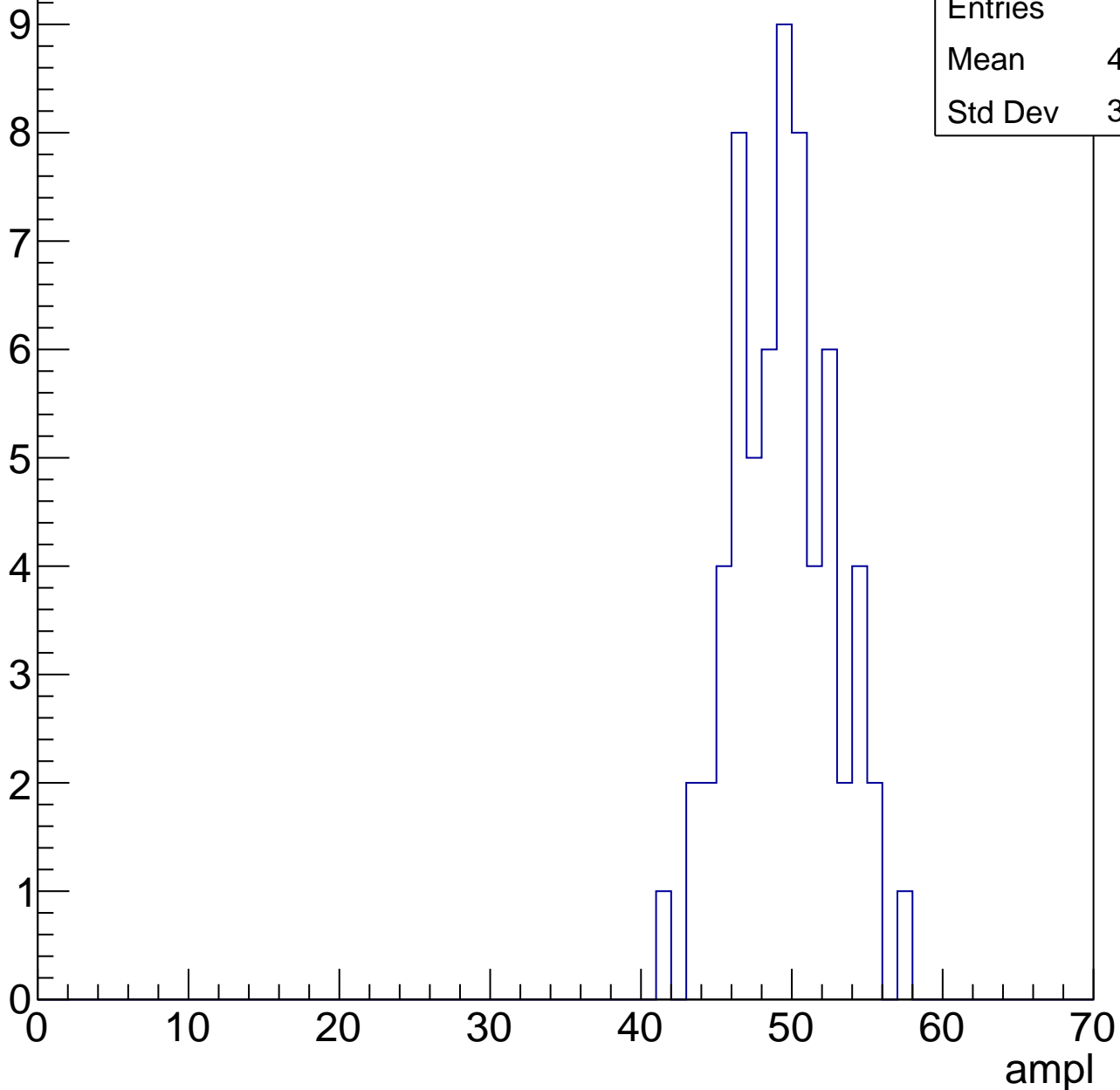


B1L101S, U1-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.94
Std Dev	3.292

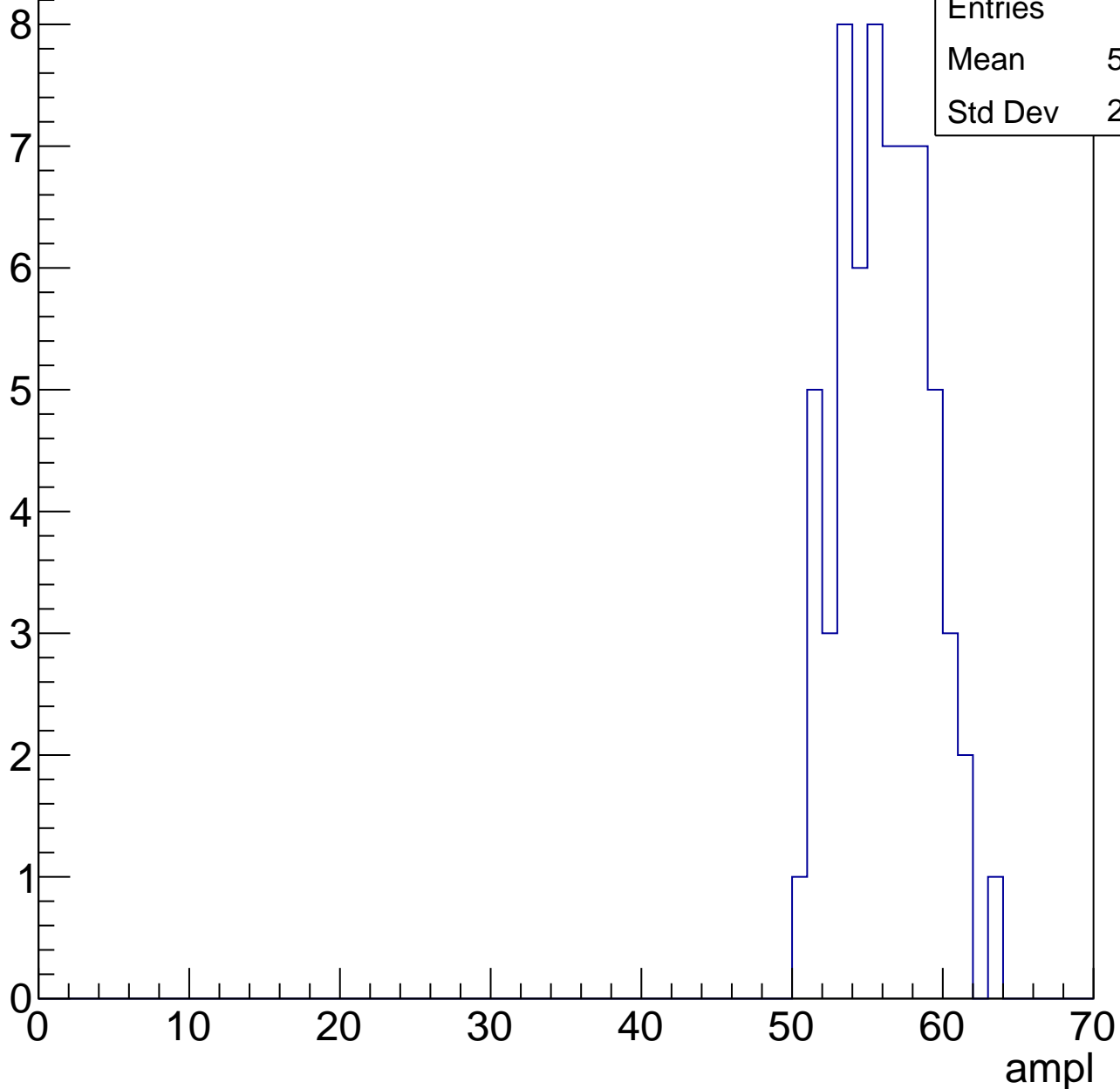


B1L101S, U1-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.65
Std Dev	2.907

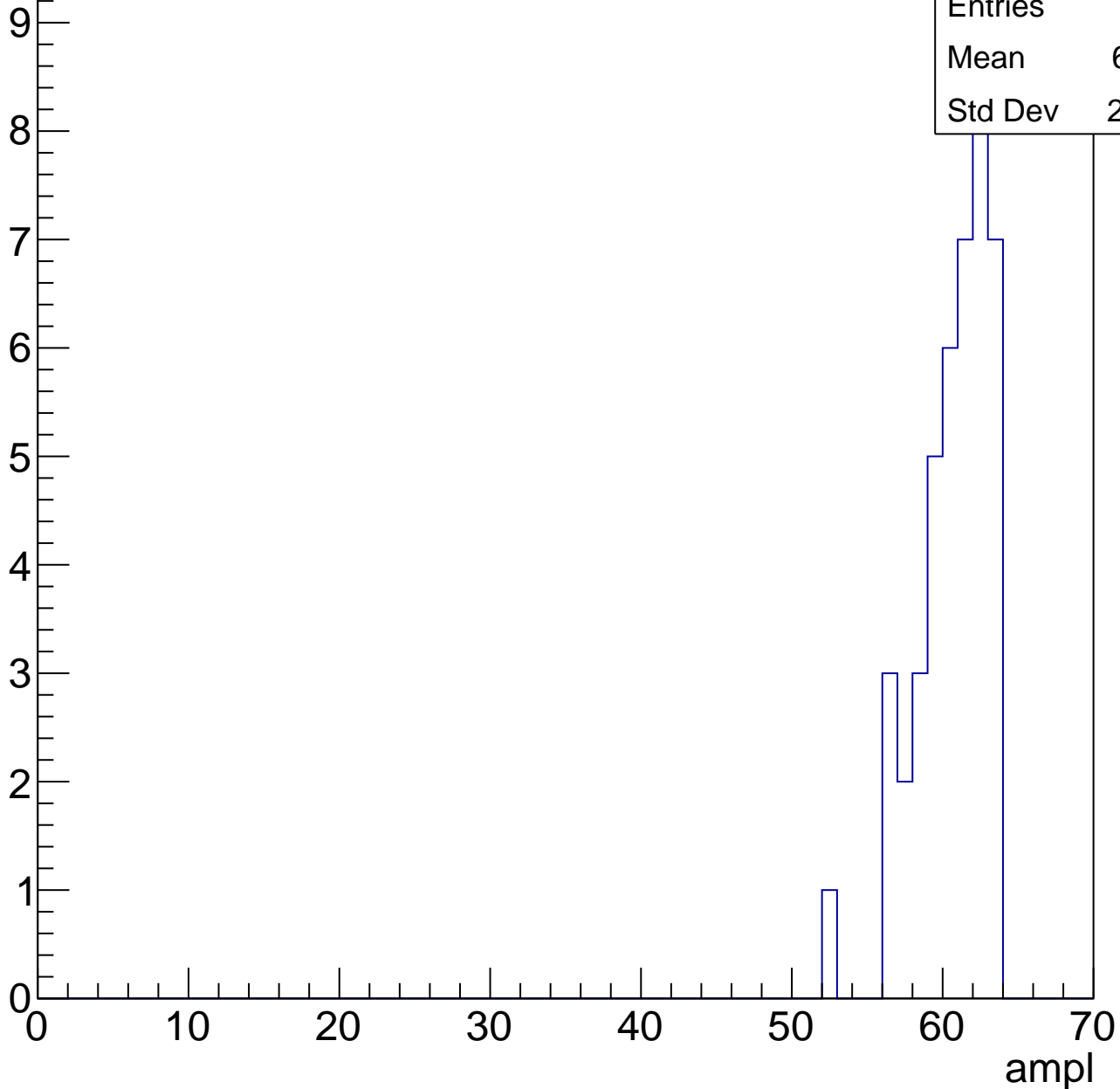


B1L101S, U1-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	60.21
Std Dev	2.417



B1L101S, U1-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

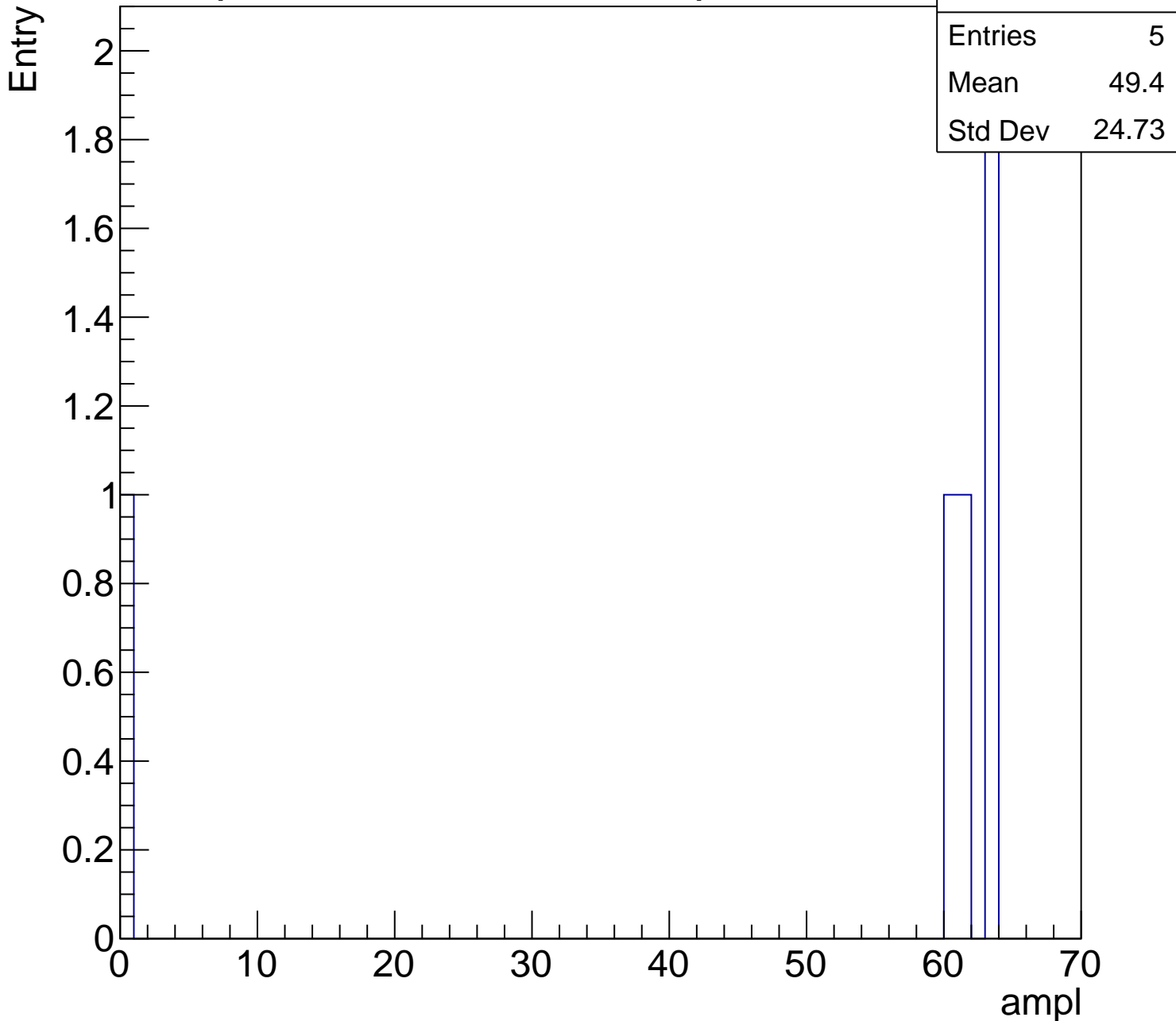
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.73

0 10 20 30 40 50 60 70

ampl



B1L101S, U1-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch116, adc0

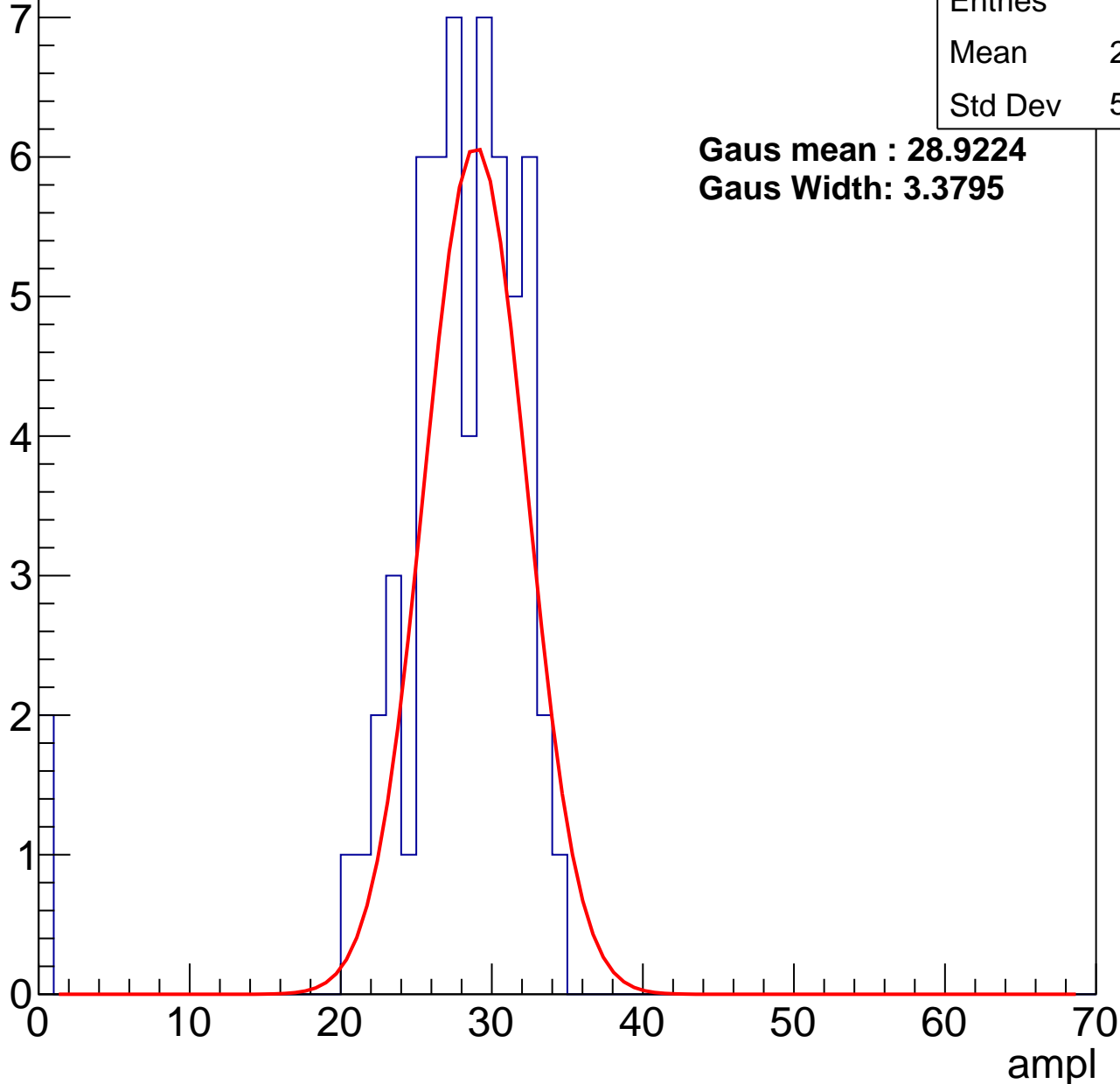
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	26.92
Std Dev	5.939

Gaus mean : 28.9224

Gaus Width: 3.3795



B1L101S, U1-ch116, adc1

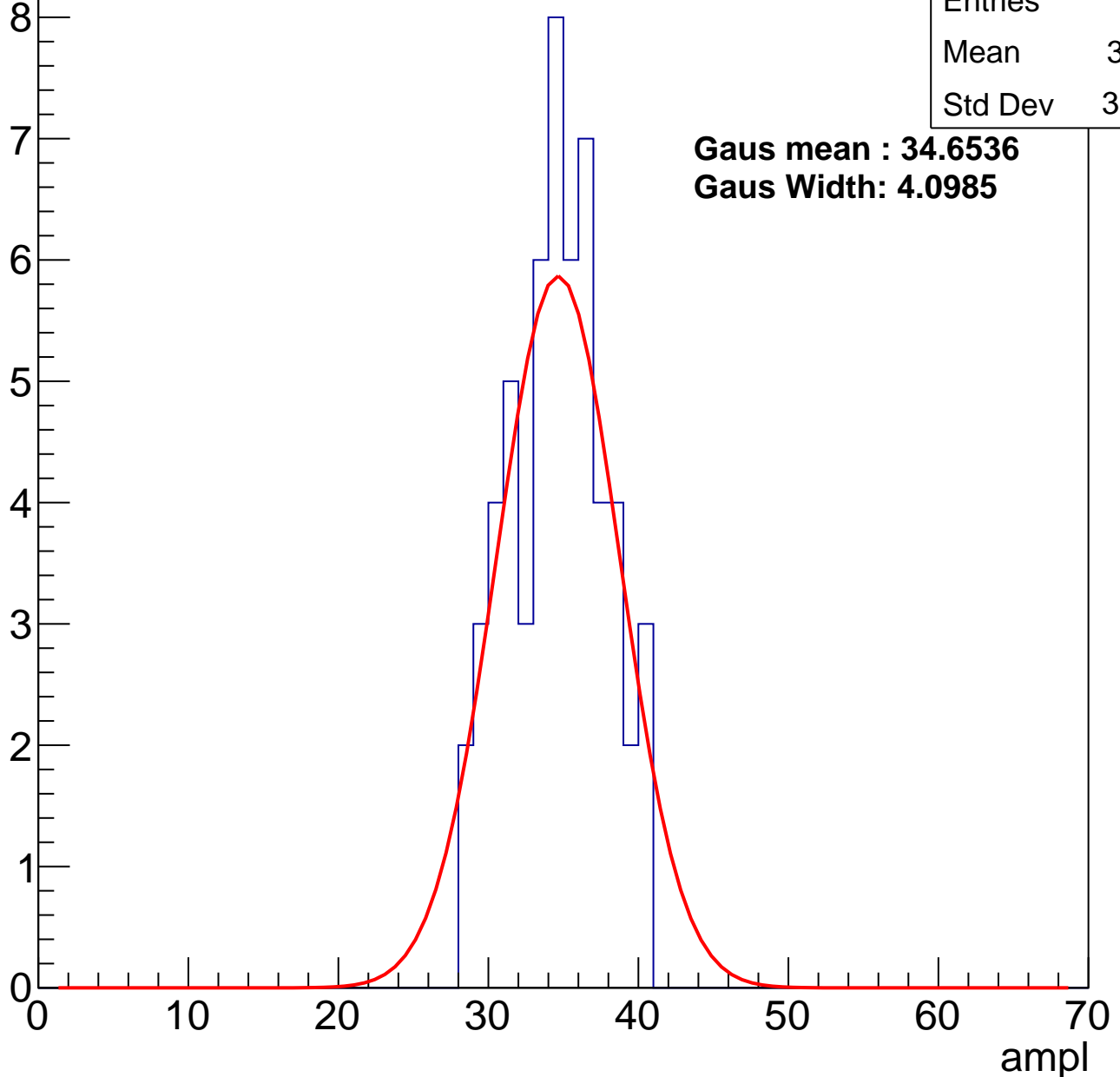
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	34.11
Std Dev	3.149

Gaus mean : 34.6536

Gaus Width: 4.0985



B1L101S, U1-ch116, adc2

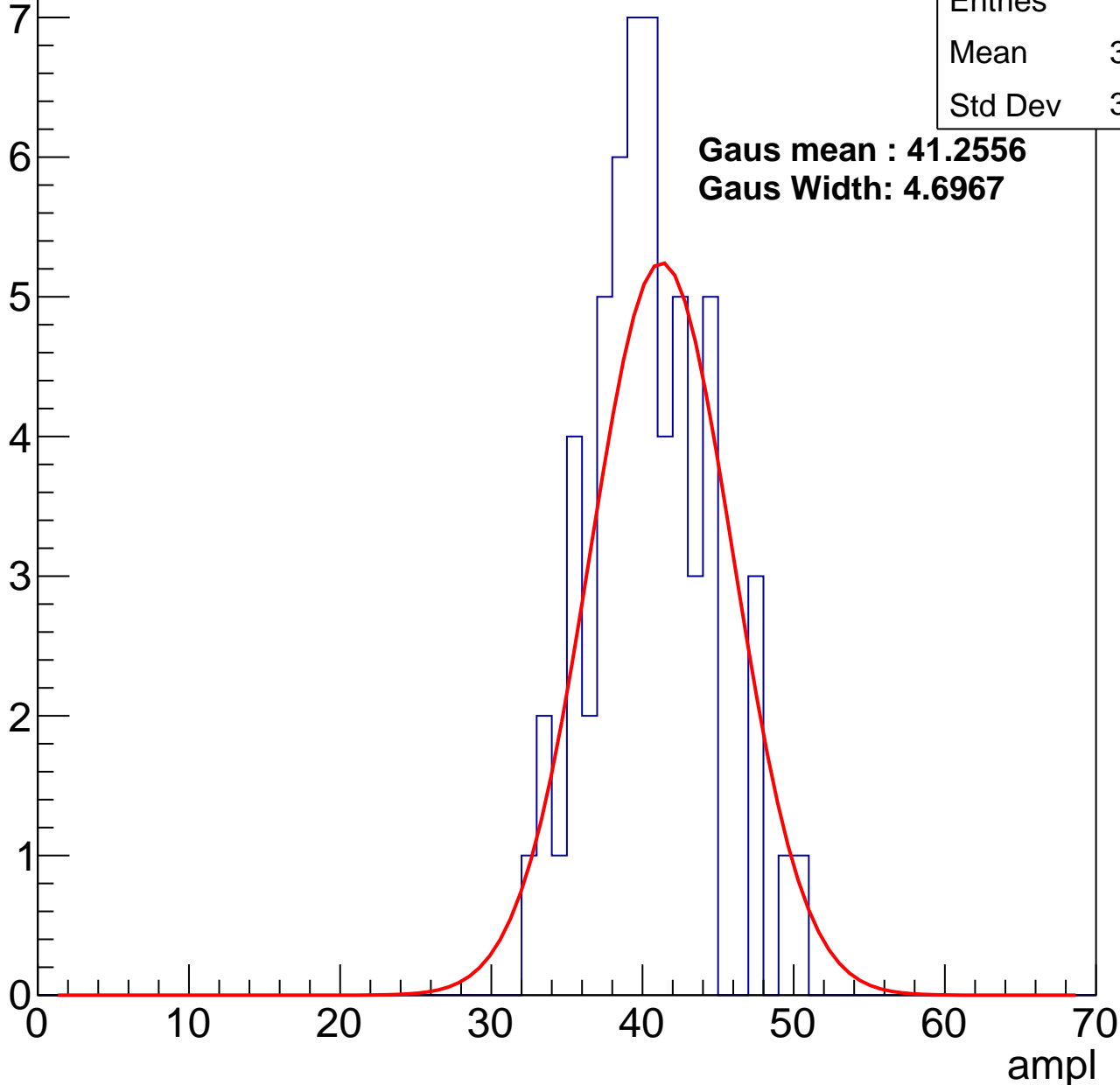
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	39.88
Std Dev	3.894

Gaus mean : 41.2556

Gaus Width: 4.6967



B1L101S, U1-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

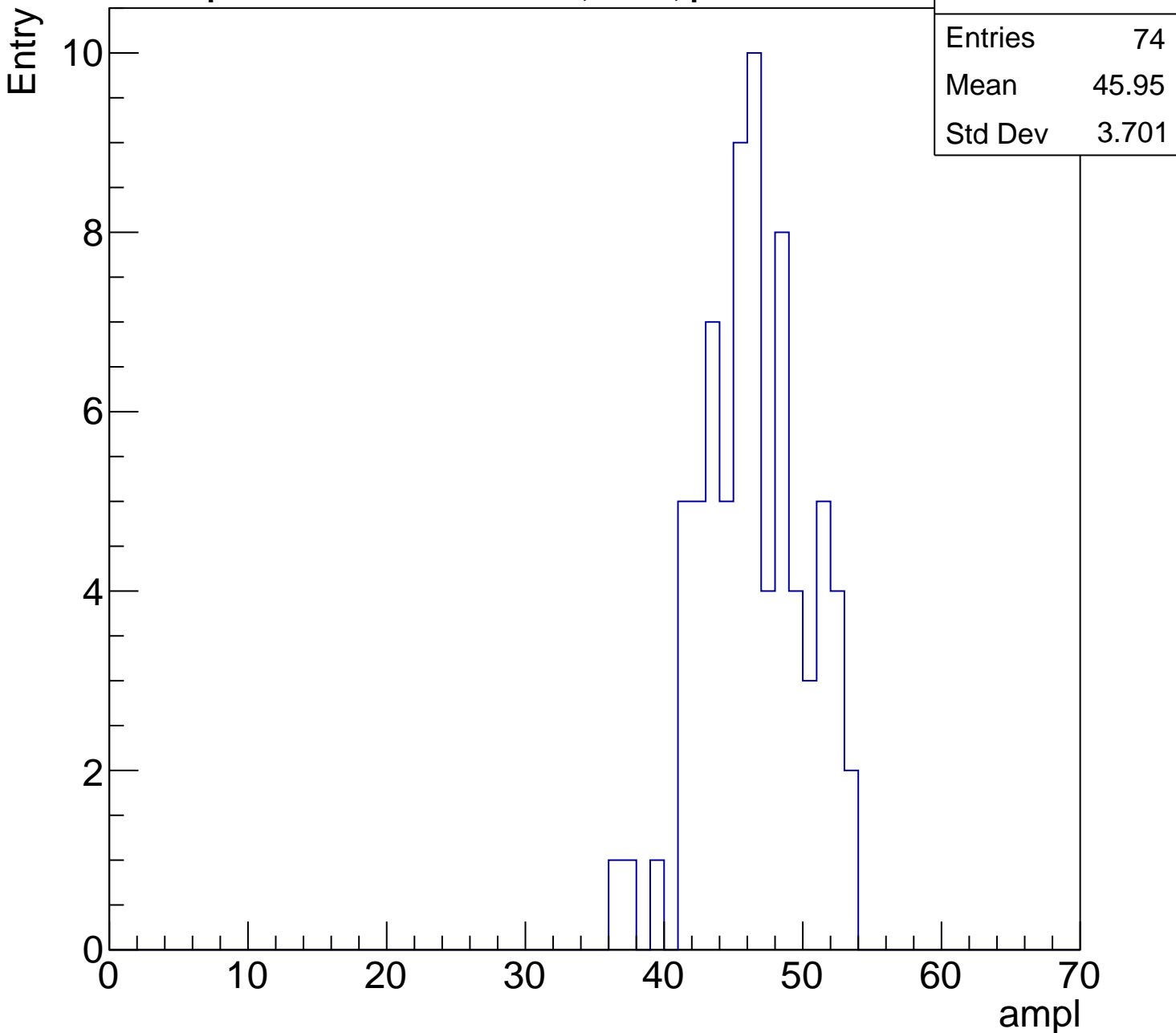
Entries	74
Mean	45.95
Std Dev	3.701

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

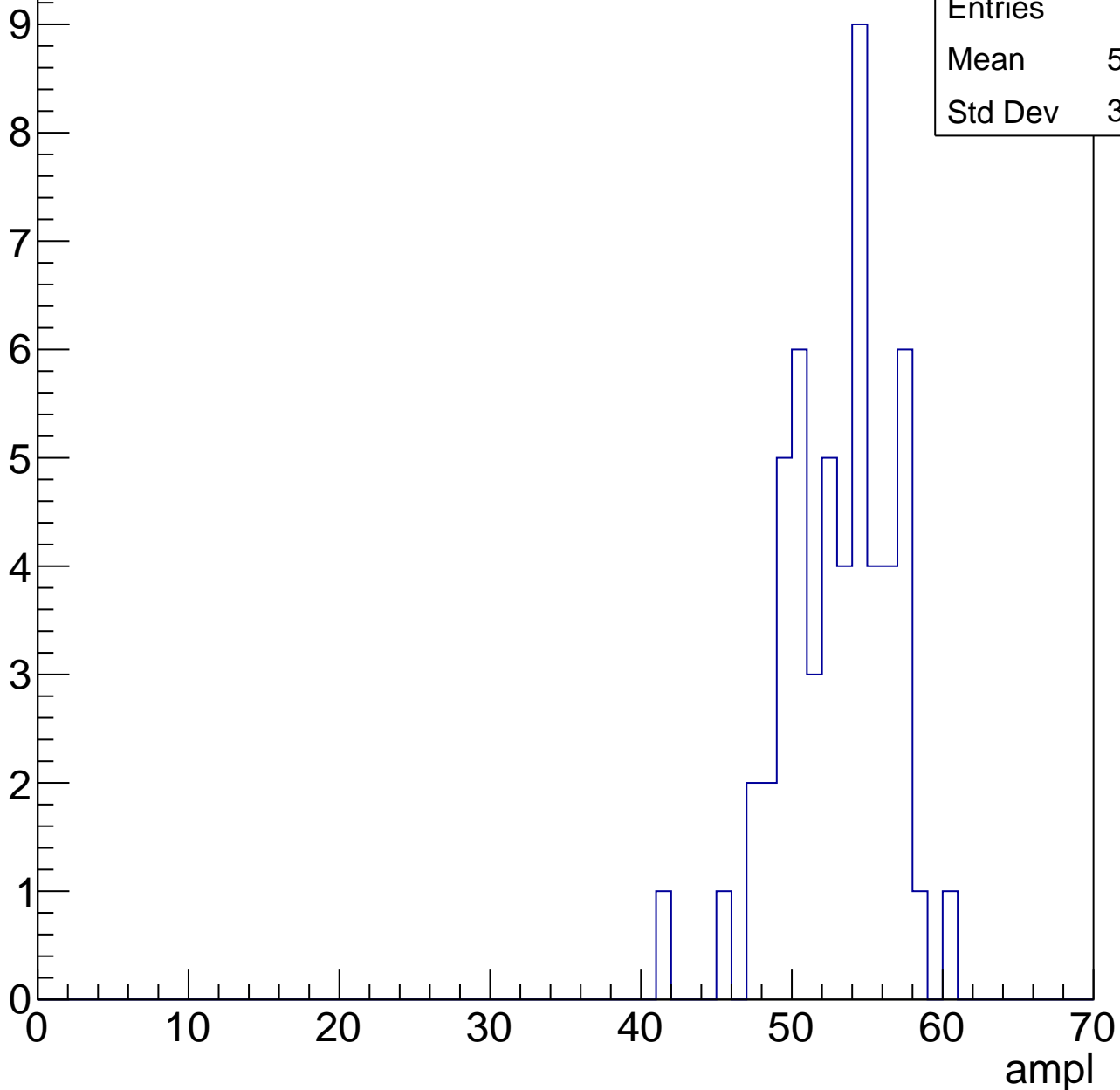


B1L101S, U1-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	52.52
Std Dev	3.599

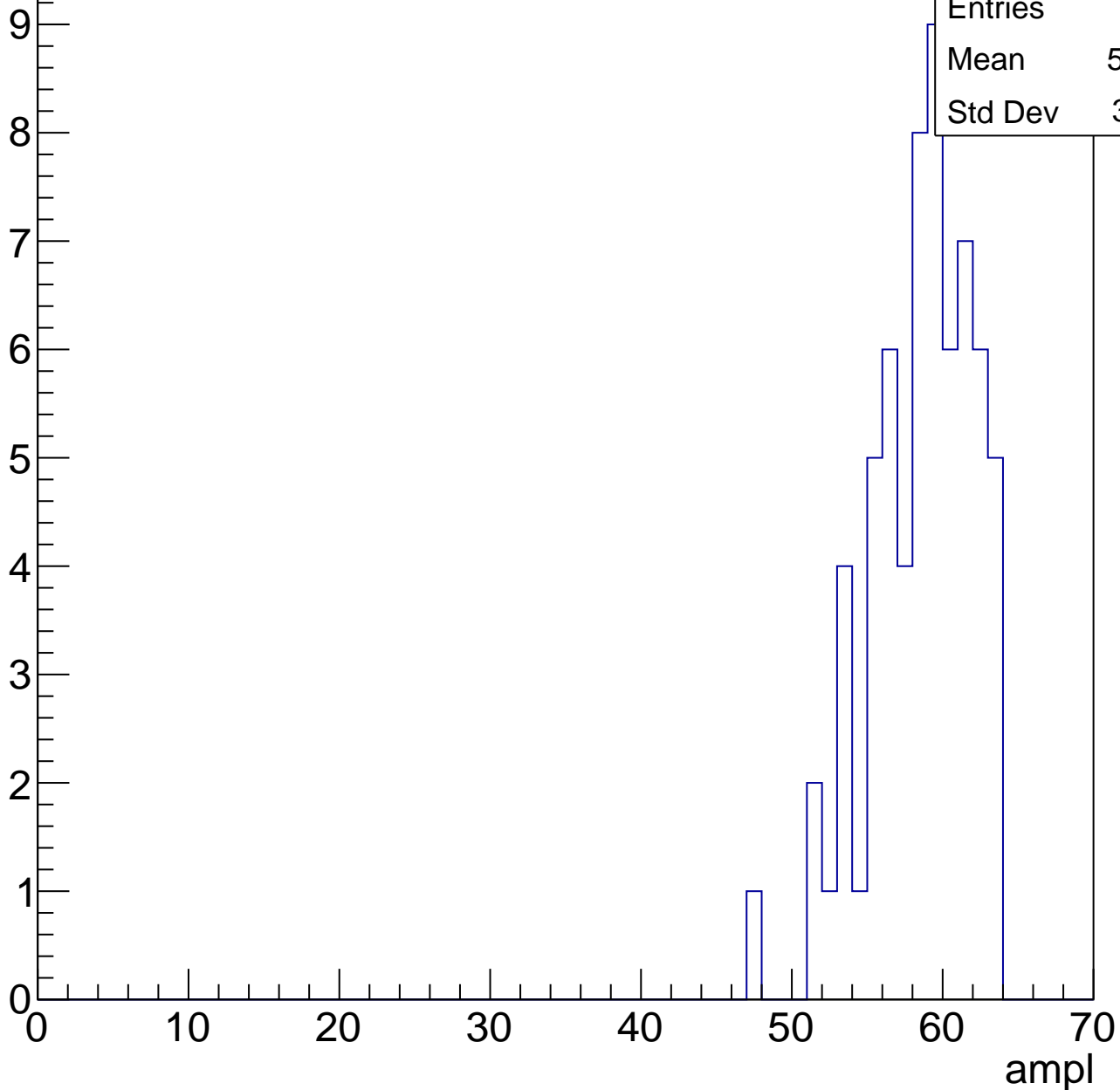


B1L101S, U1-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	58.08
Std Dev	3.421

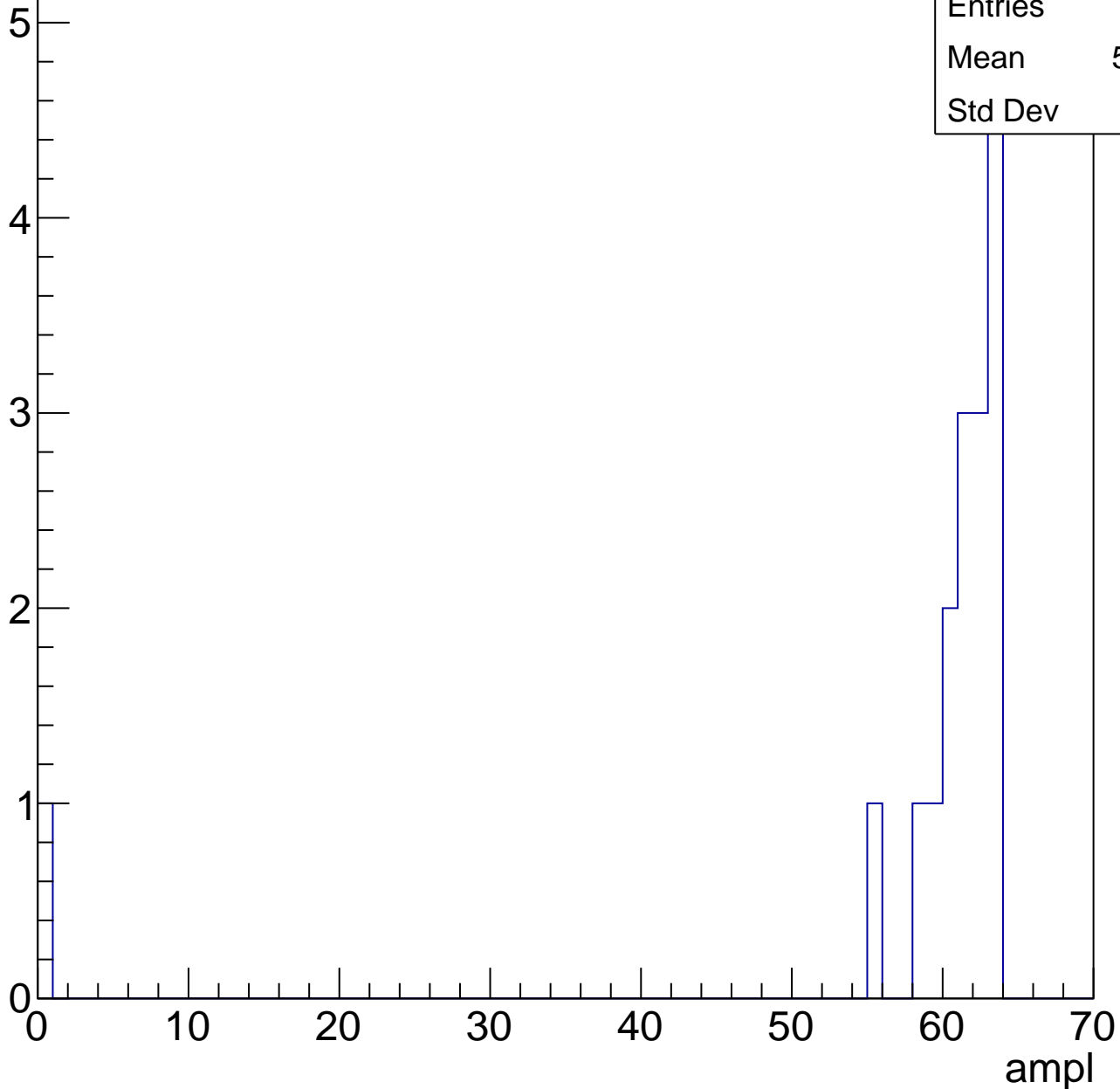


B1L101S, U1-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

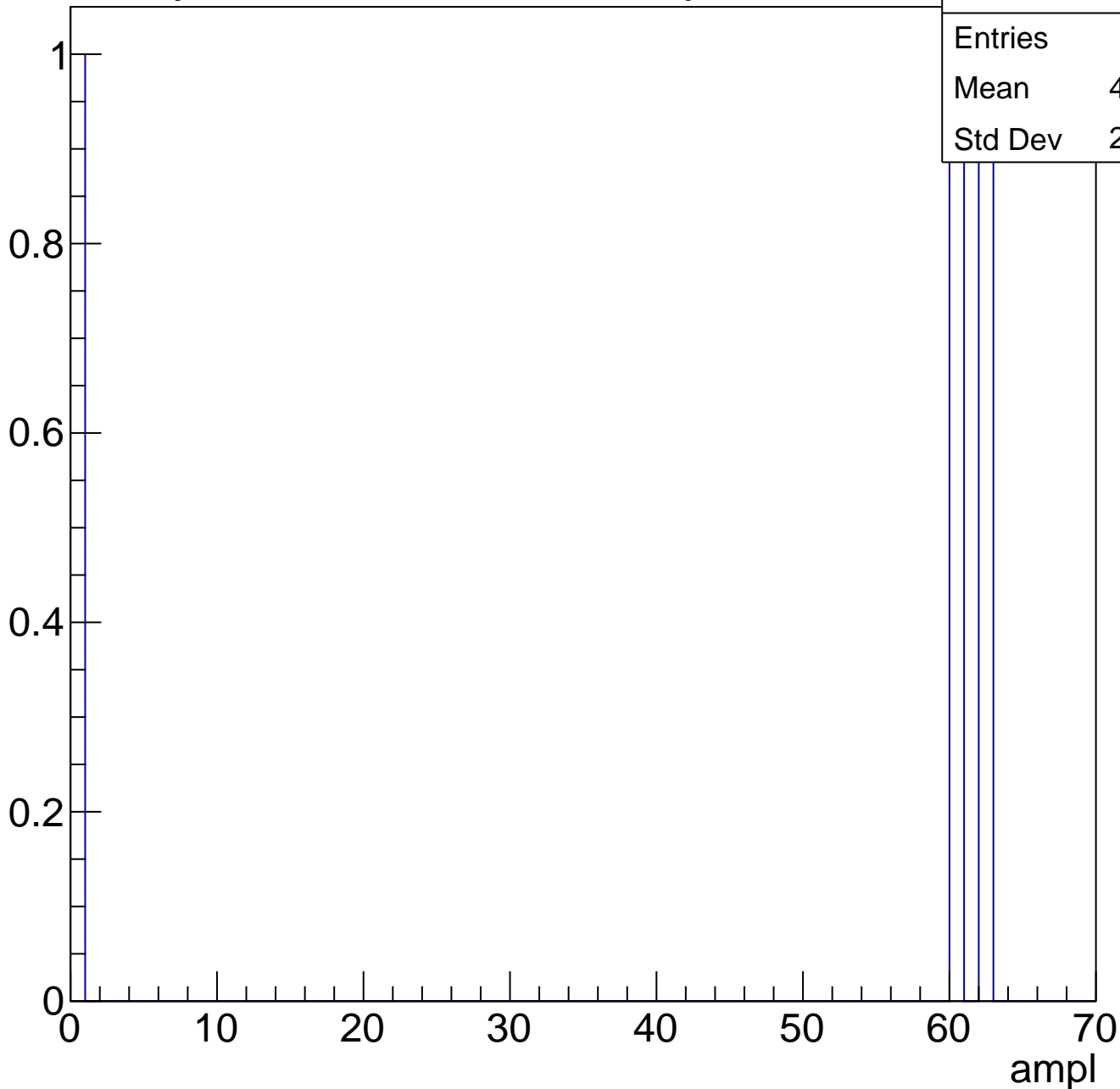
Entries	17
Mean	57.41
Std Dev	14.5



B1L101S, U1-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch117, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	28.9
Std Dev	6.015

Gaus mean : 29.6357

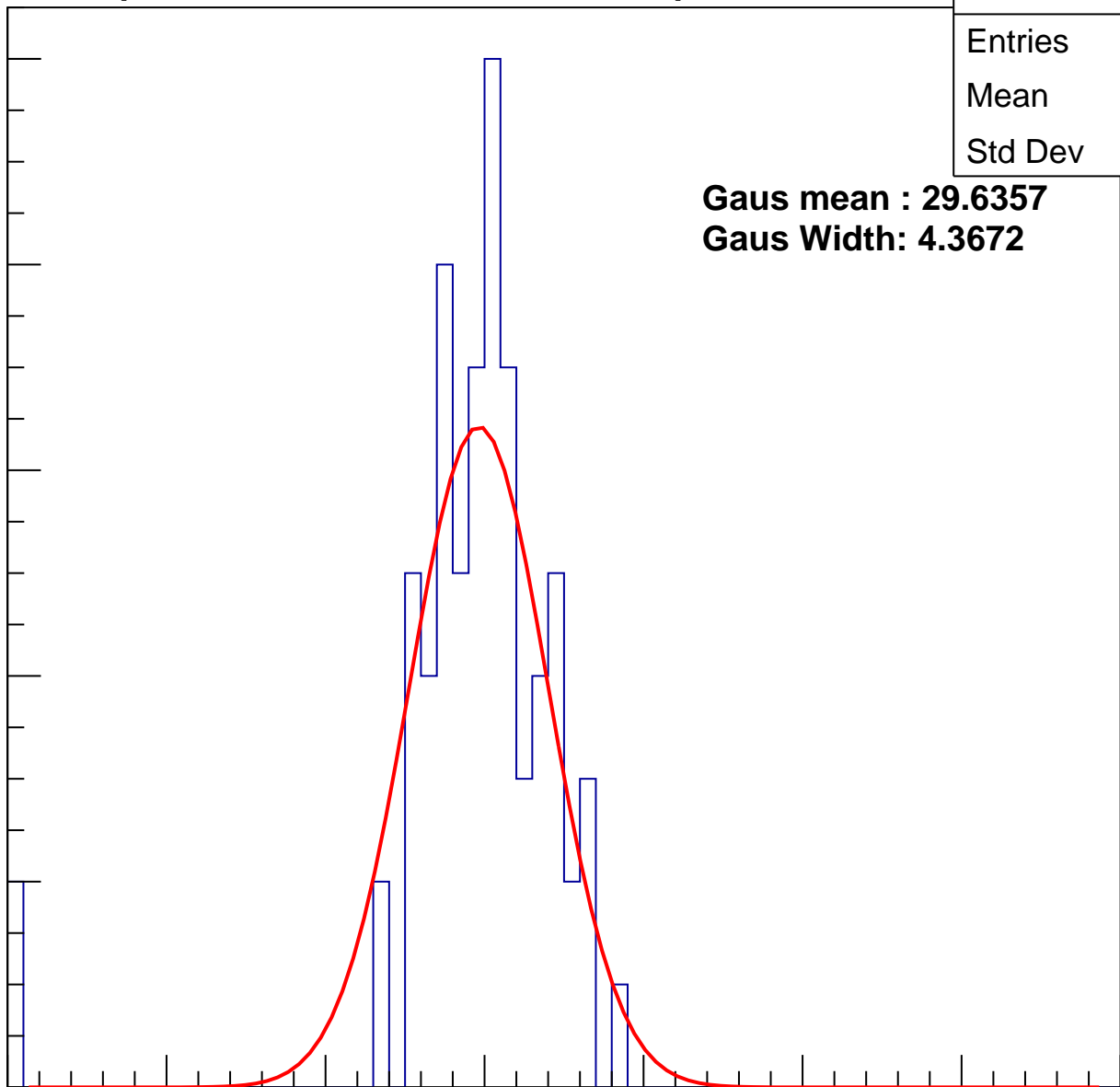
Gaus Width: 4.3672

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



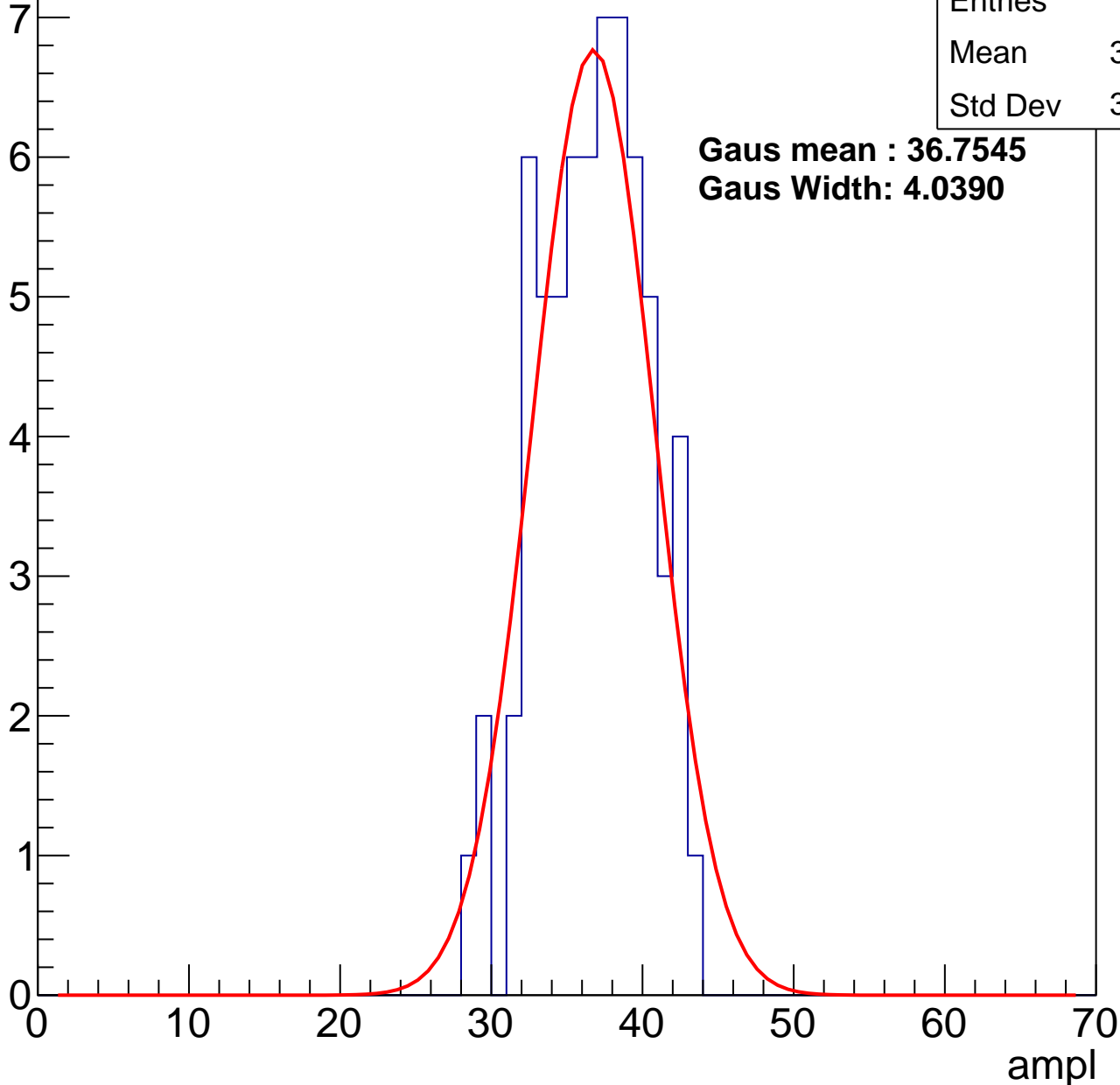
B1L101S, U1-ch117, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.27
Std Dev	3.506

Gaus mean : 36.7545
Gaus Width: 4.0390



B1L101S, U1-ch117, adc2

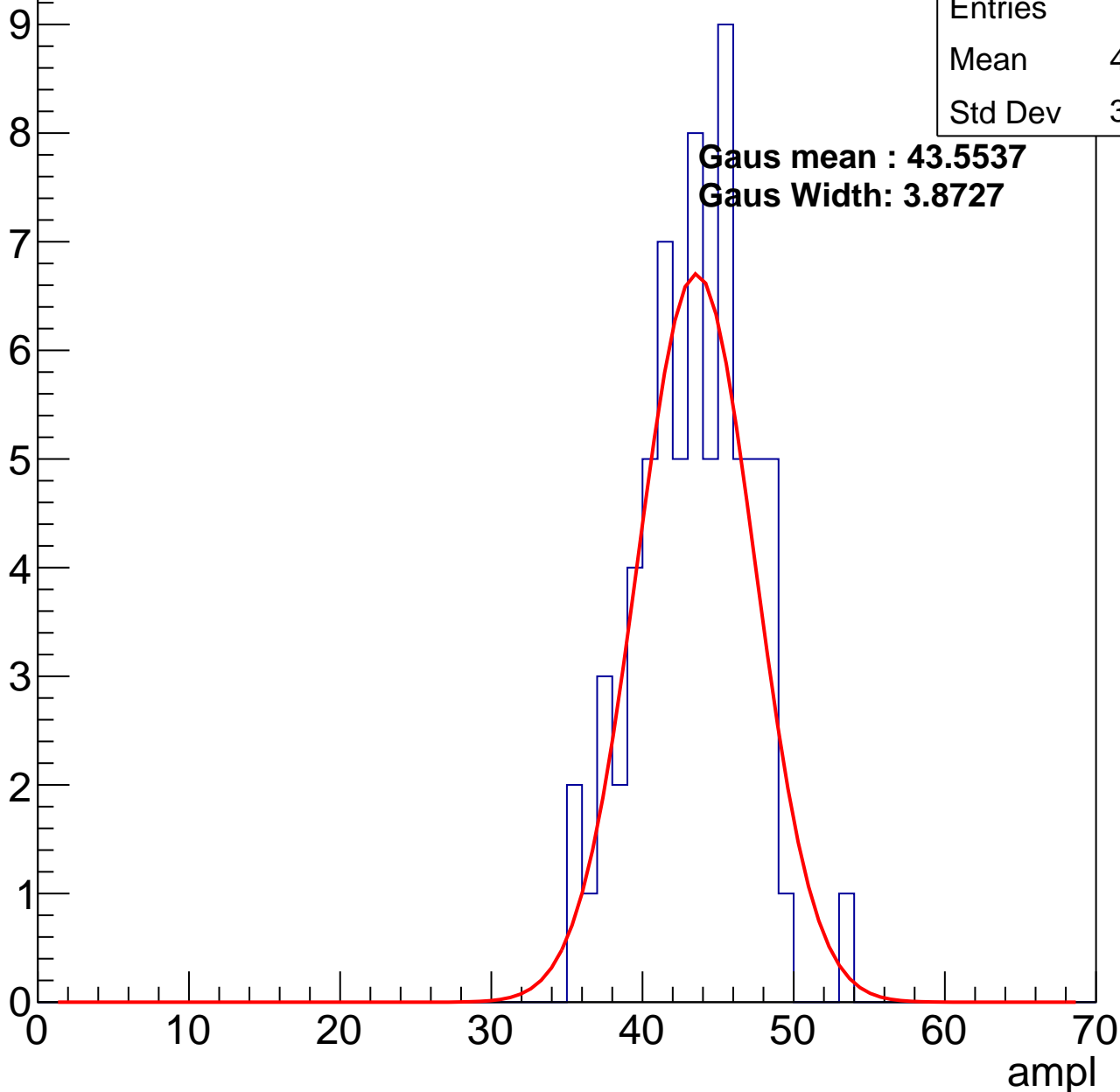
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.97
Std Dev	3.646

Gaus mean : 43.5537

Gaus Width: 3.8727

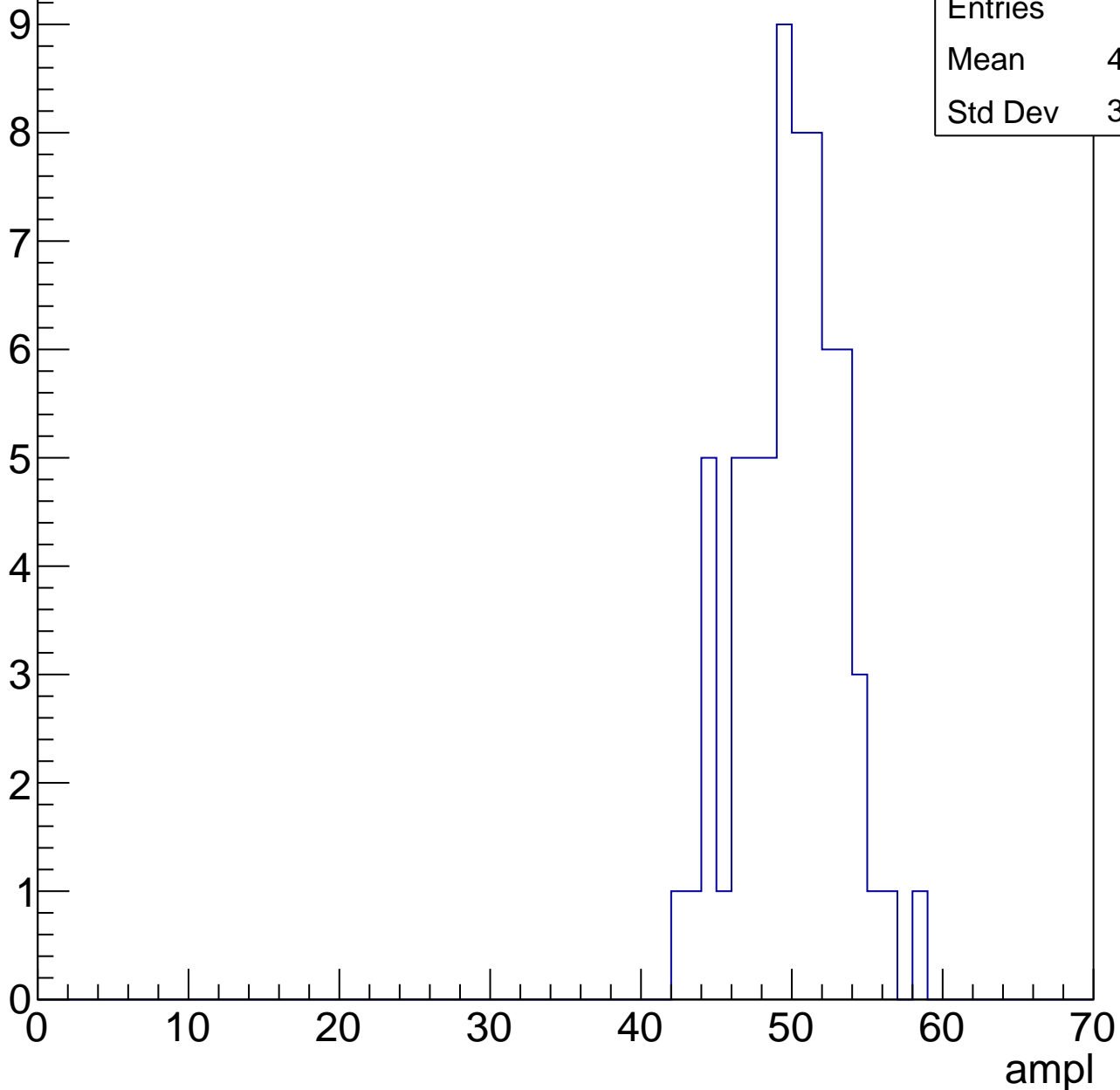


B1L101S, U1-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

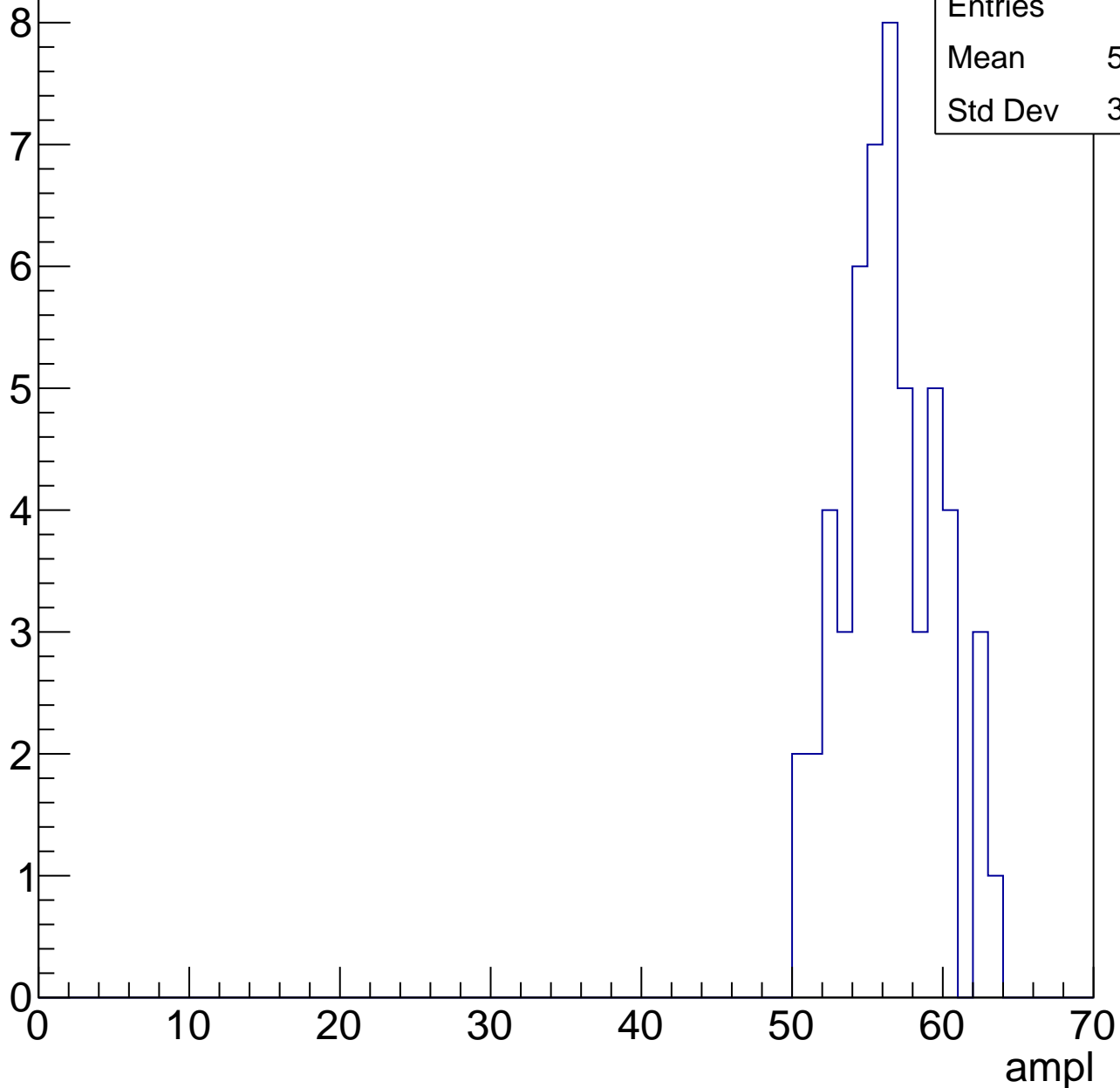
Entries	66
Mean	49.47
Std Dev	3.286



B1L101S, U1-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

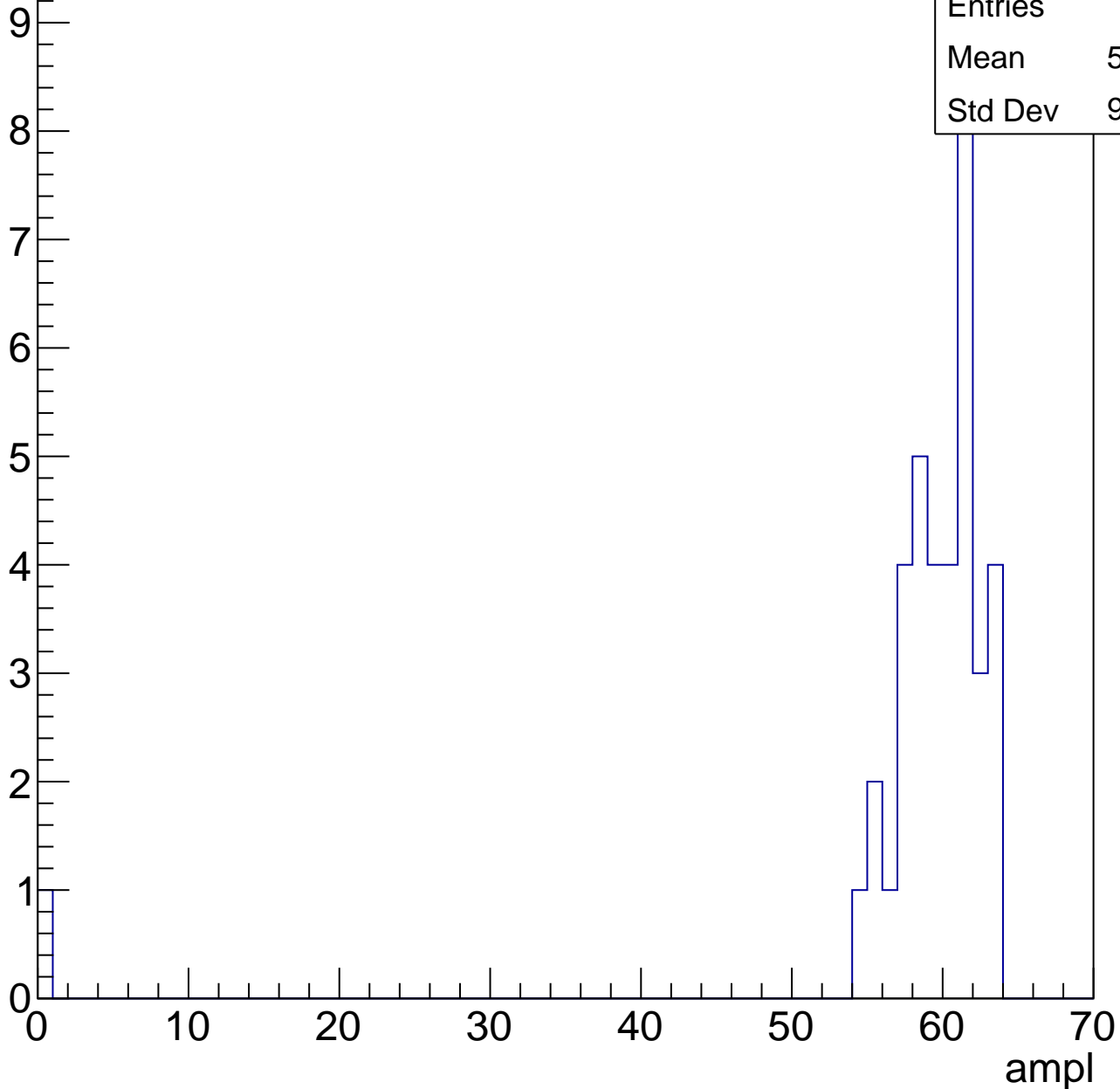


B1L101S, U1-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	57.92
Std Dev	9.807

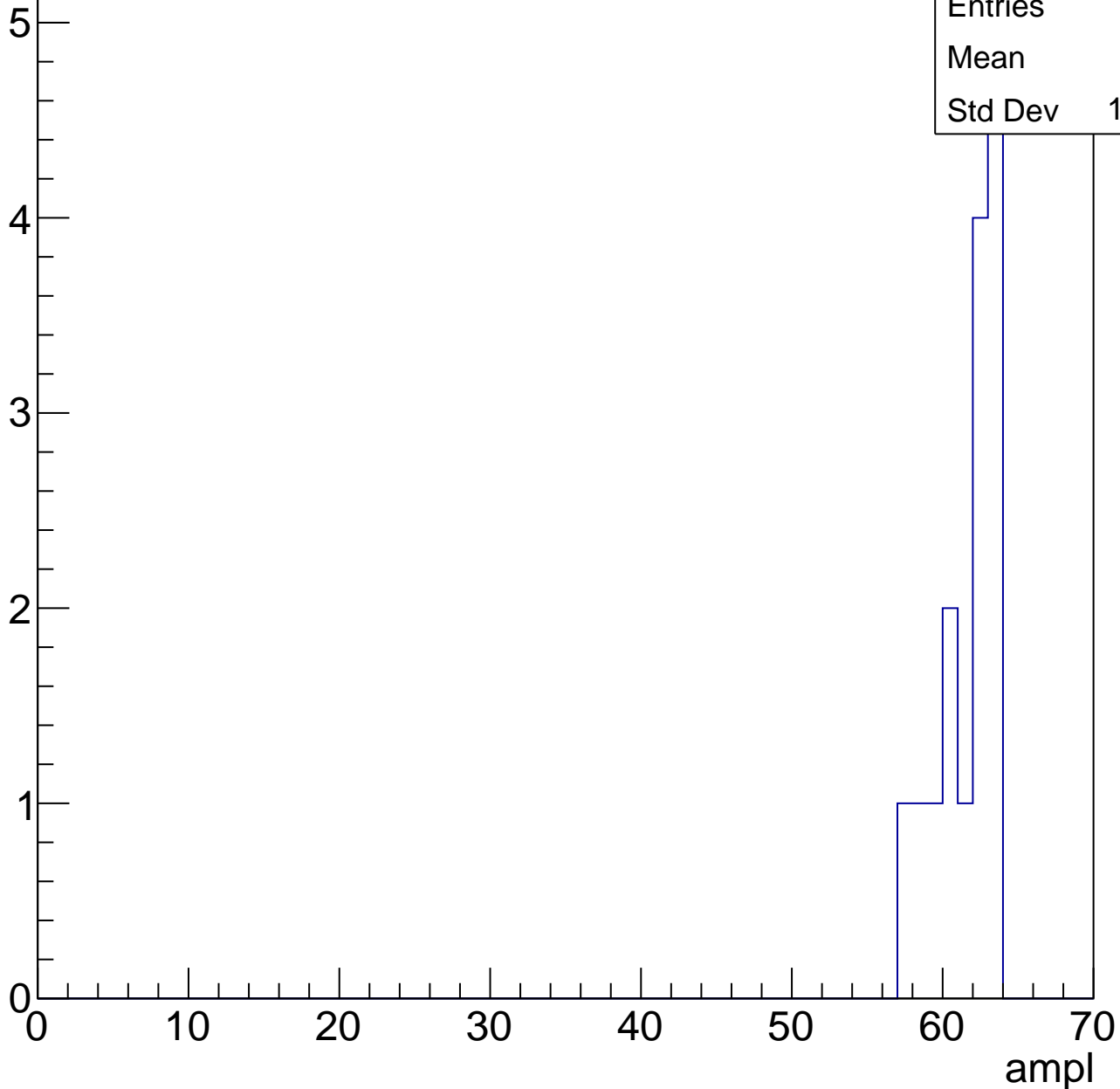


B1L101S, U1-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.2
Std Dev	1.904



B1L101S, U1-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U1-ch118, adc0

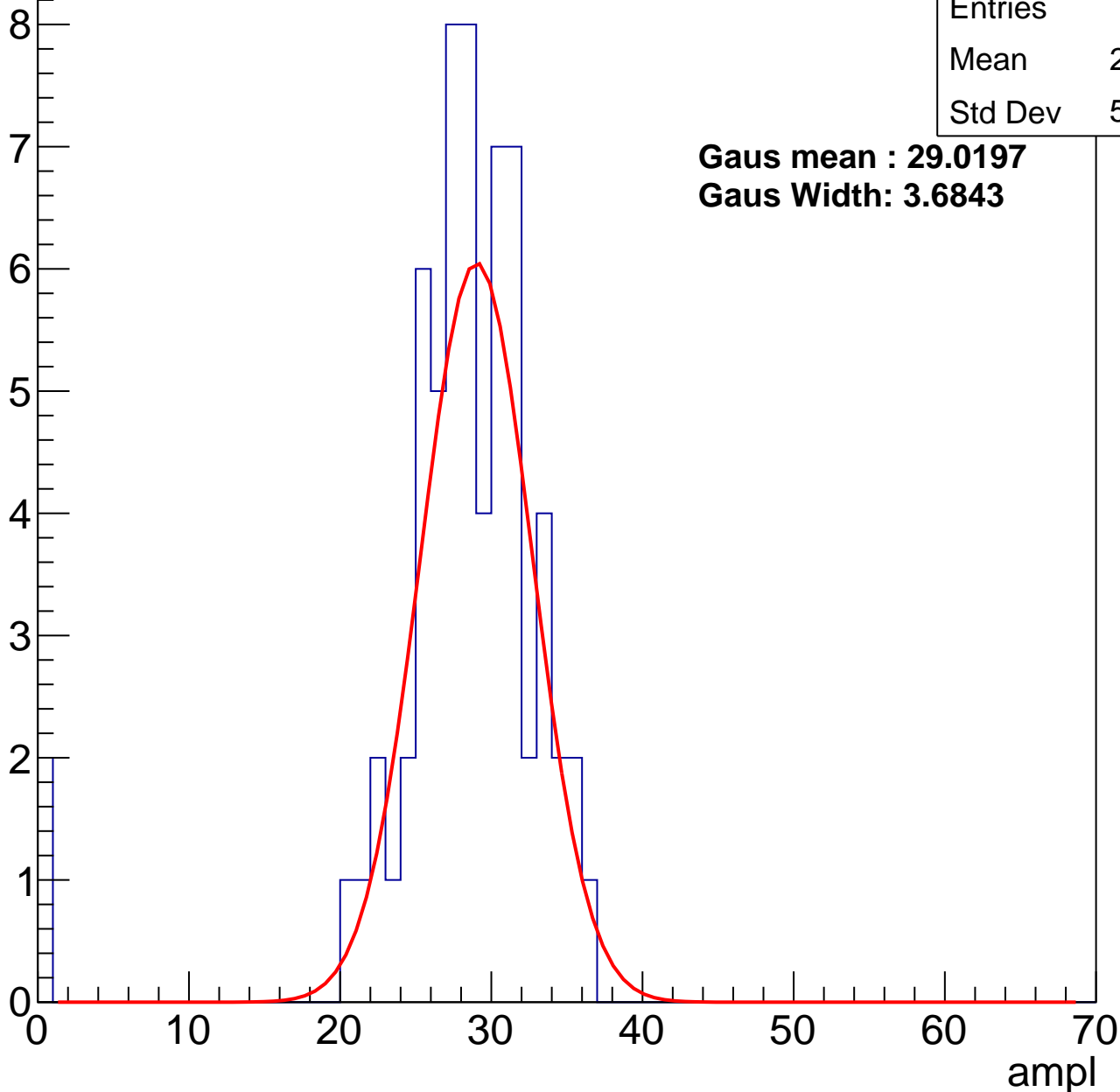
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.52
Std Dev	5.998

Gaus mean : 29.0197

Gaus Width: 3.6843



B1L101S, U1-ch118, adc1

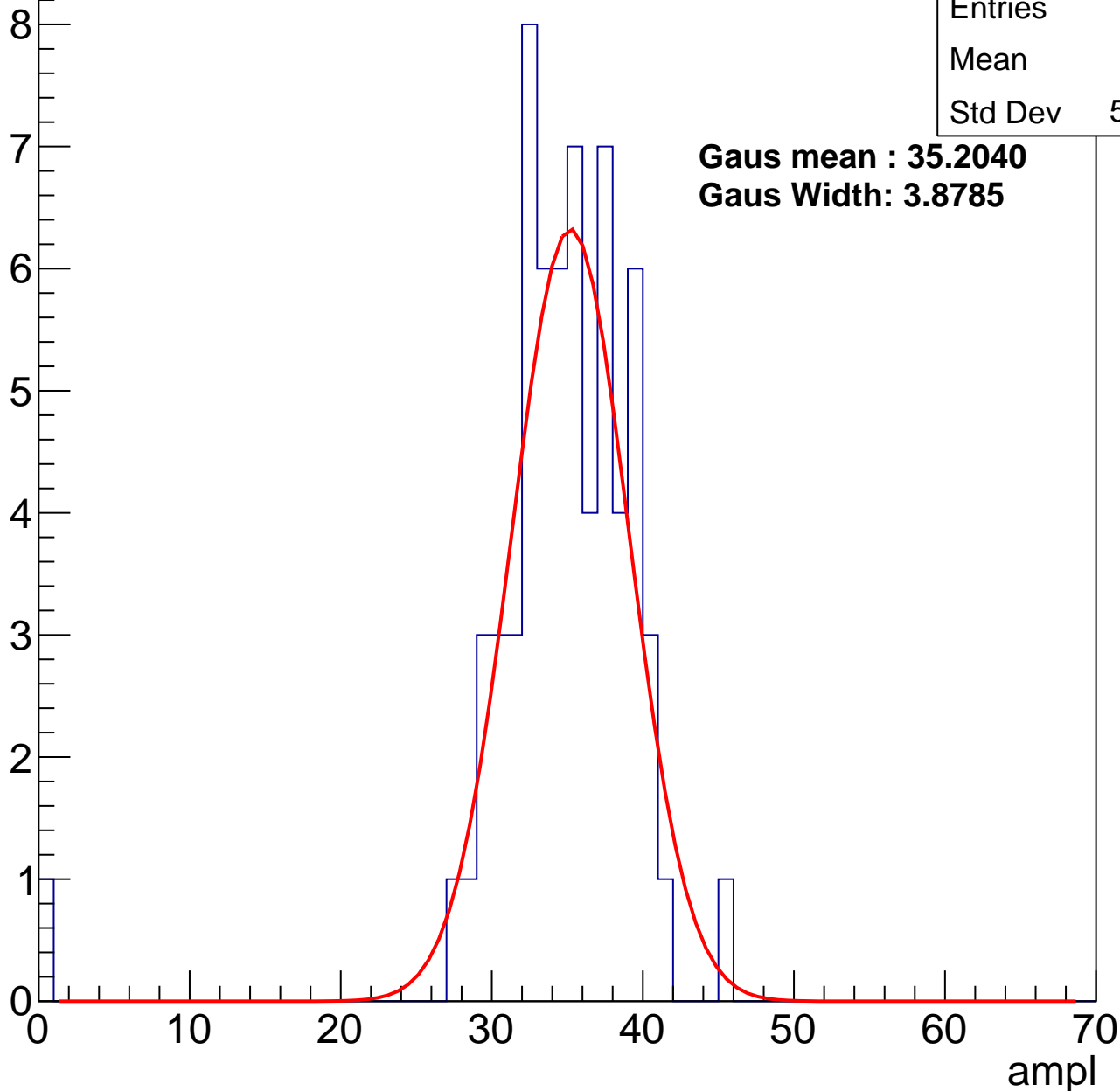
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	34.2
Std Dev	5.553

Gaus mean : 35.2040

Gaus Width: 3.8785



B1L101S, U1-ch118, adc2

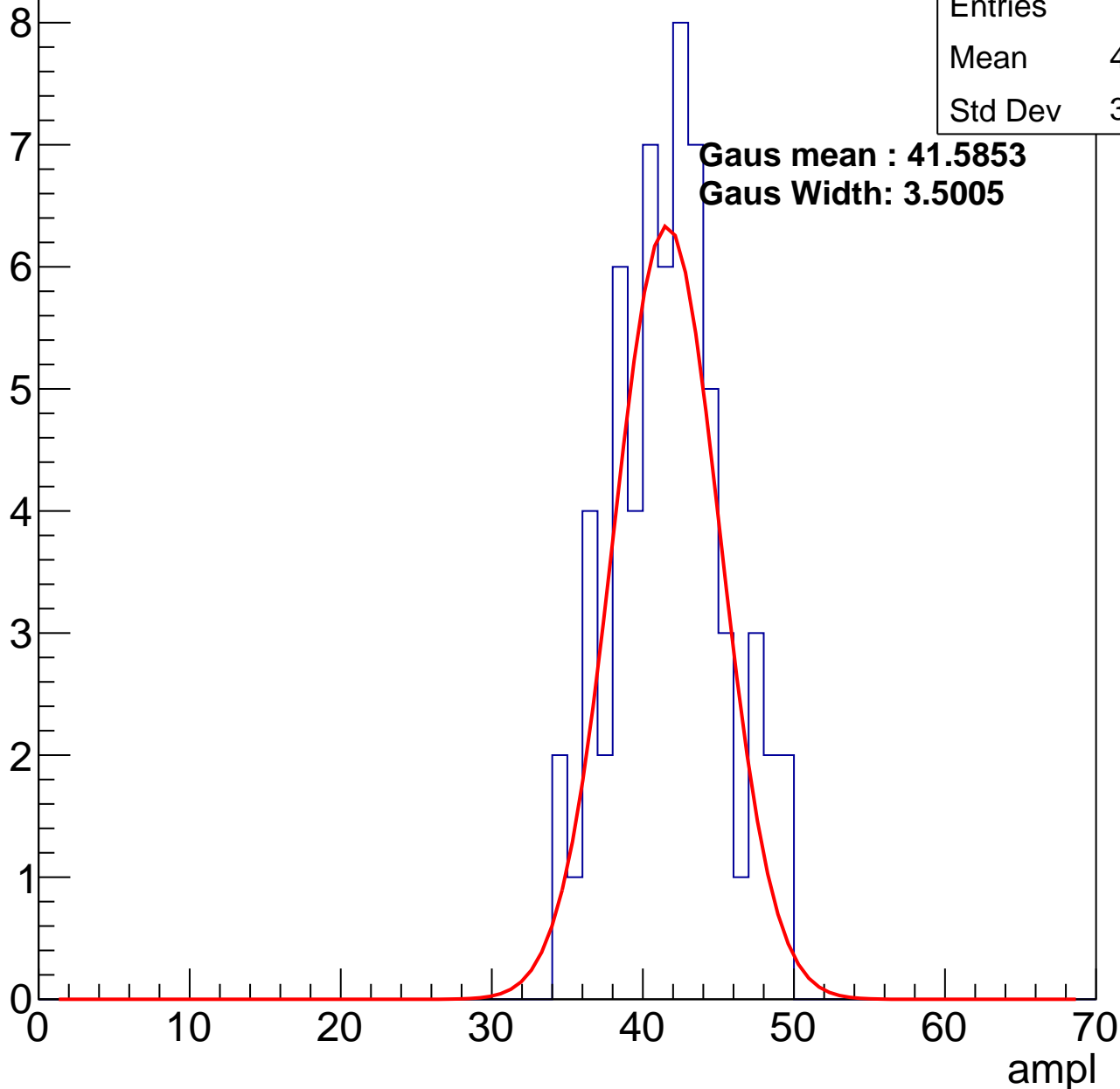
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	41.33
Std Dev	3.617

Gaus mean : 41.5853

Gaus Width: 3.5005

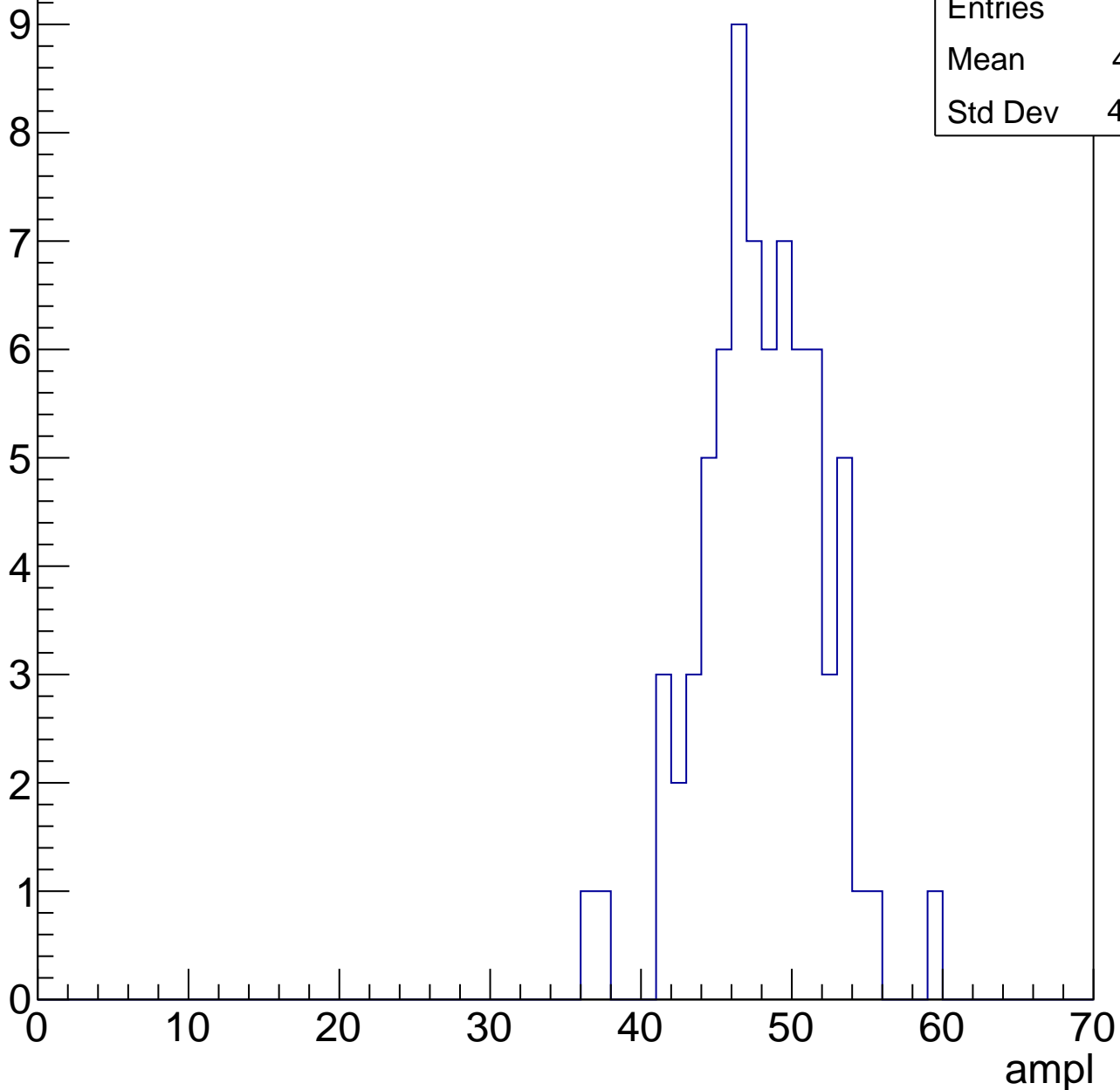


B1L101S, U1-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	47.51
Std Dev	4.024

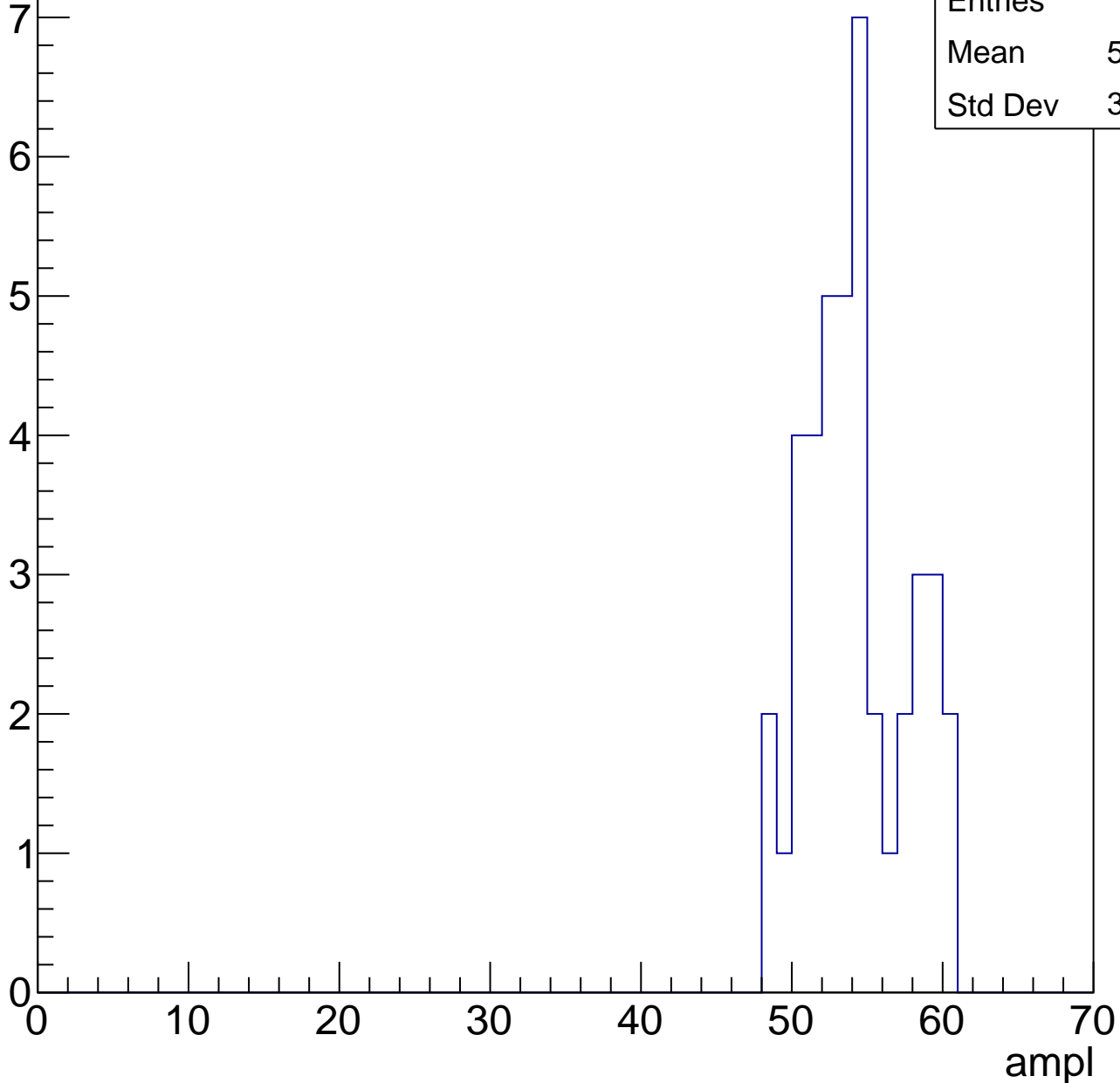


B1L101S, U1-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	53.73
Std Dev	3.269



B1L101S, U1-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

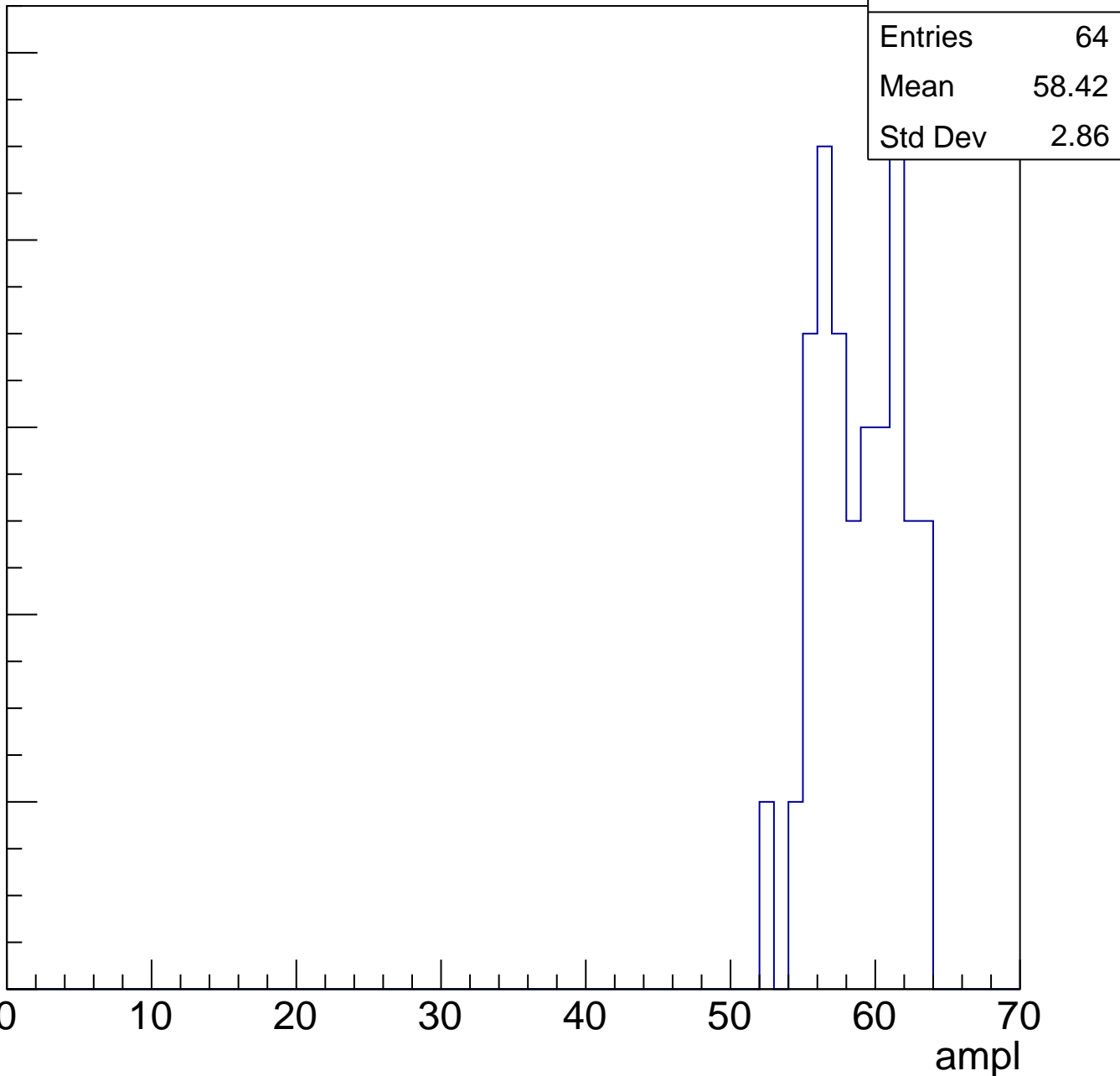
40

50

60

ampl

Entries	64
Mean	58.42
Std Dev	2.86

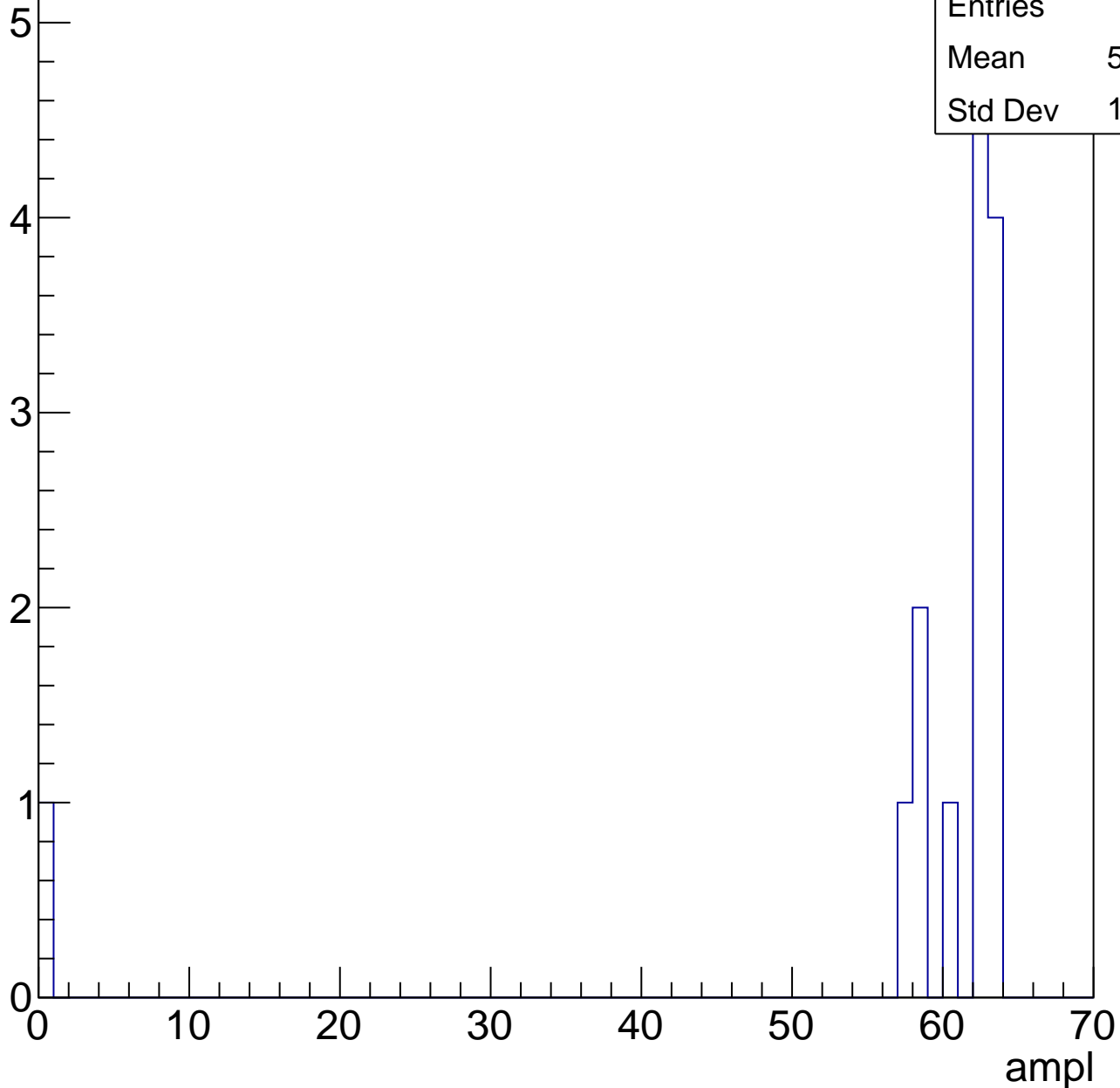


B1L101S, U1-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

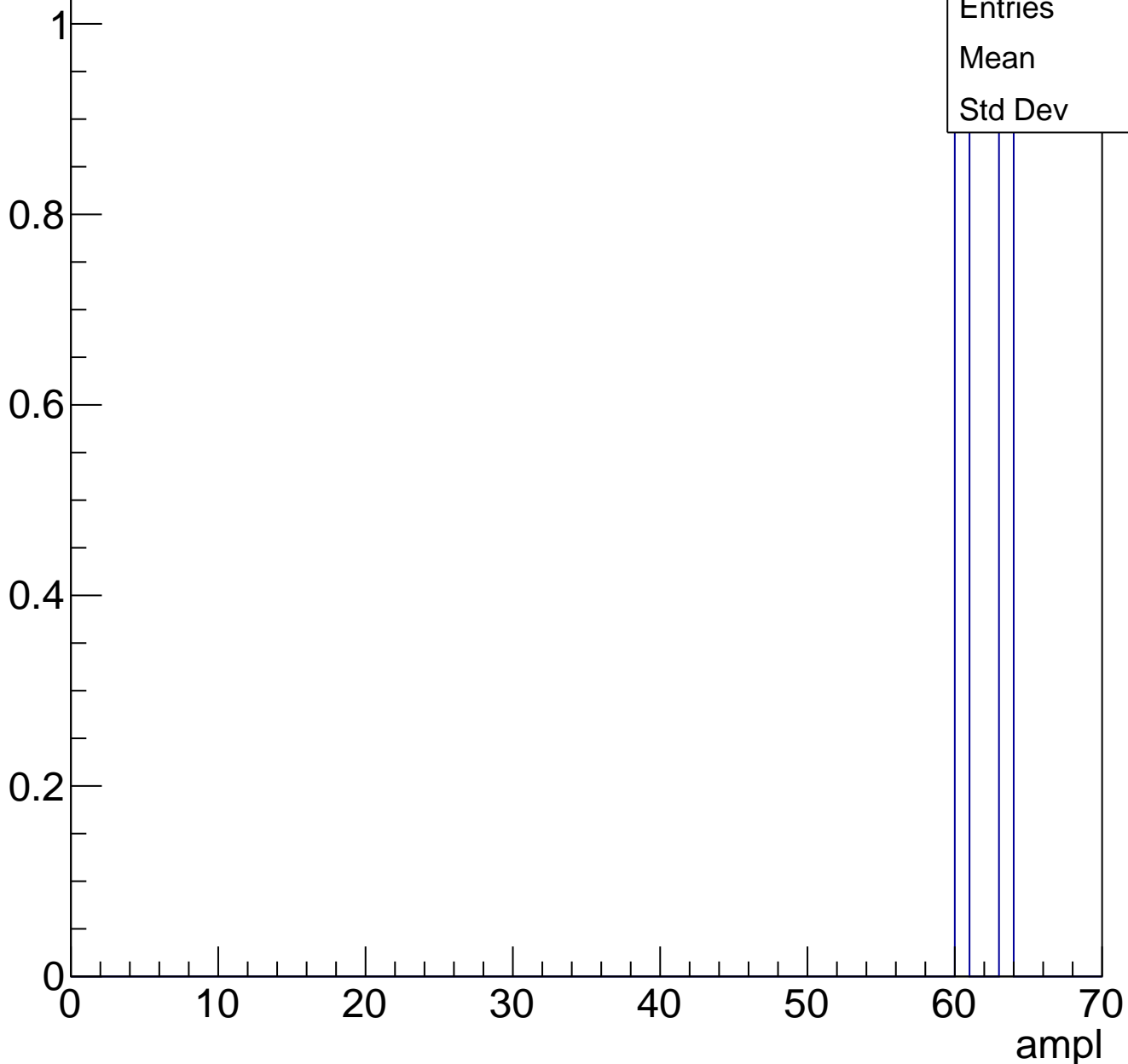
Entries	14
Mean	56.79
Std Dev	15.88



B1L101S, U1-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	61.5
Std Dev	1.5

B1L101S, U1-ch119, adc0

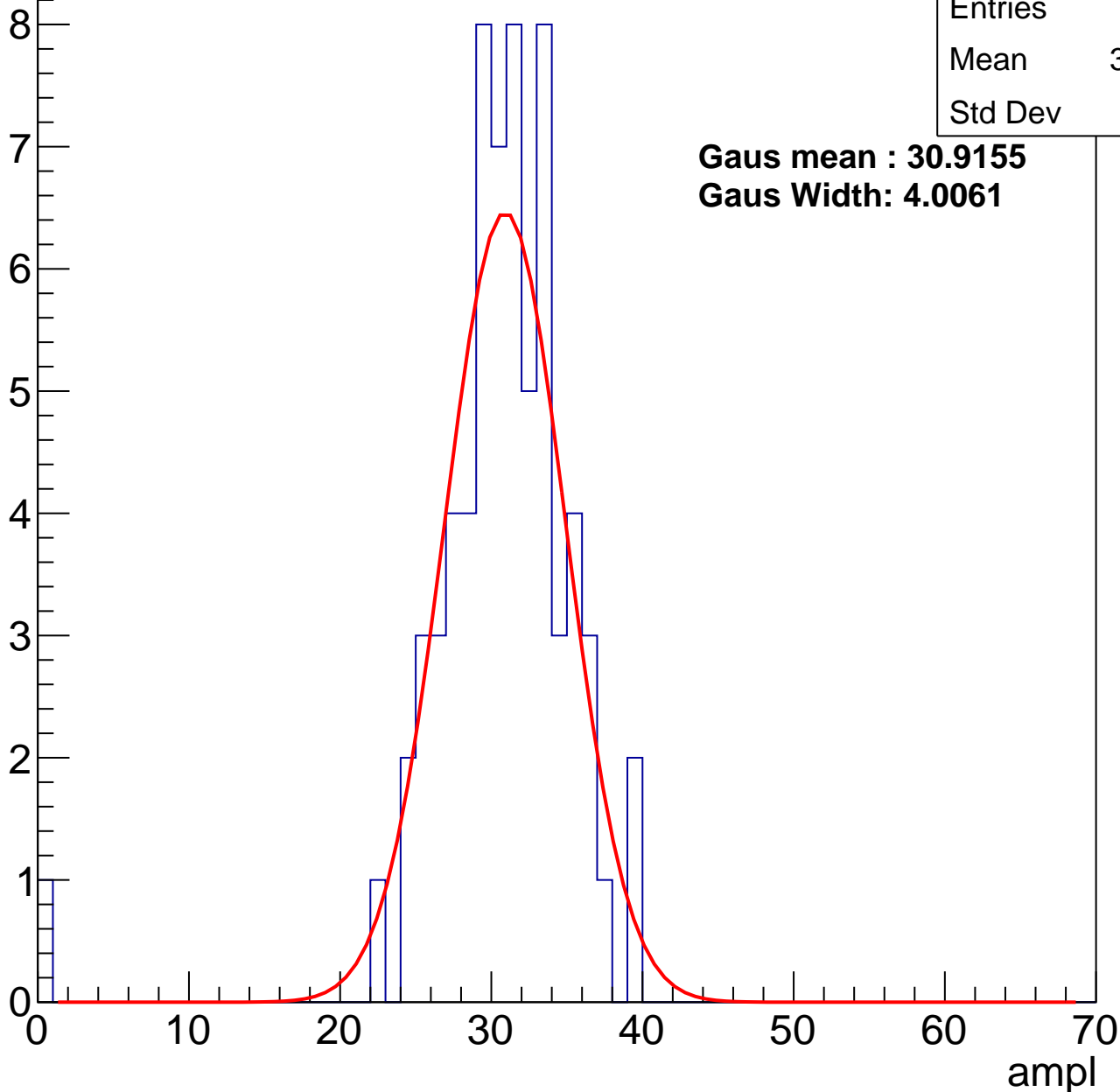
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.18
Std Dev	5.16

Gaus mean : 30.9155

Gaus Width: 4.0061



B1L101S, U1-ch119, adc1

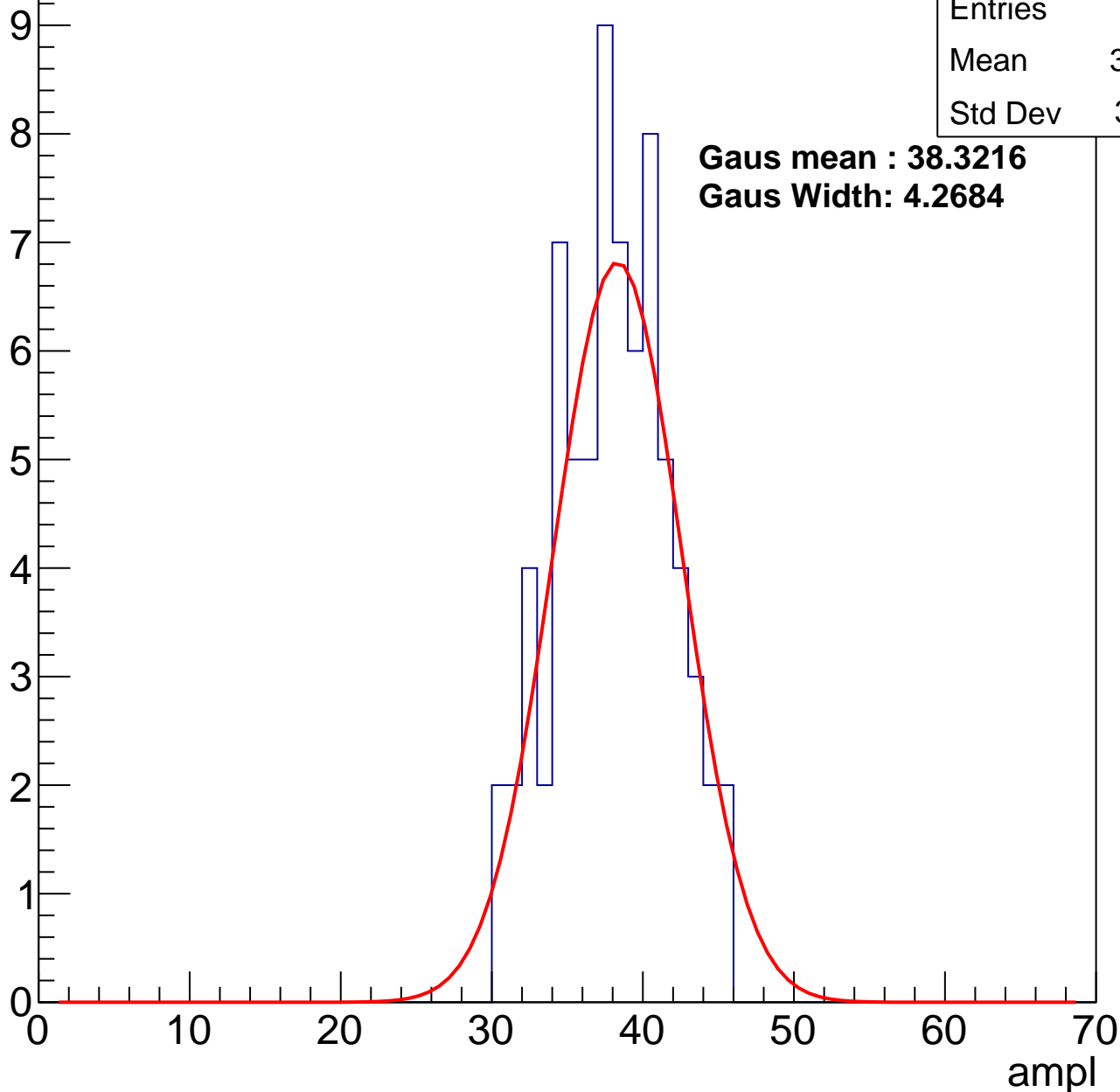
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	37.56
Std Dev	3.671

Gaus mean : 38.3216

Gaus Width: 4.2684



B1L101S, U1-ch119, adc2

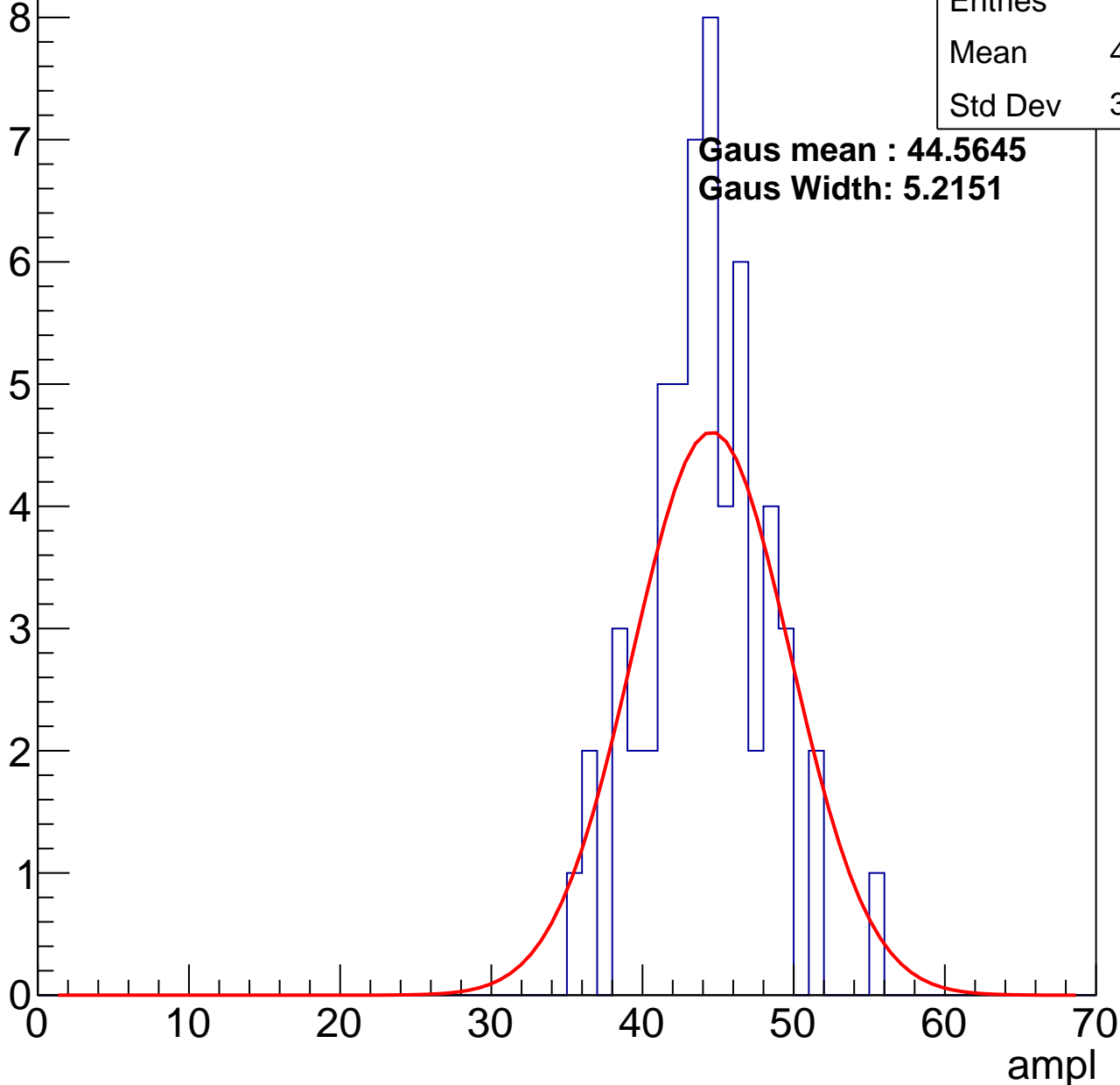
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.74
Std Dev	3.909

Gaus mean : 44.5645

Gaus Width: 5.2151

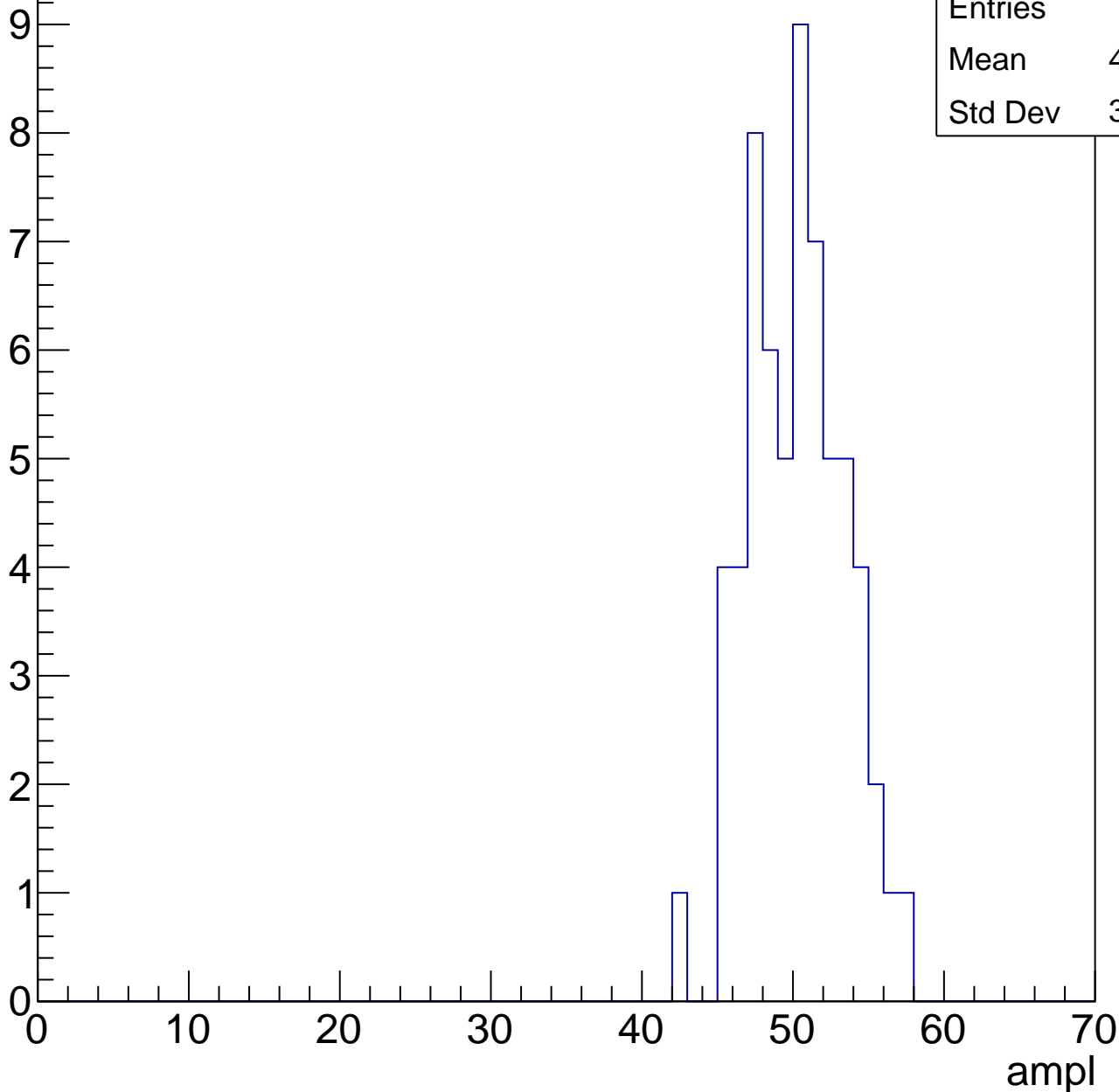


B1L101S, U1-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.77
Std Dev	3.103

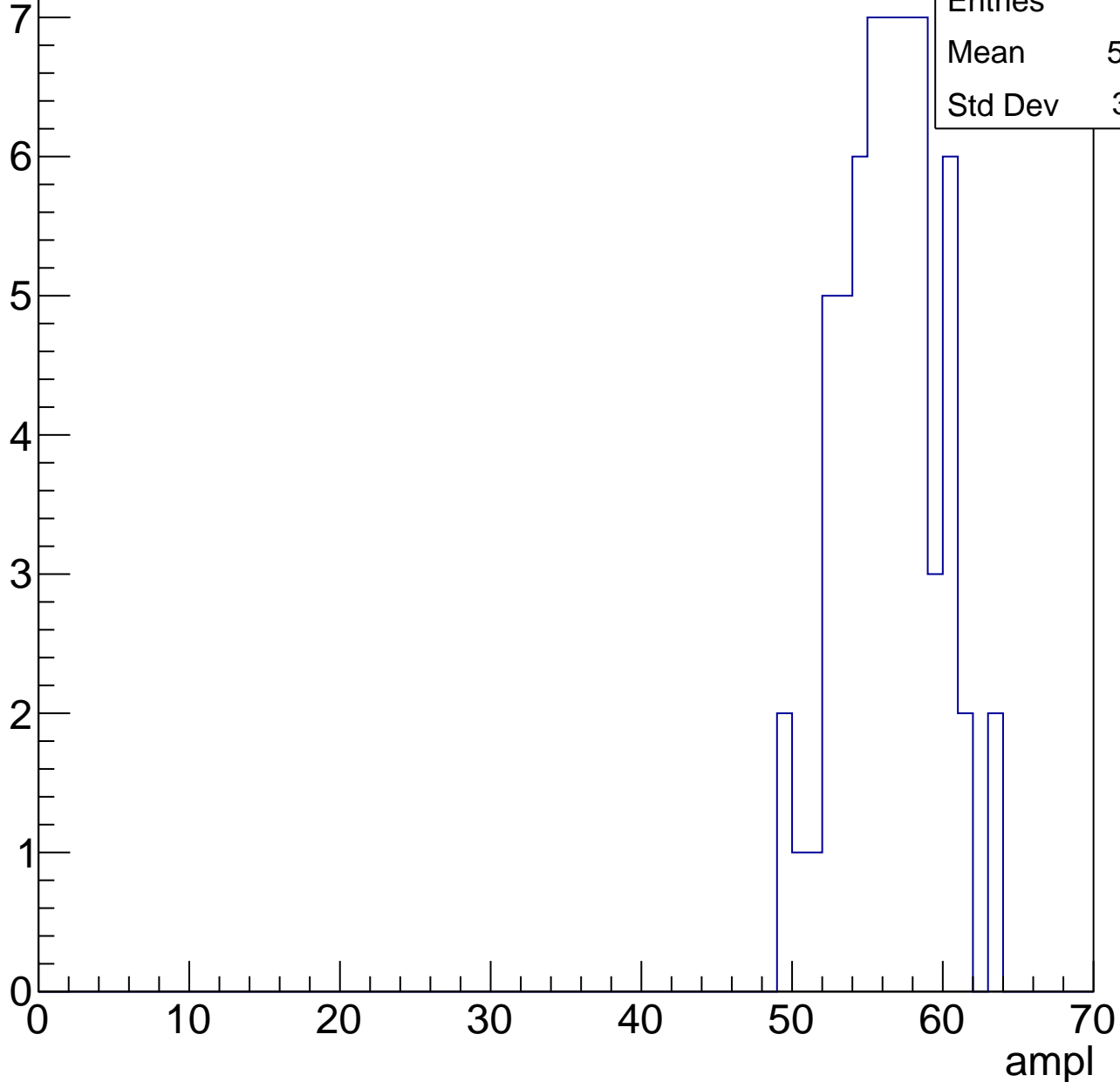


B1L101S, U1-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.98
Std Dev	3.191

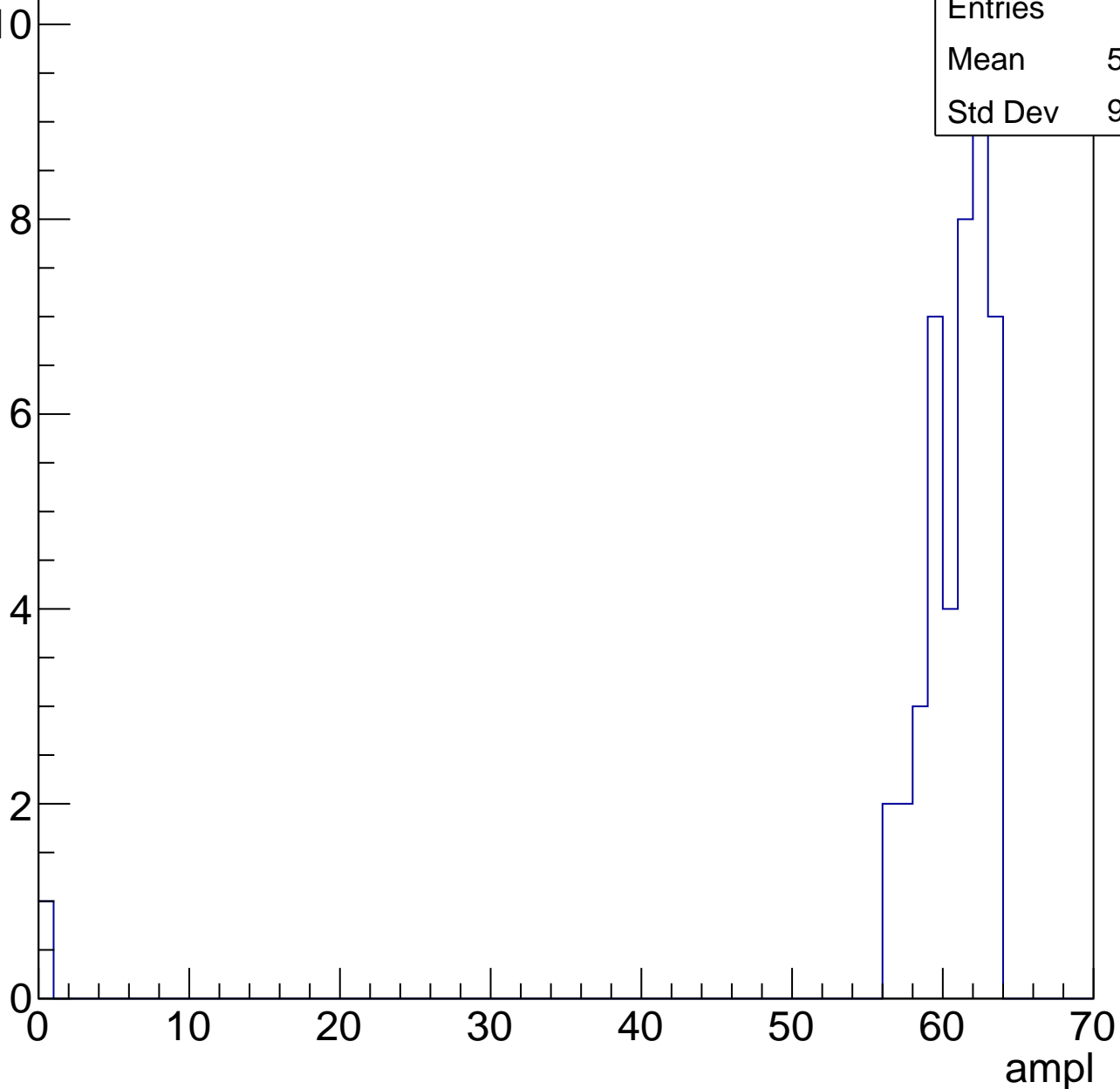


B1L101S, U1-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	59.14
Std Dev	9.228



B1L101S, U1-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U1-ch120, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	27.72
Std Dev	3.707

Gaus mean : 27.5842

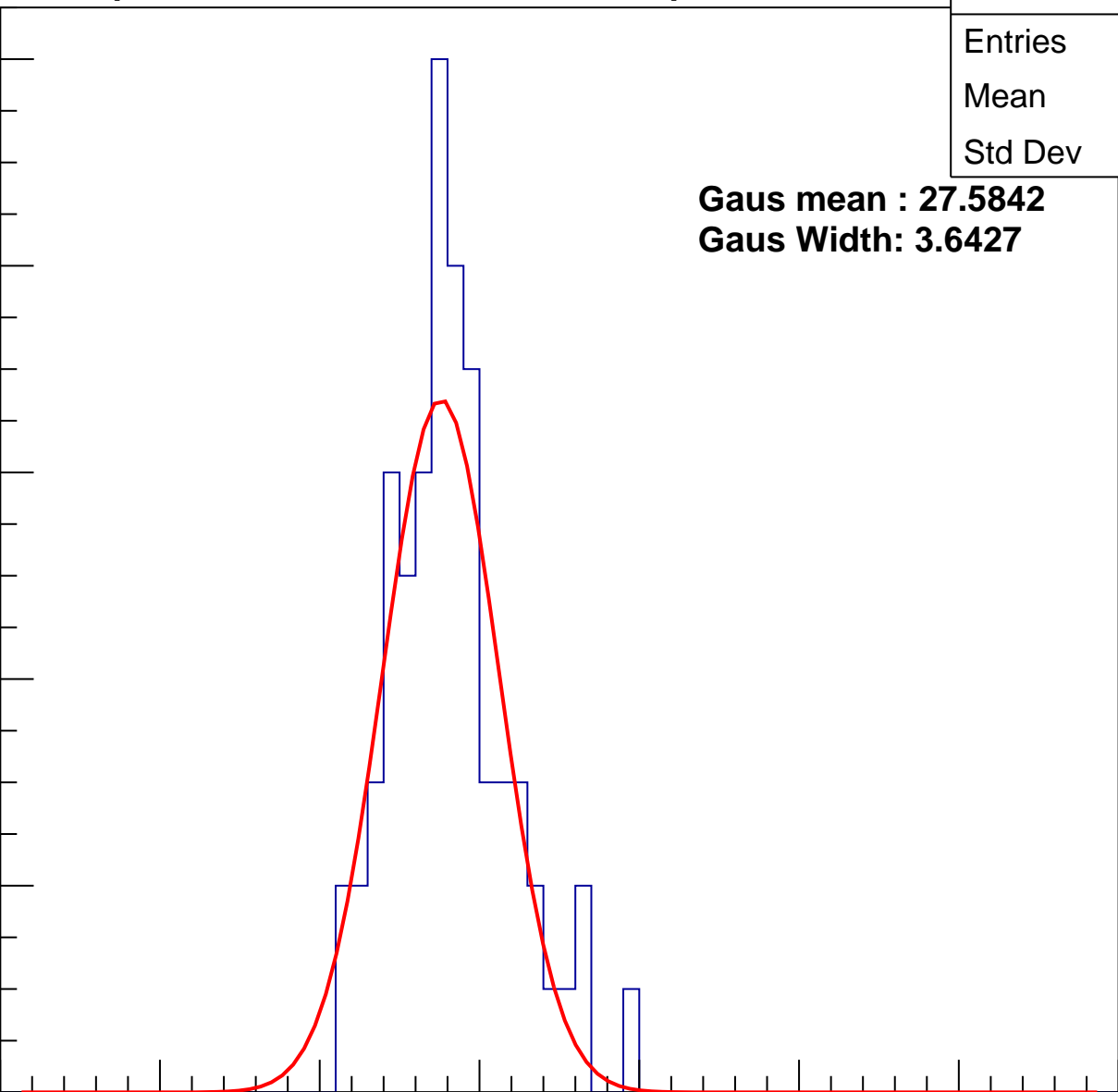
Gaus Width: 3.6427

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch120, adc1

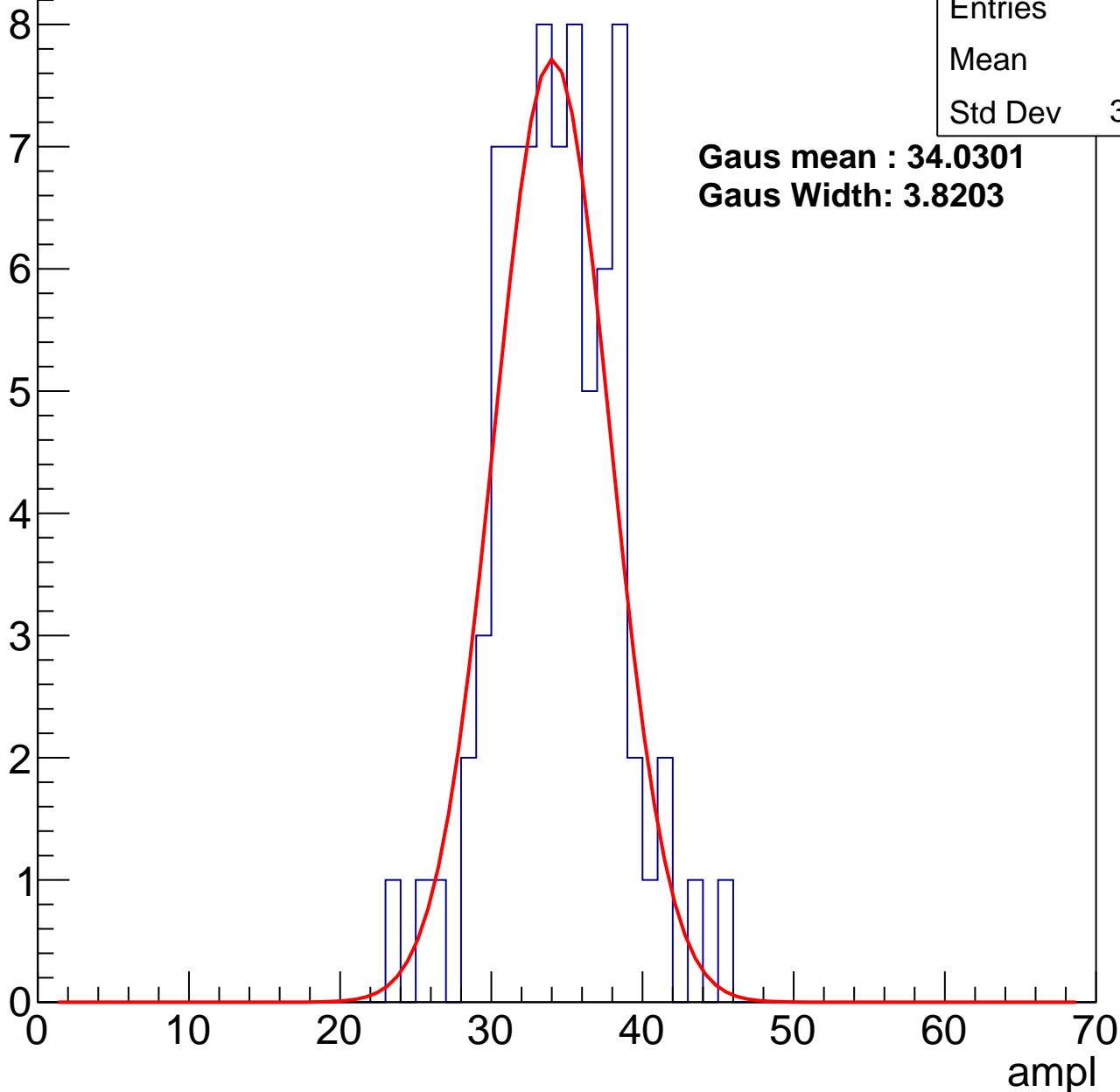
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	33.9
Std Dev	3.954

Gaus mean : 34.0301

Gaus Width: 3.8203



B1L101S, U1-ch120, adc2

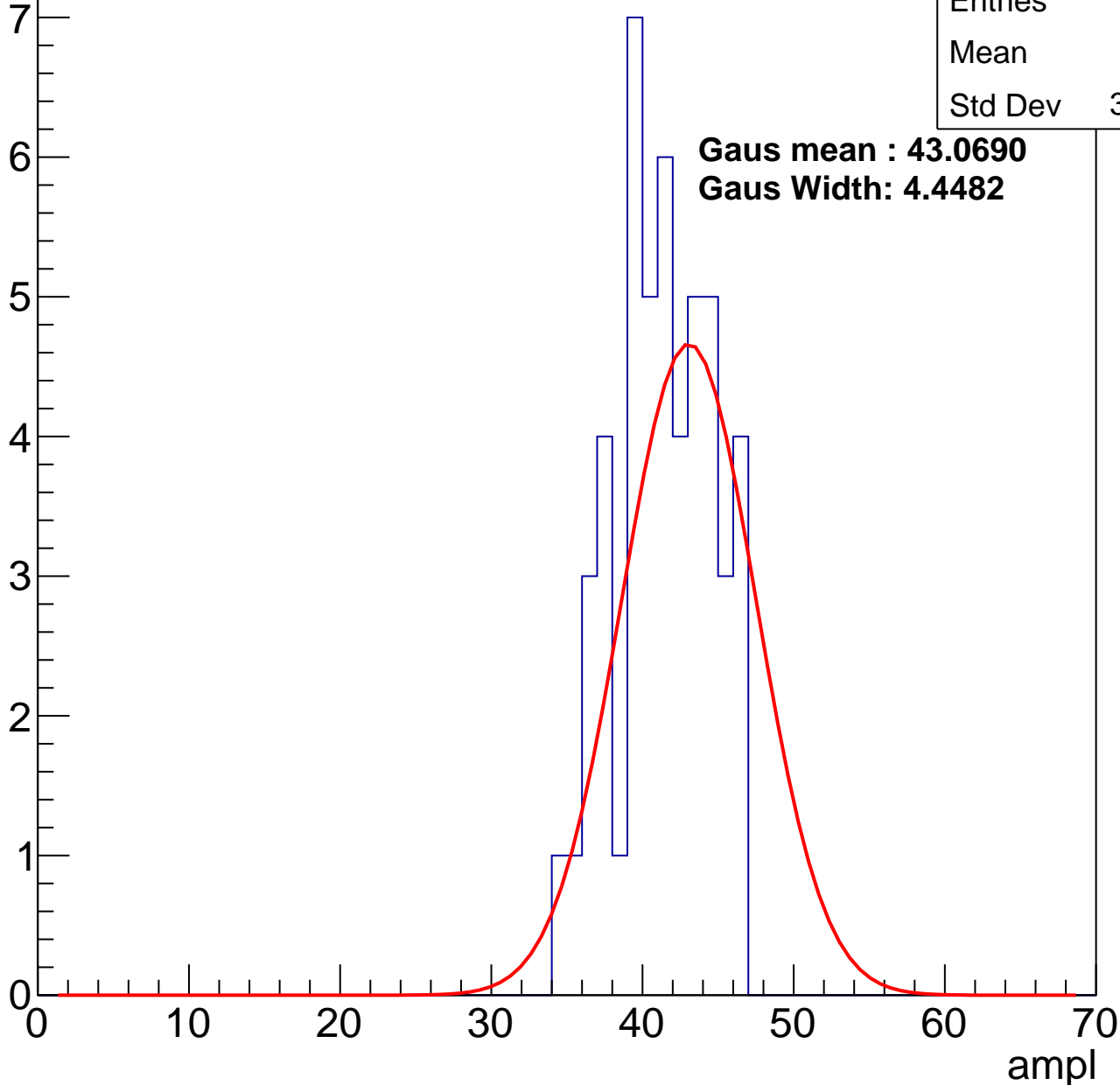
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	40.9
Std Dev	3.138

Gaus mean : 43.0690

Gaus Width: 4.4482

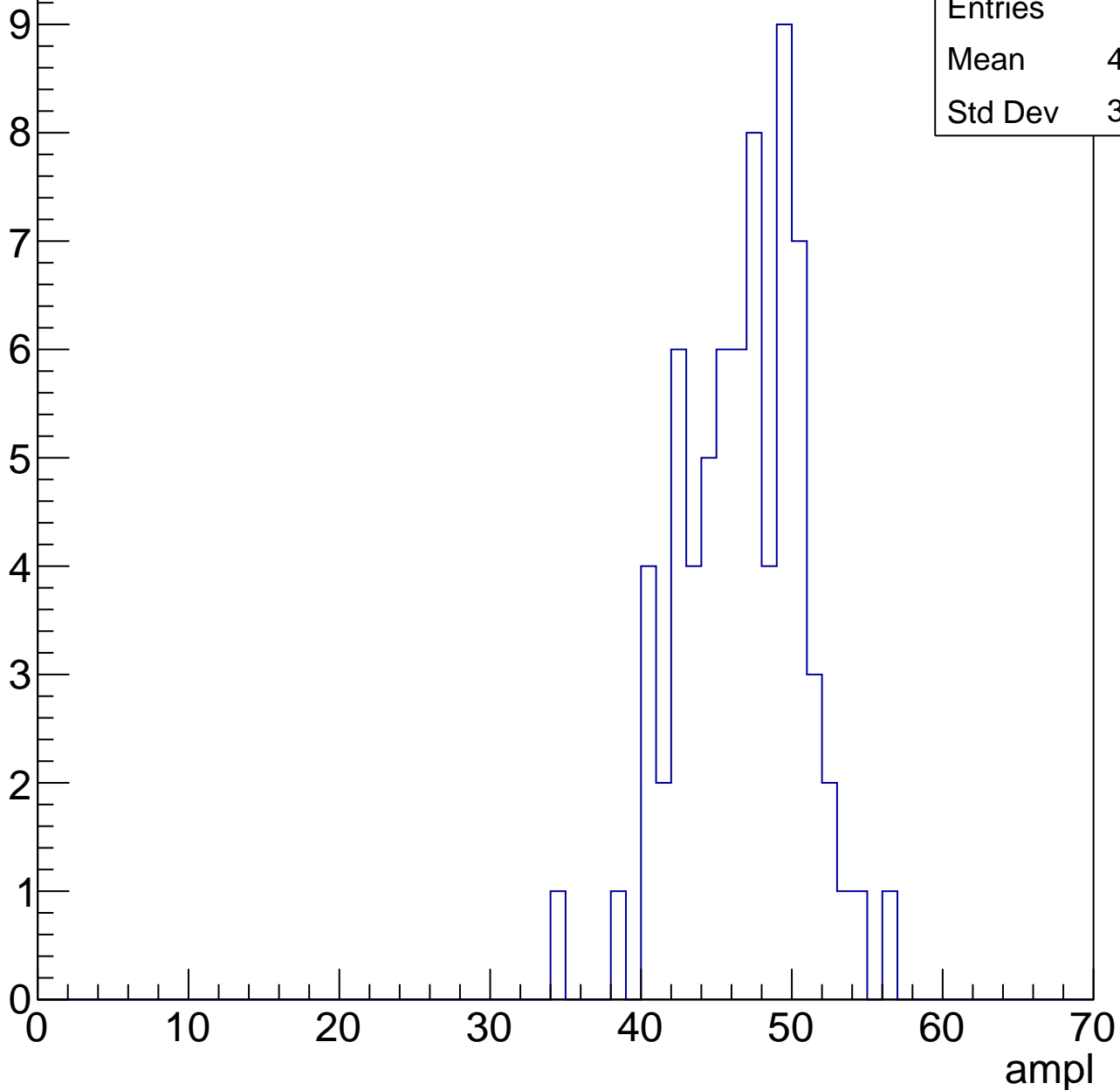


B1L101S, U1-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

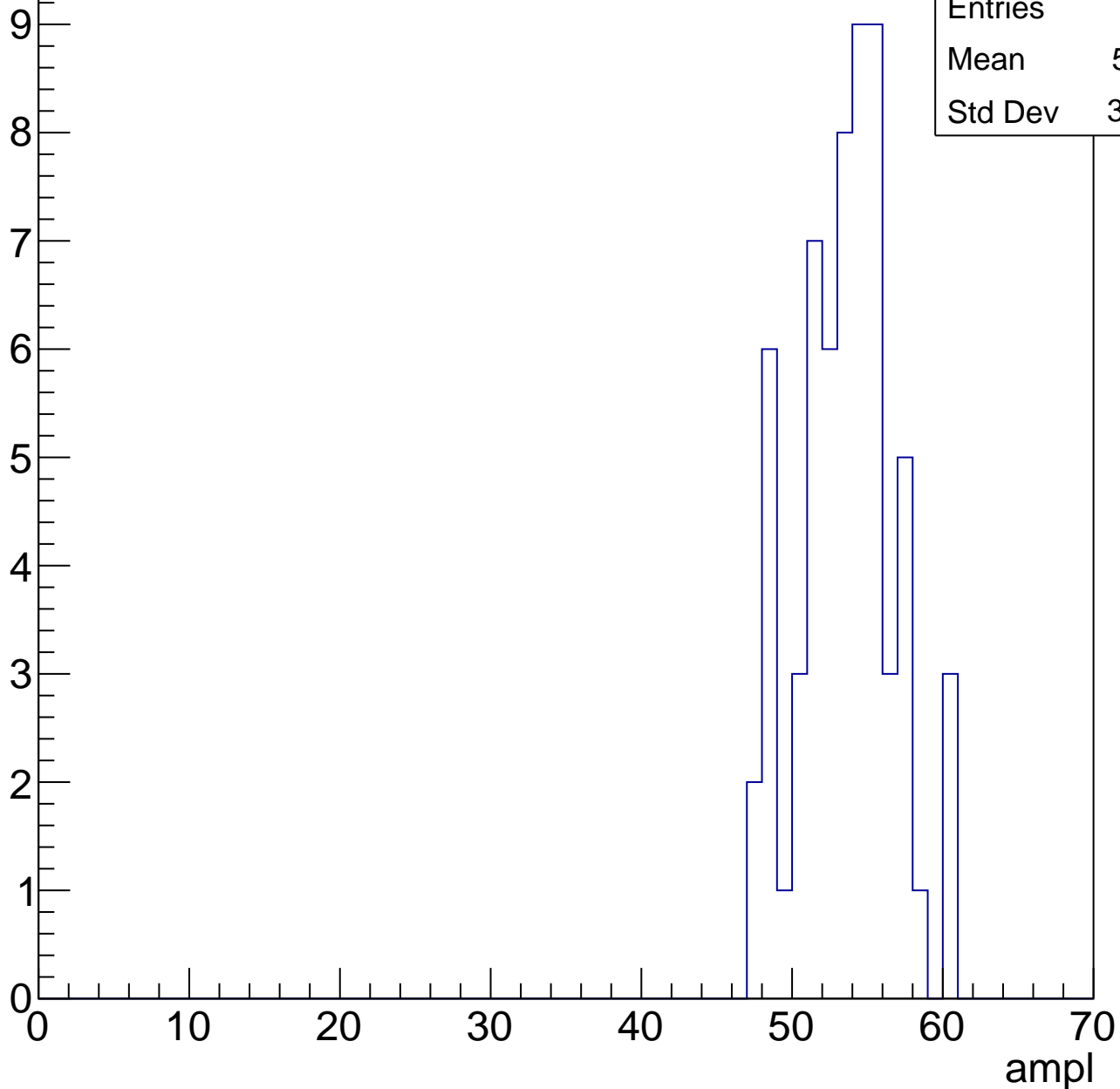
Entries	71
Mean	46.24
Std Dev	3.995



B1L101S, U1-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



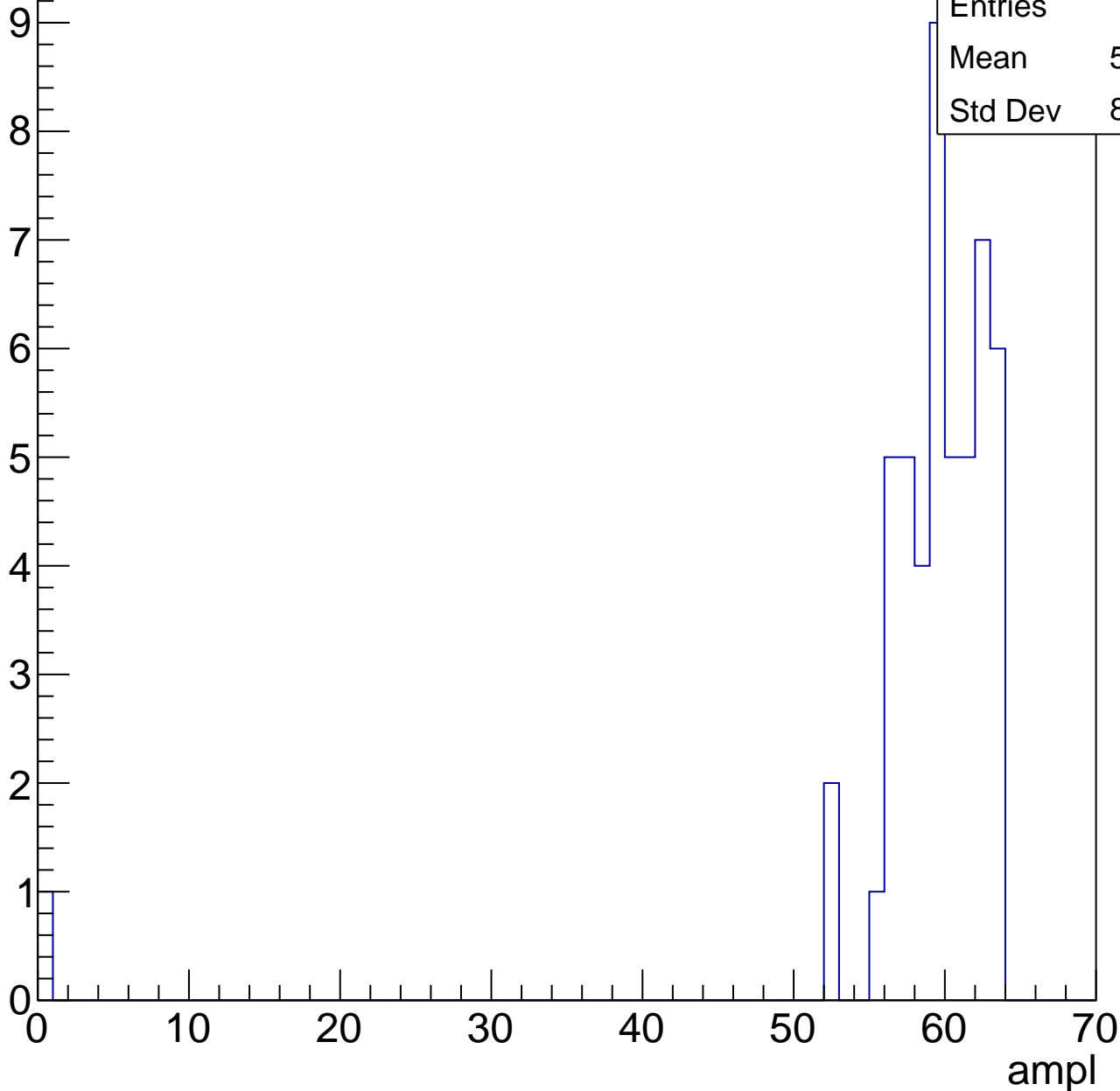
Entries	63
Mean	53.11
Std Dev	3.143

B1L101S, U1-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.08
Std Dev	8.724

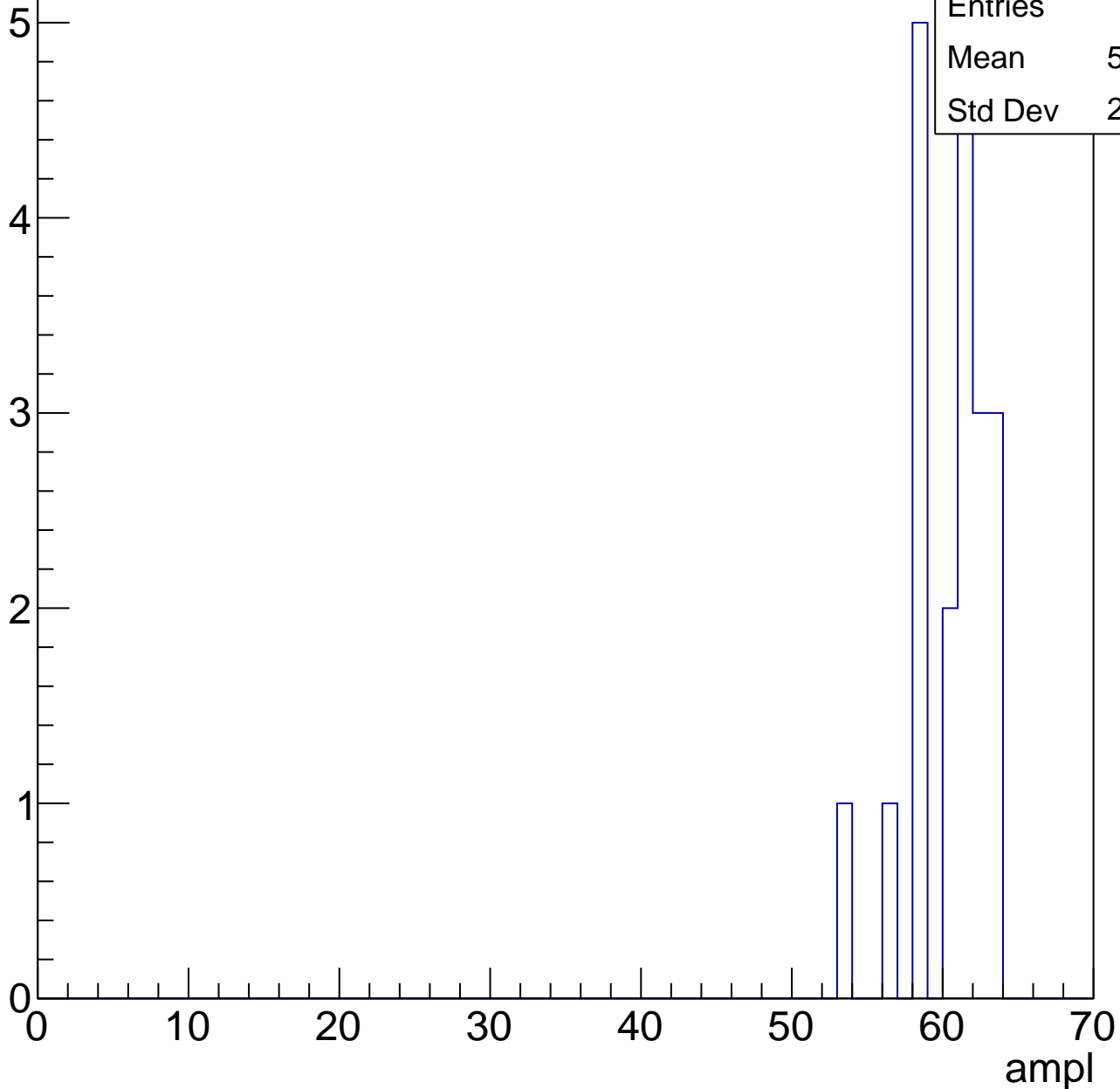


B1L101S, U1-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

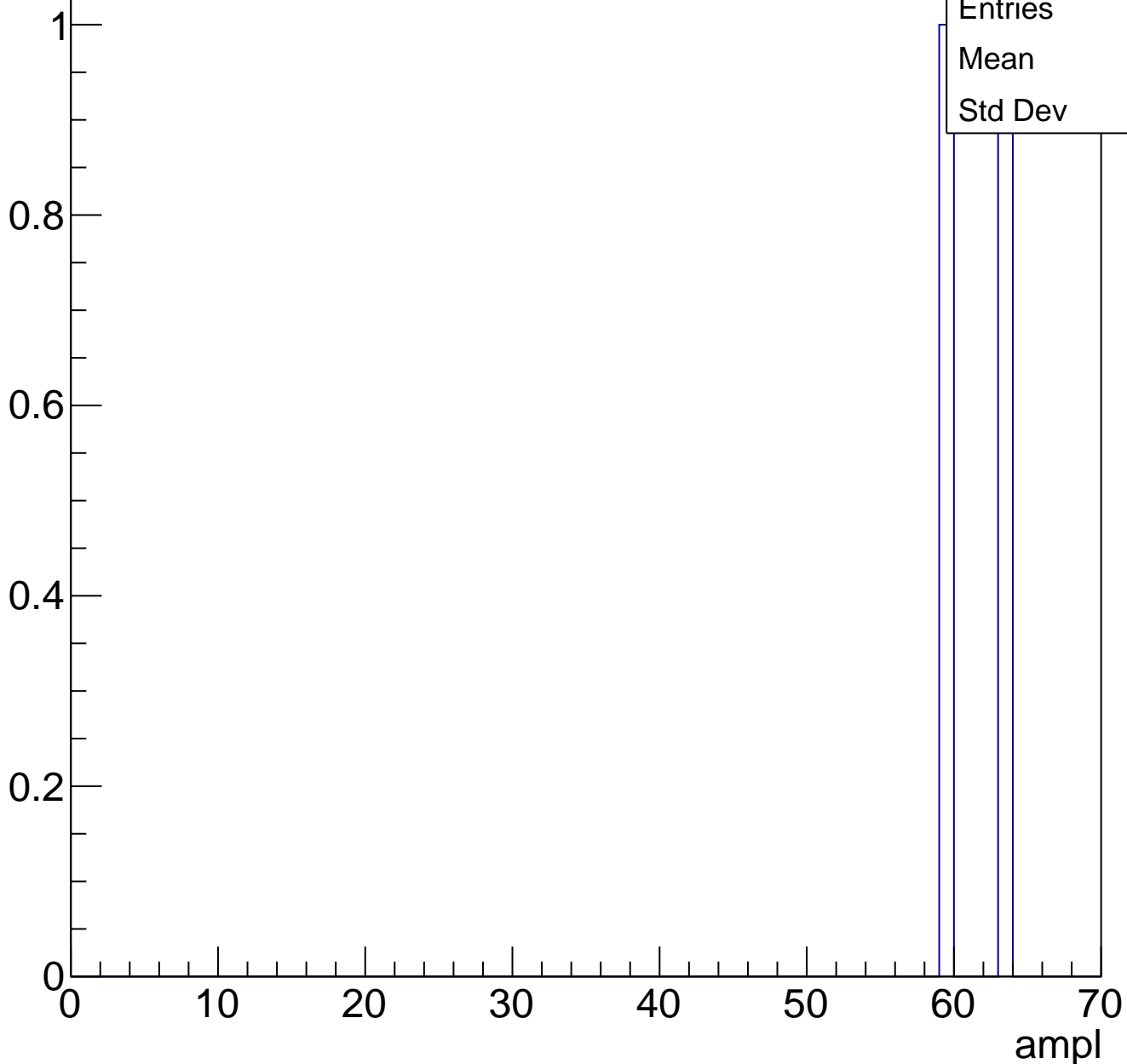
Entries	20
Mean	59.95
Std Dev	2.539



B1L101S, U1-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch121, adc0

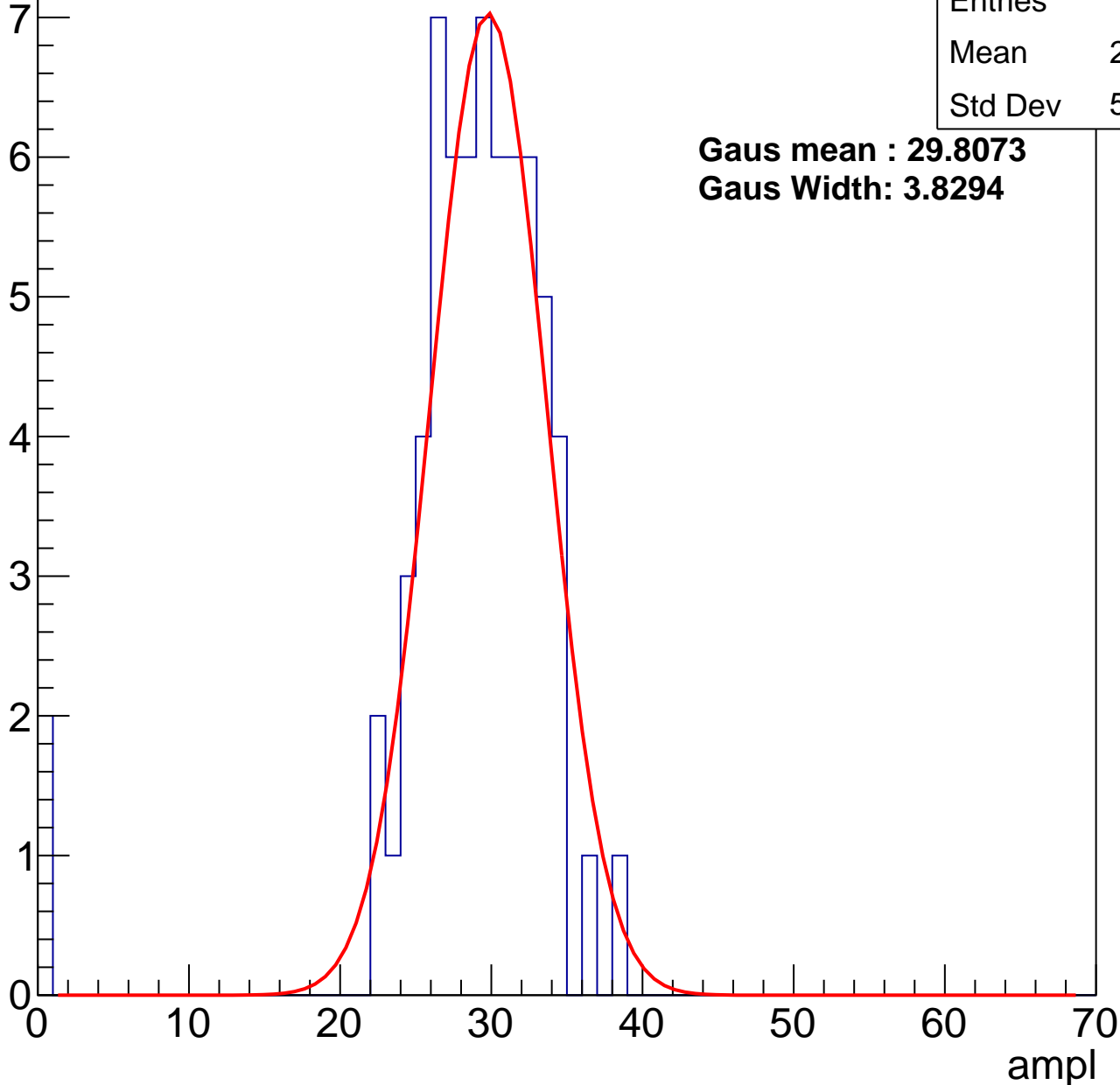
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.16
Std Dev	5.979

Gaus mean : 29.8073

Gaus Width: 3.8294



B1L101S, U1-ch121, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	35.3
Std Dev	5.659

Gaus mean : 36.7225

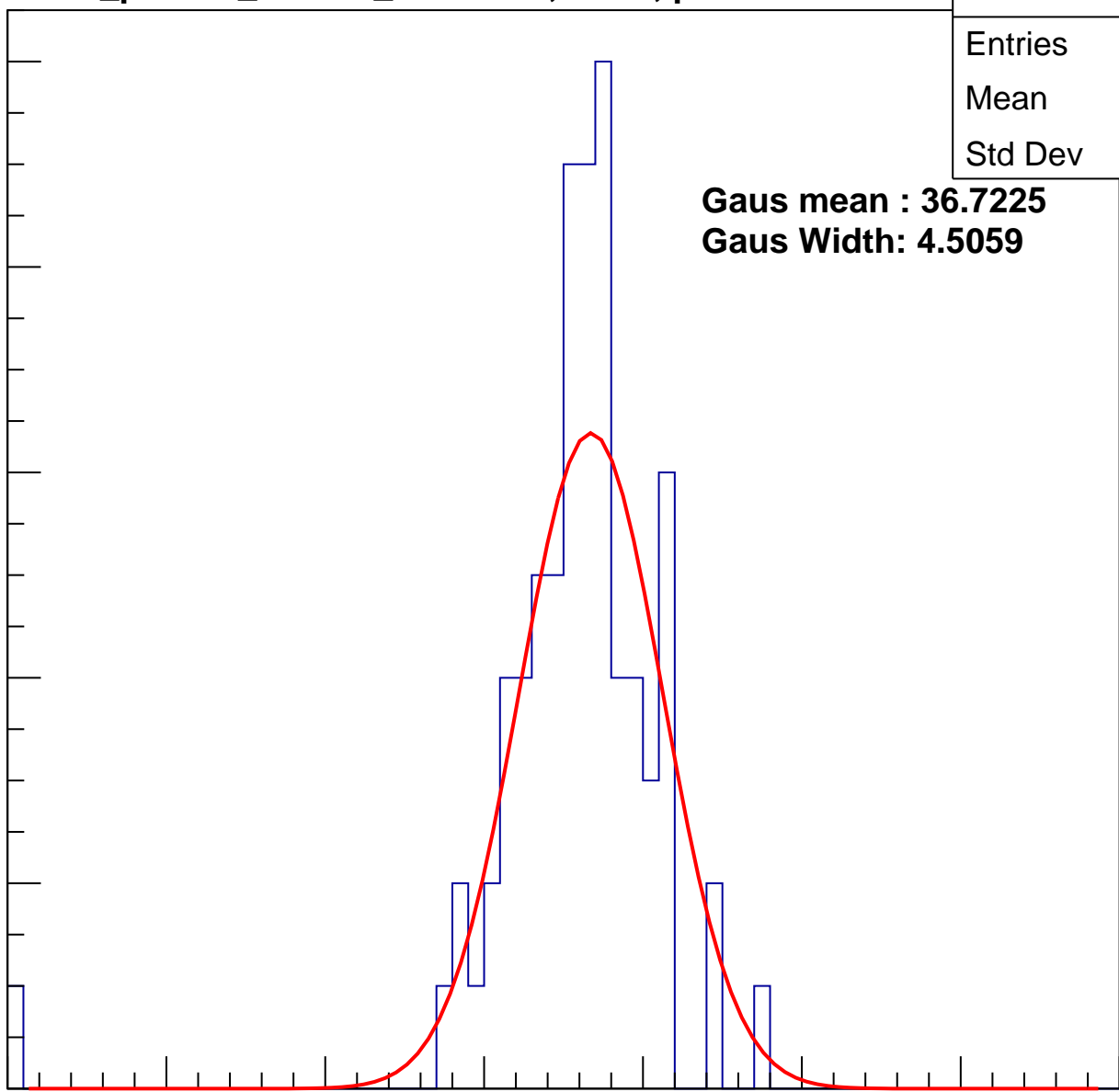
Gaus Width: 4.5059

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U1-ch121, adc2

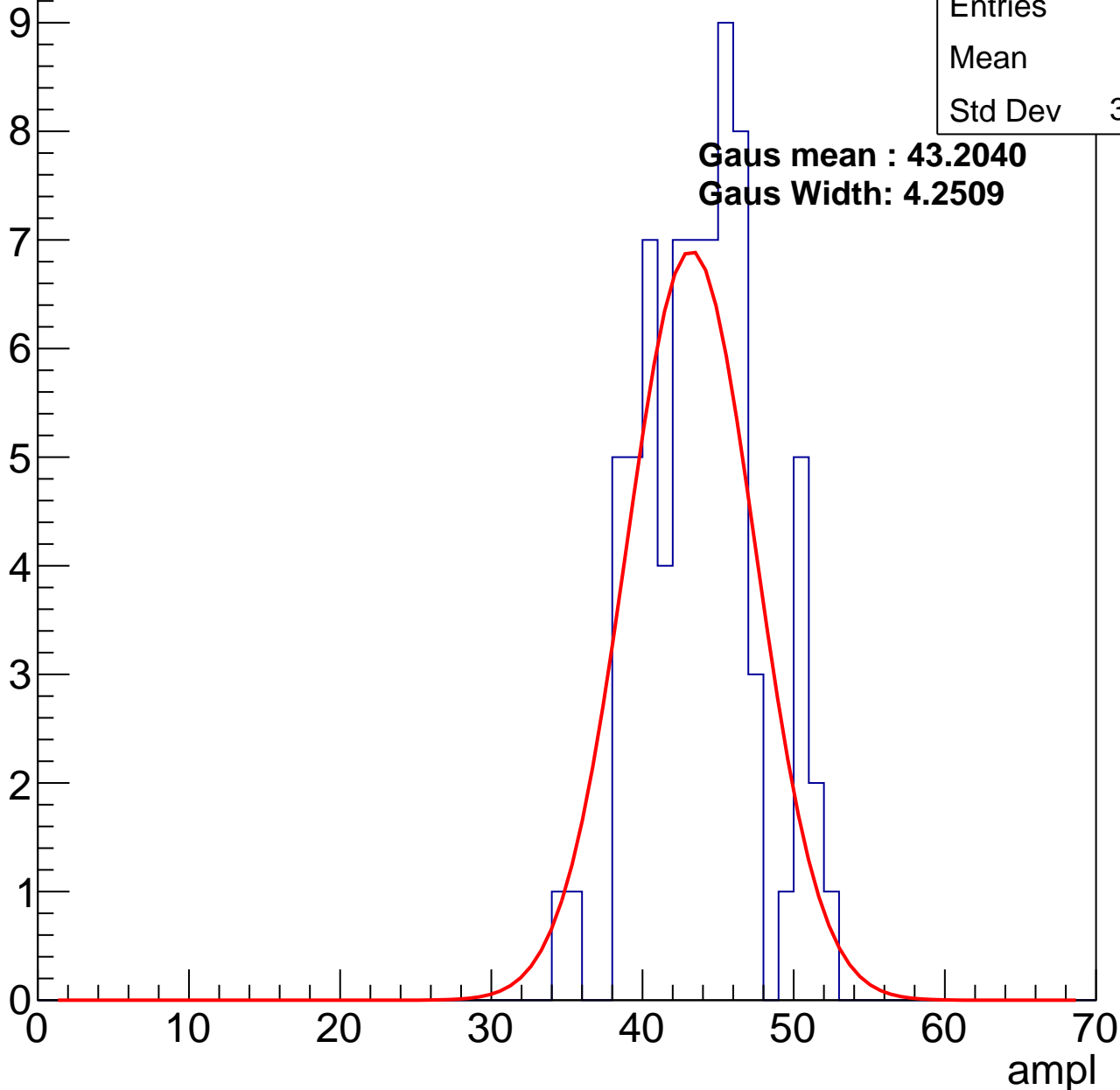
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.4
Std Dev	3.842

Gaus mean : 43.2040

Gaus Width: 4.2509

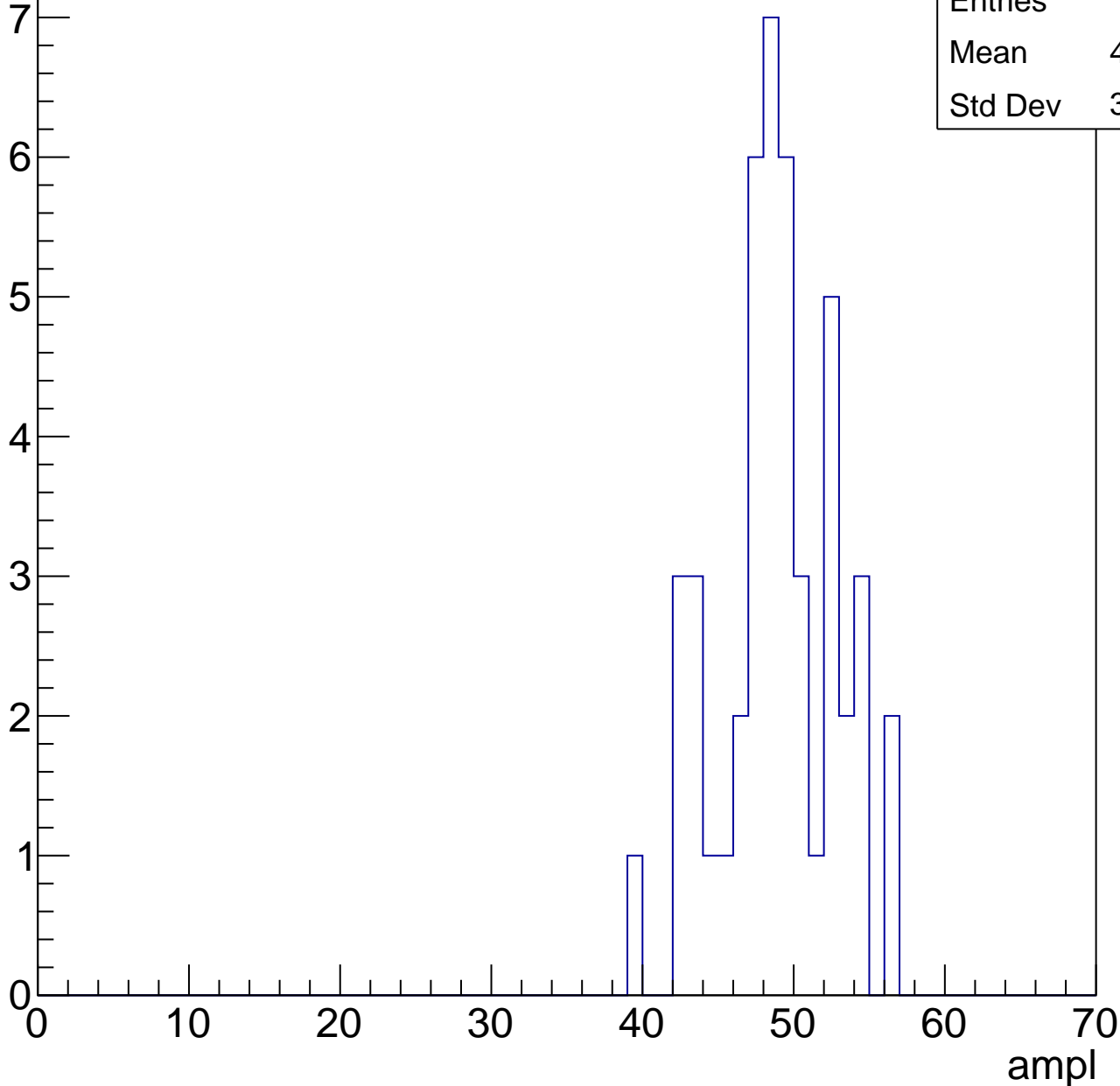


B1L101S, U1-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

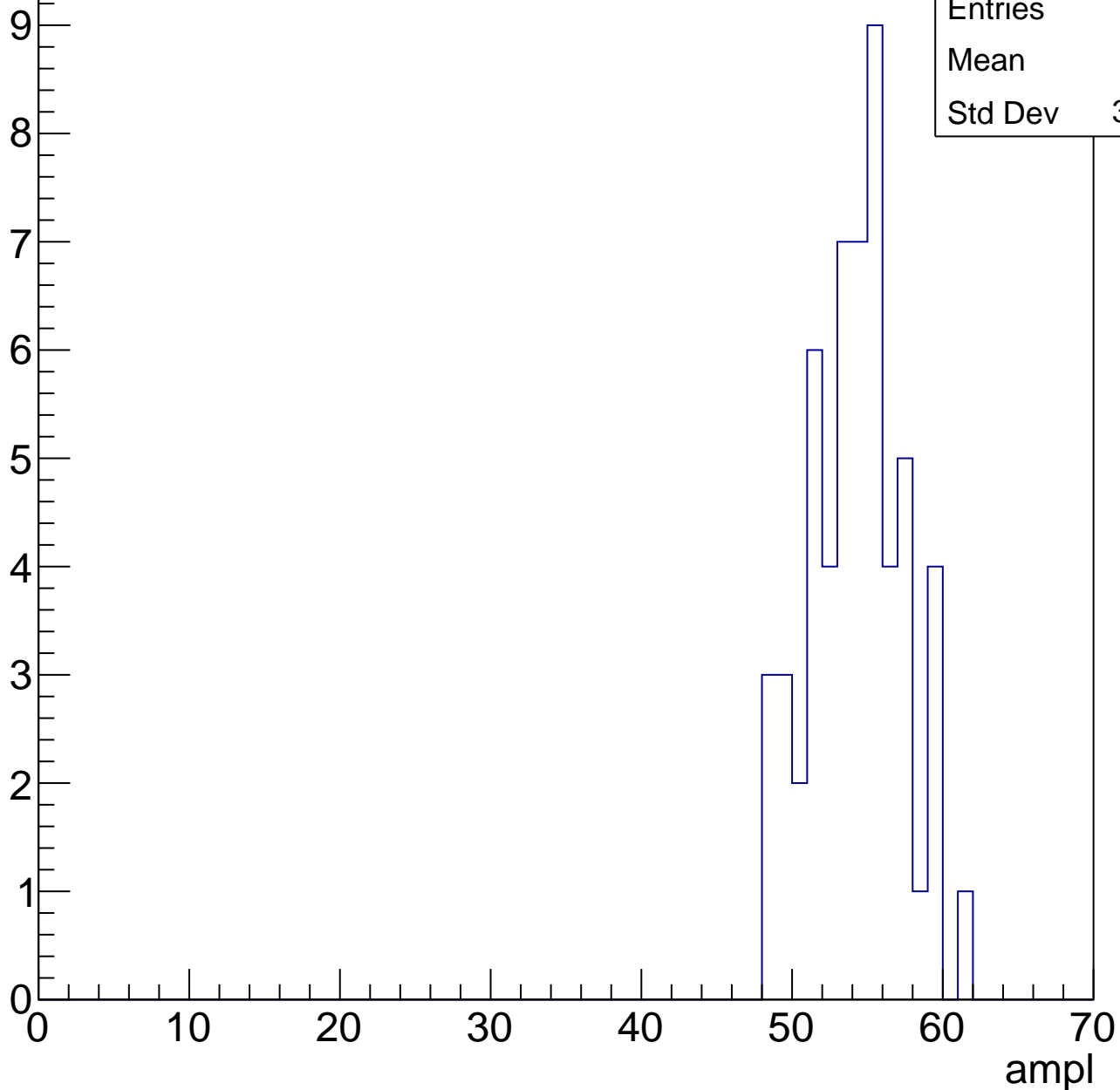
Entries	46
Mean	48.43
Std Dev	3.865



B1L101S, U1-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

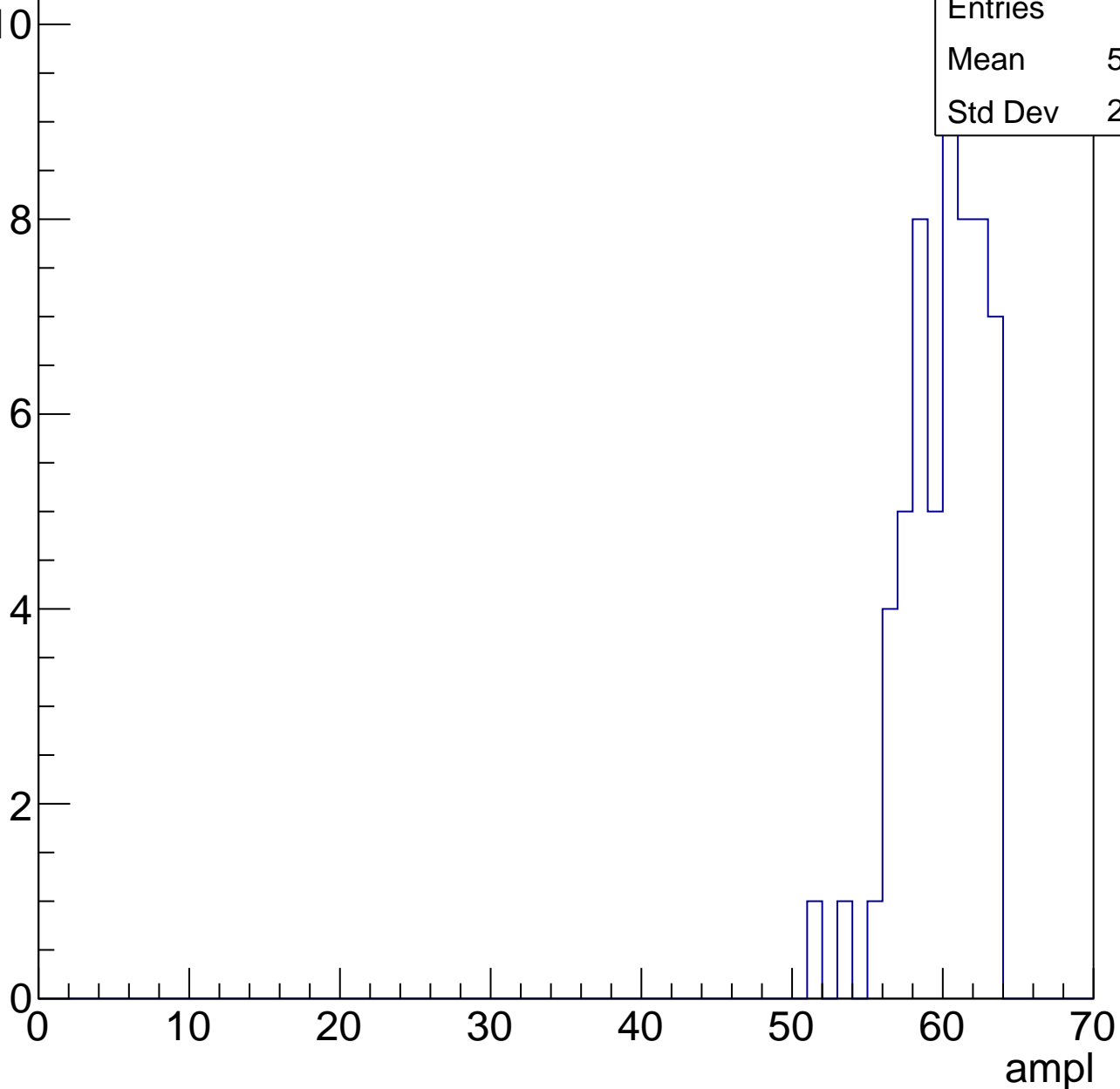


B1L101S, U1-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

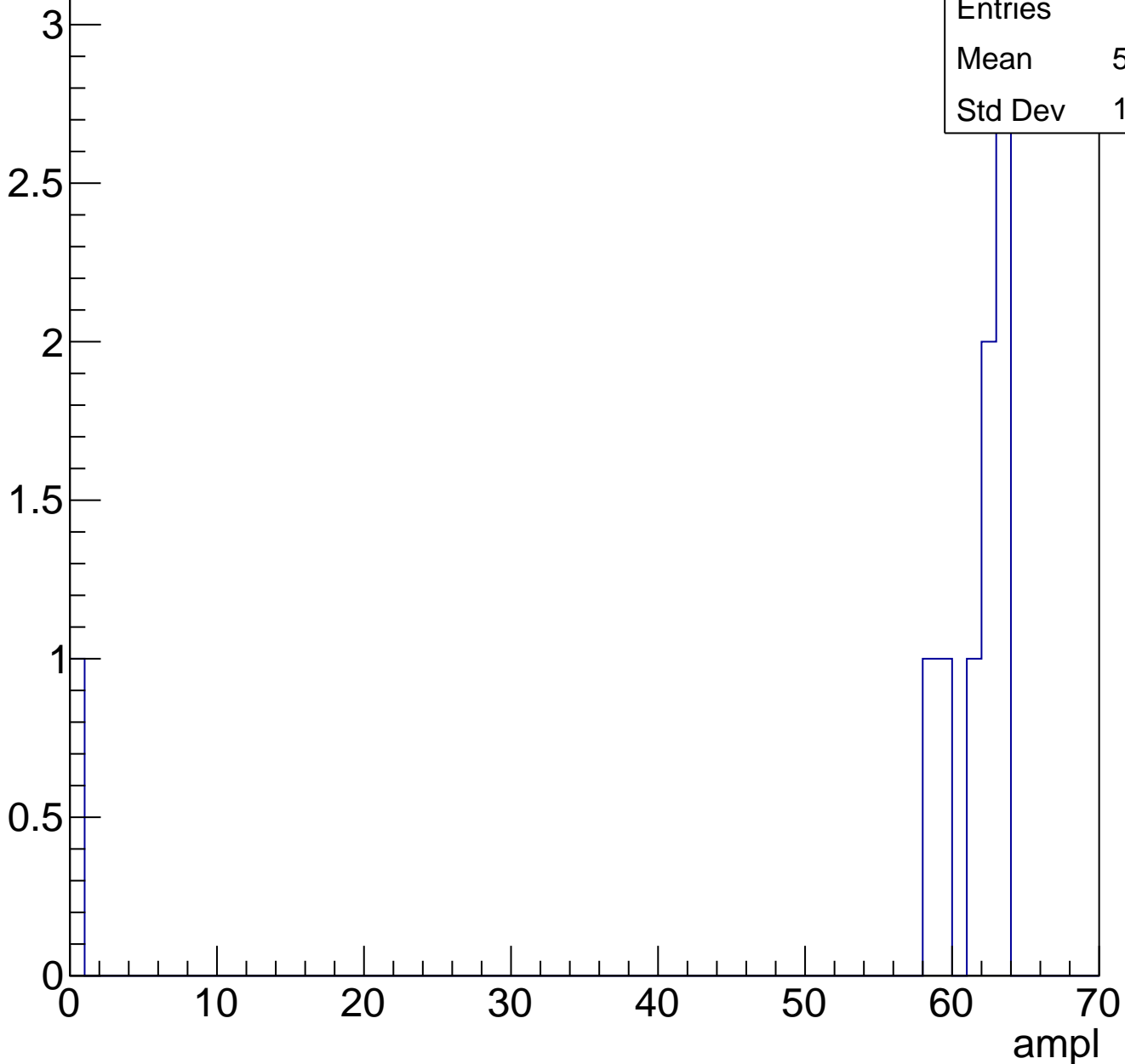
Entries	58
Mean	59.52
Std Dev	2.595



B1L101S, U1-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch122, adc0

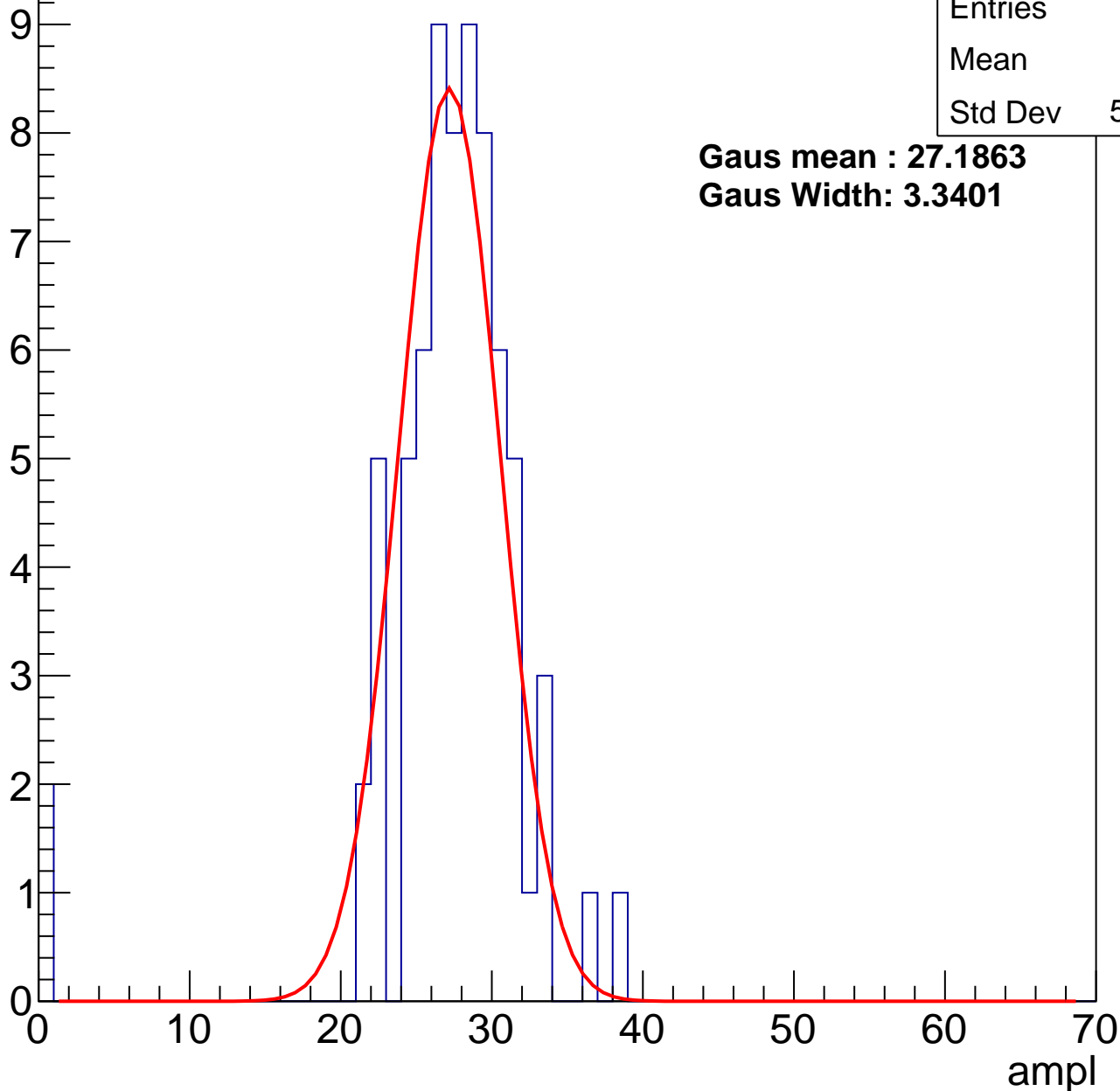
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	26.7
Std Dev	5.613

Gaus mean : 27.1863

Gaus Width: 3.3401



B1L101S, U1-ch122, adc1

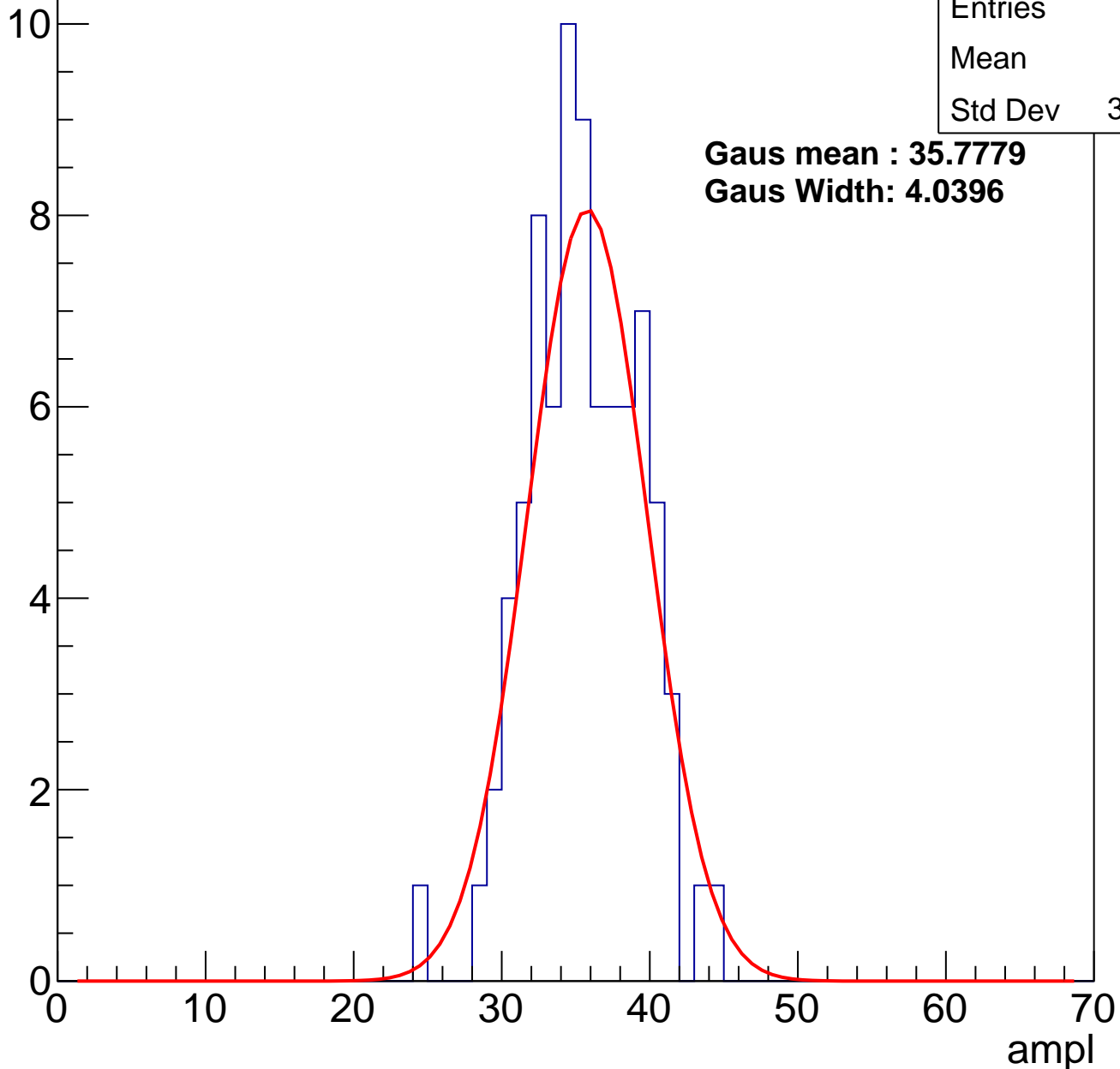
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	35.1
Std Dev	3.687

Gaus mean : 35.7779

Gaus Width: 4.0396

Entry



B1L101S, U1-ch122, adc2

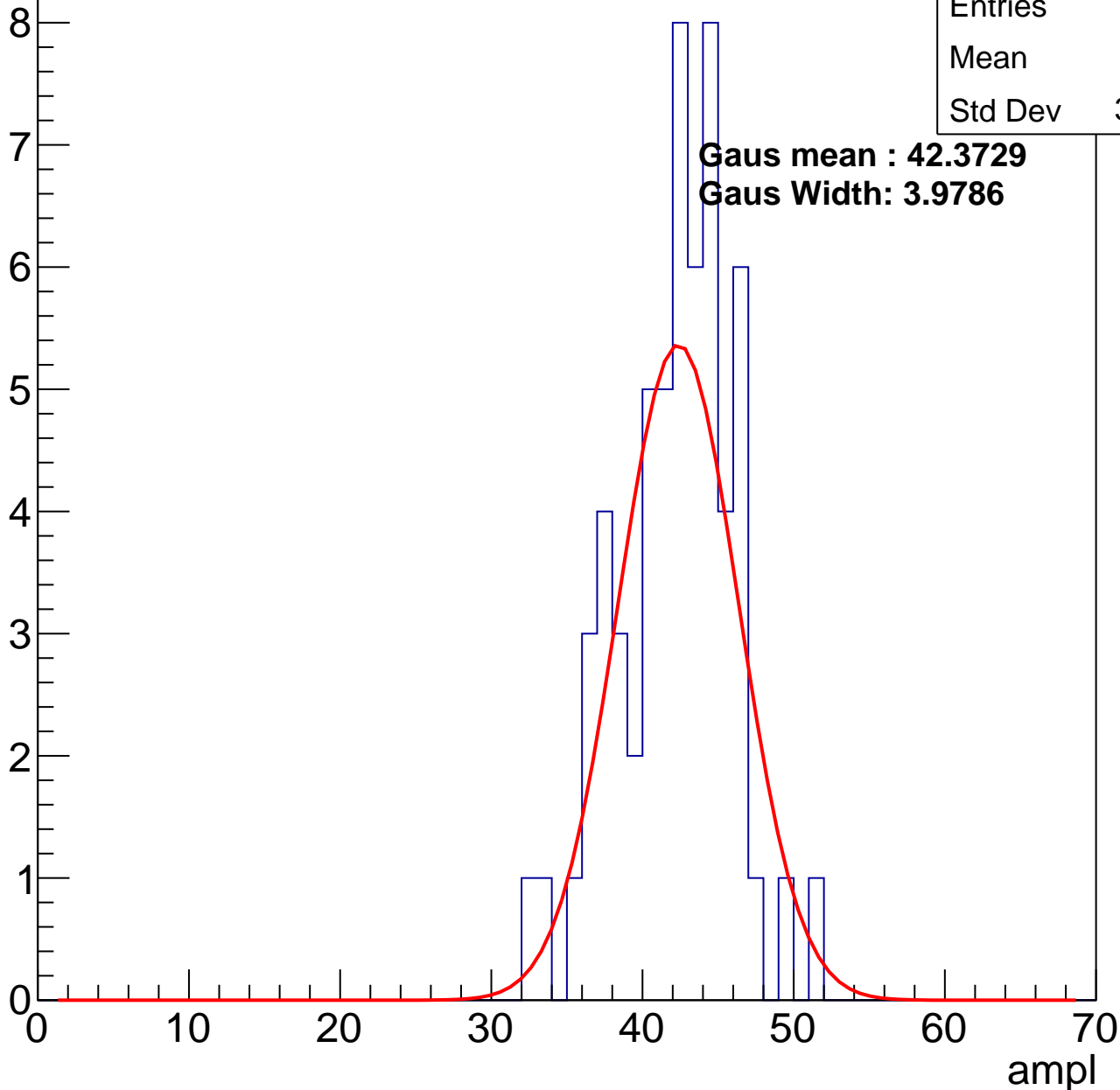
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.7
Std Dev	3.761

Gaus mean : 42.3729

Gaus Width: 3.9786

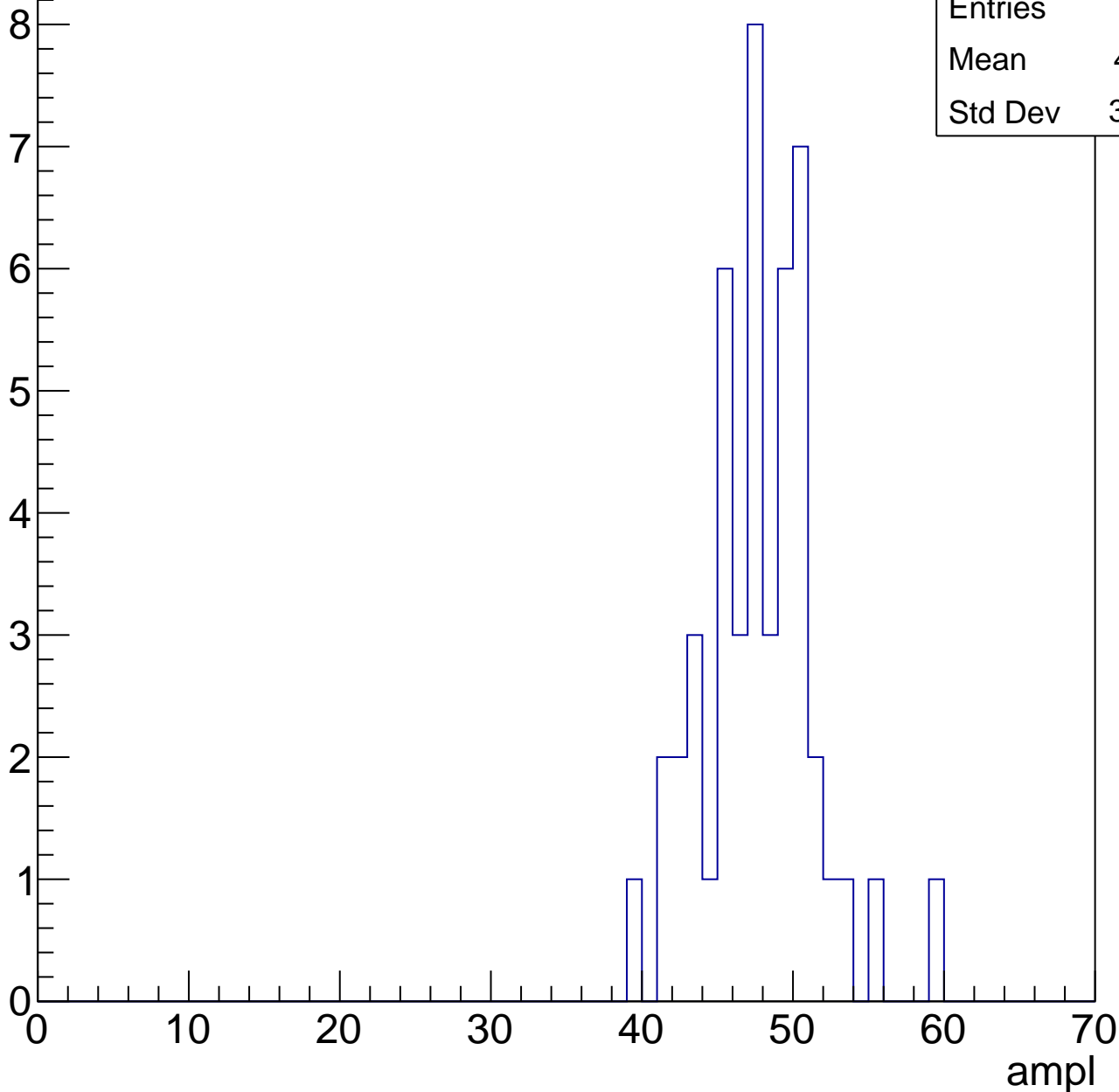


B1L101S, U1-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	47.31
Std Dev	3.698

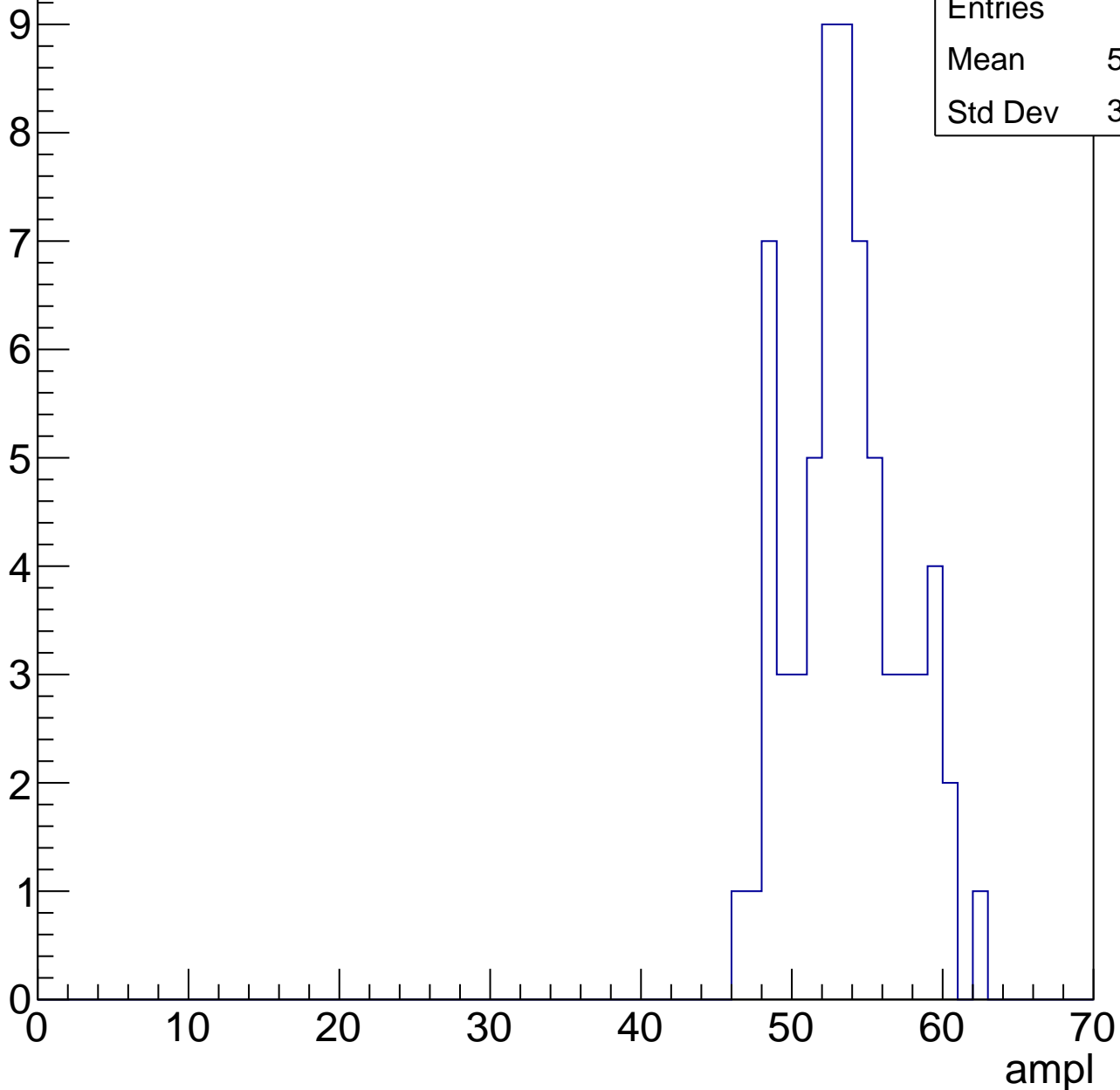


B1L101S, U1-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	53.18
Std Dev	3.614

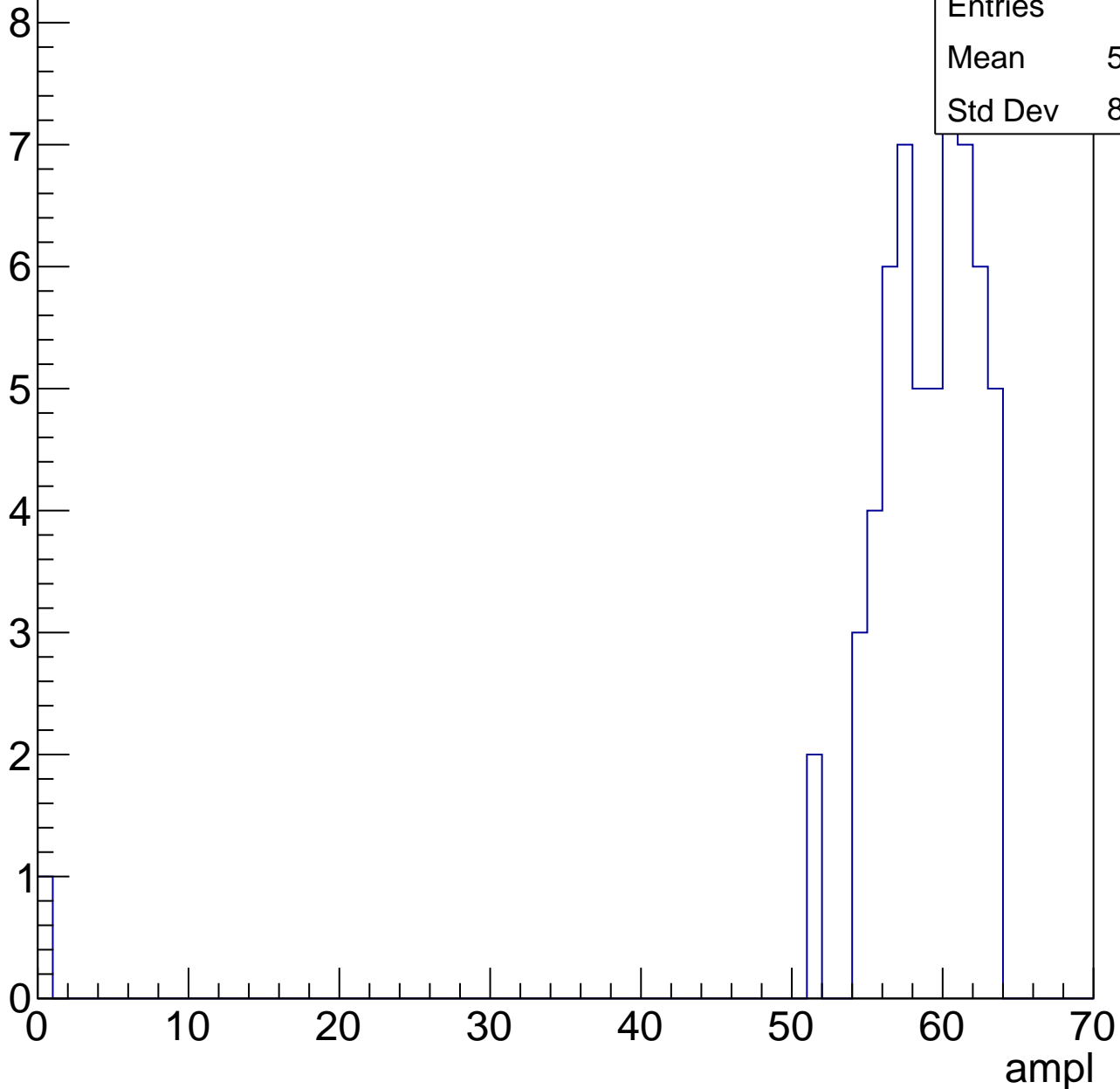


B1L101S, U1-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

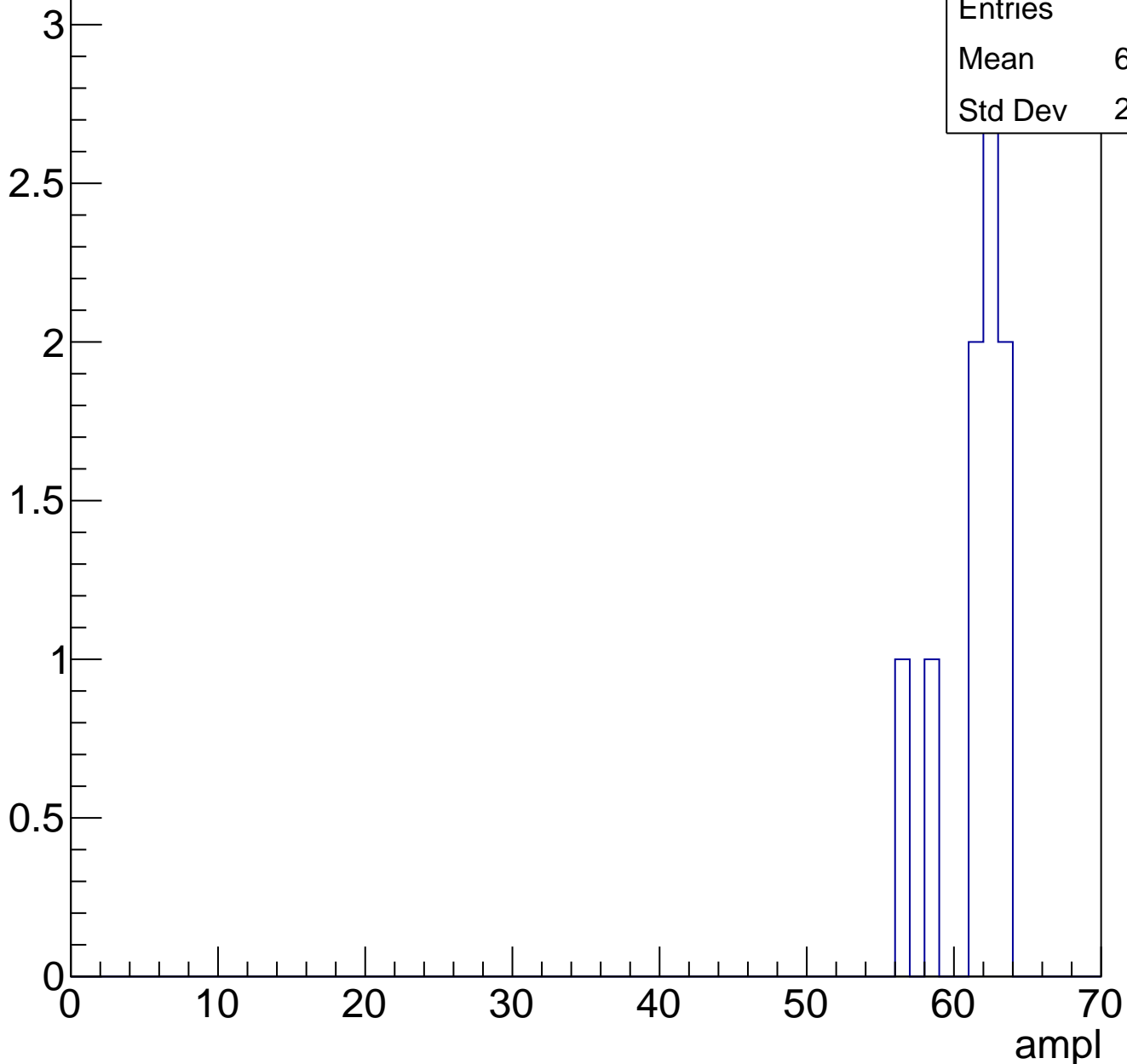
Entries	59
Mean	57.59
Std Dev	8.118



B1L101S, U1-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

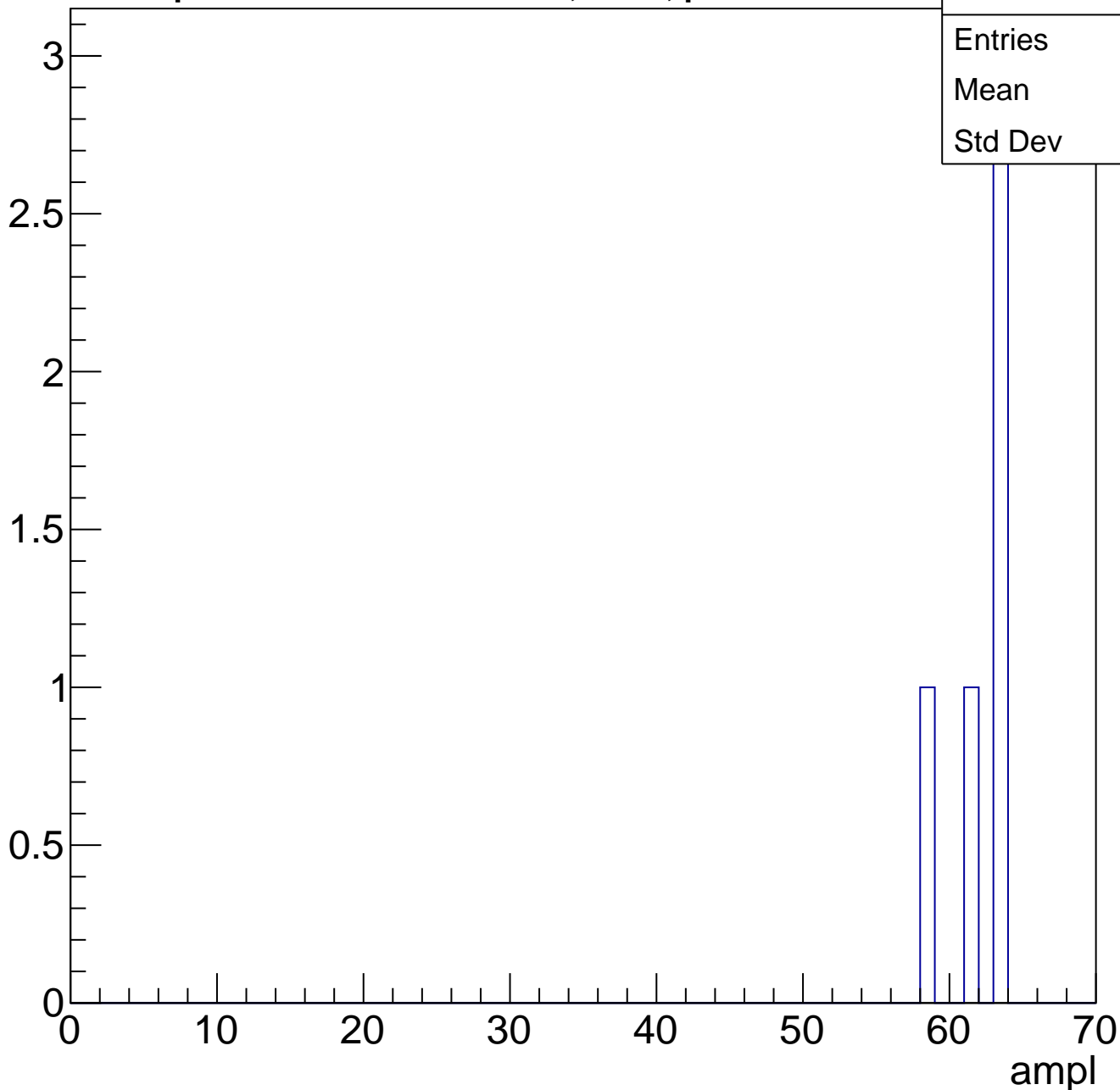


Entries	9
Mean	60.89
Std Dev	2.233

B1L101S, U1-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	61.6
Std Dev	1.96

B1L101S, U1-ch123, adc0

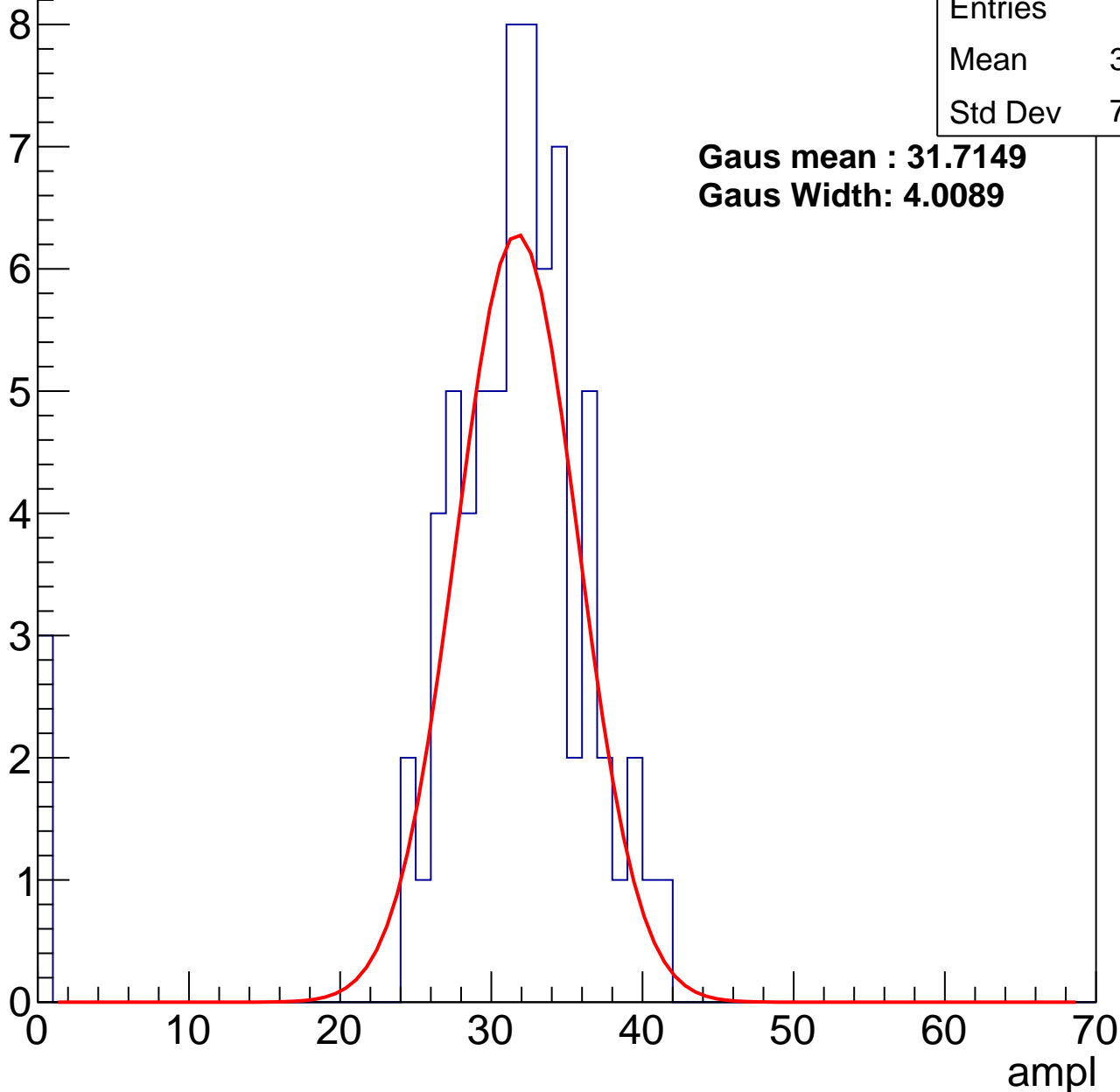
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.28
Std Dev	7.362

Gaus mean : 31.7149

Gaus Width: 4.0089



B1L101S, U1-ch123, adc1

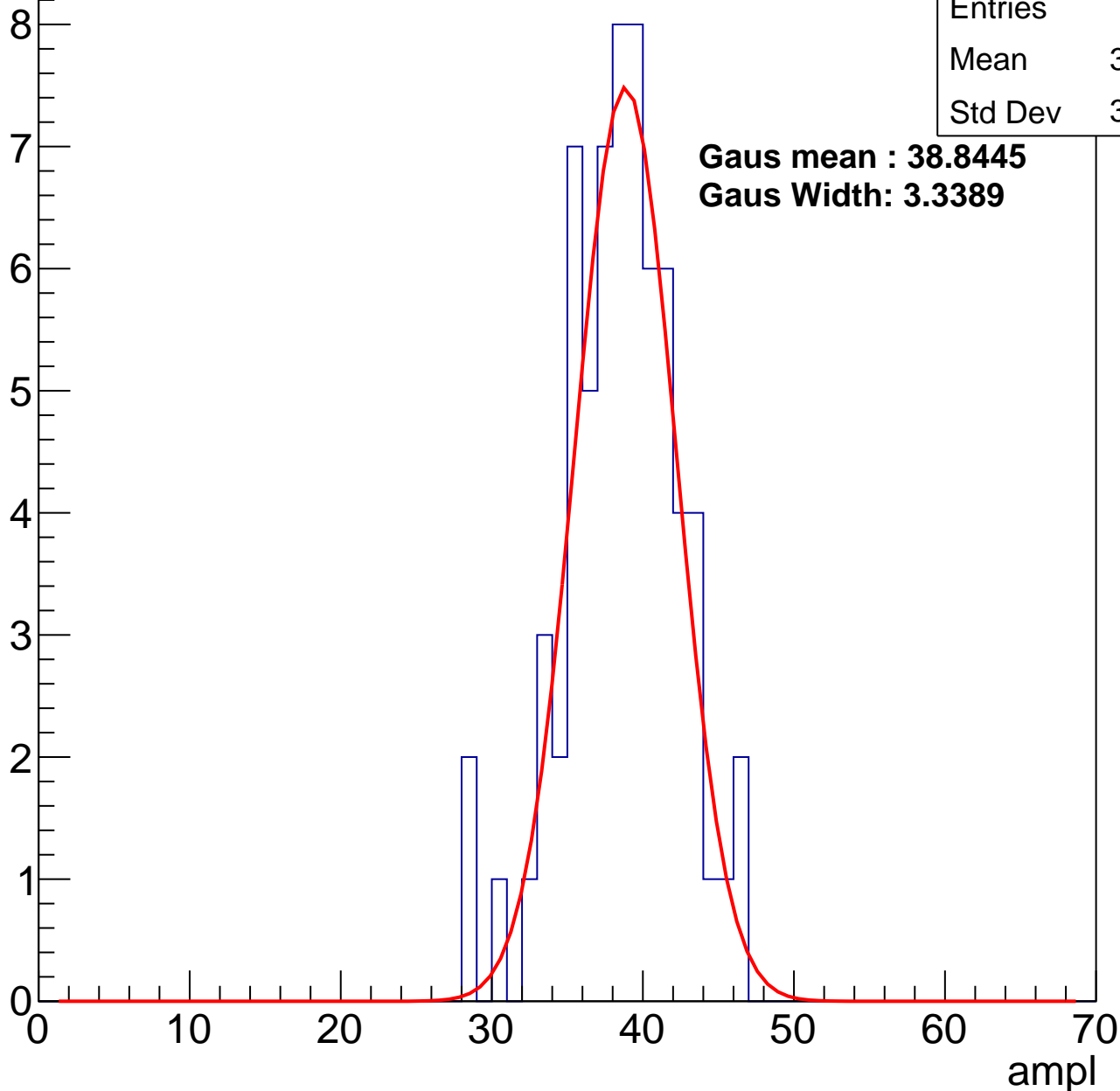
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	38.12
Std Dev	3.756

Gaus mean : 38.8445

Gaus Width: 3.3389



B1L101S, U1-ch123, adc2

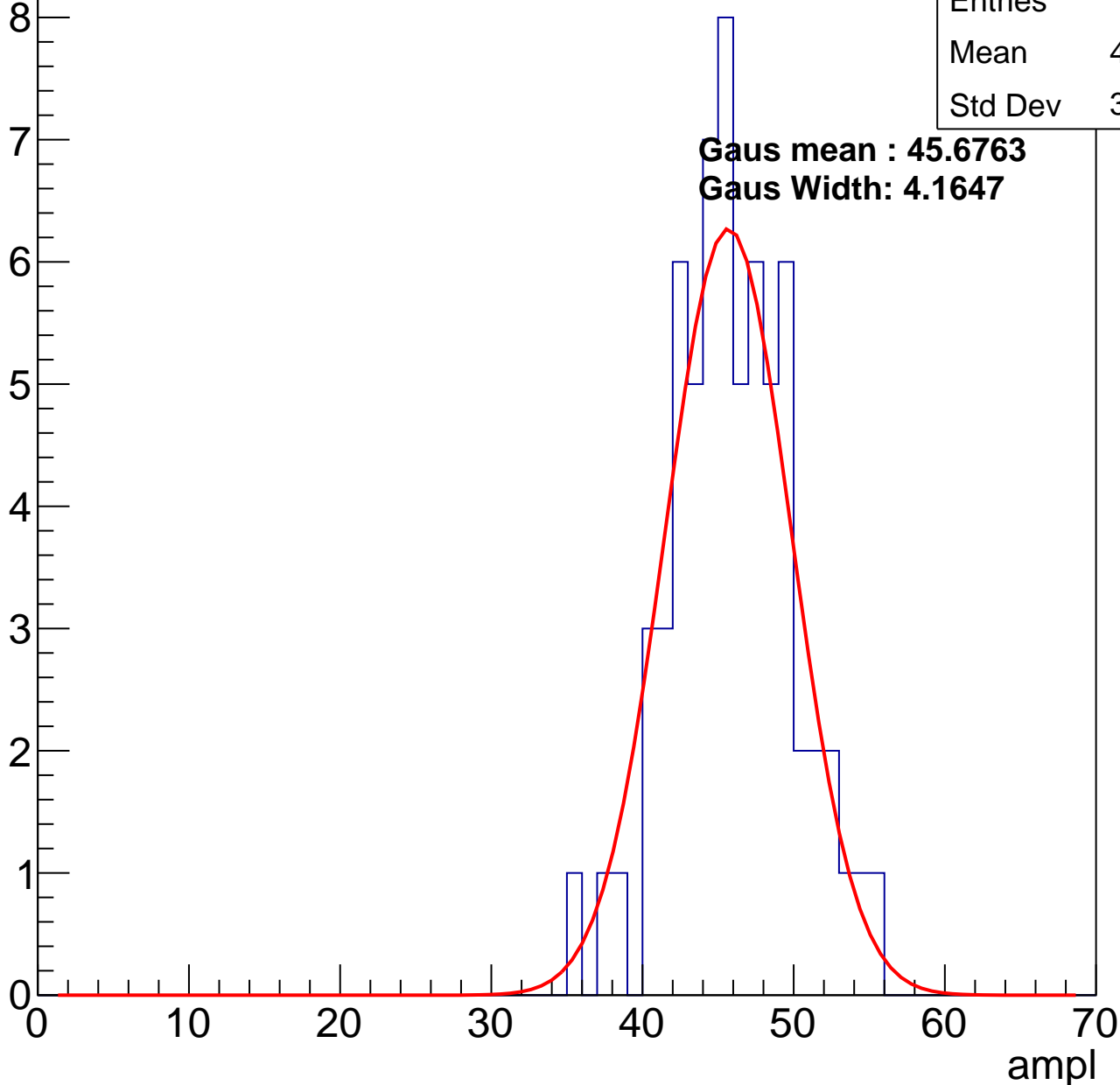
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.48
Std Dev	3.963

Gaus mean : 45.6763

Gaus Width: 4.1647

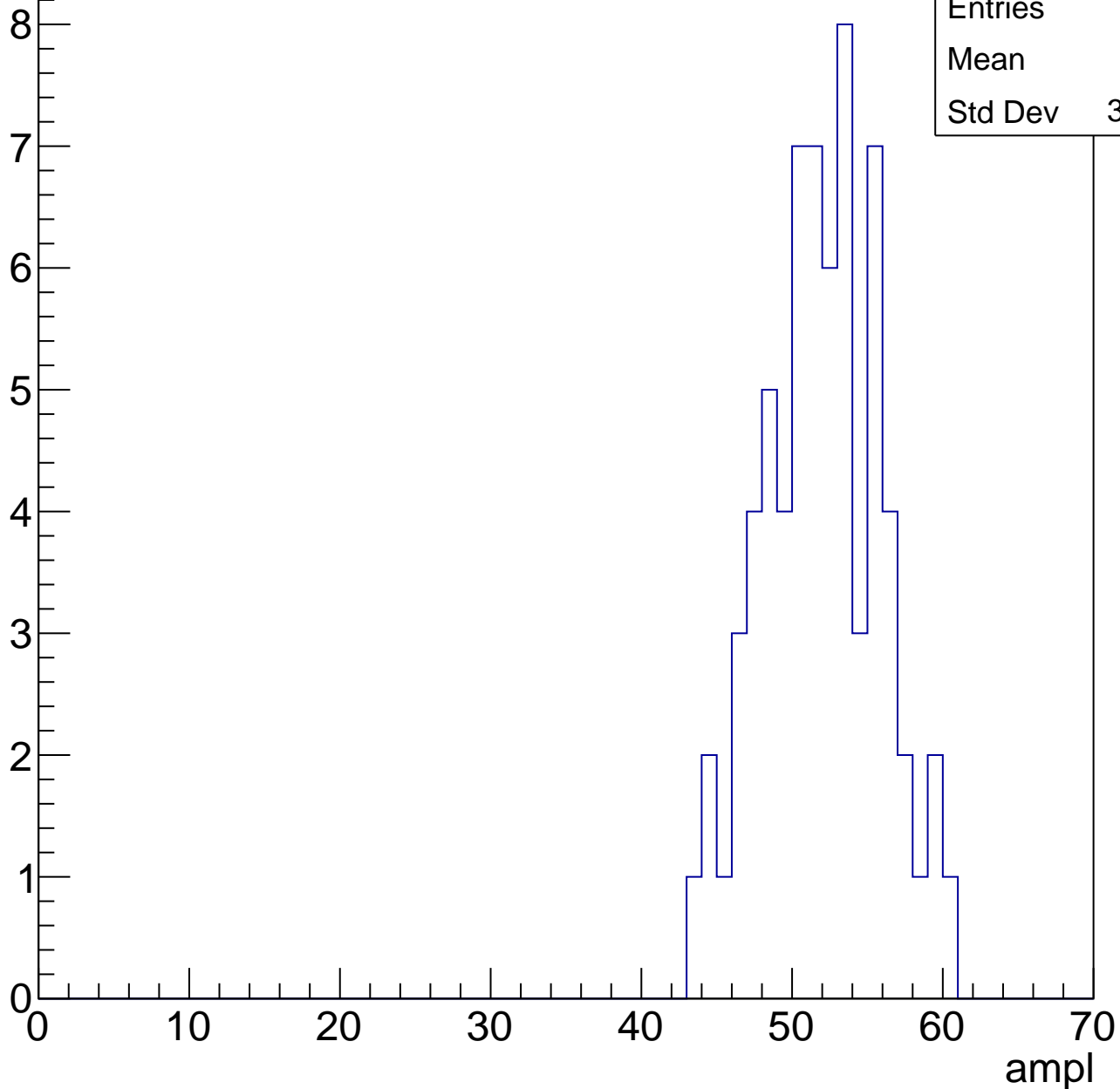


B1L101S, U1-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51.5
Std Dev	3.825

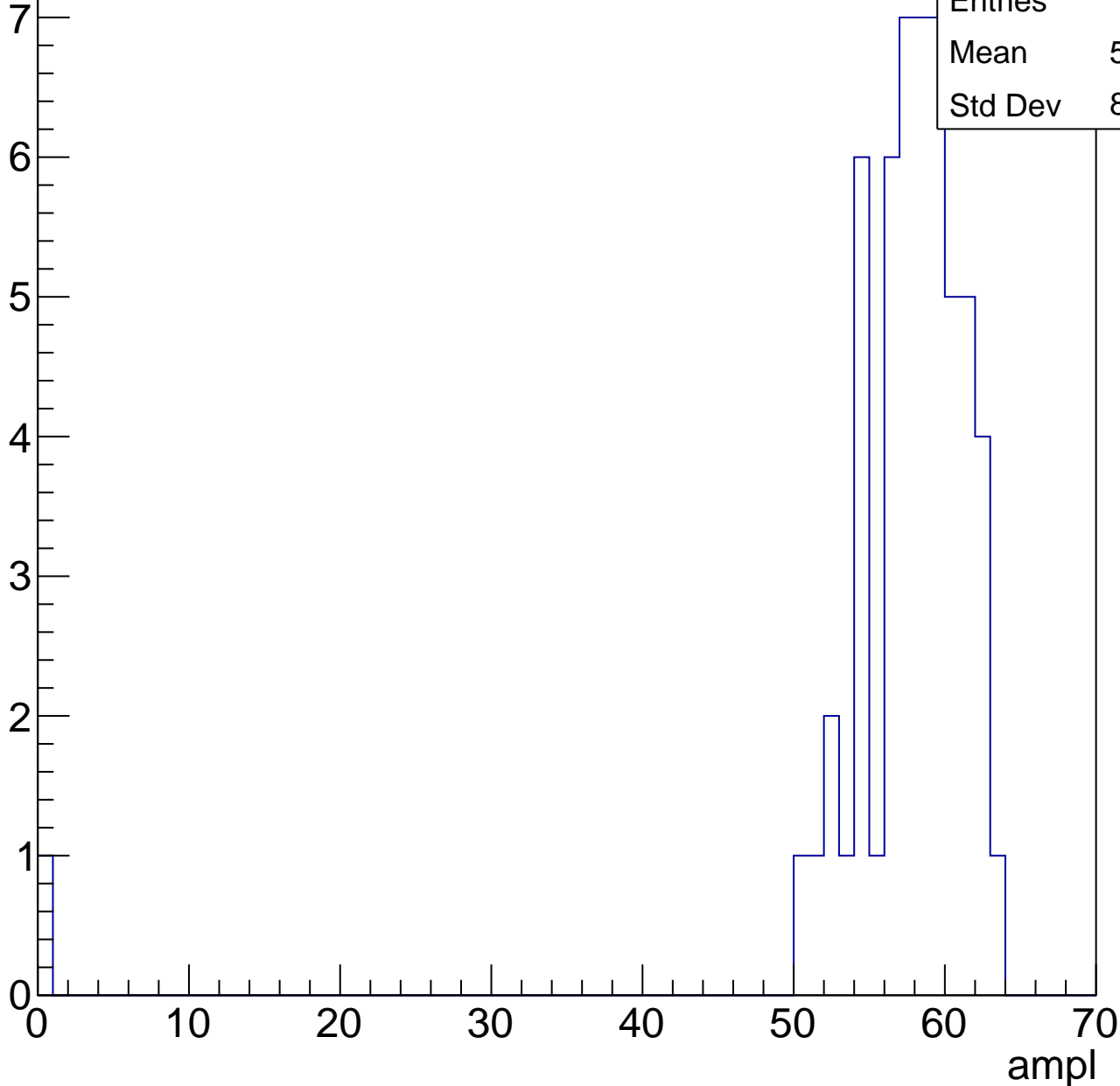


B1L101S, U1-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.49
Std Dev	8.253

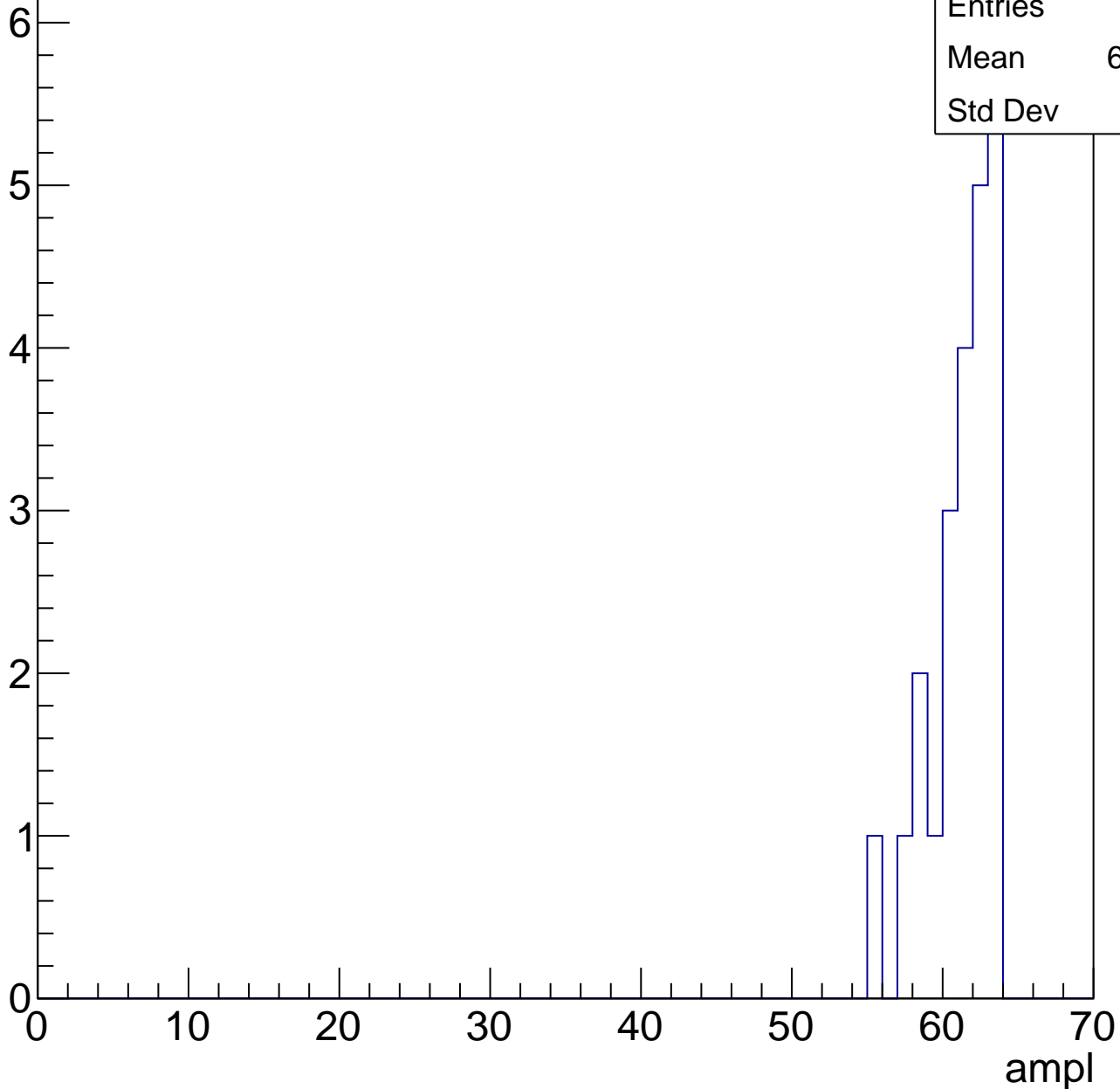


B1L101S, U1-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	60.83
Std Dev	2.14



B1L101S, U1-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch124, adc0

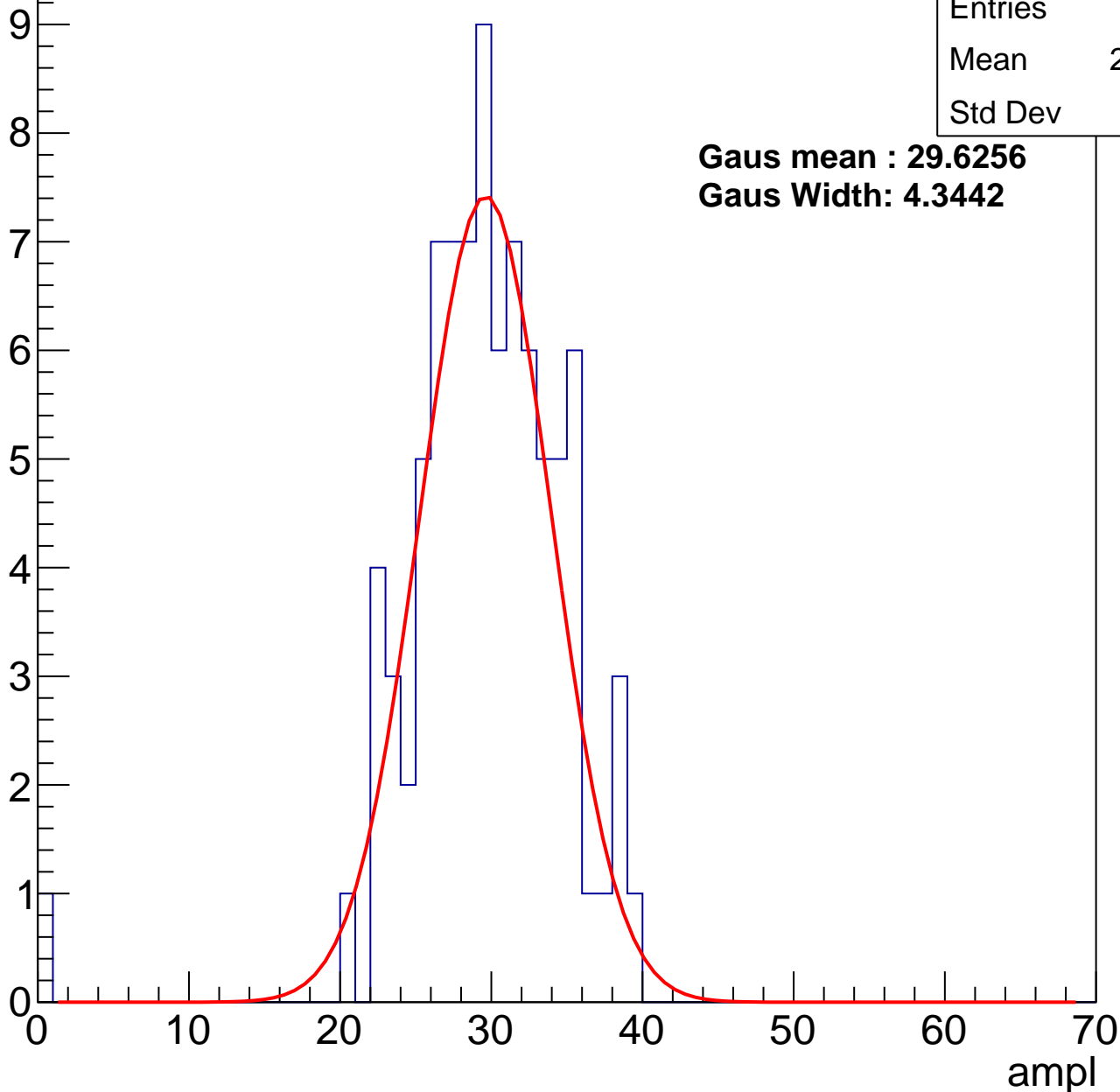
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	29.17
Std Dev	5.27

Gaus mean : 29.6256

Gaus Width: 4.3442



B1L101S, U1-ch124, adc1

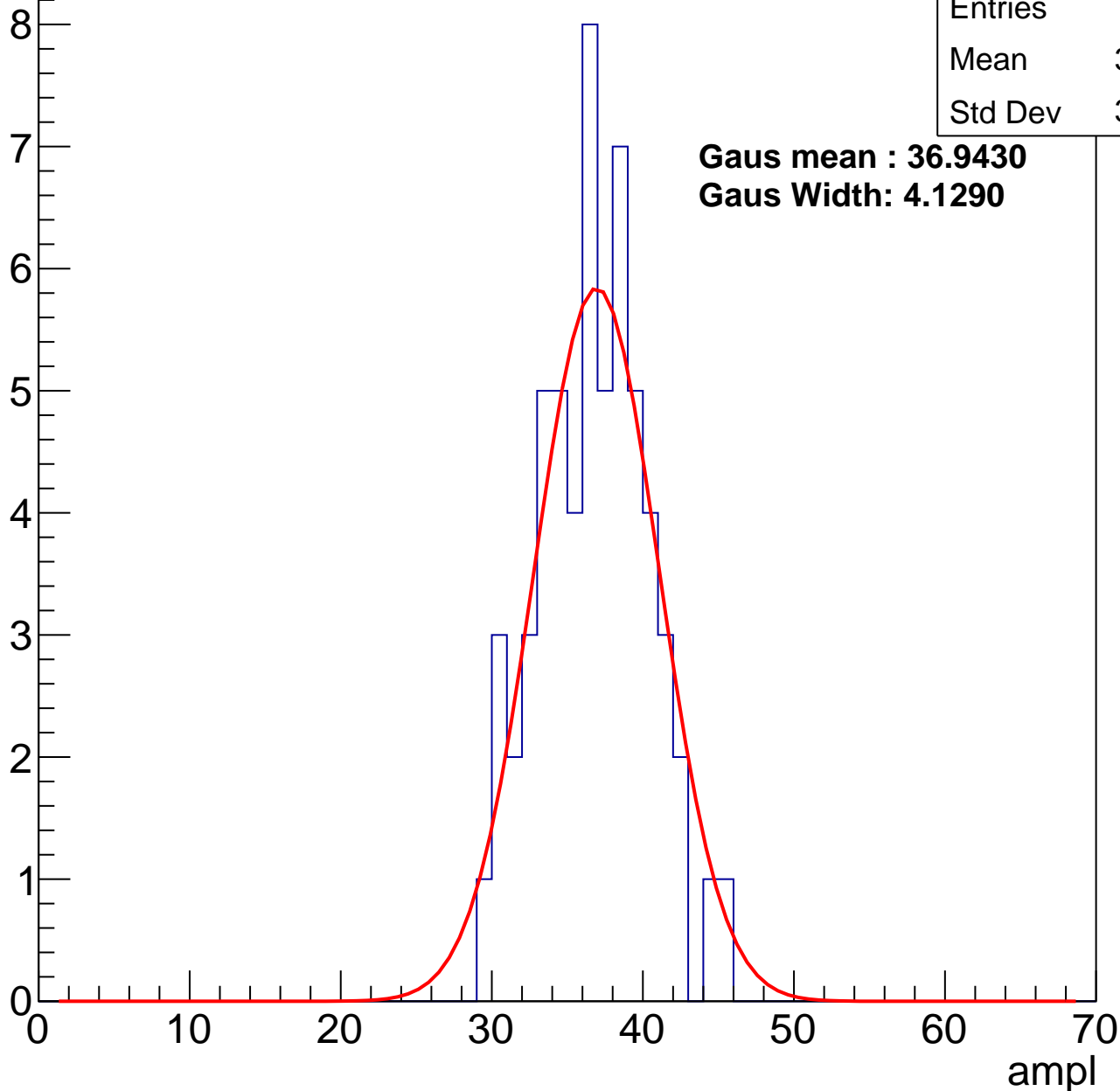
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.31
Std Dev	3.571

Gaus mean : 36.9430

Gaus Width: 4.1290

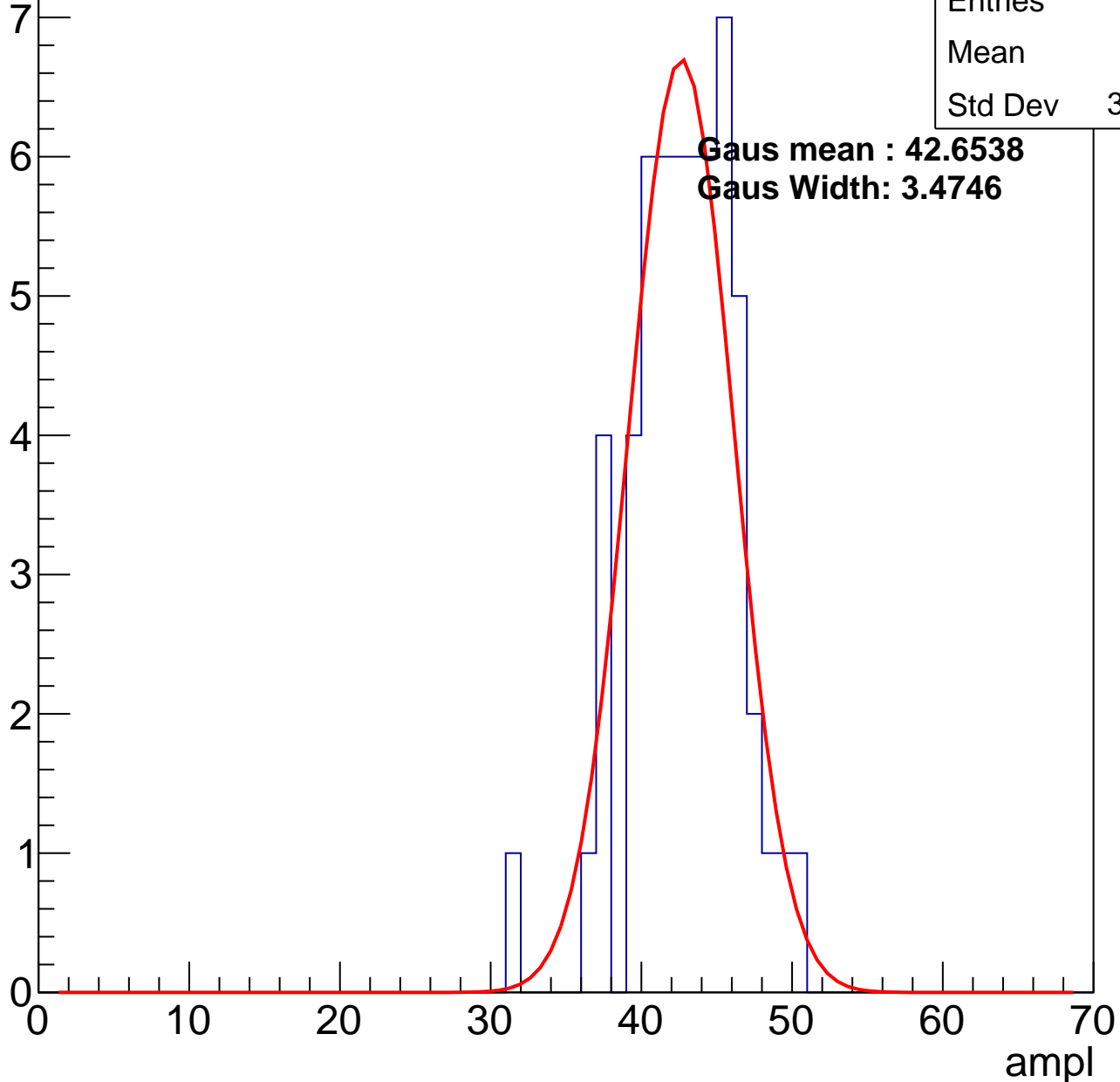


B1L101S, U1-ch124, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.4
Std Dev	3.468

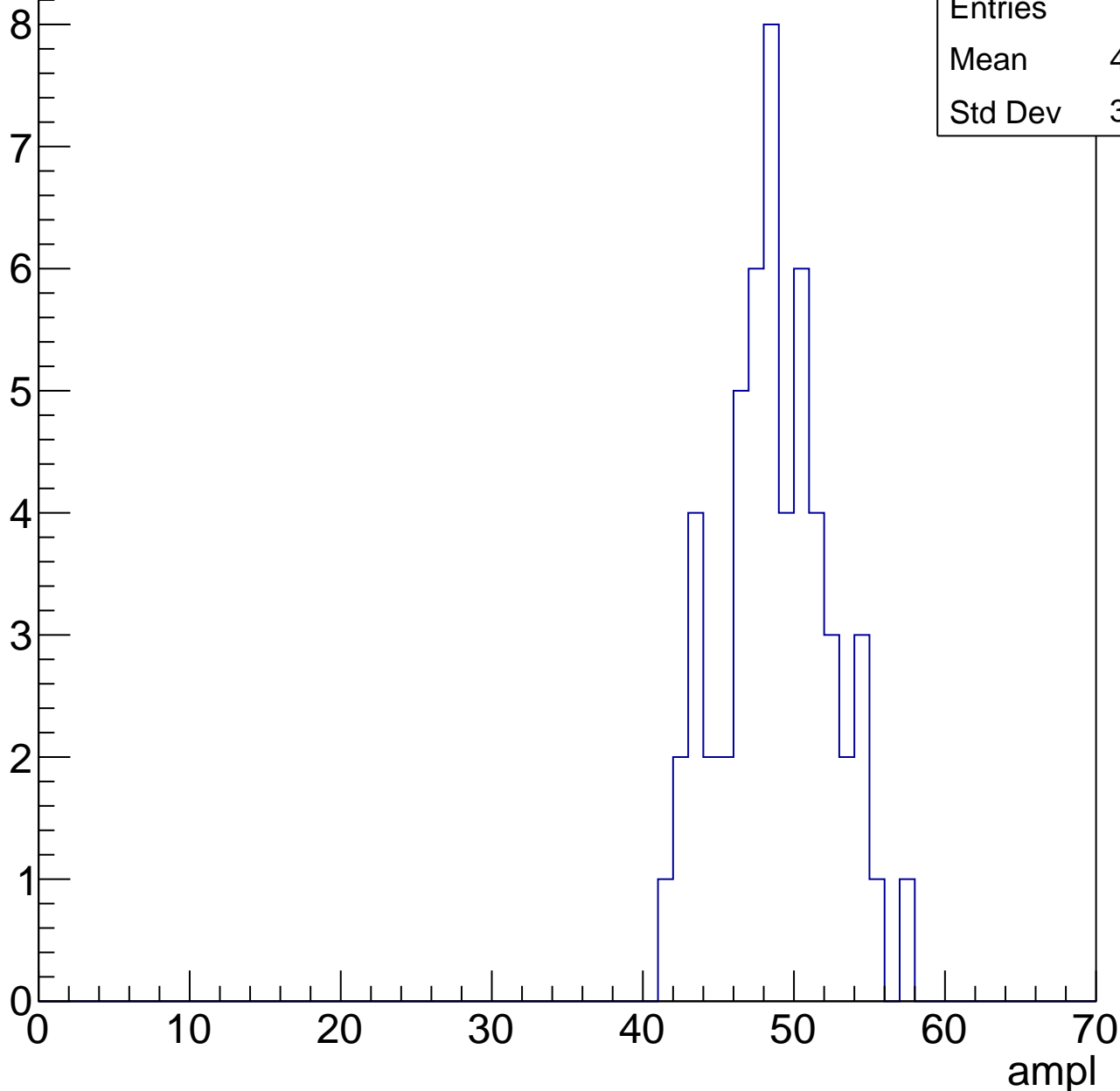


B1L101S, U1-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	48.28
Std Dev	3.582

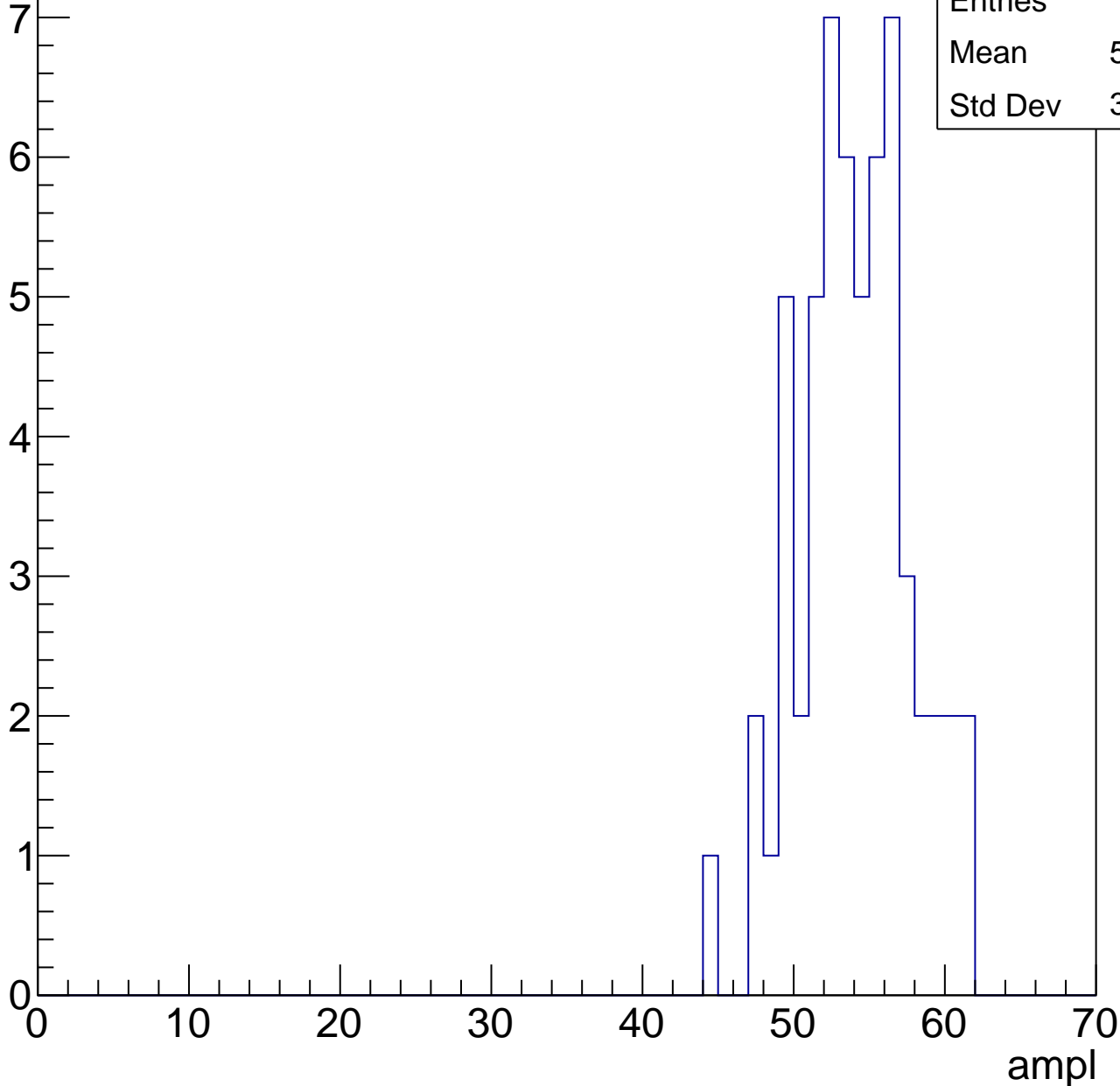


B1L101S, U1-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	53.57
Std Dev	3.649

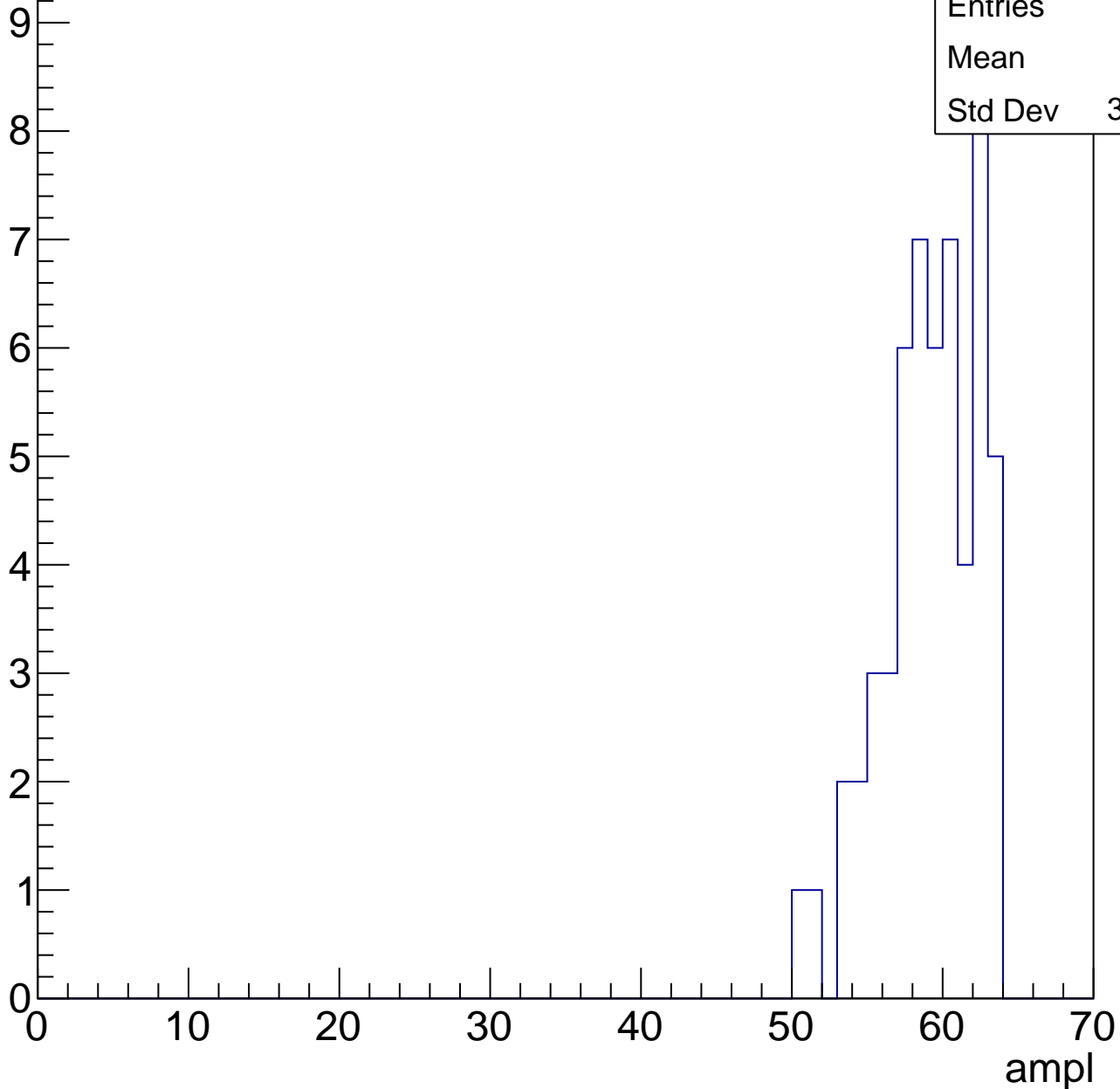


B1L101S, U1-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

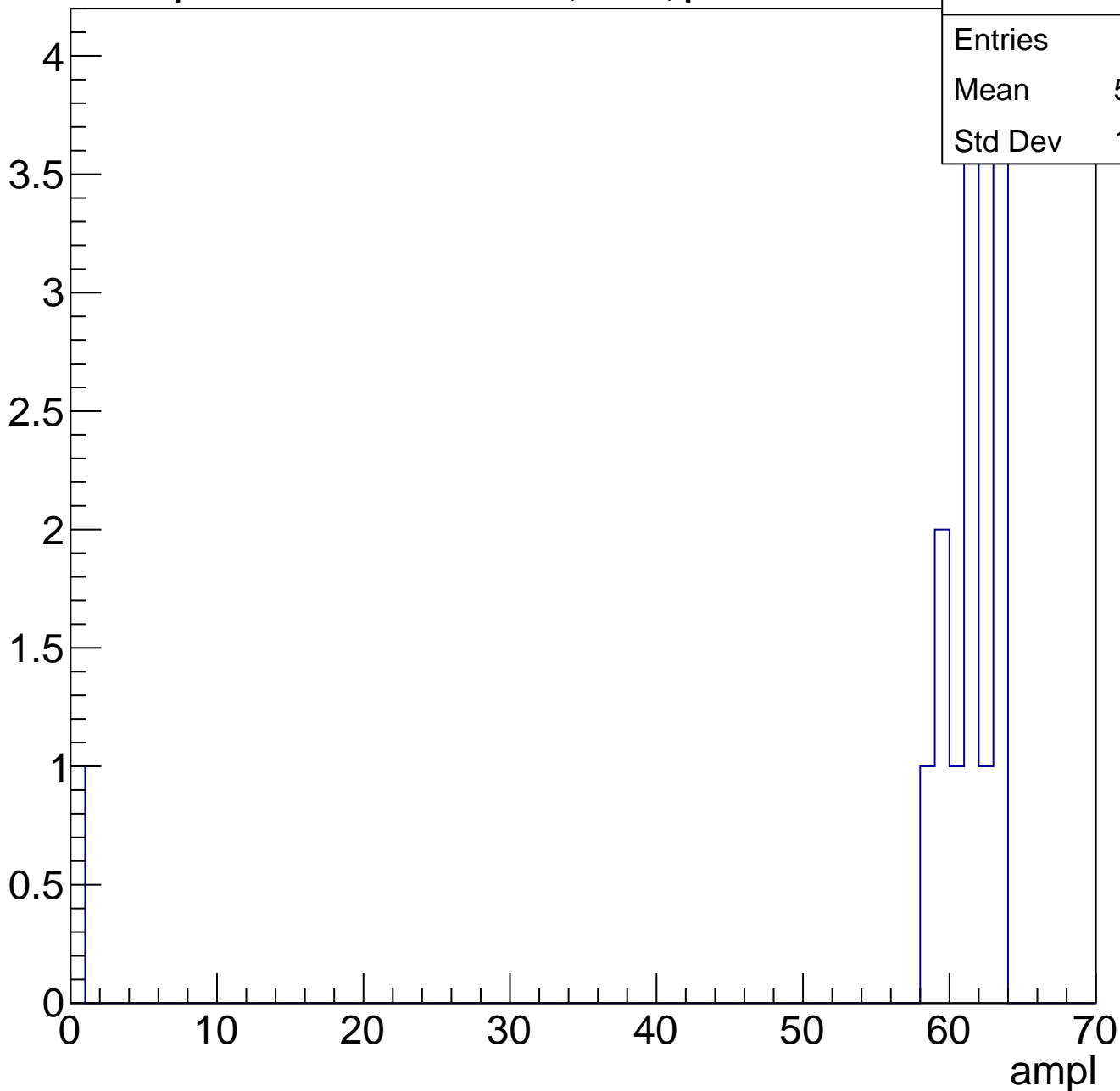
Entries	56
Mean	58.7
Std Dev	3.139



B1L101S, U1-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch125, adc0

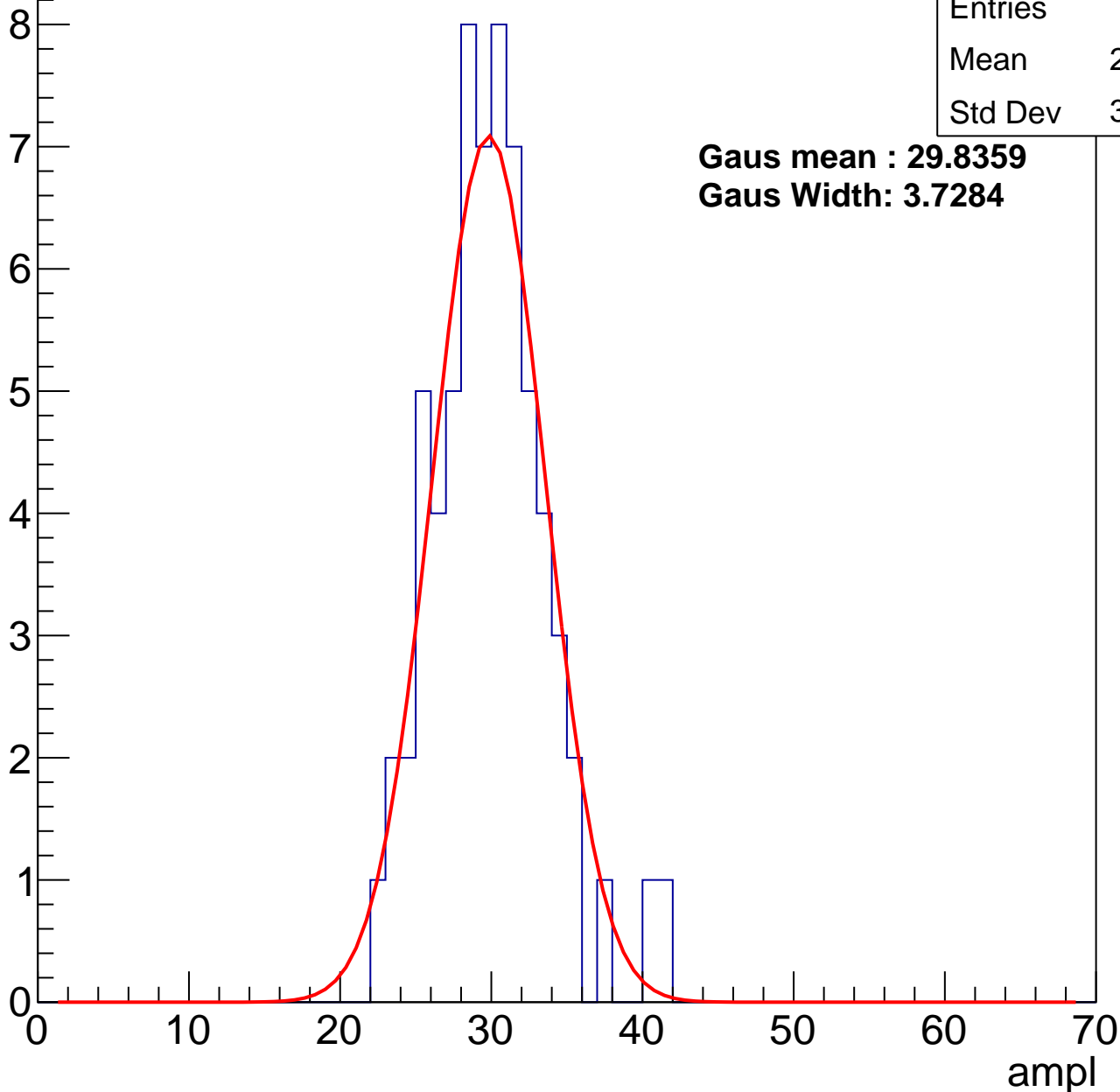
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.48
Std Dev	3.735

Gaus mean : 29.8359

Gaus Width: 3.7284



B1L101S, U1-ch125, adc1

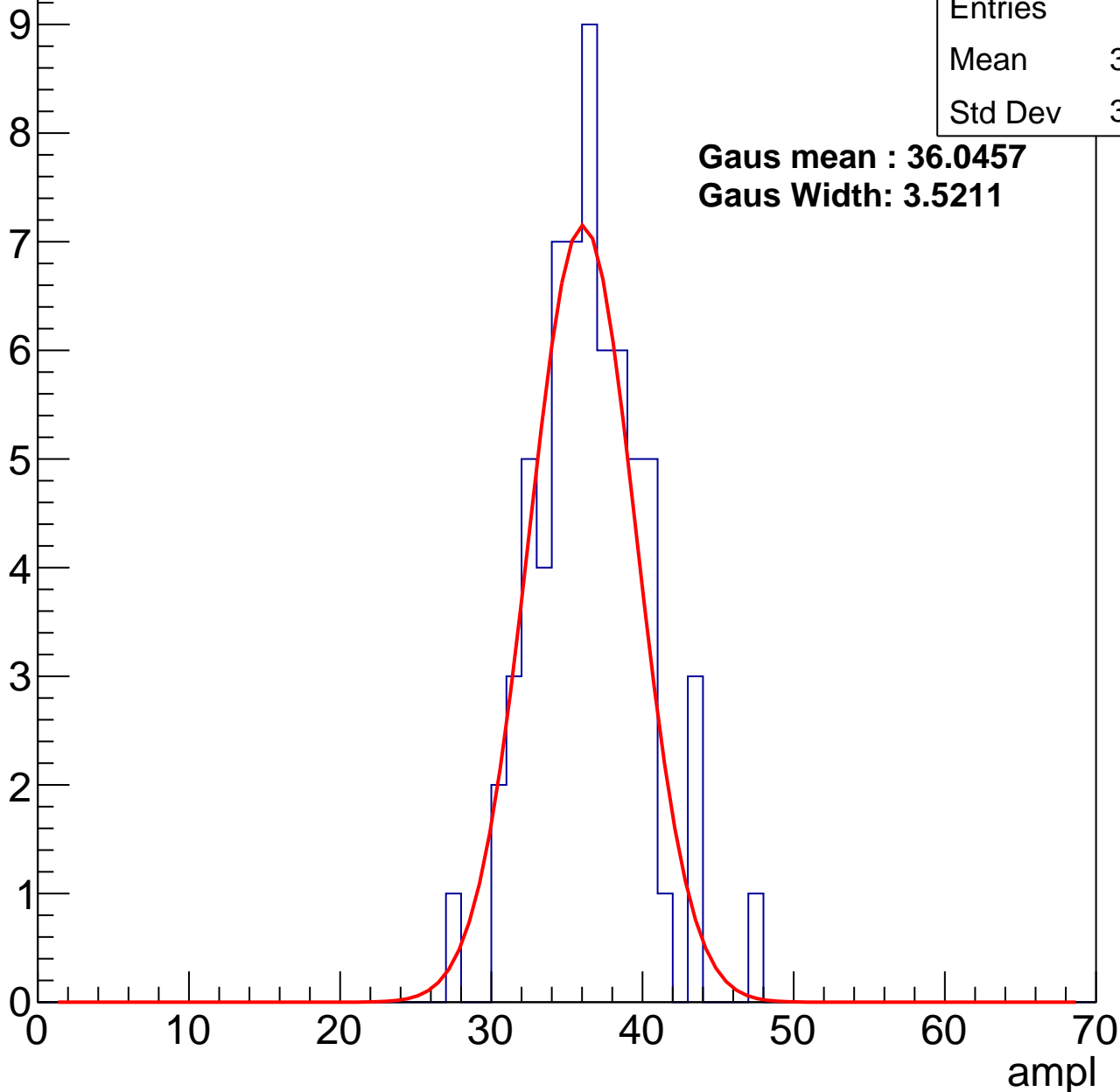
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.02
Std Dev	3.567

Gaus mean : 36.0457

Gaus Width: 3.5211



B1L101S, U1-ch125, adc2

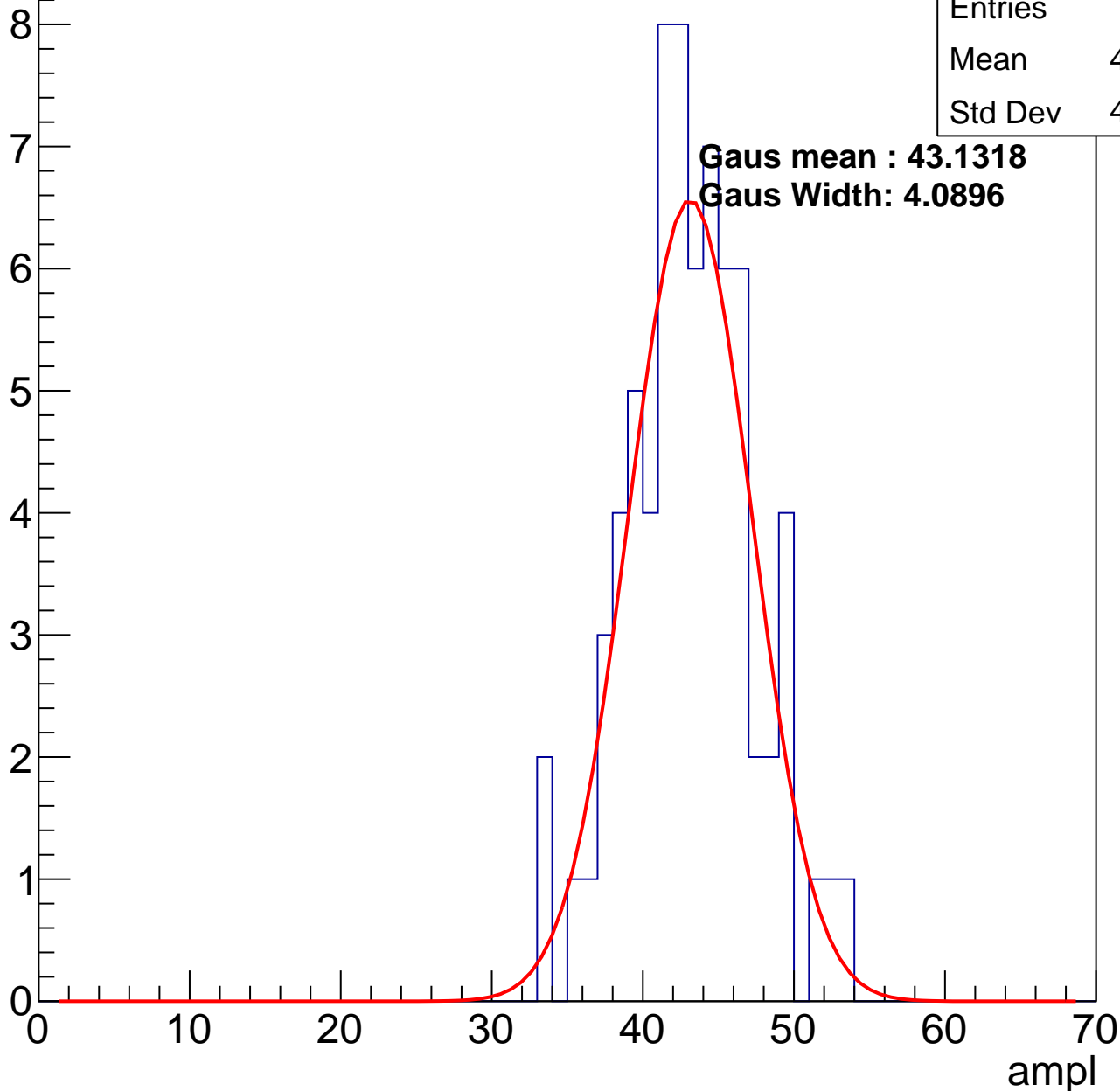
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.68
Std Dev	4.126

Gaus mean : 43.1318

Gaus Width: 4.0896

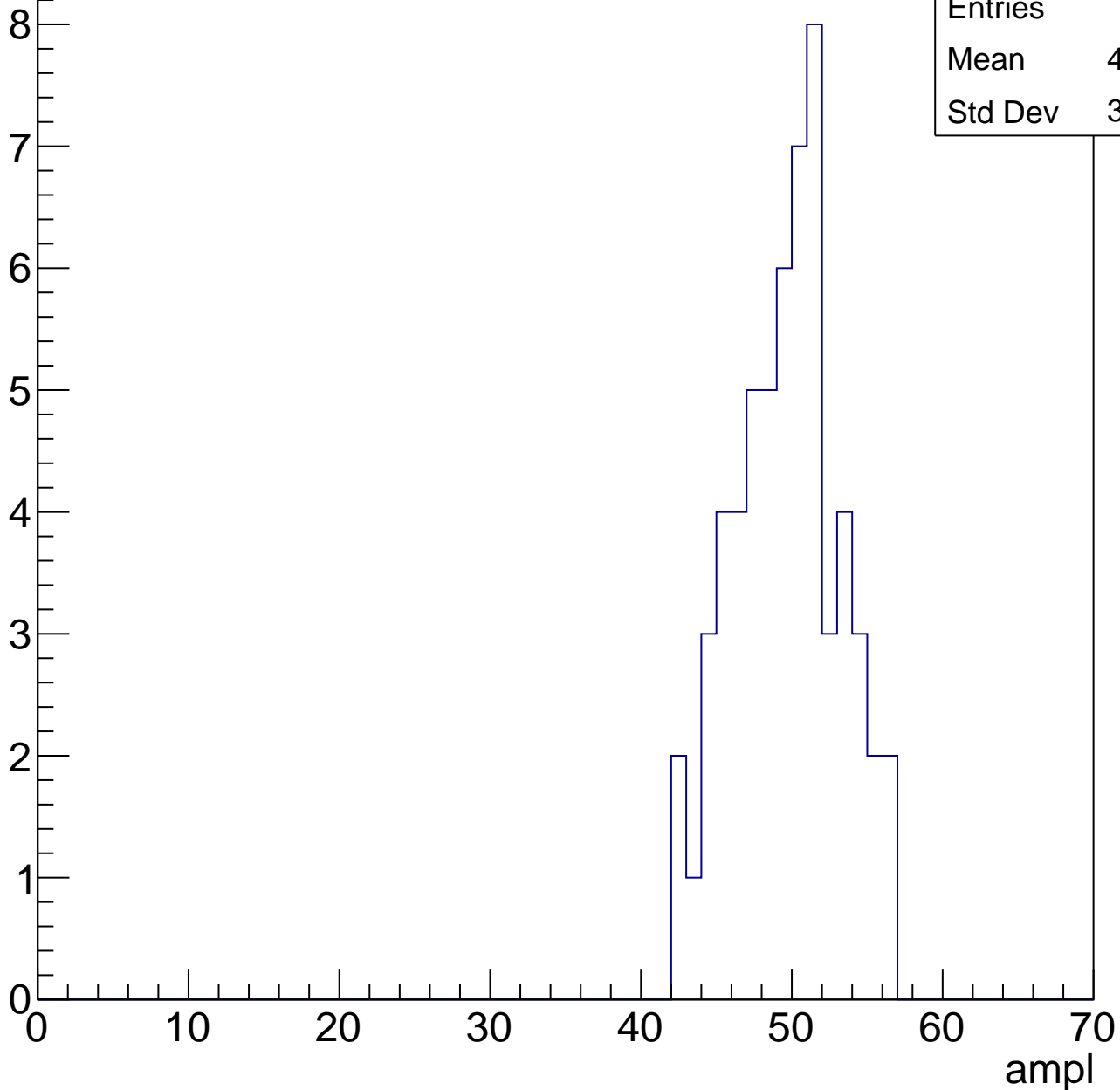


B1L101S, U1-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

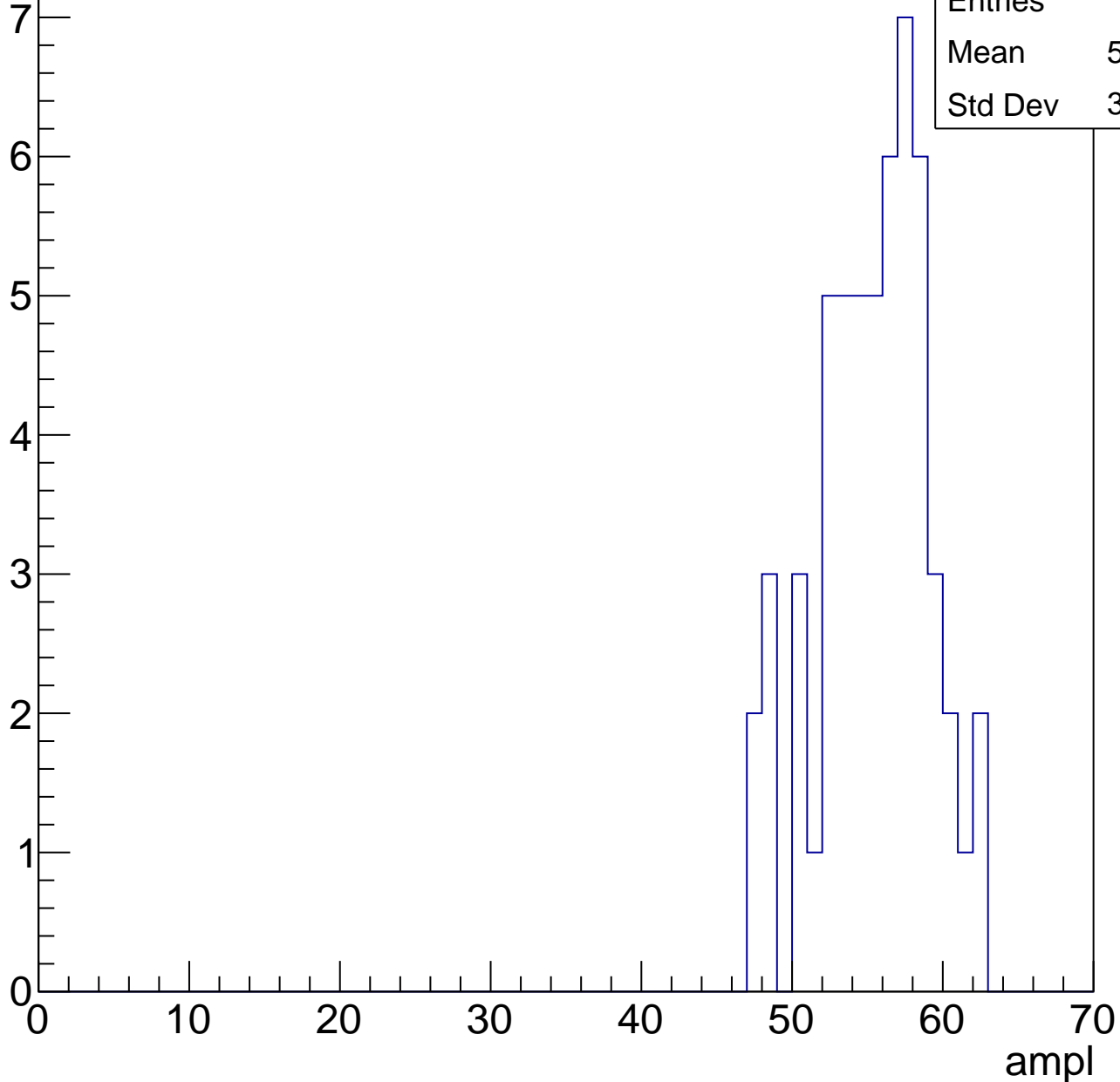
Entries	59
Mean	49.19
Std Dev	3.462



B1L101S, U1-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch125, adc5

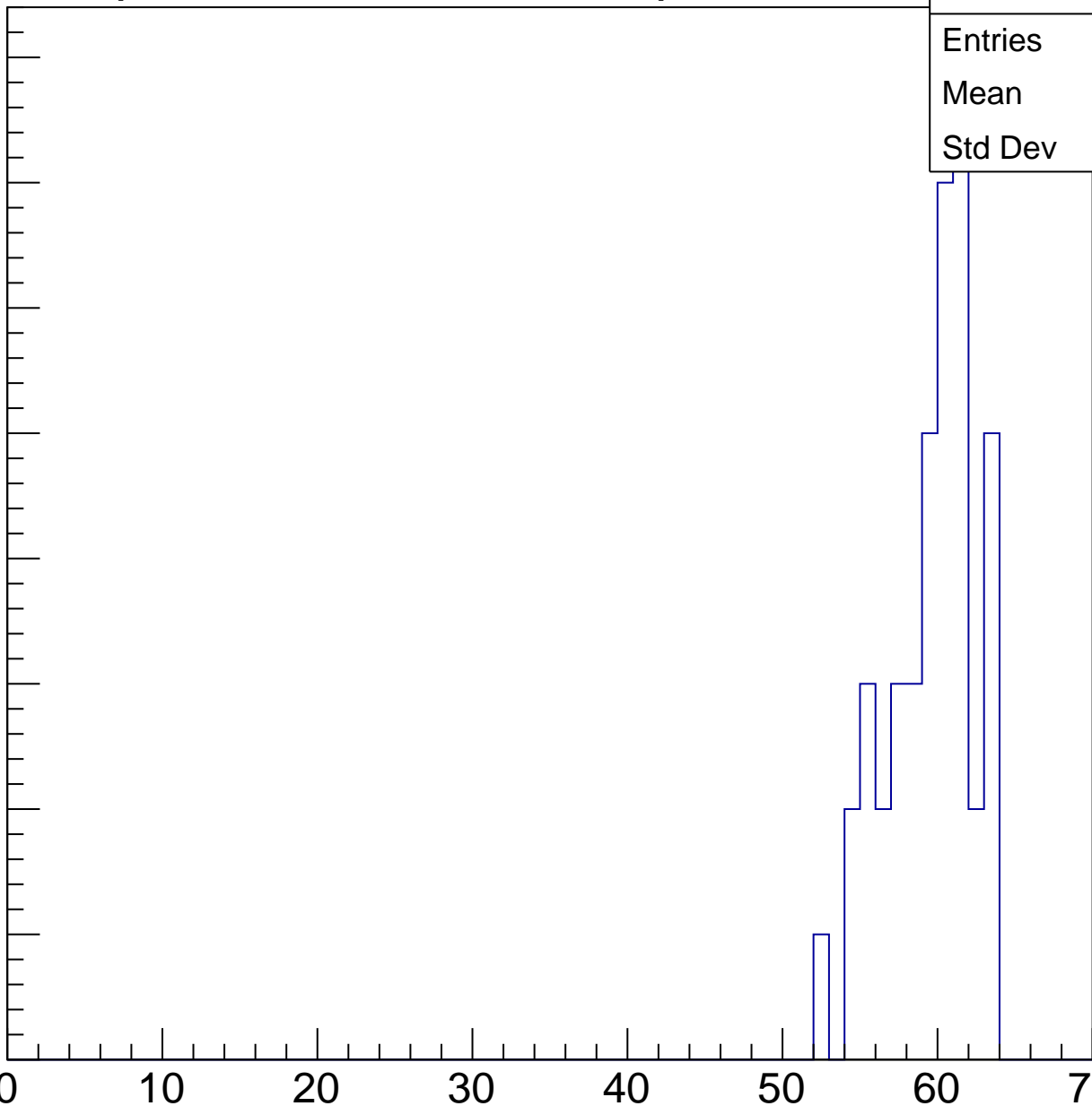
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.12
Std Dev	2.778

ampl

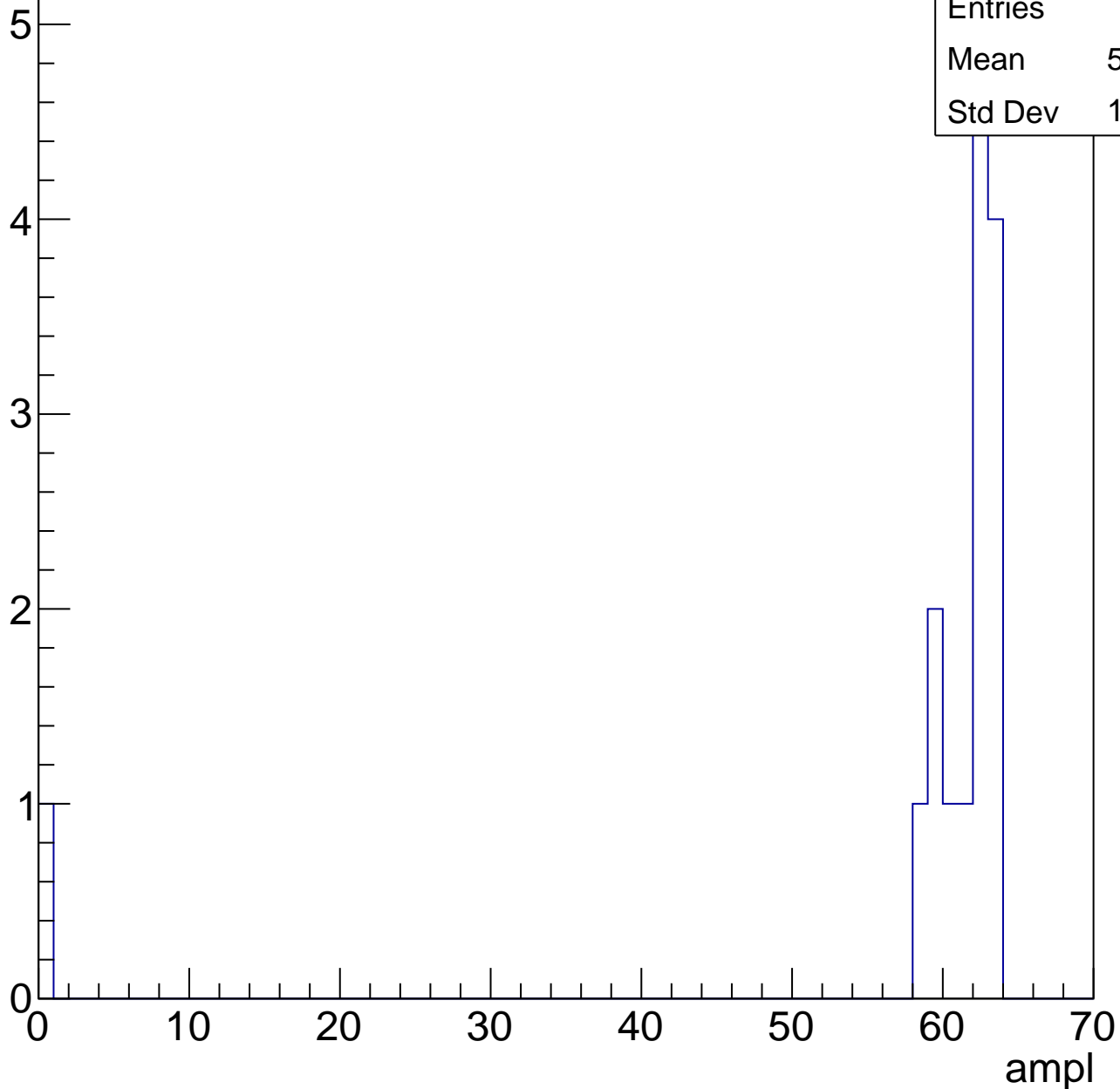


B1L101S, U1-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.27
Std Dev	15.39



B1L101S, U1-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch126, adc0

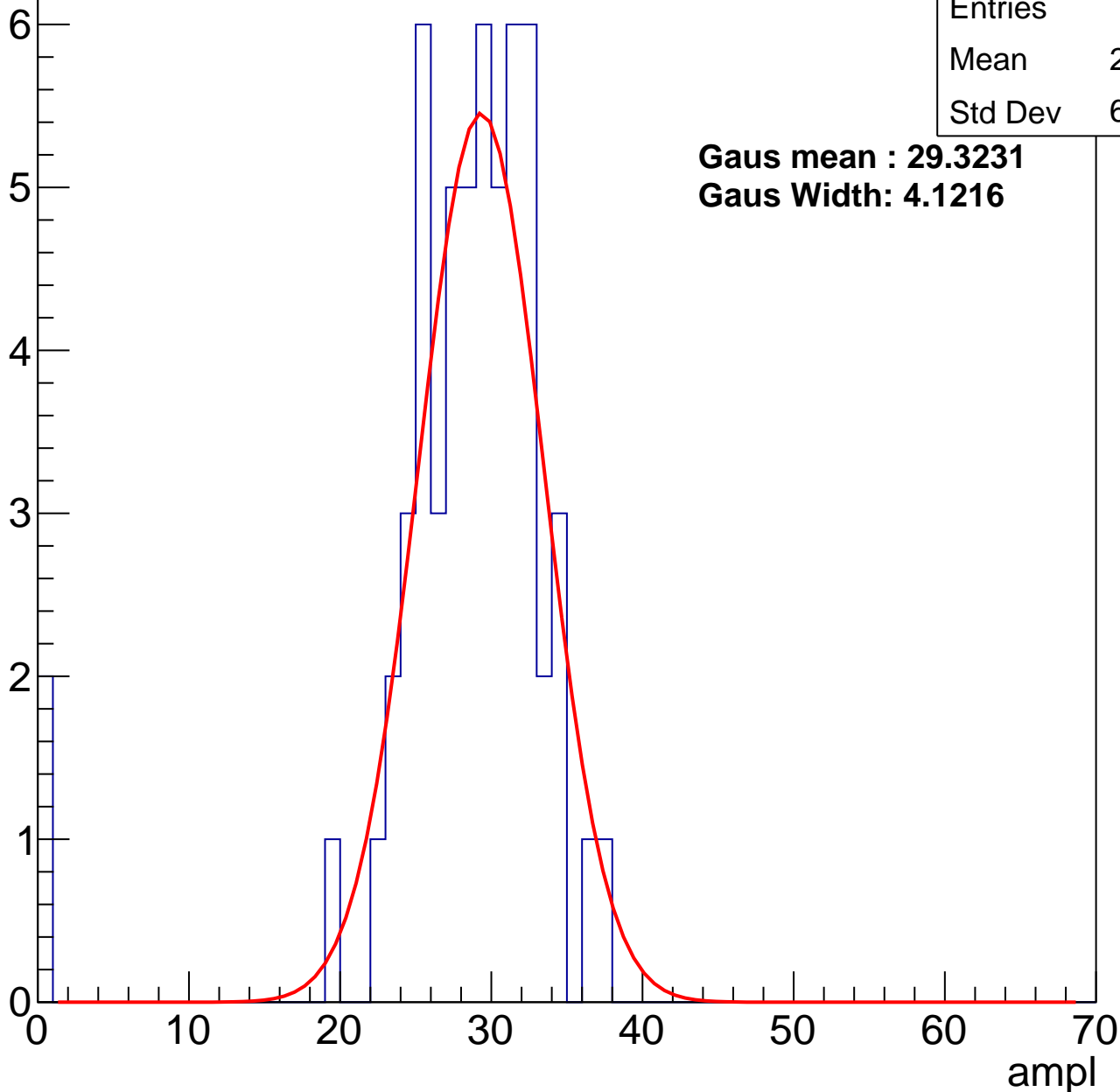
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	27.67
Std Dev	6.334

Gaus mean : 29.3231

Gaus Width: 4.1216



B1L101S, U1-ch126, adc1

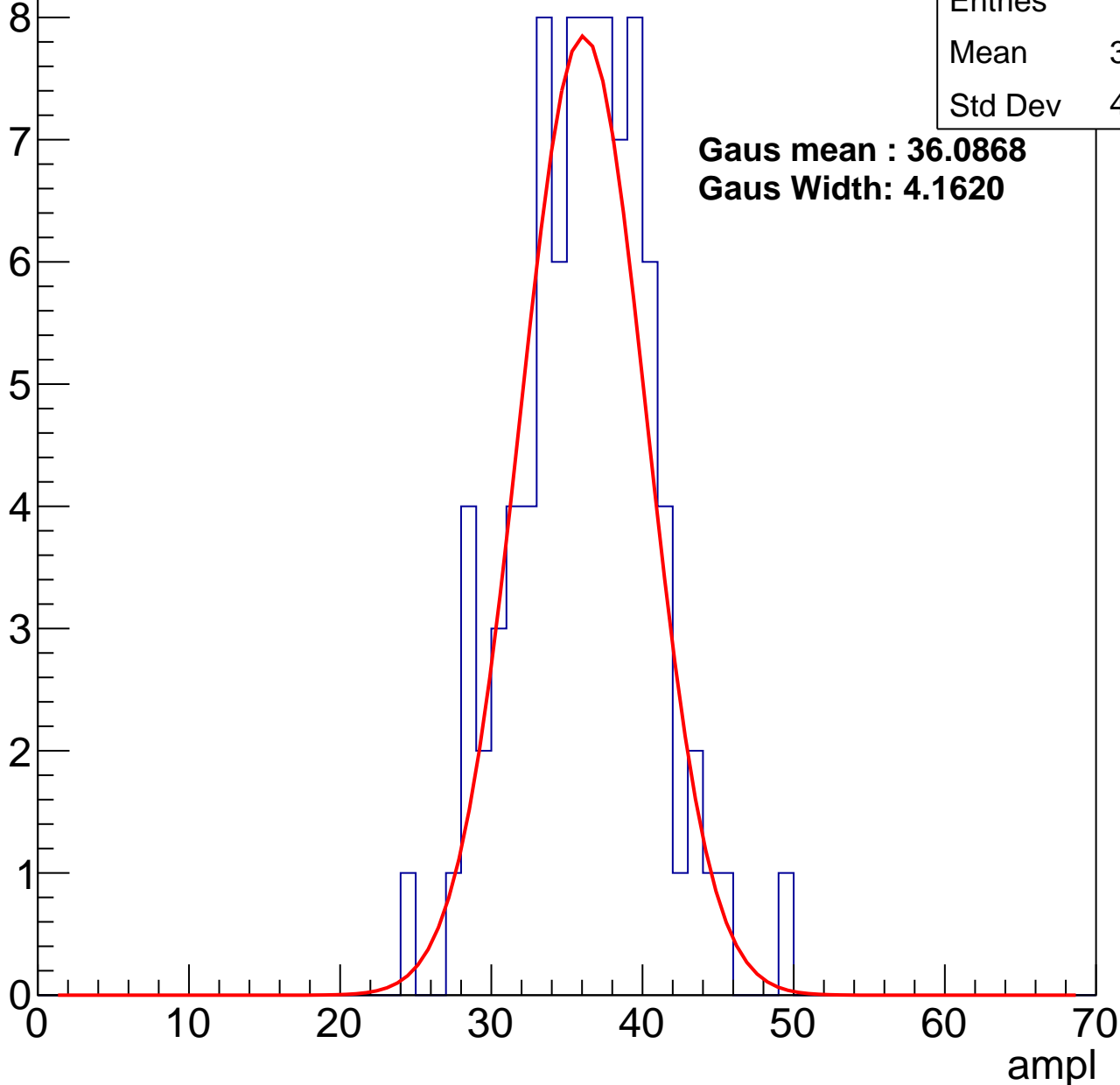
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	35.72
Std Dev	4.395

Gaus mean : 36.0868

Gaus Width: 4.1620



B1L101S, U1-ch126, adc2

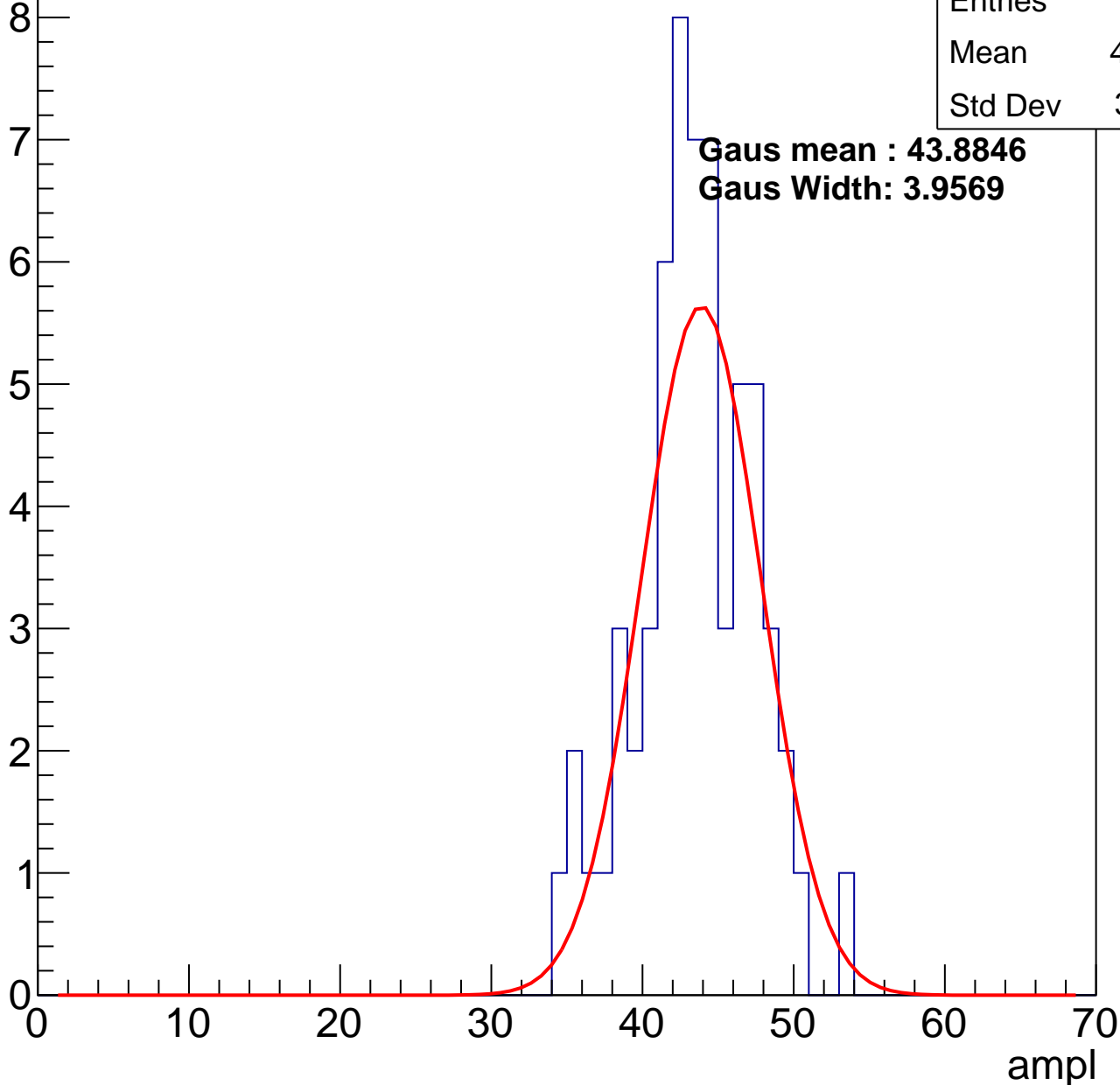
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.03
Std Dev	3.841

Gaus mean : 43.8846

Gaus Width: 3.9569

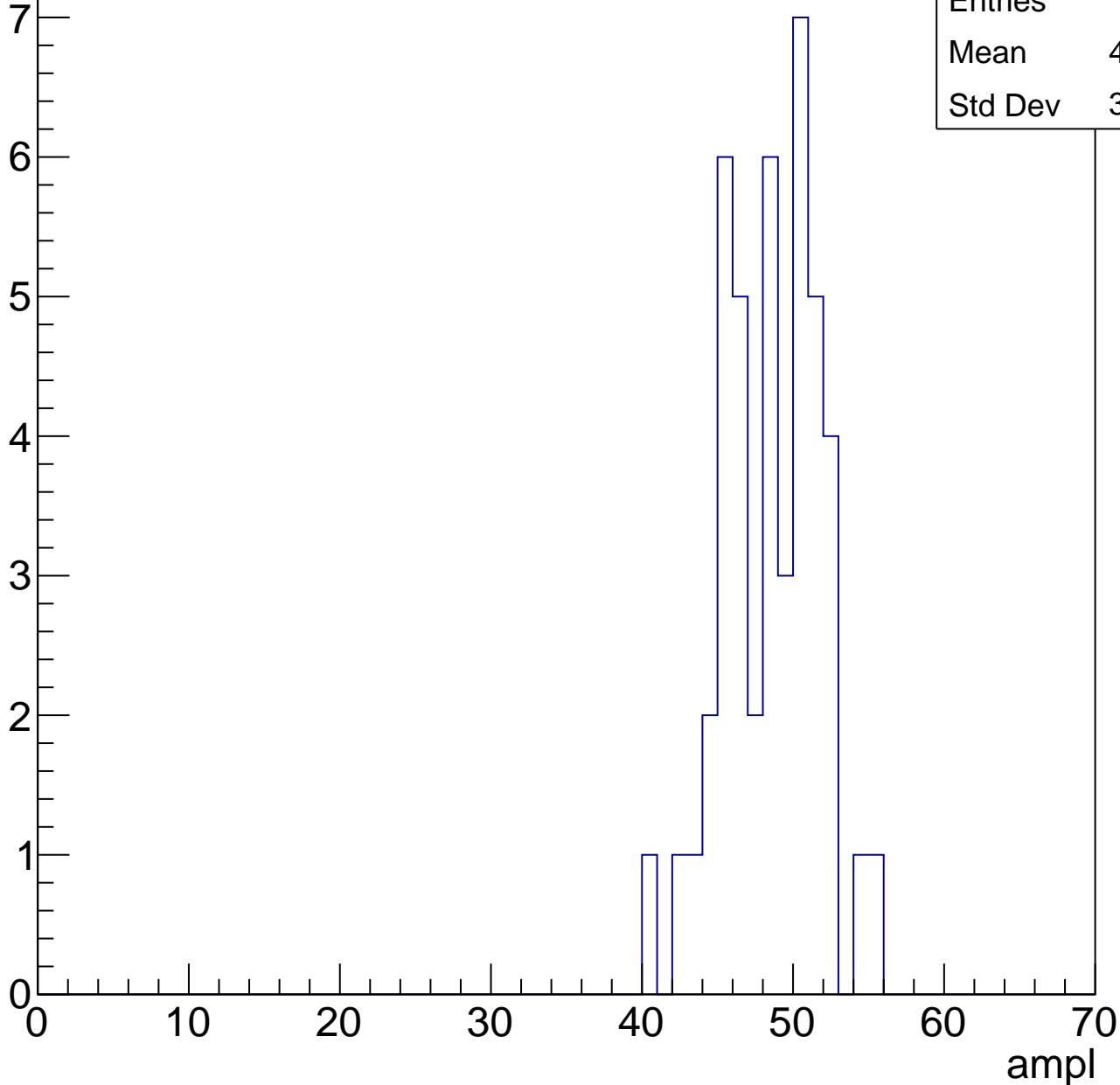


B1L101S, U1-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	48.09
Std Dev	3.189

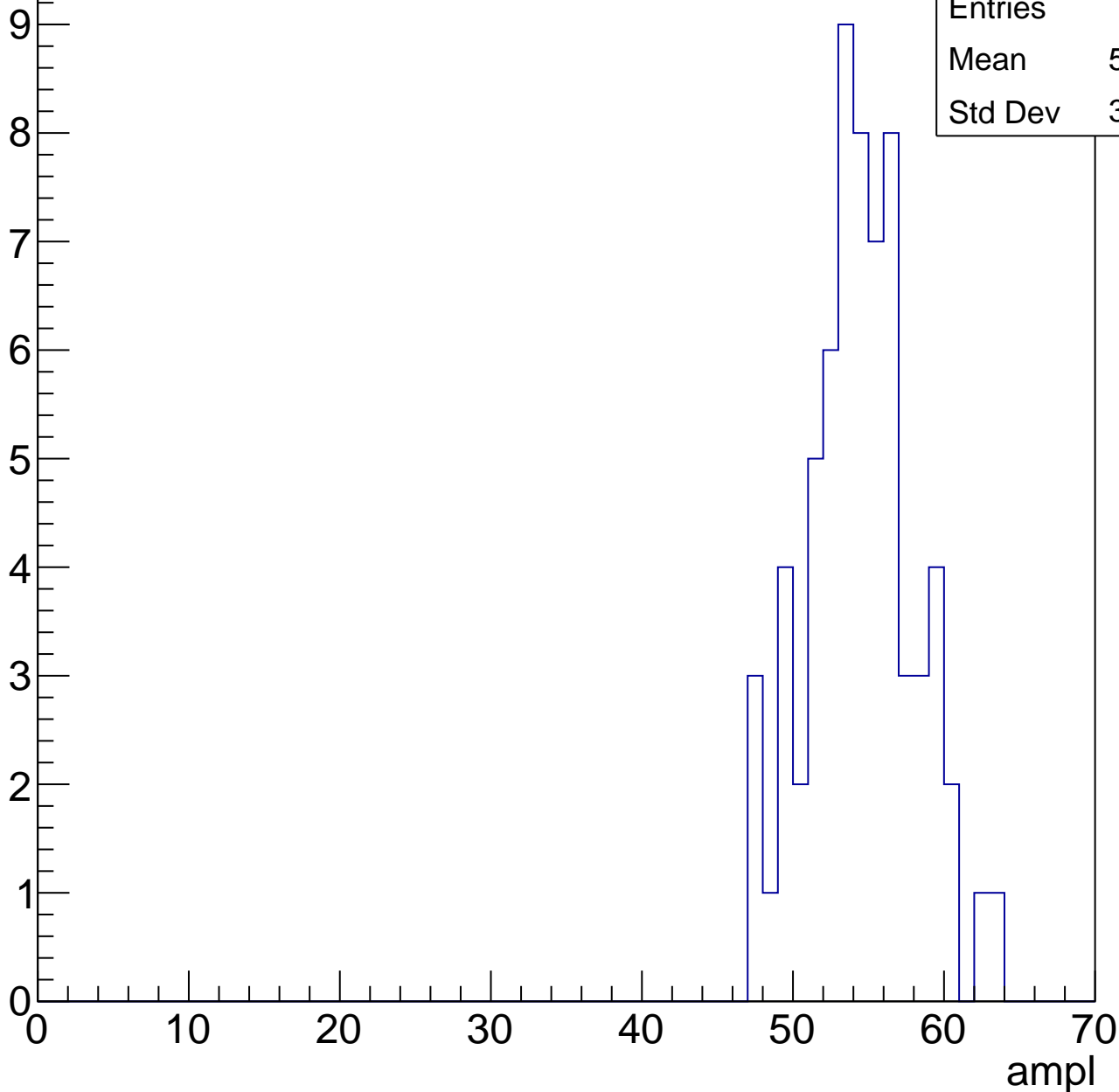


B1L101S, U1-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	54.03
Std Dev	3.507

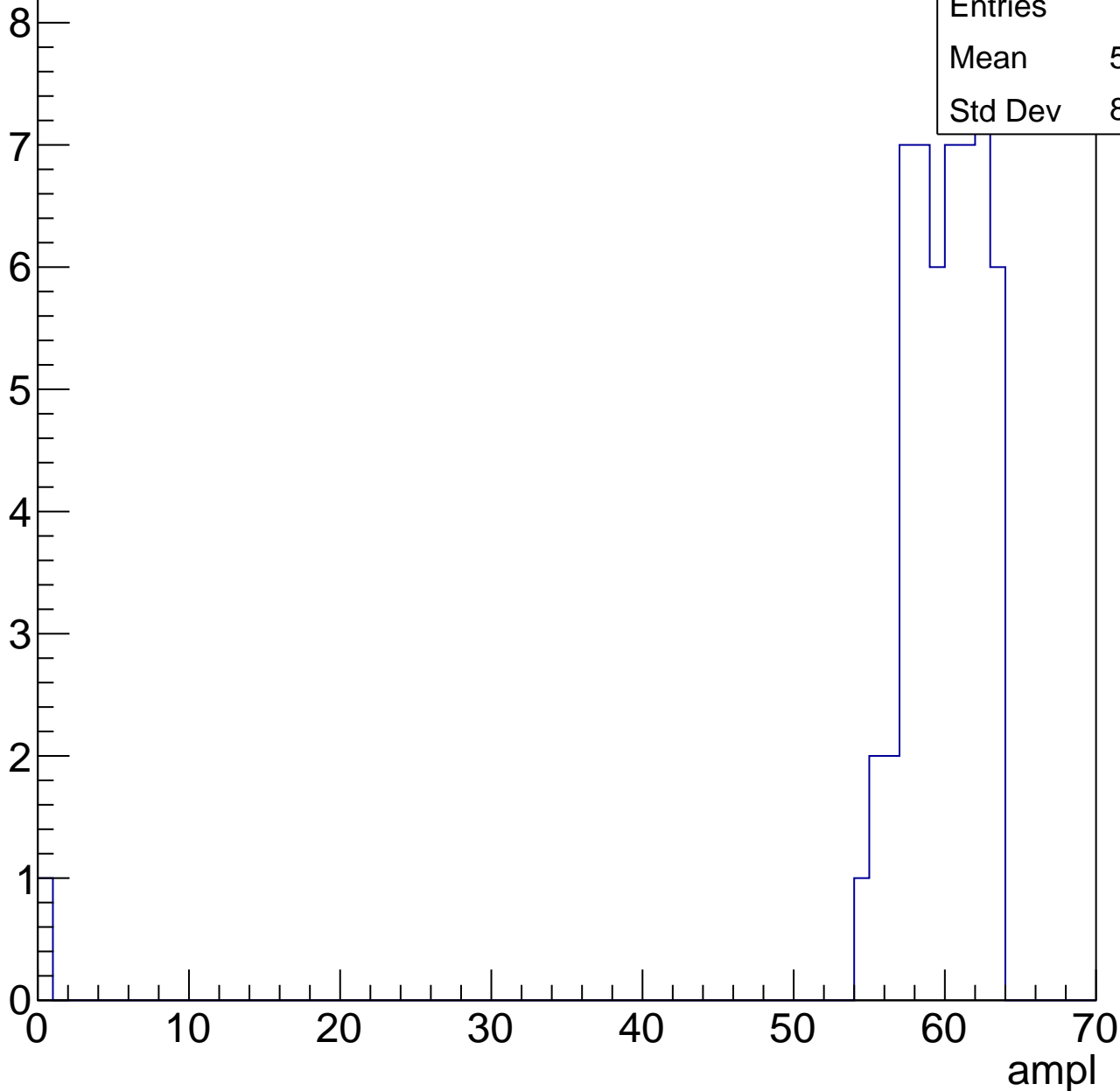


B1L101S, U1-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

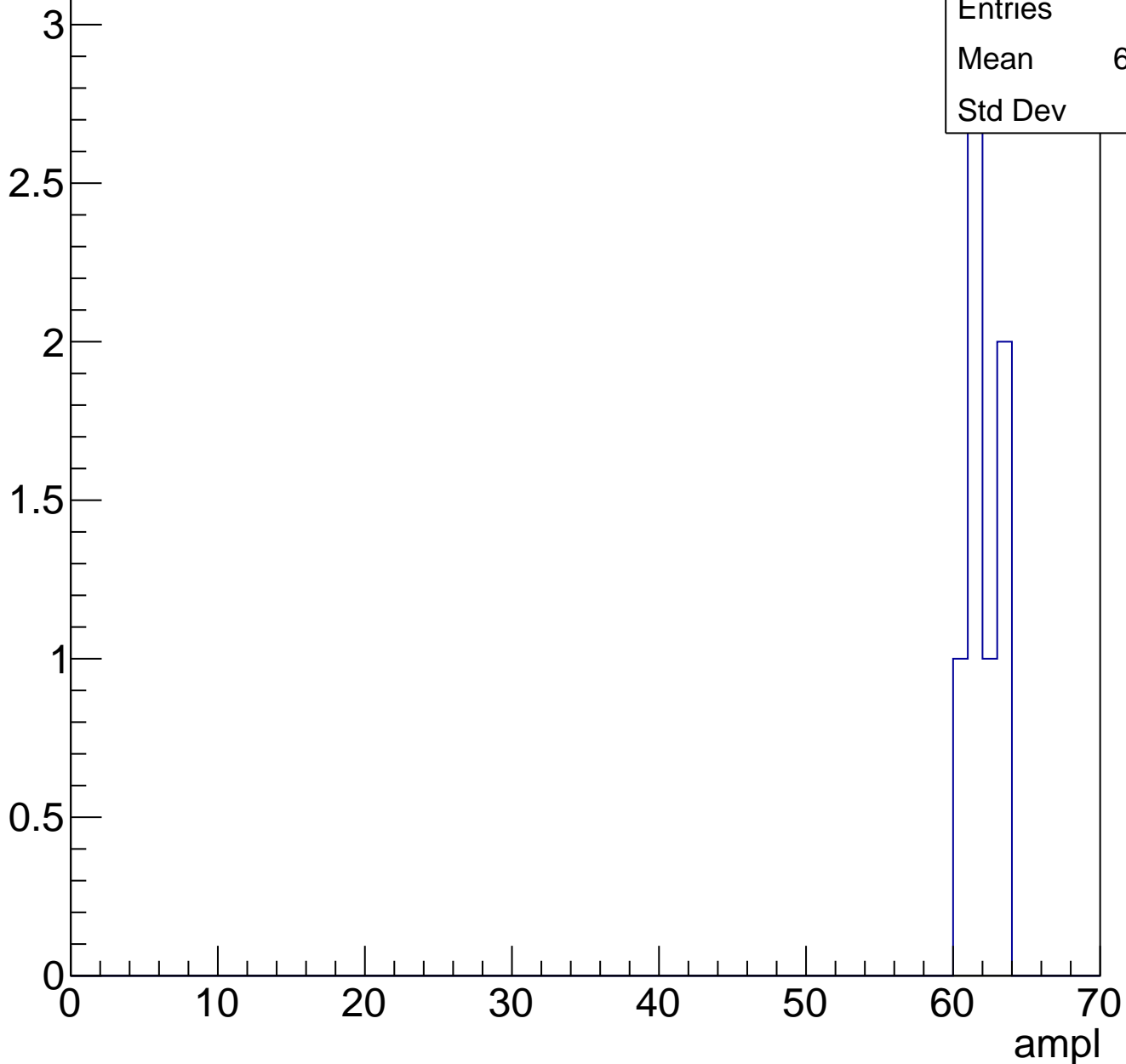
Entries	54
Mean	58.44
Std Dev	8.364



B1L101S, U1-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch127, adc0

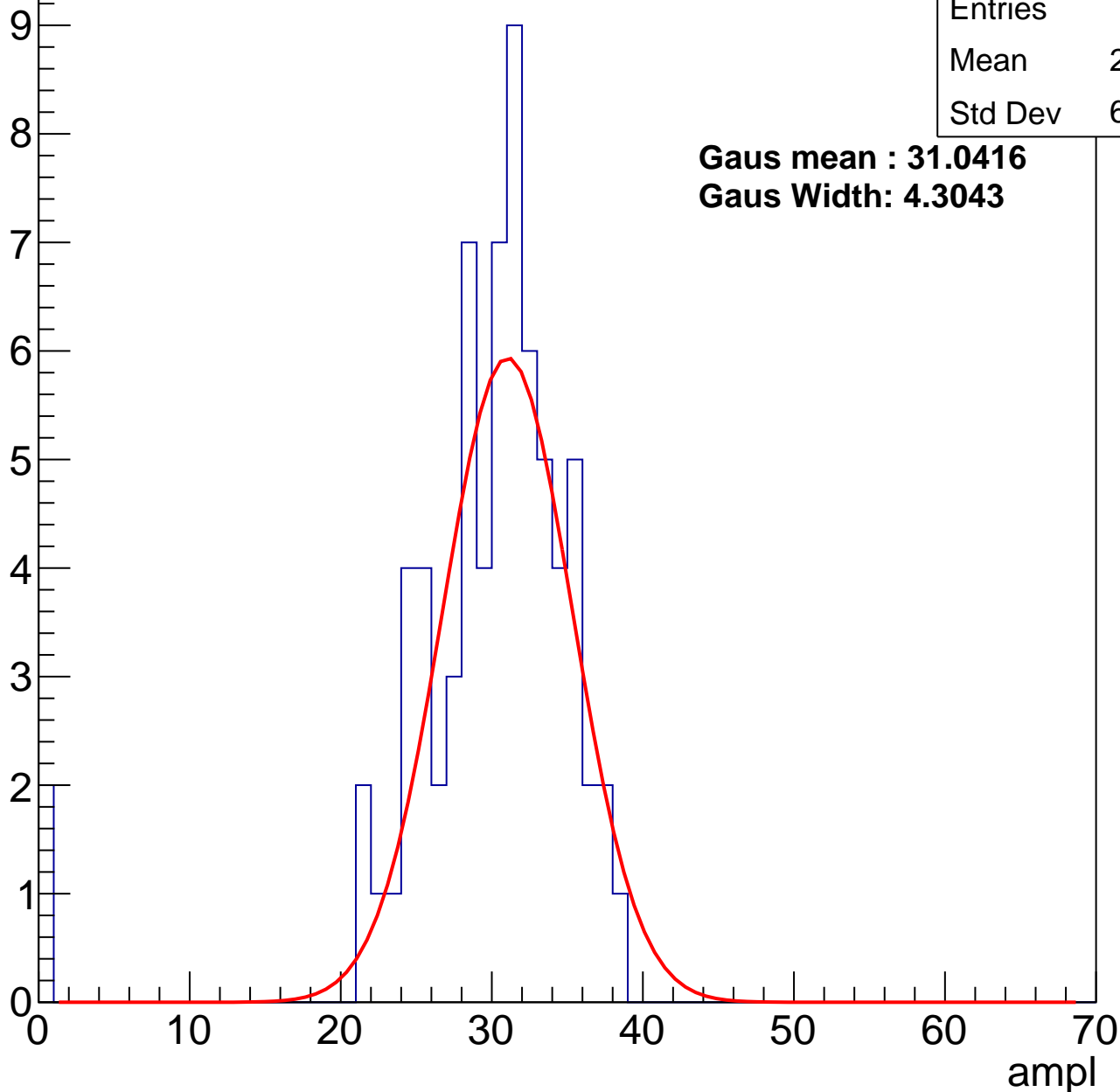
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.14
Std Dev	6.345

Gaus mean : 31.0416

Gaus Width: 4.3043



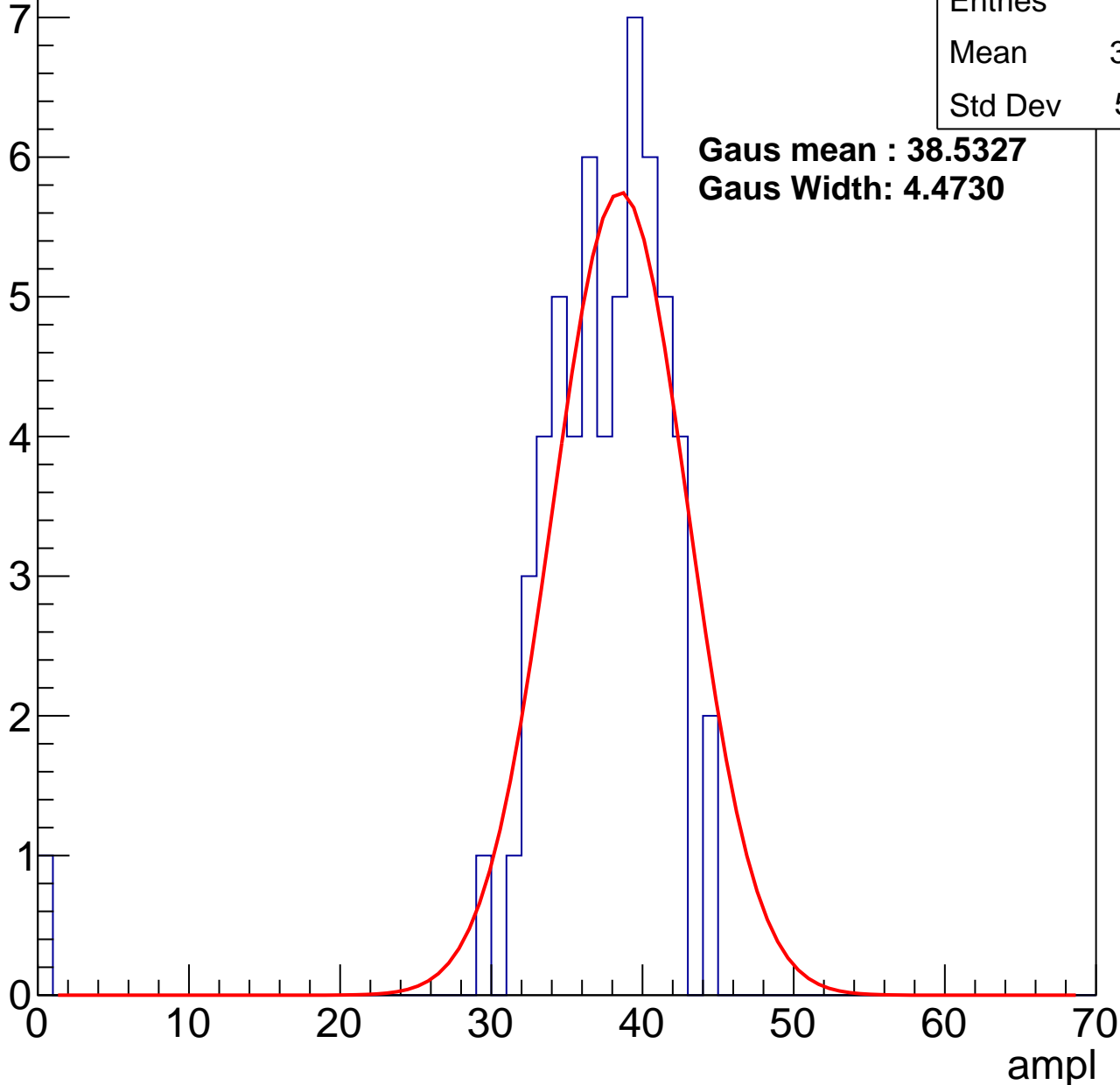
B1L101S, U1-ch127, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.66
Std Dev	5.921

Gaus mean : 38.5327
Gaus Width: 4.4730



B1L101S, U1-ch127, adc2

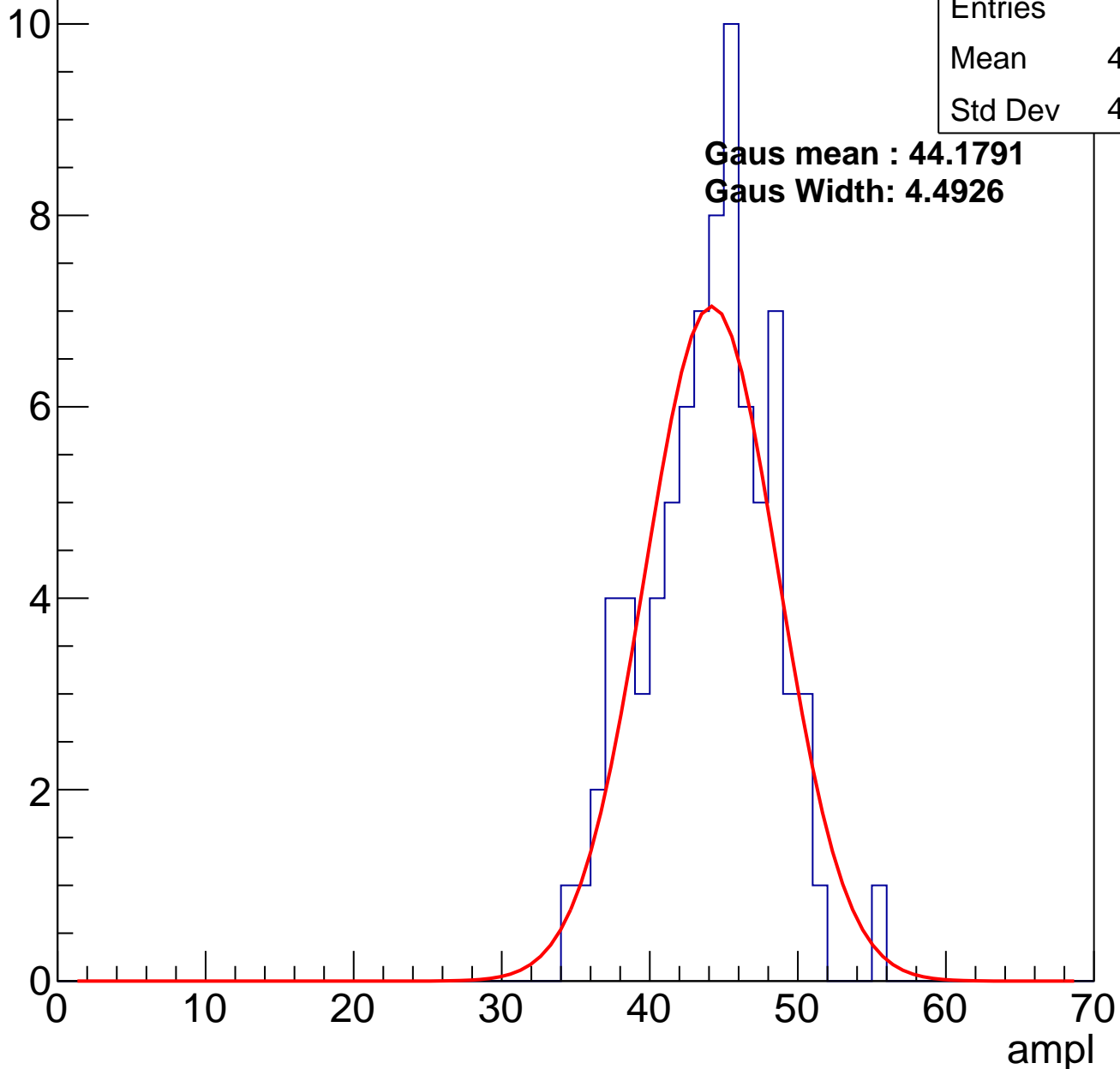
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	43.56
Std Dev	4.146

Gaus mean : 44.1791

Gaus Width: 4.4926

Entry

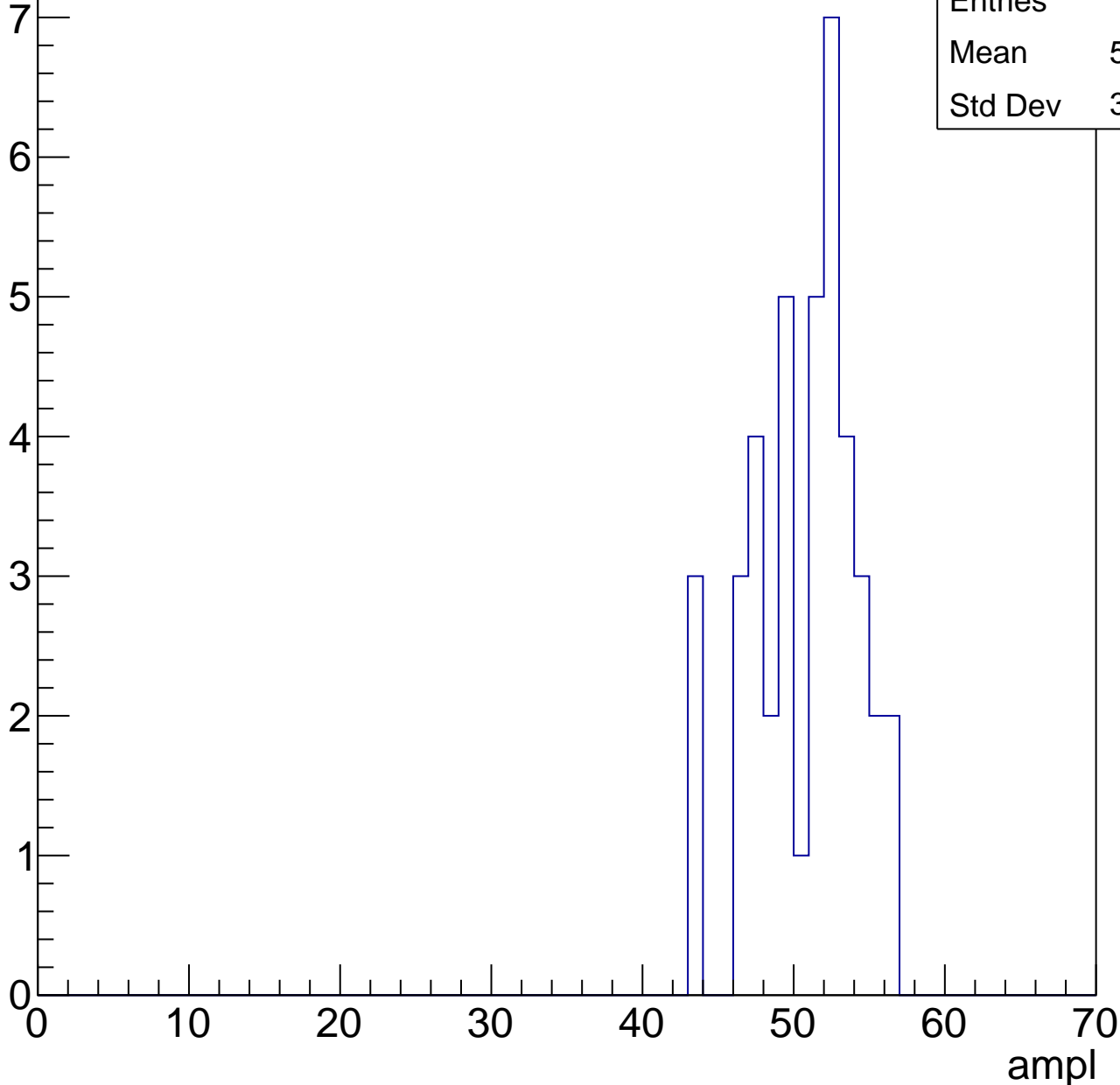


B1L101S, U1-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	50.27
Std Dev	3.422

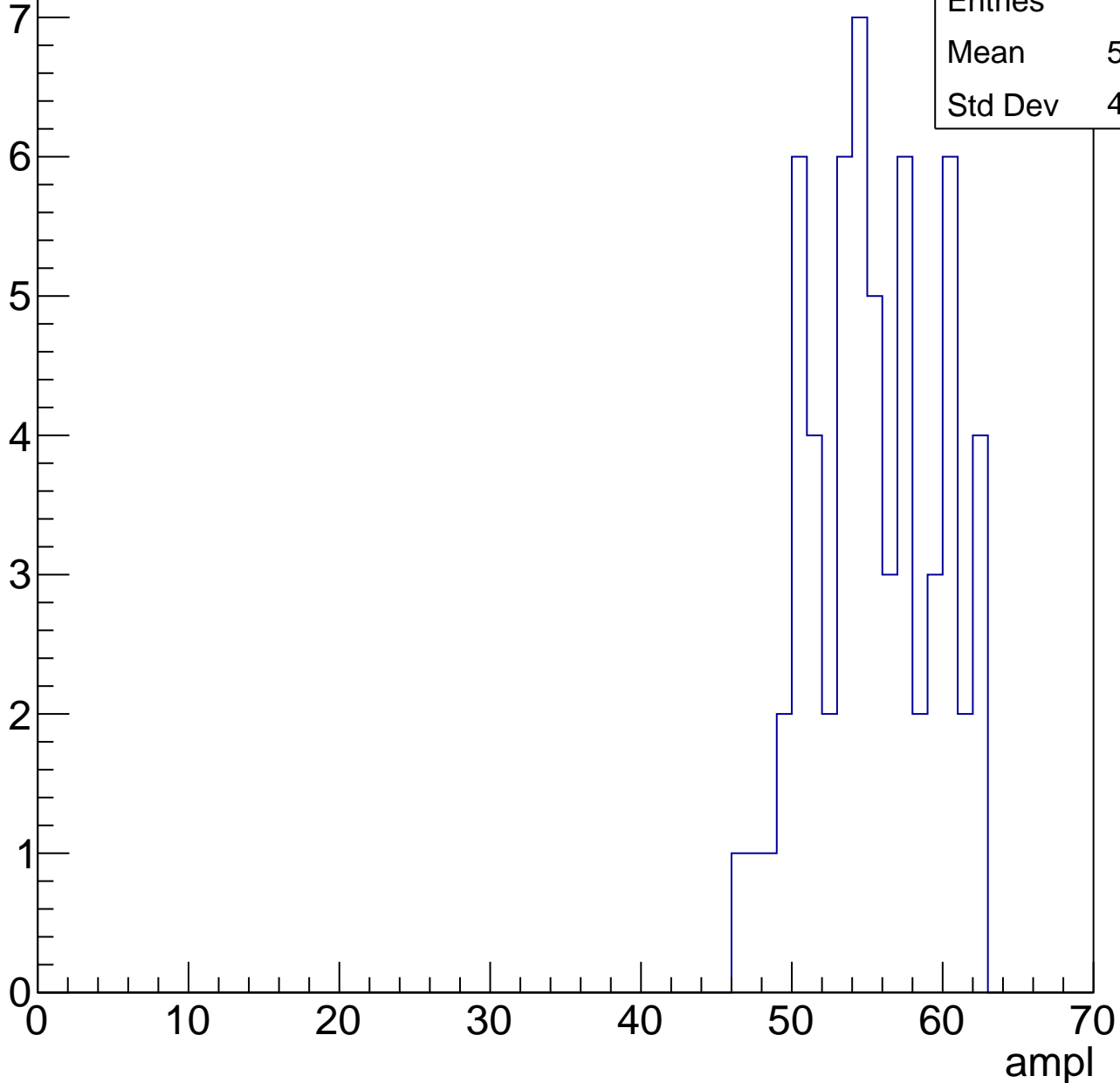


B1L101S, U1-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	54.93
Std Dev	4.129

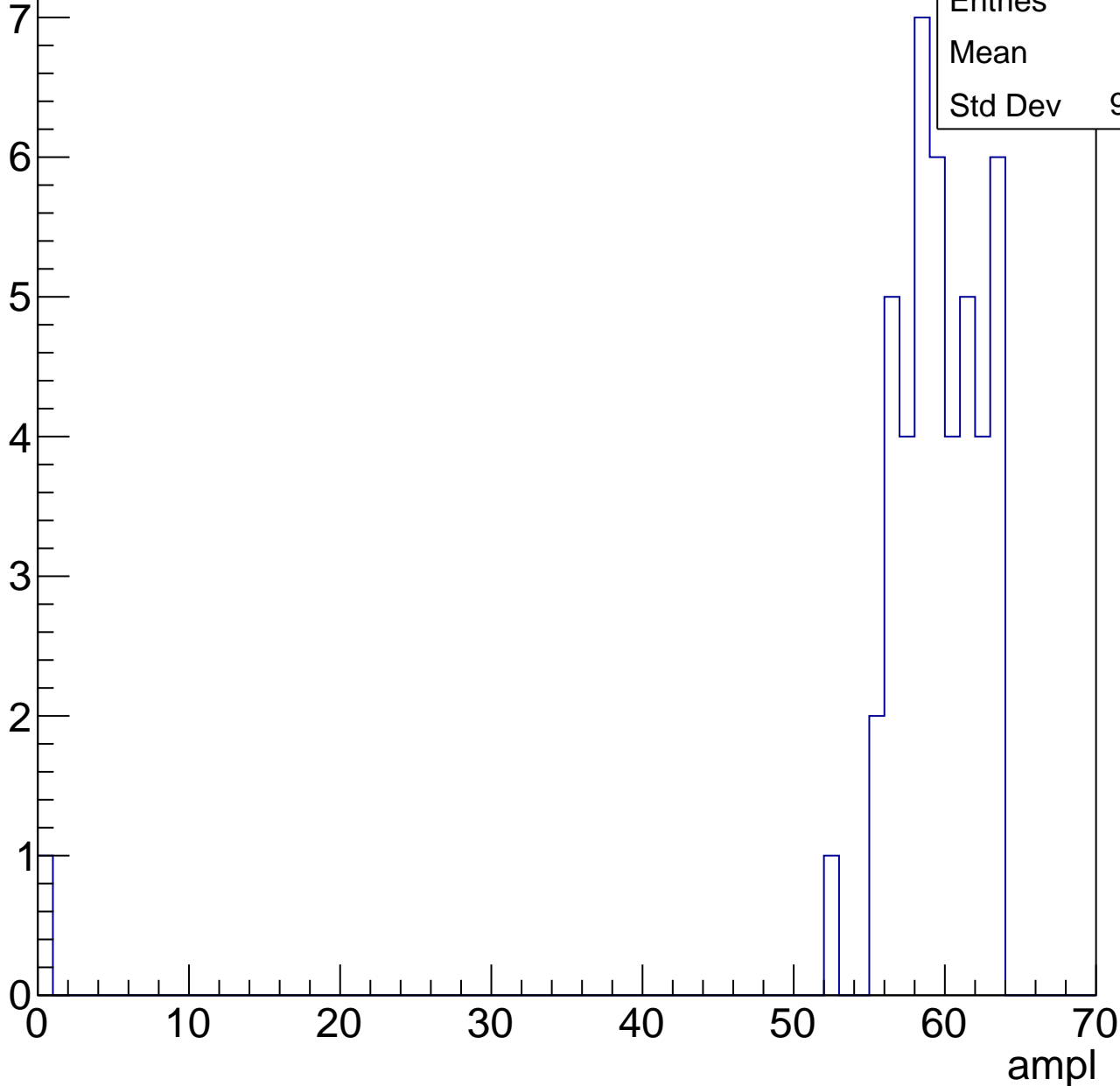


B1L101S, U1-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

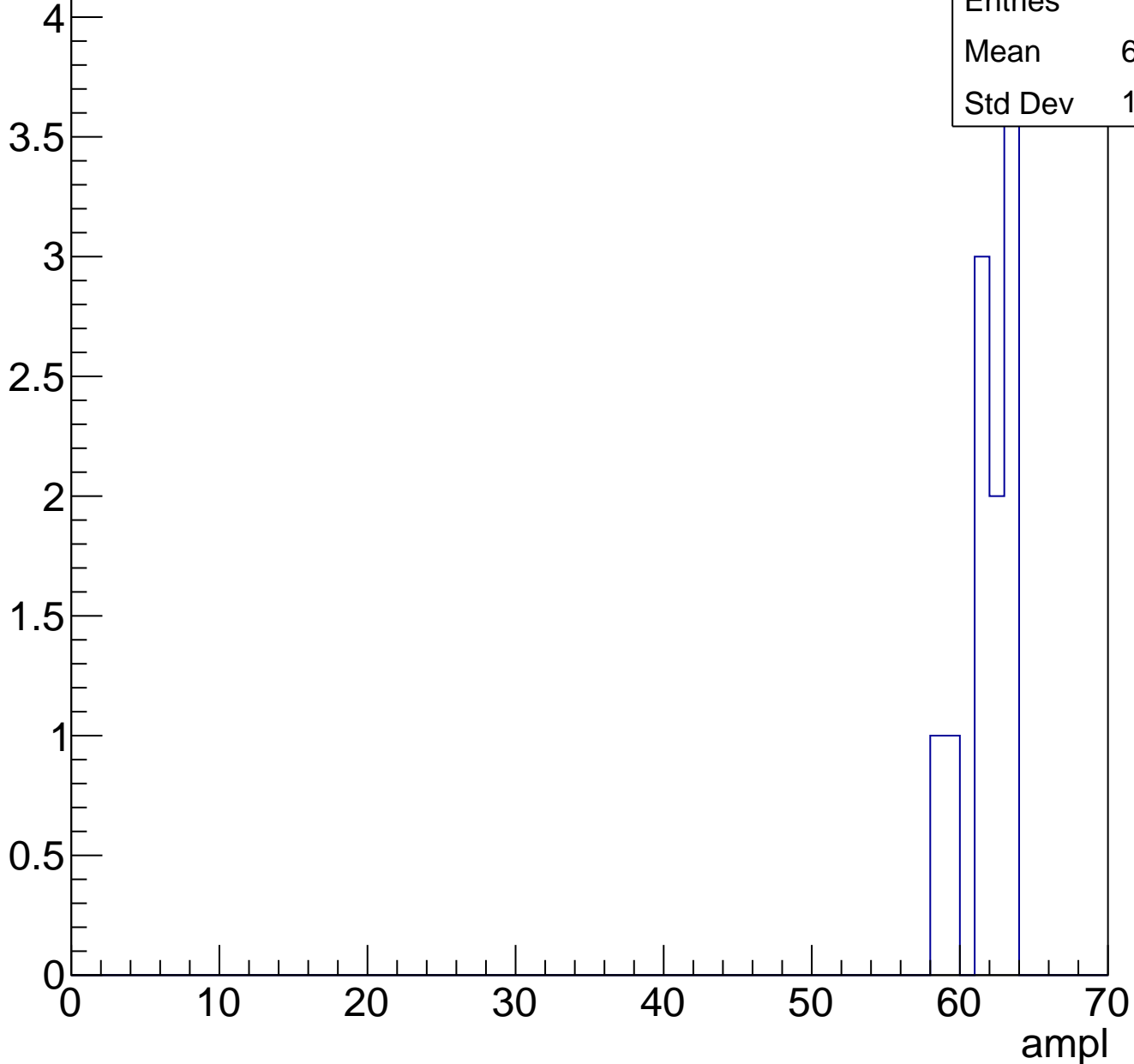
Entries	45
Mean	57.8
Std Dev	9.094



B1L101S, U1-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U1-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

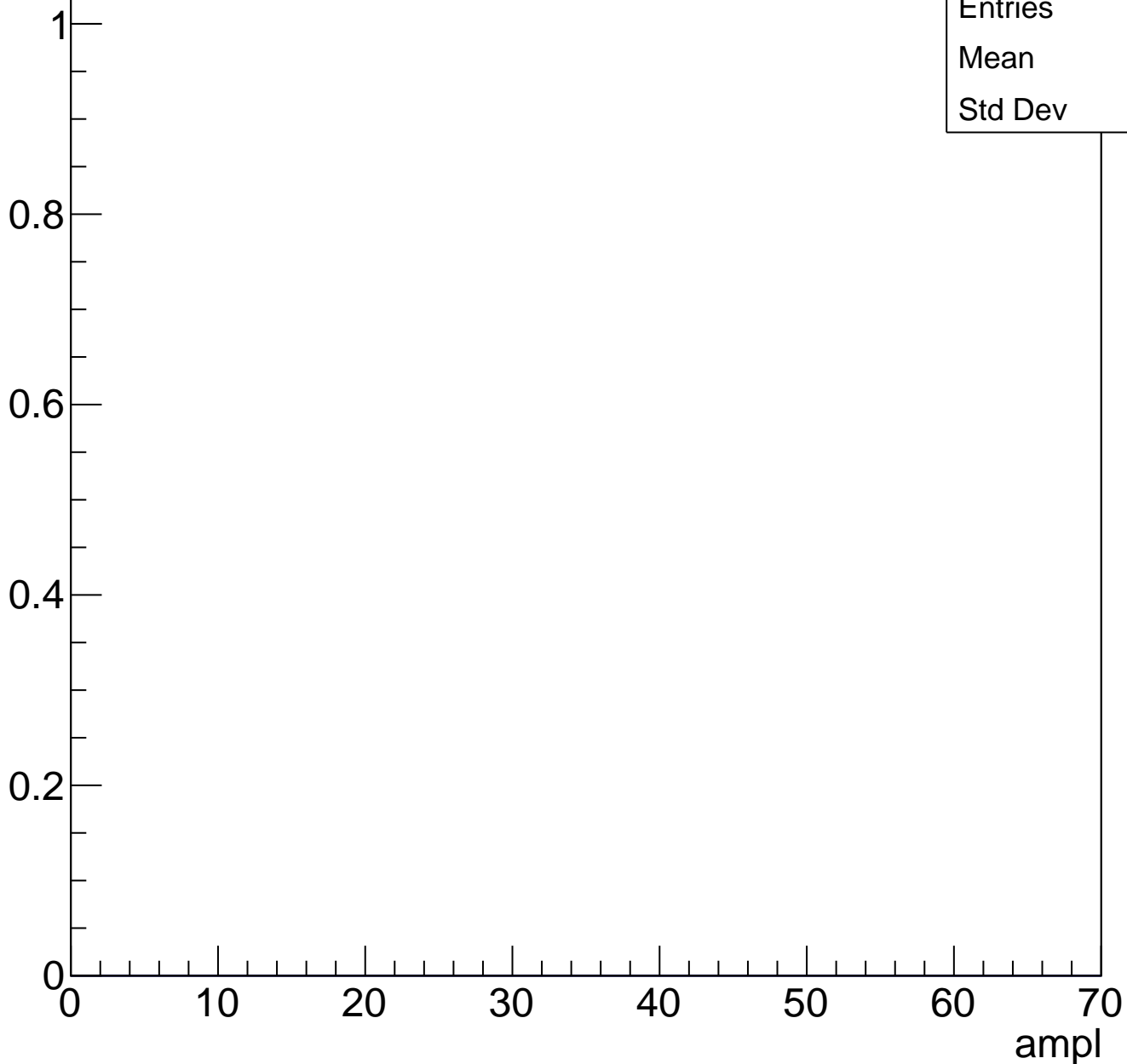


Entries	0
Mean	0
Std Dev	0

B1L101S, U1-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0