

B0L001S, U11-ch0, adc0

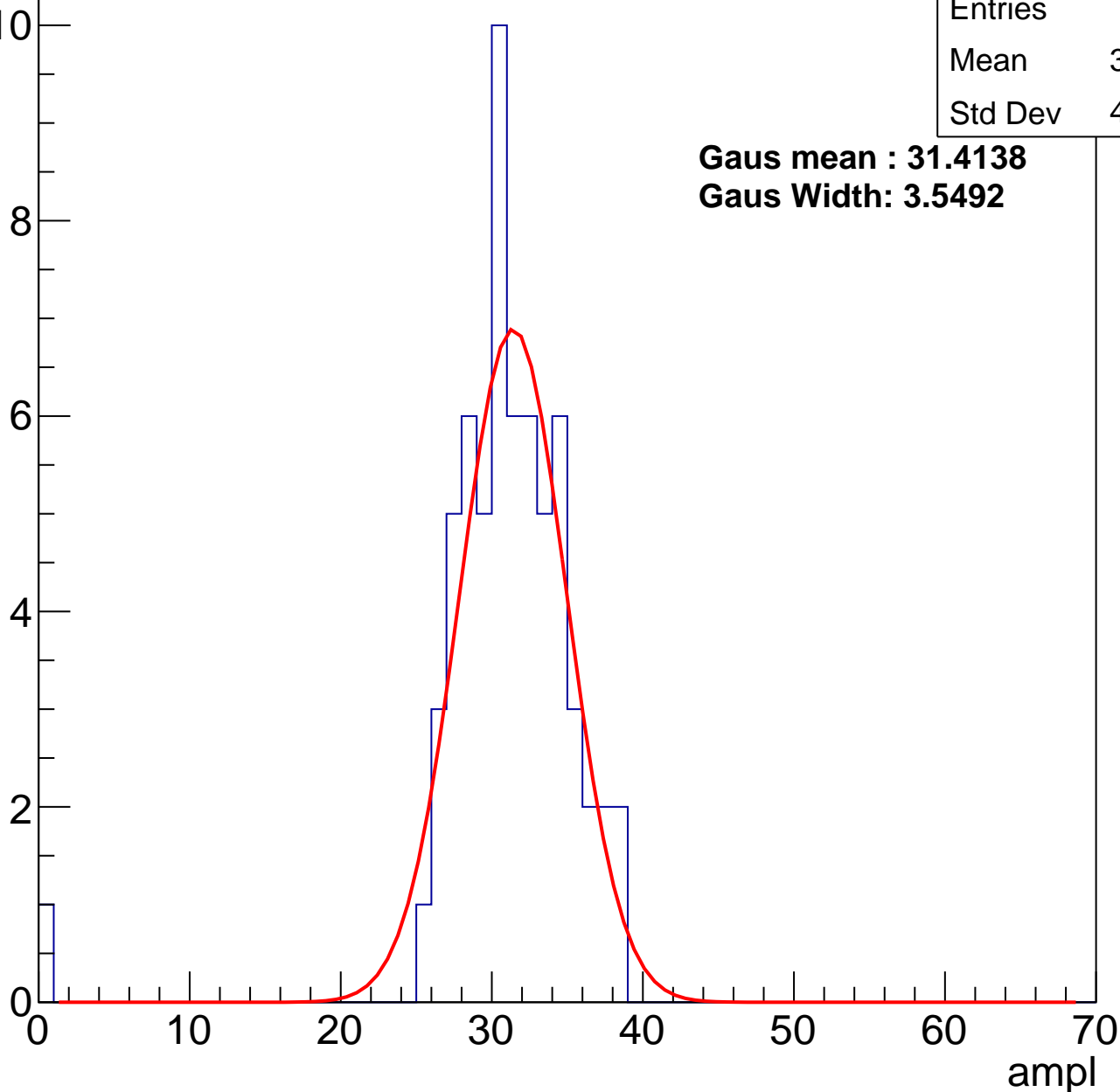
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.56
Std Dev	4.995

Gaus mean : 31.4138

Gaus Width: 3.5492



B0L001S, U11-ch0, adc1

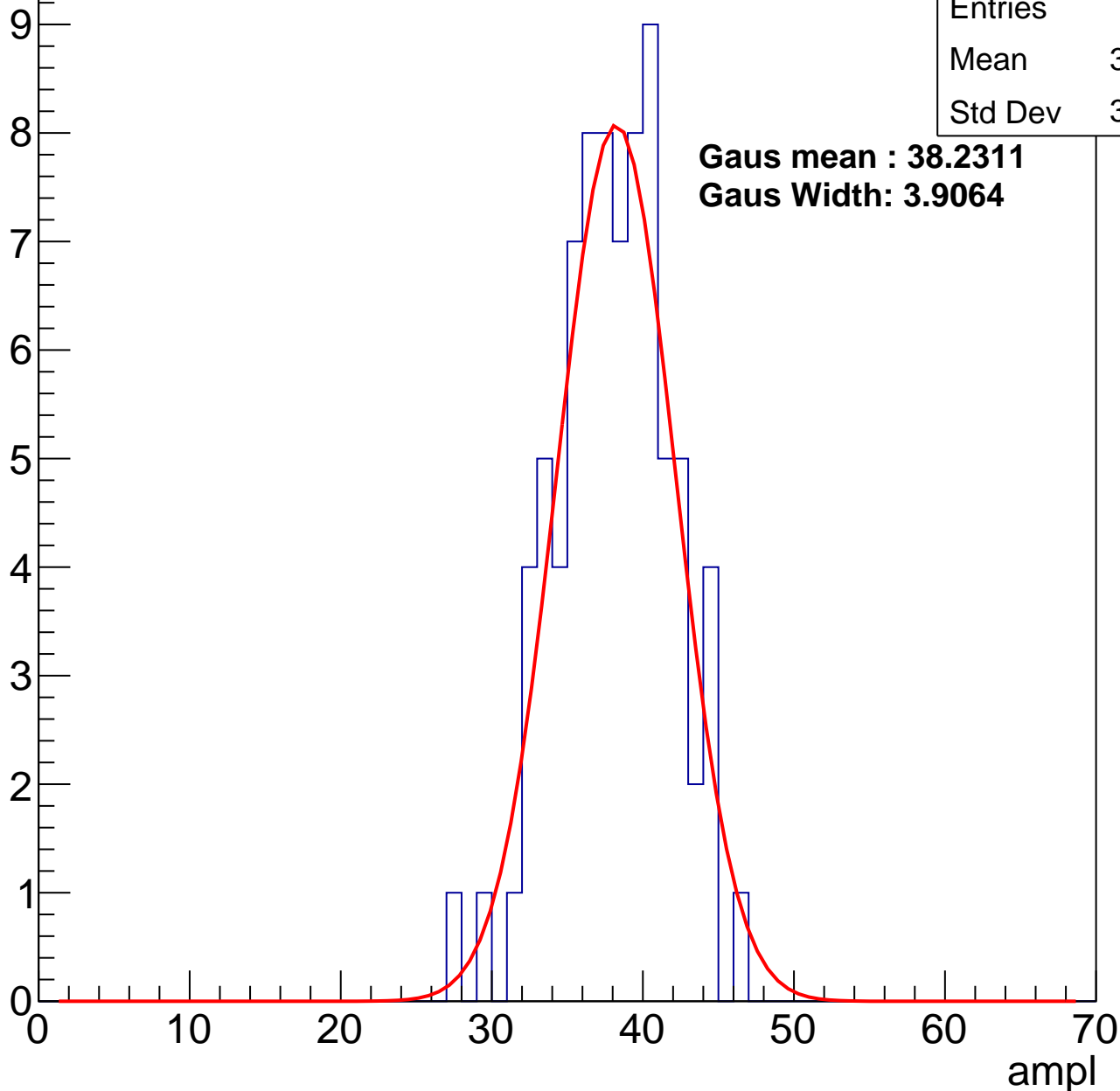
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	37.58
Std Dev	3.707

Gaus mean : 38.2311

Gaus Width: 3.9064



B0L001S, U11-ch0, adc2

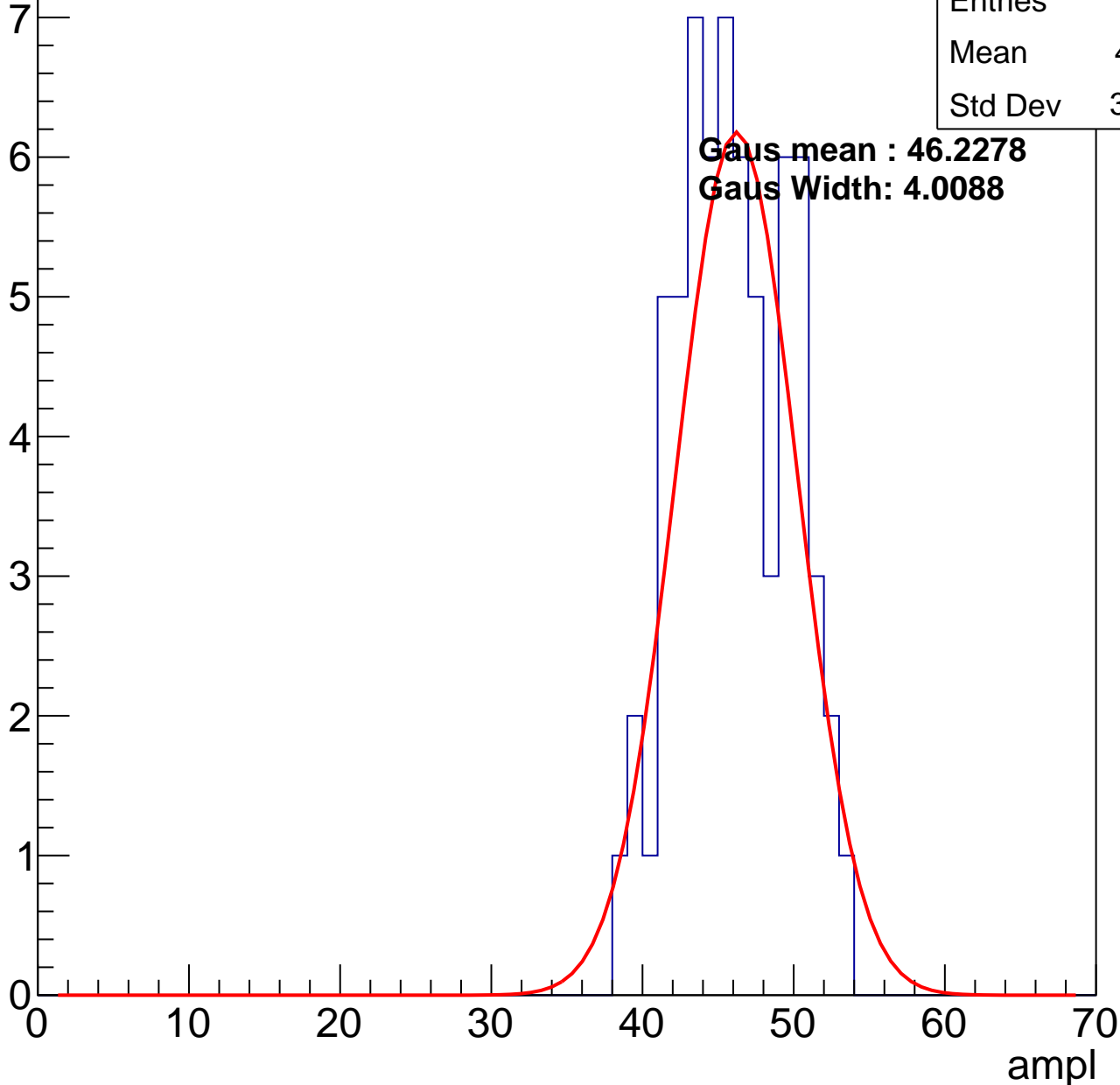
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.61
Std Dev	3.588

Gaus mean : 46.2278

Gaus Width: 4.0088

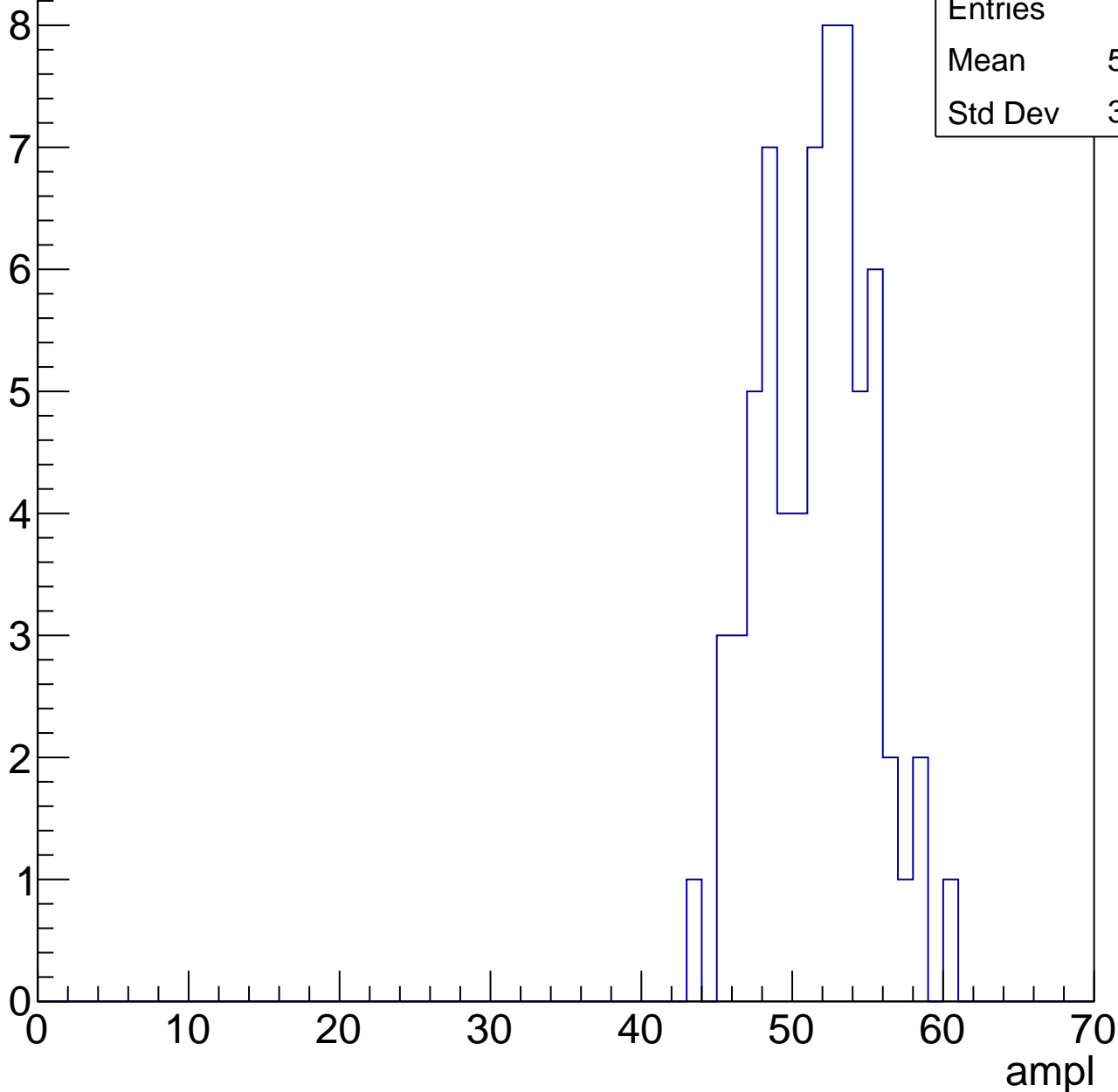


B0L001S, U11-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

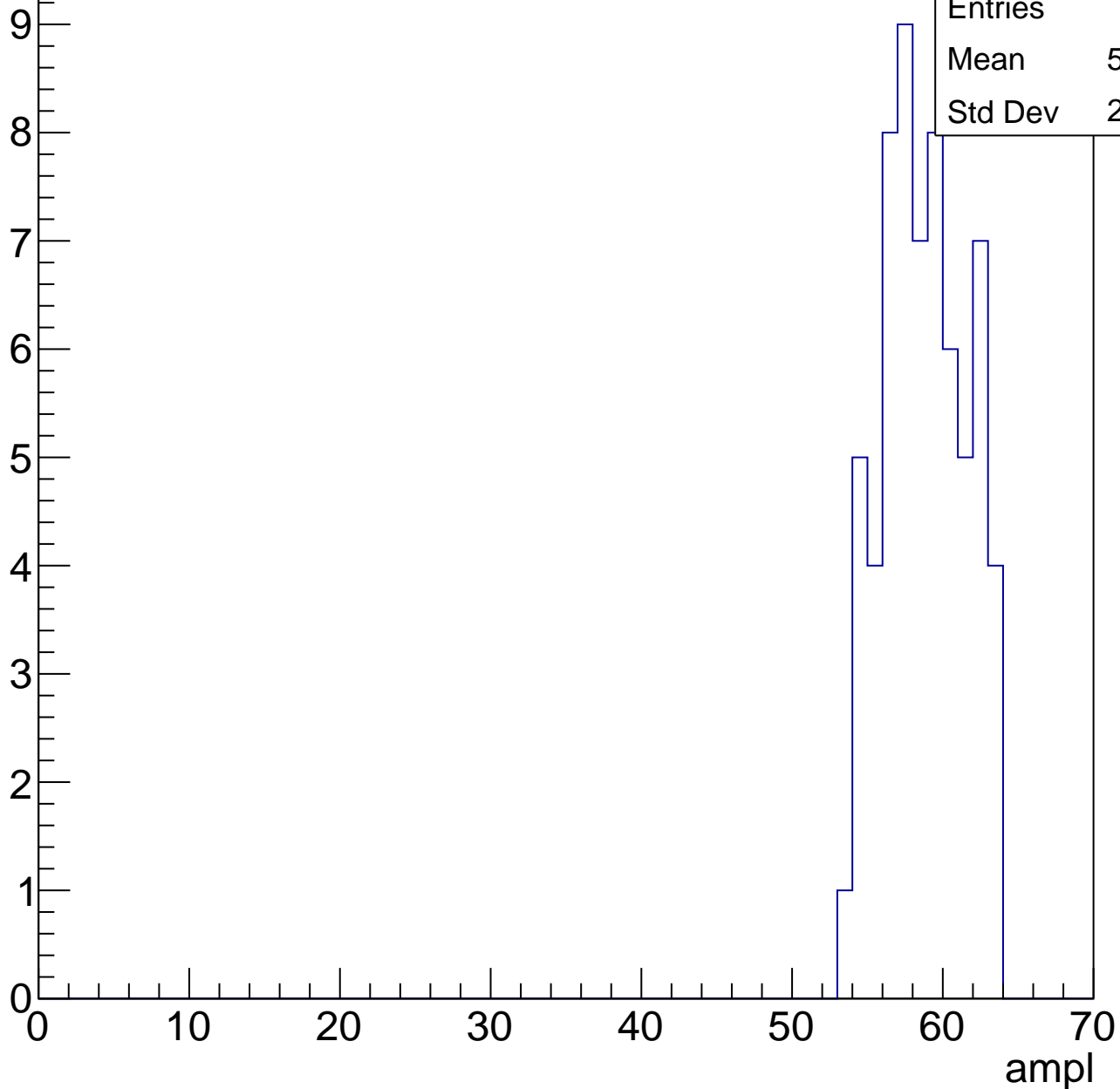
Entries	67
Mean	51.12
Std Dev	3.572



B0L001S, U11-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

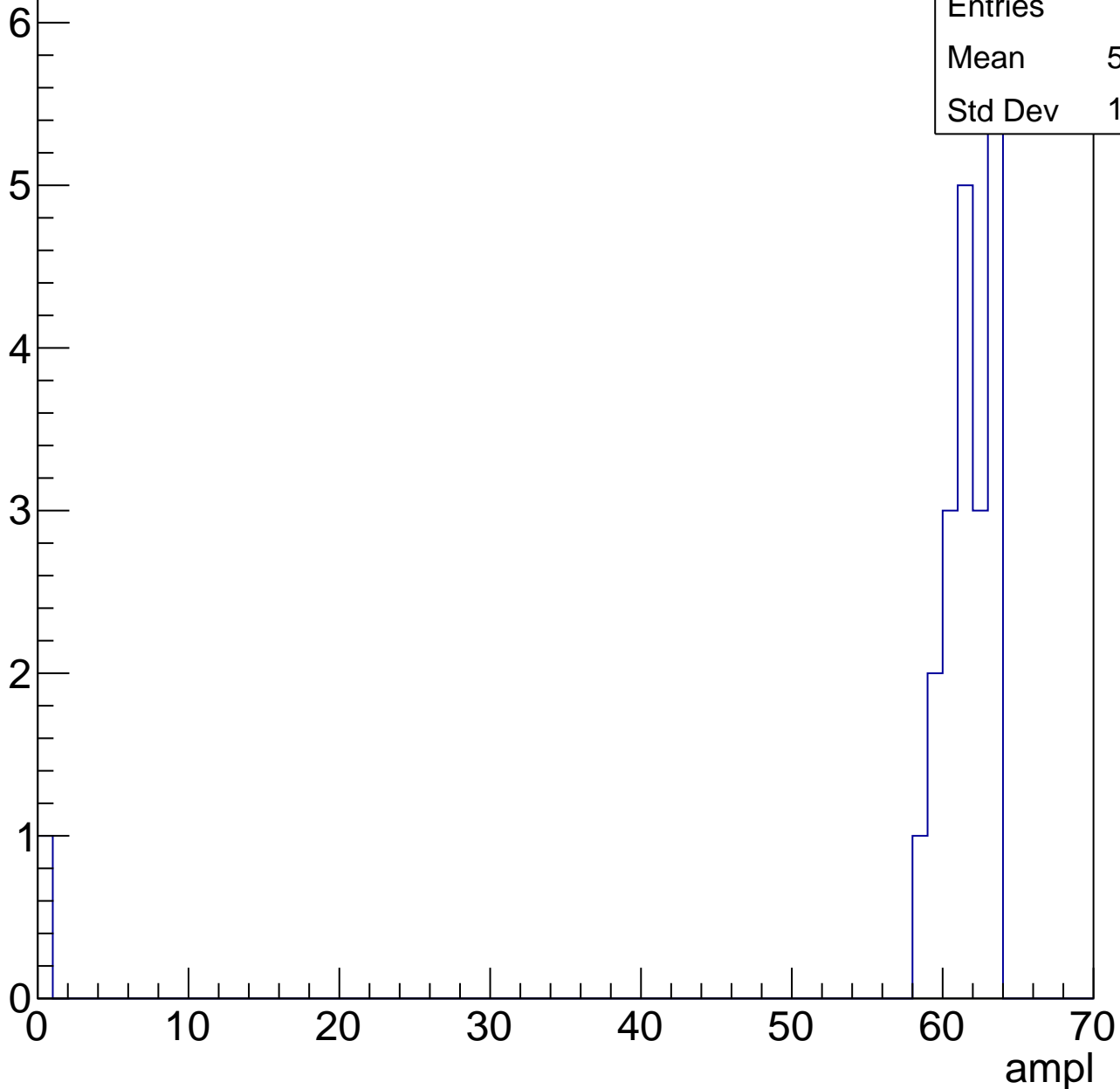


B0L001S, U11-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.33
Std Dev	13.13



B0L001S, U11-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch1, adc0

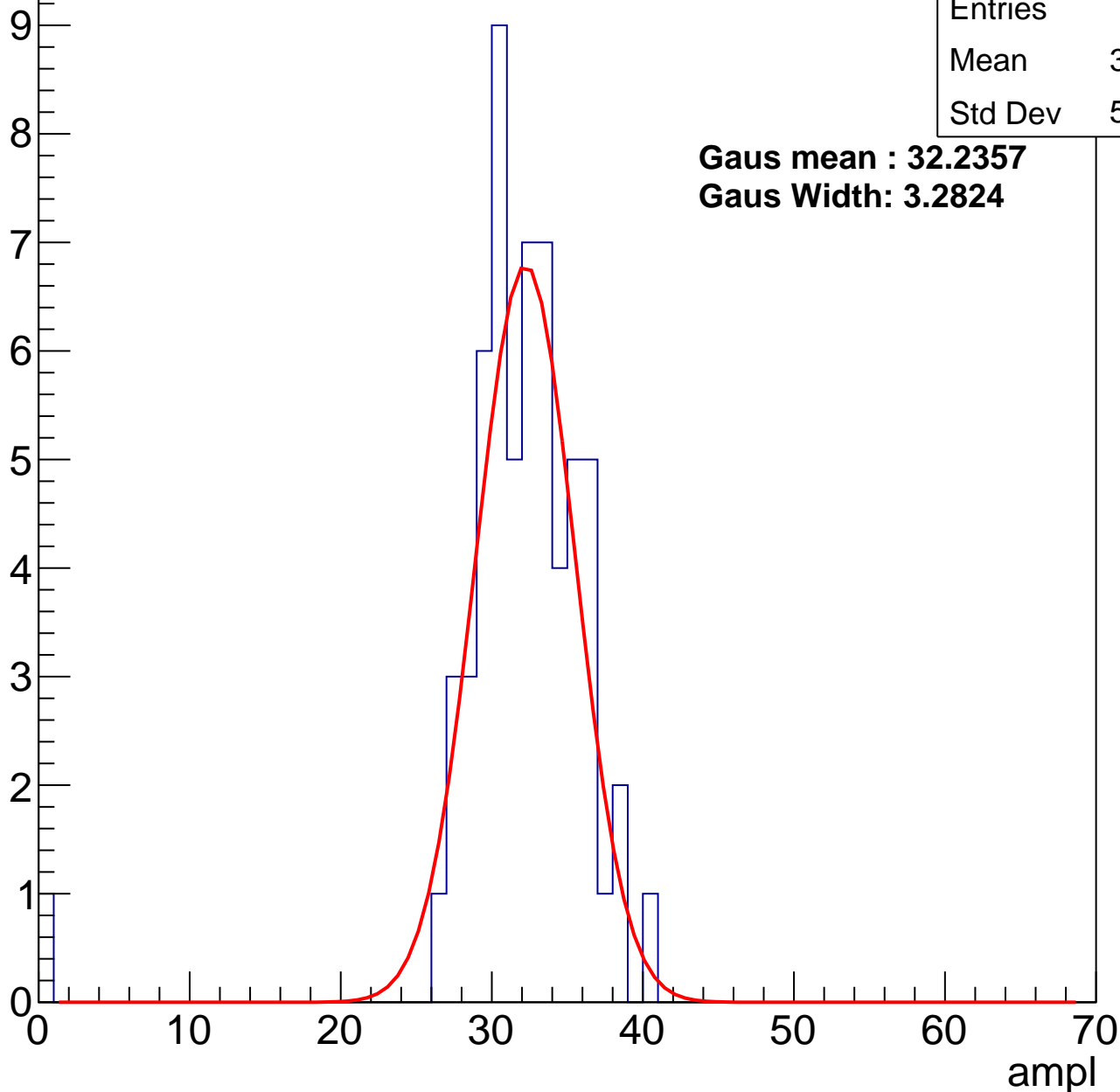
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.48
Std Dev	5.117

Gaus mean : 32.2357

Gaus Width: 3.2824



B0L001S, U11-ch1, adc1

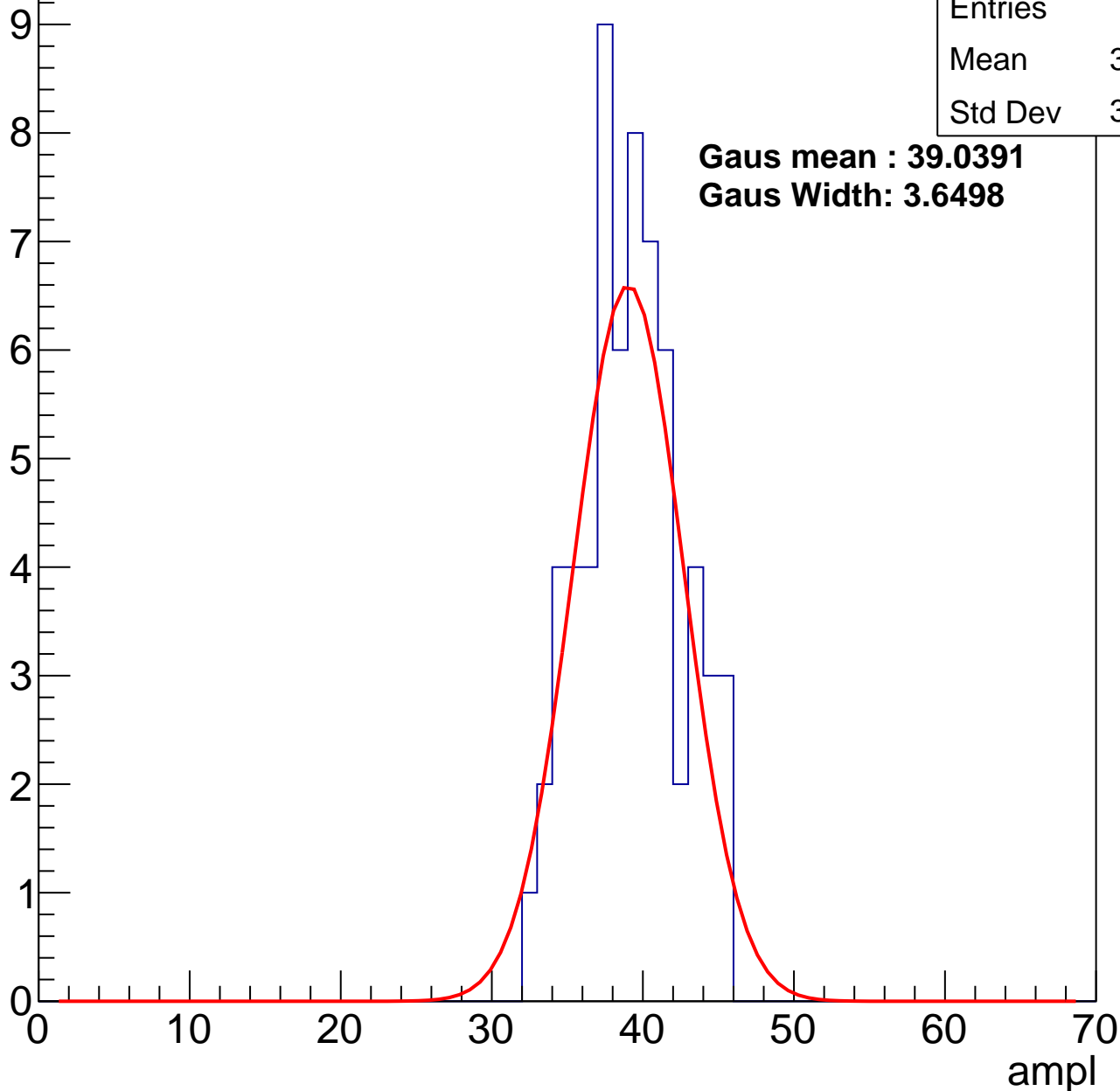
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.73
Std Dev	3.223

Gaus mean : 39.0391

Gaus Width: 3.6498



B0L001S, U11-ch1, adc2

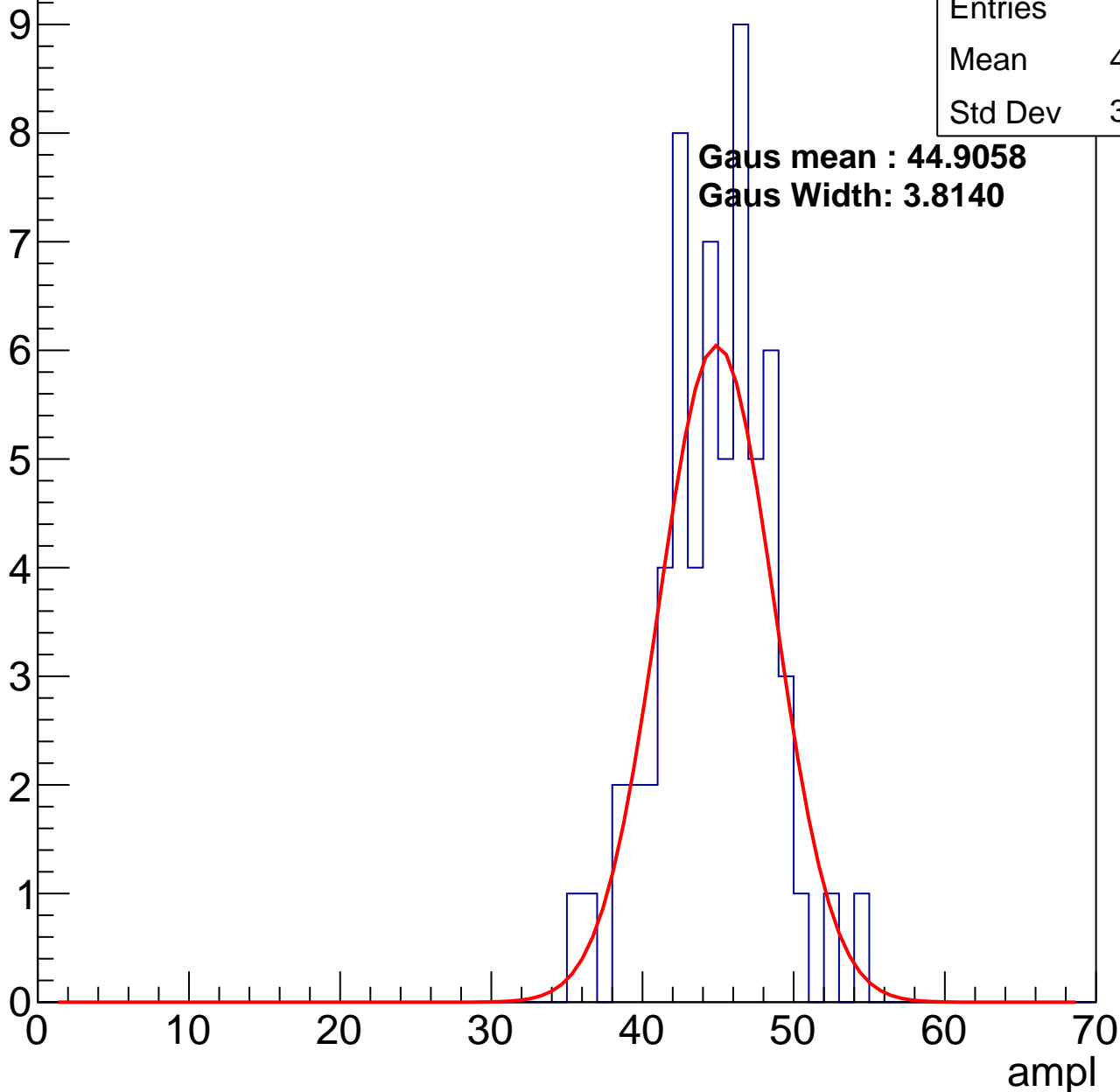
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.35
Std Dev	3.637

Gaus mean : 44.9058

Gaus Width: 3.8140

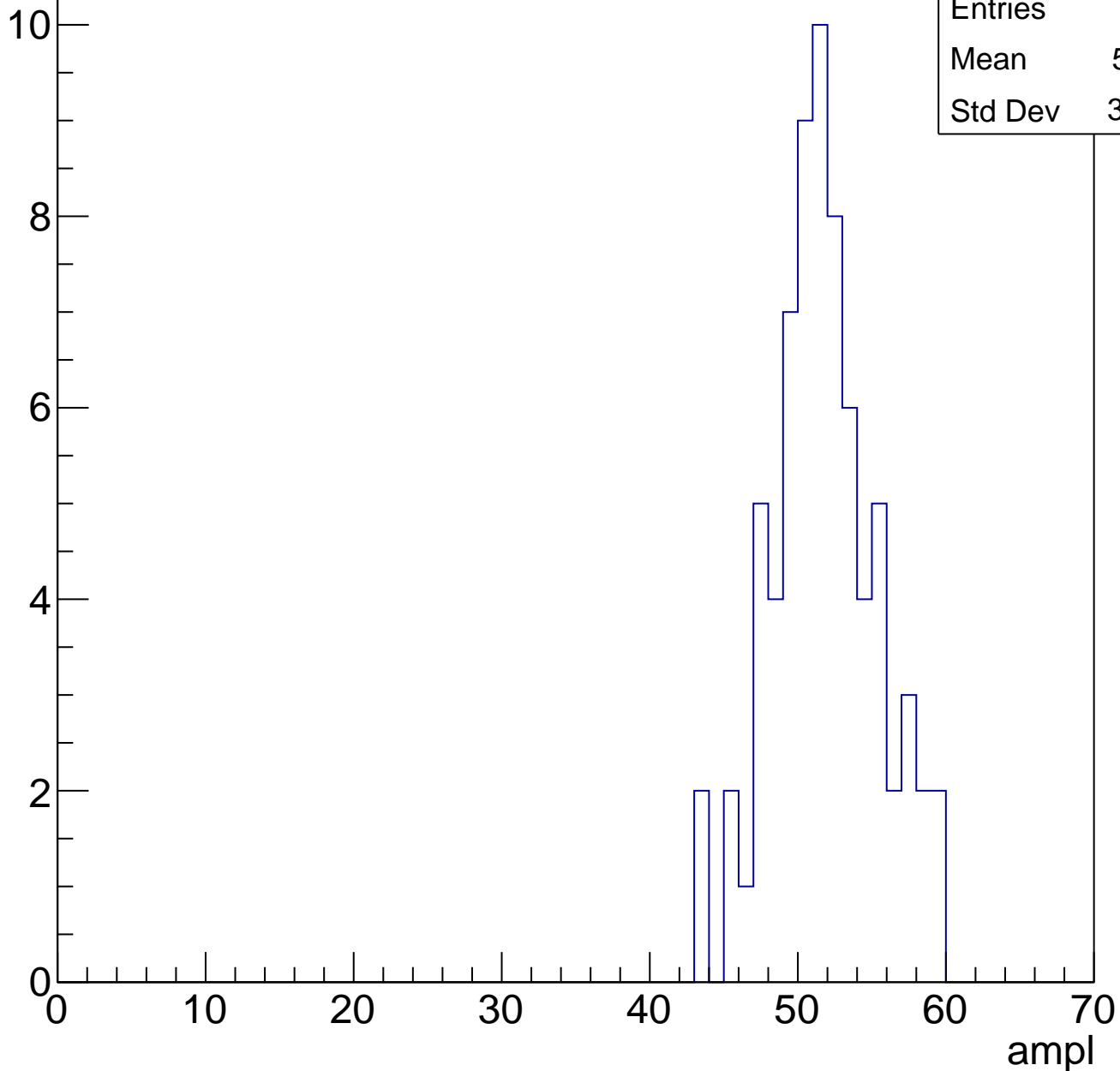


B0L001S, U11-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	51.31
Std Dev	3.542

Entry

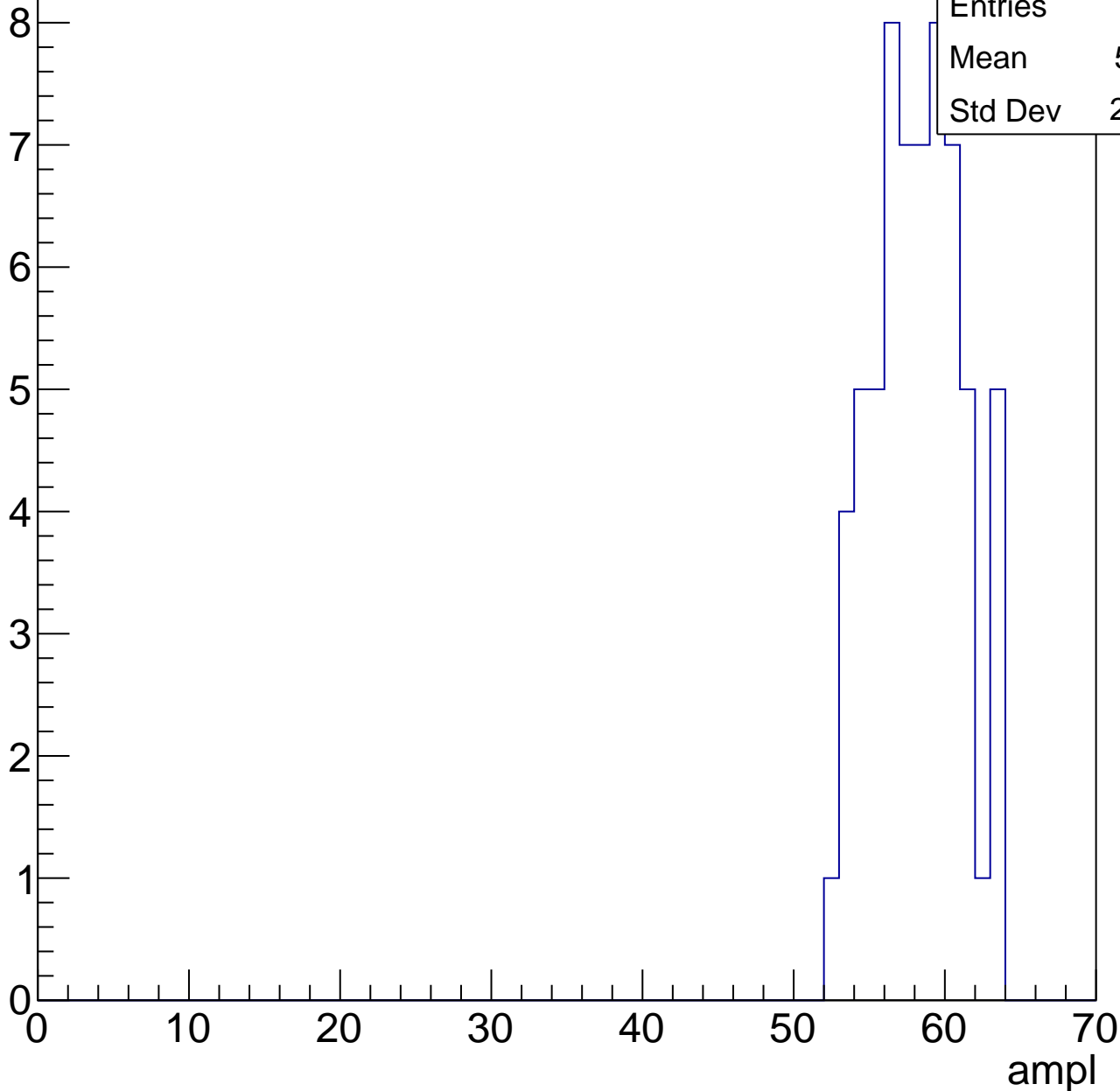


B0L001S, U11-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.71
Std Dev	2.864

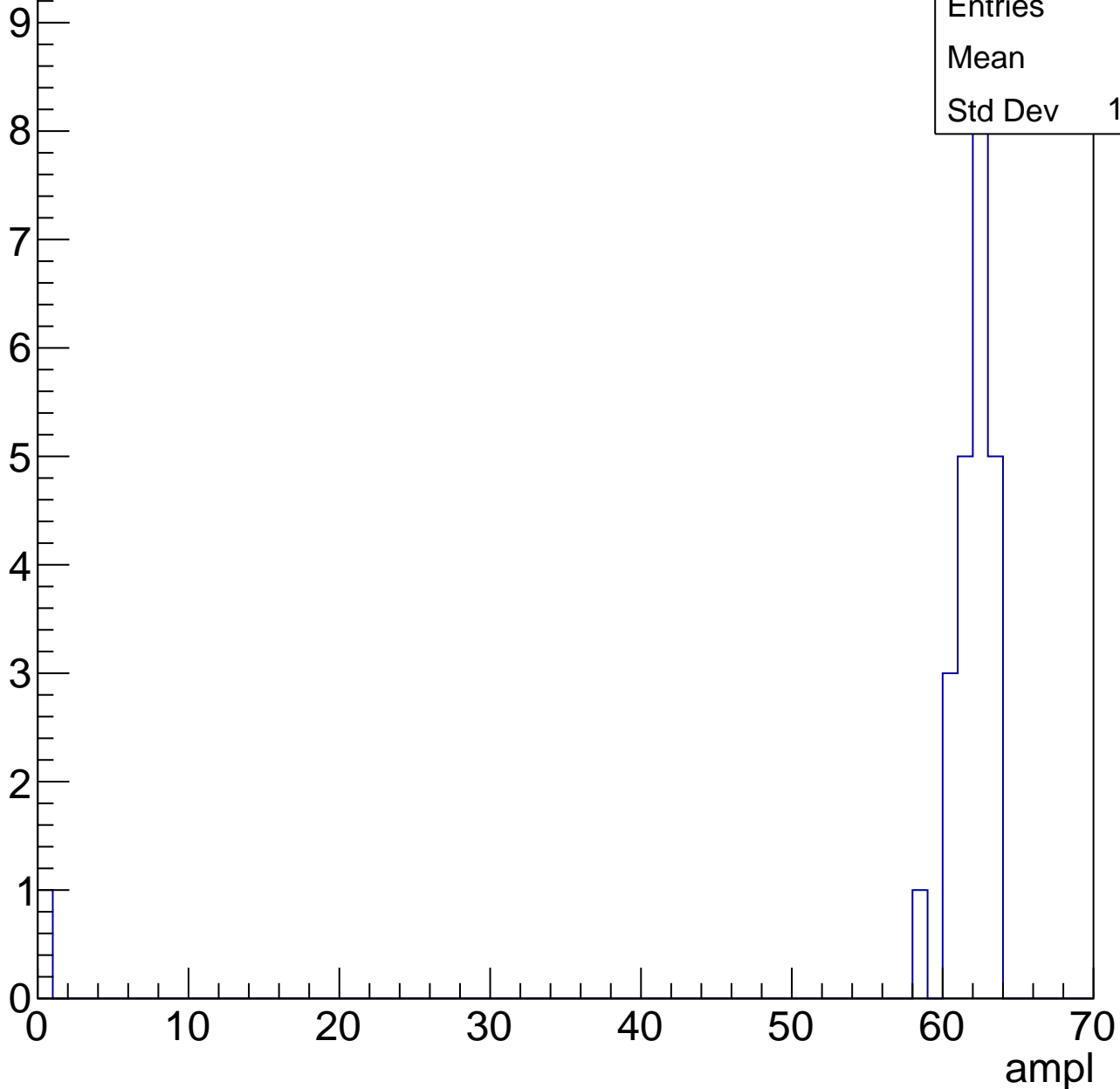


B0L001S, U11-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

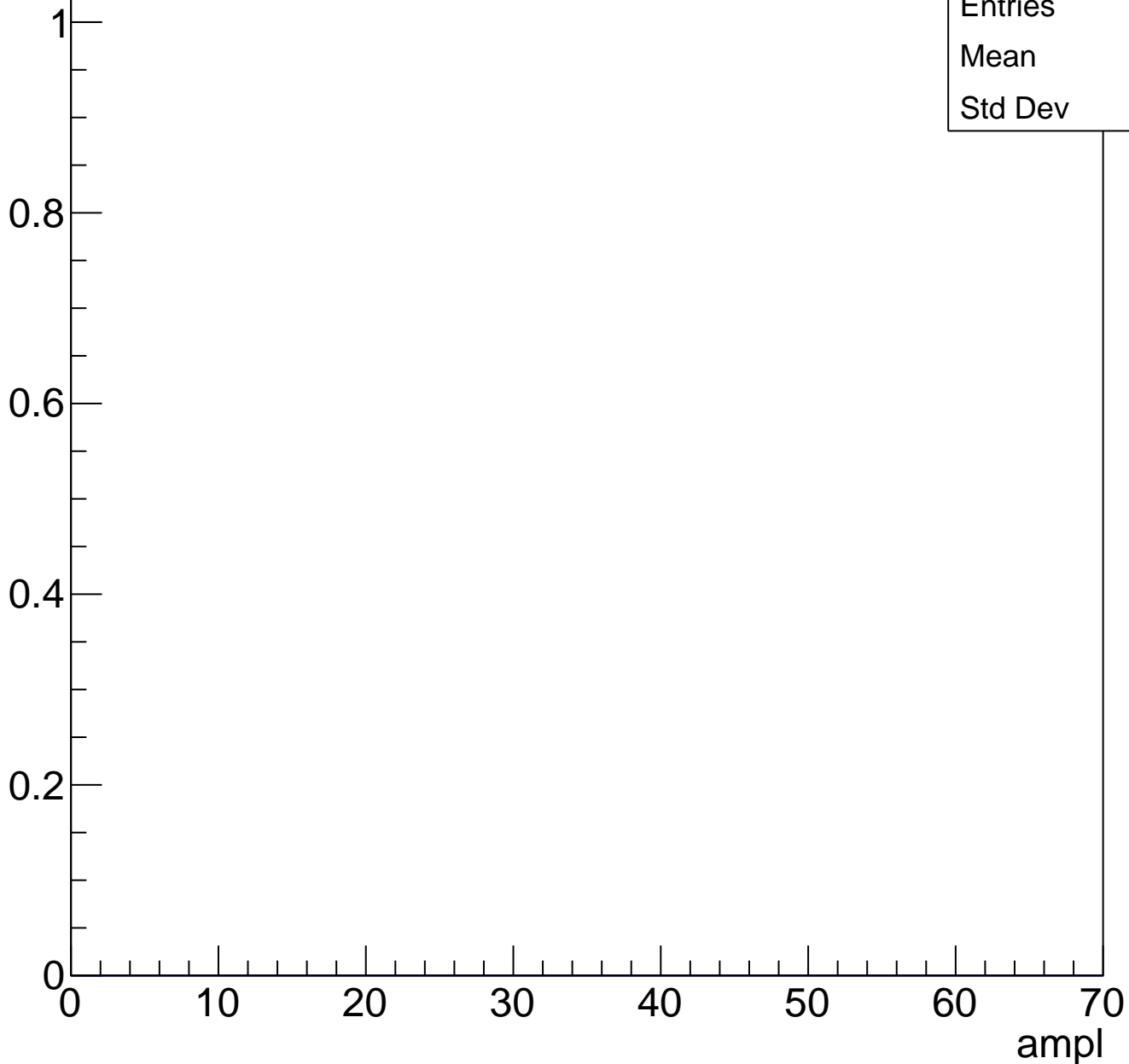
Entries	24
Mean	59
Std Dev	12.36



B0L001S, U11-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch2, adc0

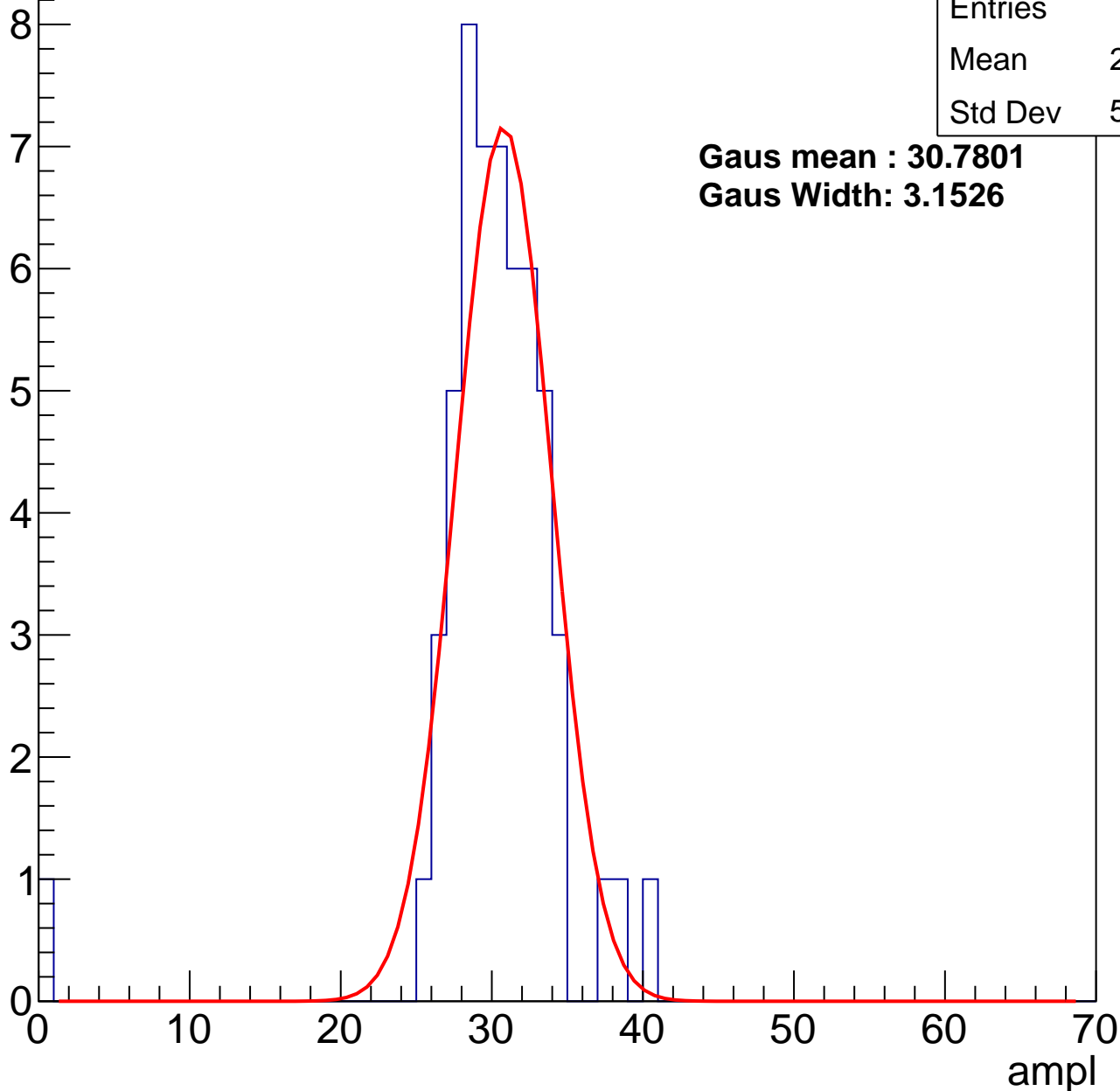
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.73
Std Dev	5.025

Gaus mean : 30.7801

Gaus Width: 3.1526



B0L001S, U11-ch2, adc1

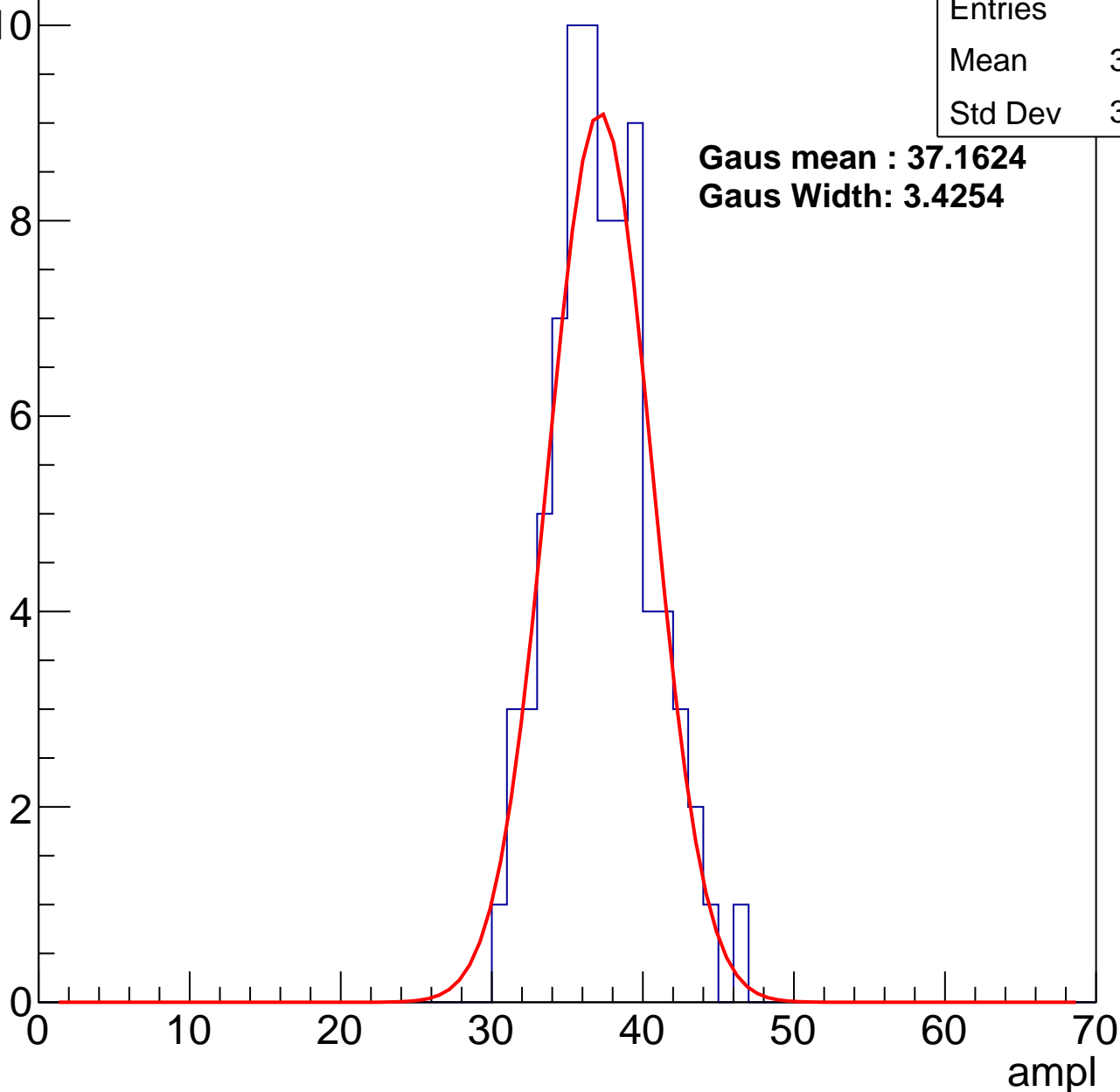
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.82
Std Dev	3.268

Gaus mean : 37.1624

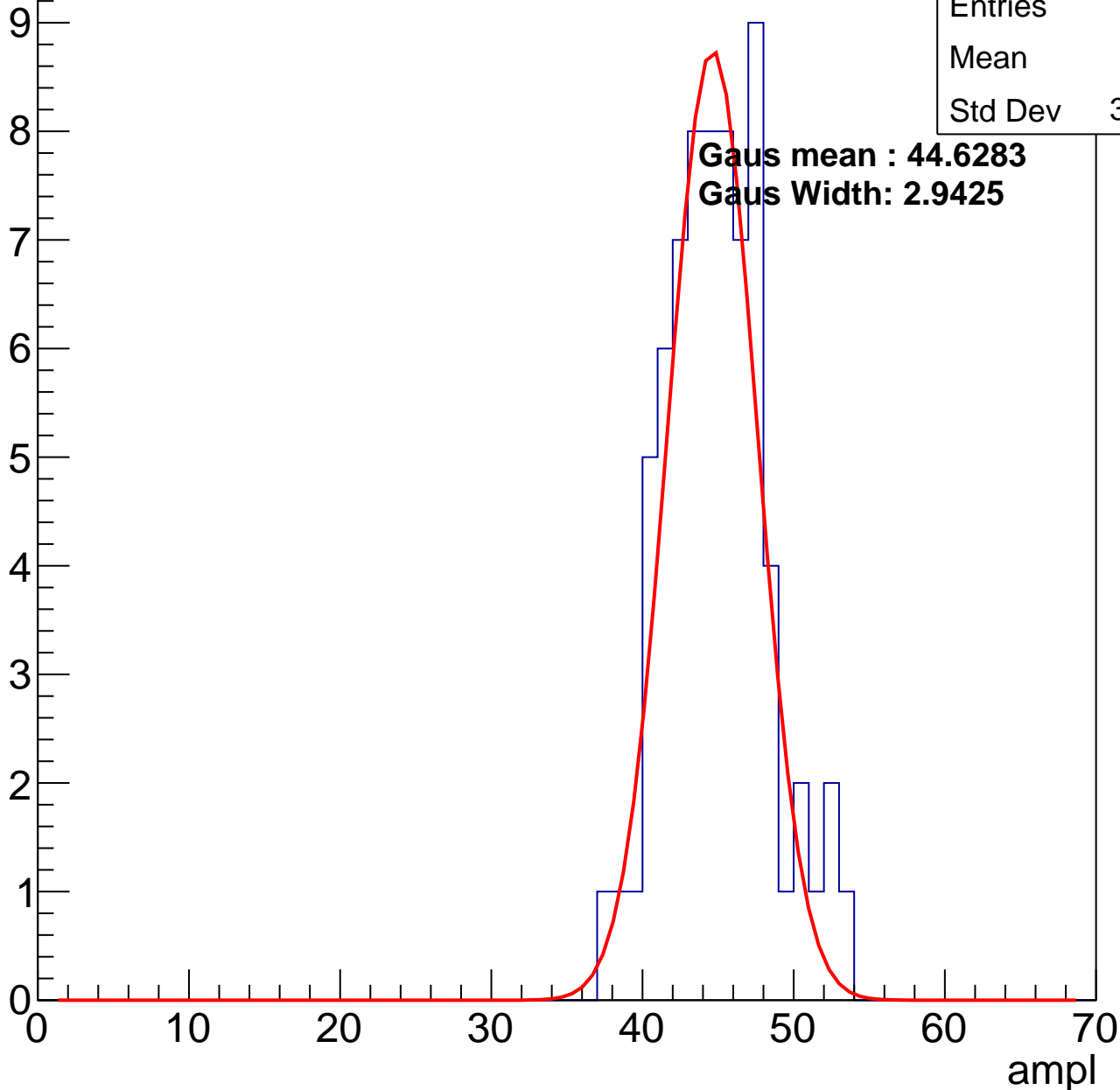
Gaus Width: 3.4254



B0L001S, U11-ch2, adc2

calib_packv5_042523_0143.root, FC#9, port A1

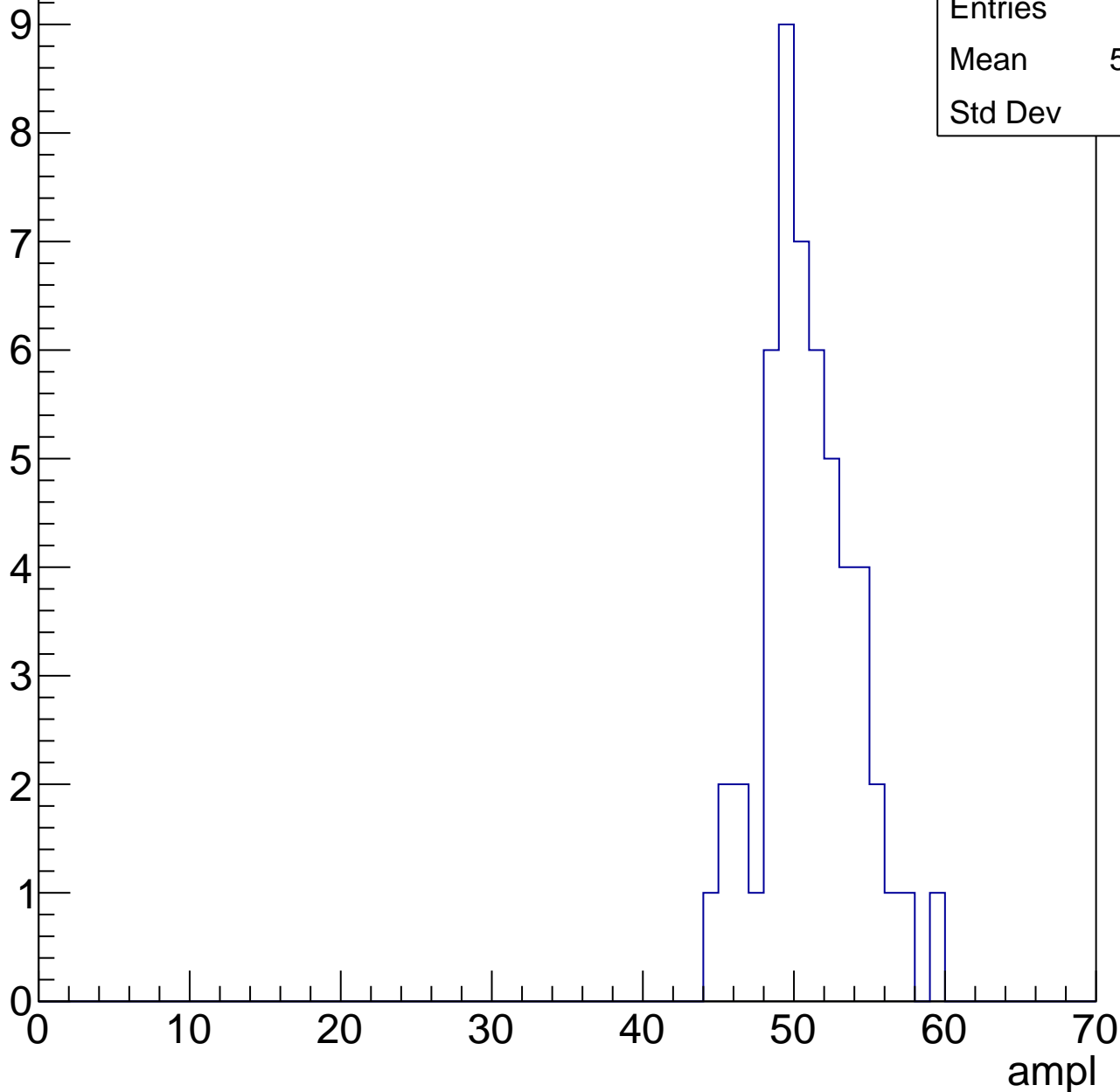
Entry



B0L001S, U11-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



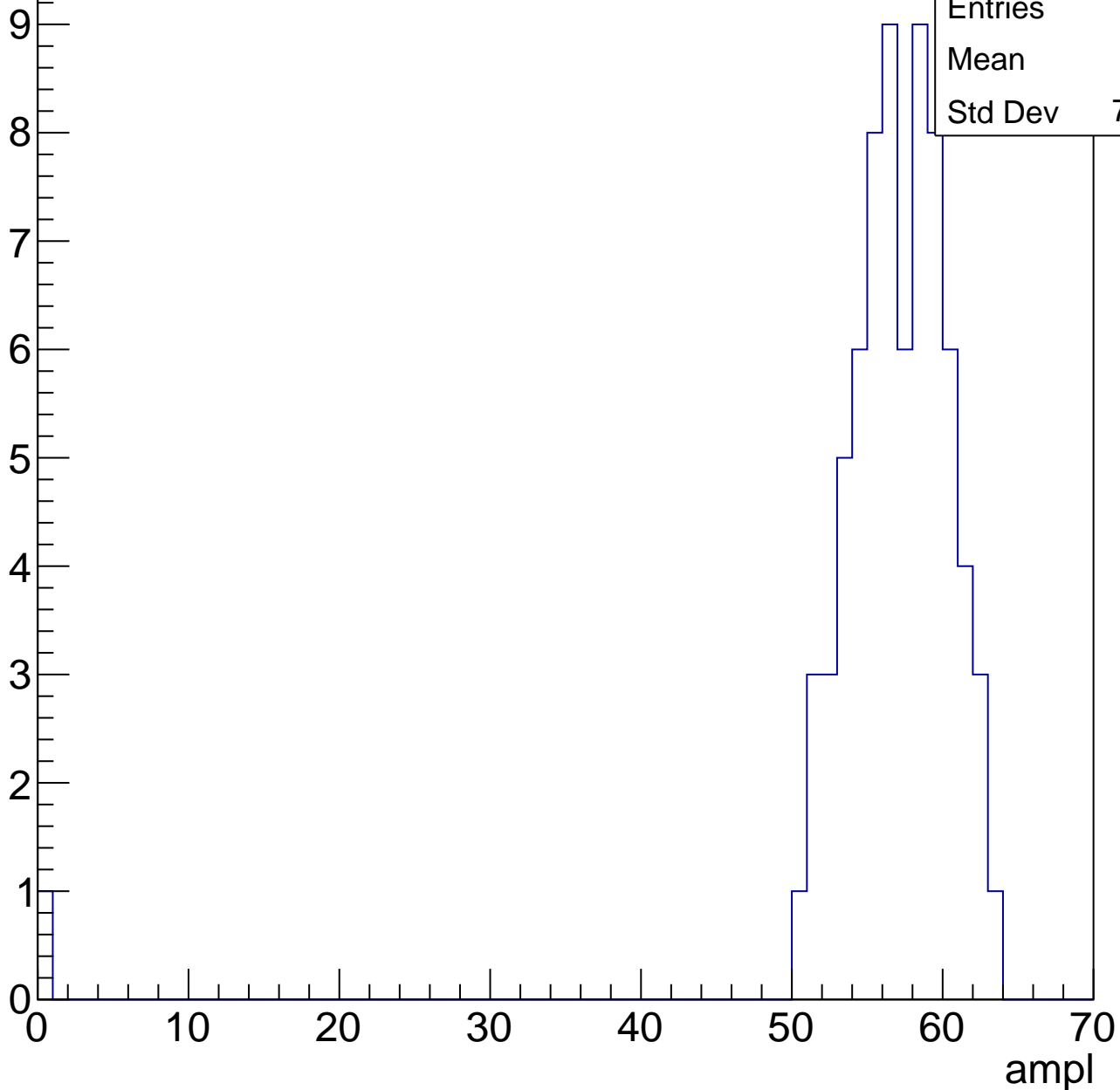
Entries	52
Mean	50.54
Std Dev	3.06

B0L001S, U11-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	55.9
Std Dev	7.251

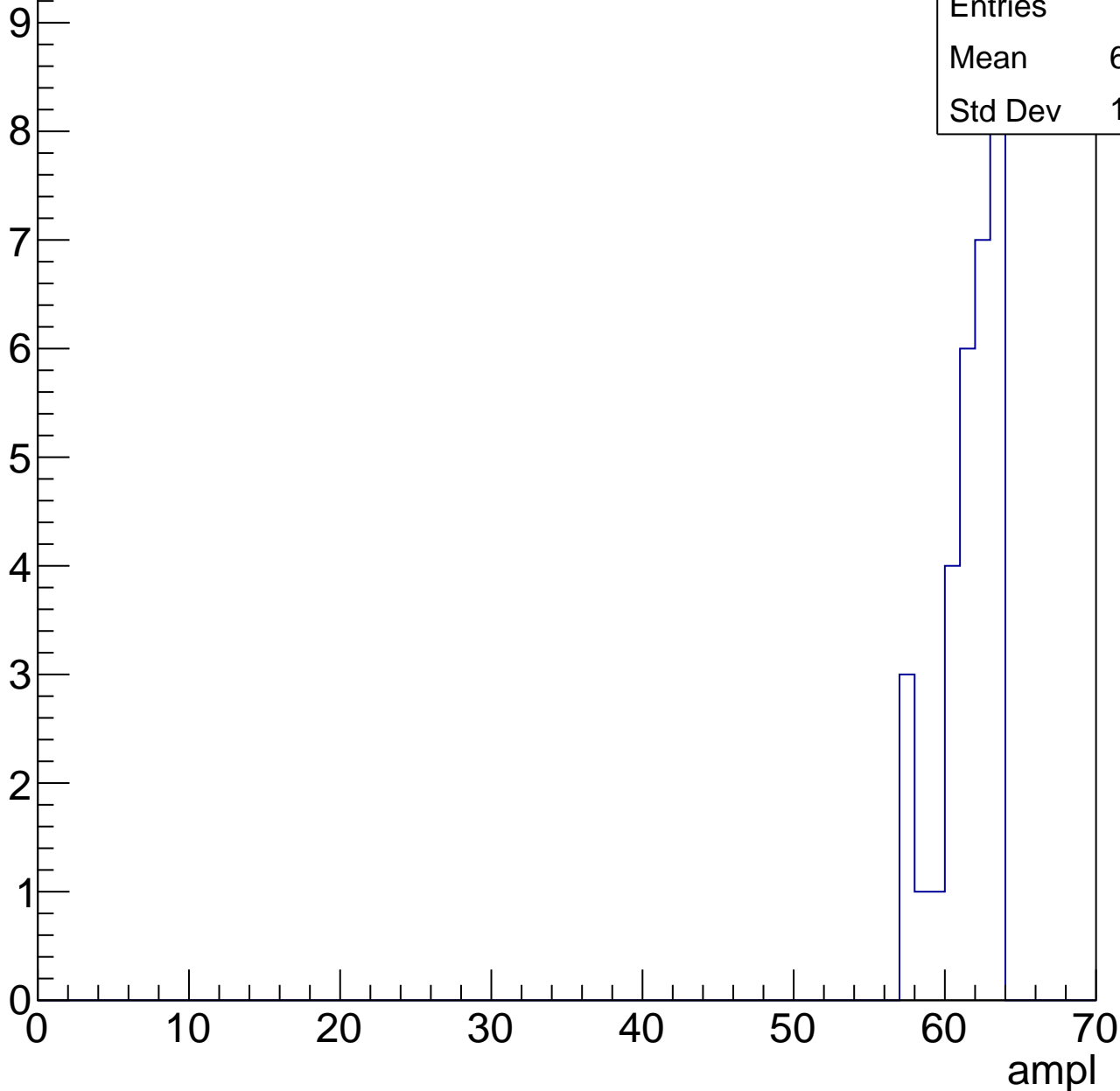


B0L001S, U11-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	61.13
Std Dev	1.862



B0L001S, U11-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	24
Std Dev	0

B0L001S, U11-ch3, adc0

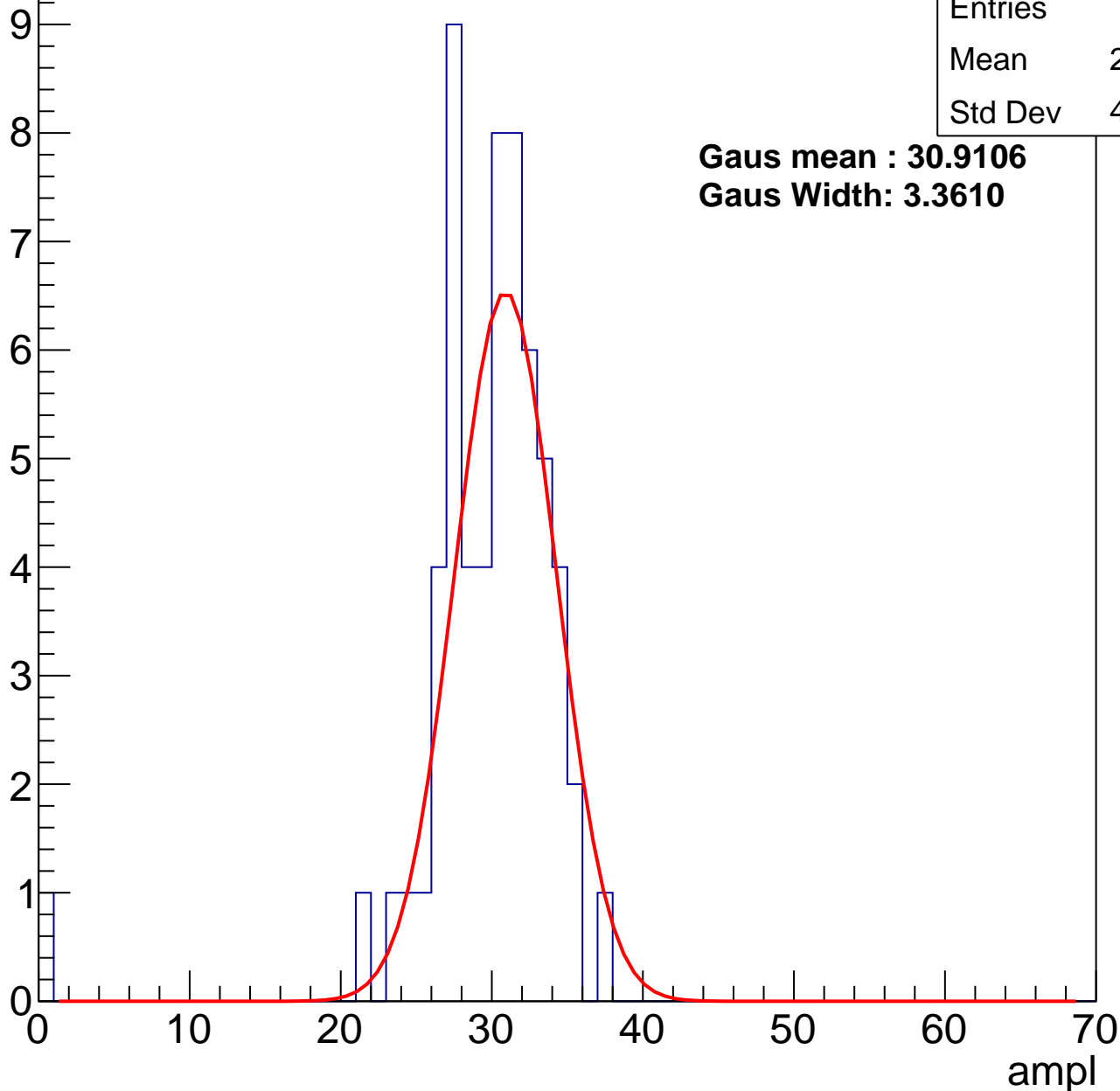
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.27
Std Dev	4.939

Gaus mean : 30.9106

Gaus Width: 3.3610



B0L001S, U11-ch3, adc1

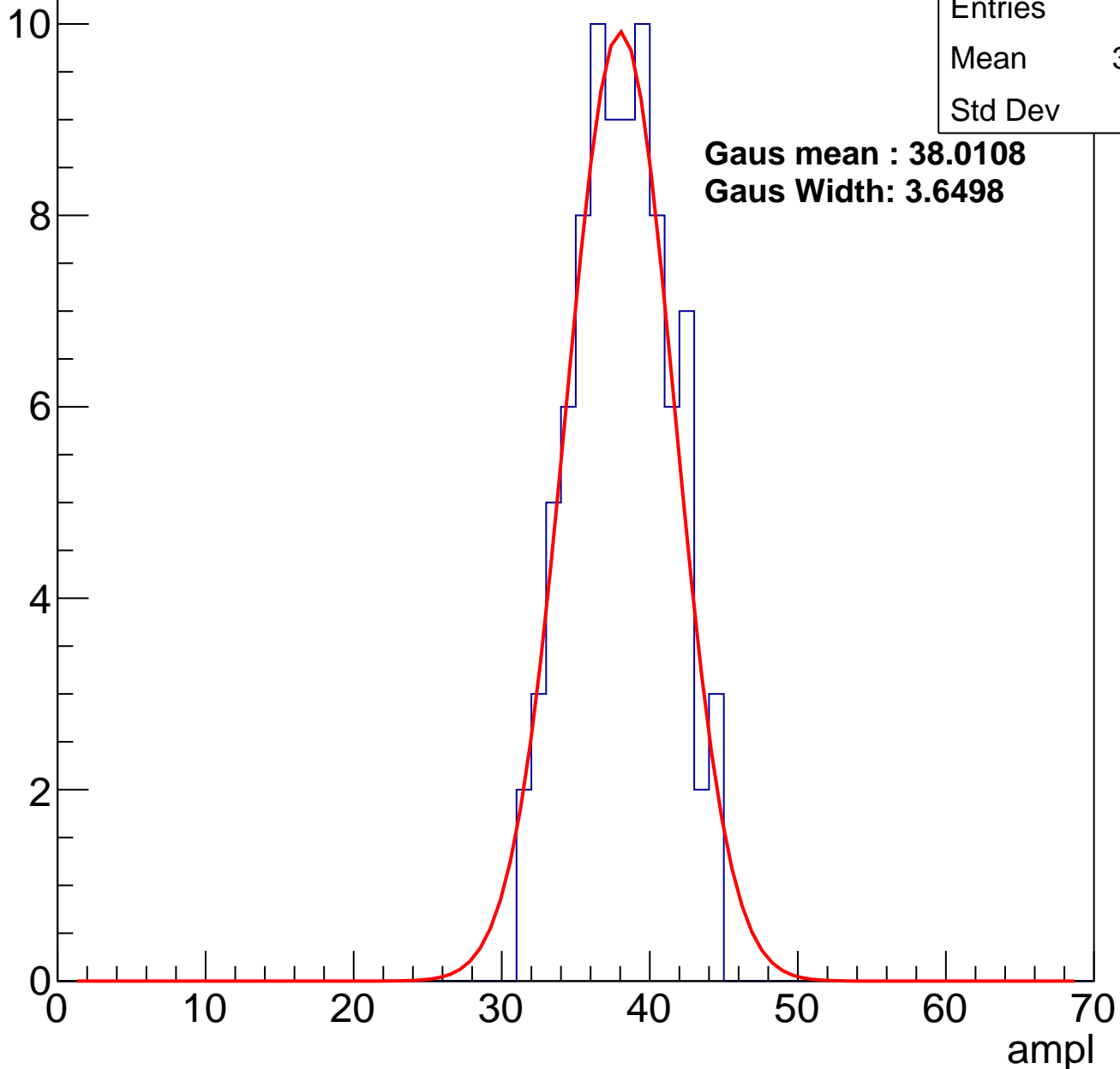
calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	37.61
Std Dev	3.2

Gaus mean : 38.0108

Gaus Width: 3.6498

Entry



B0L001S, U11-ch3, adc2

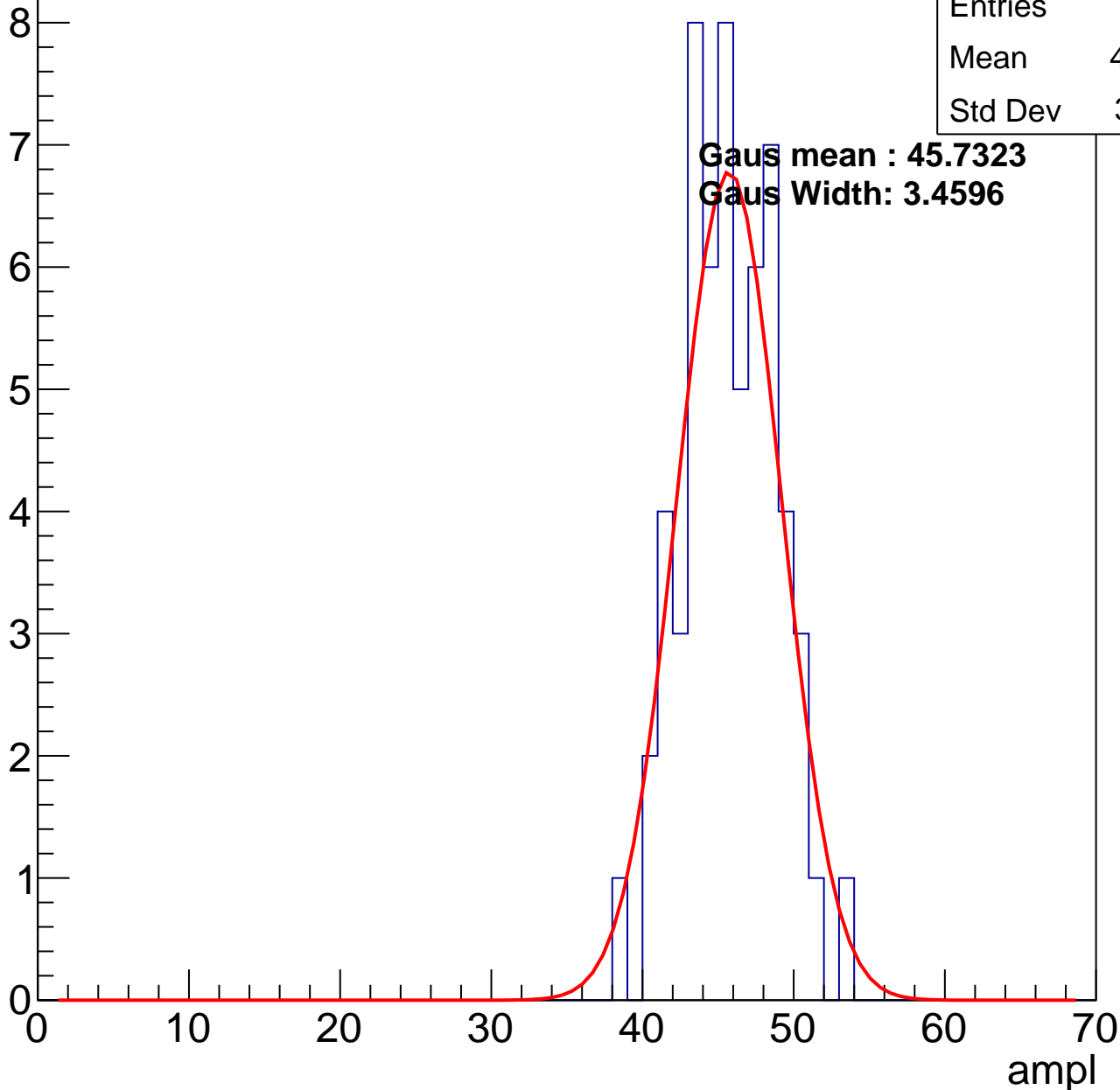
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	45.32
Std Dev	3.061

Gaus mean : 45.7323

Gaus Width: 3.4596

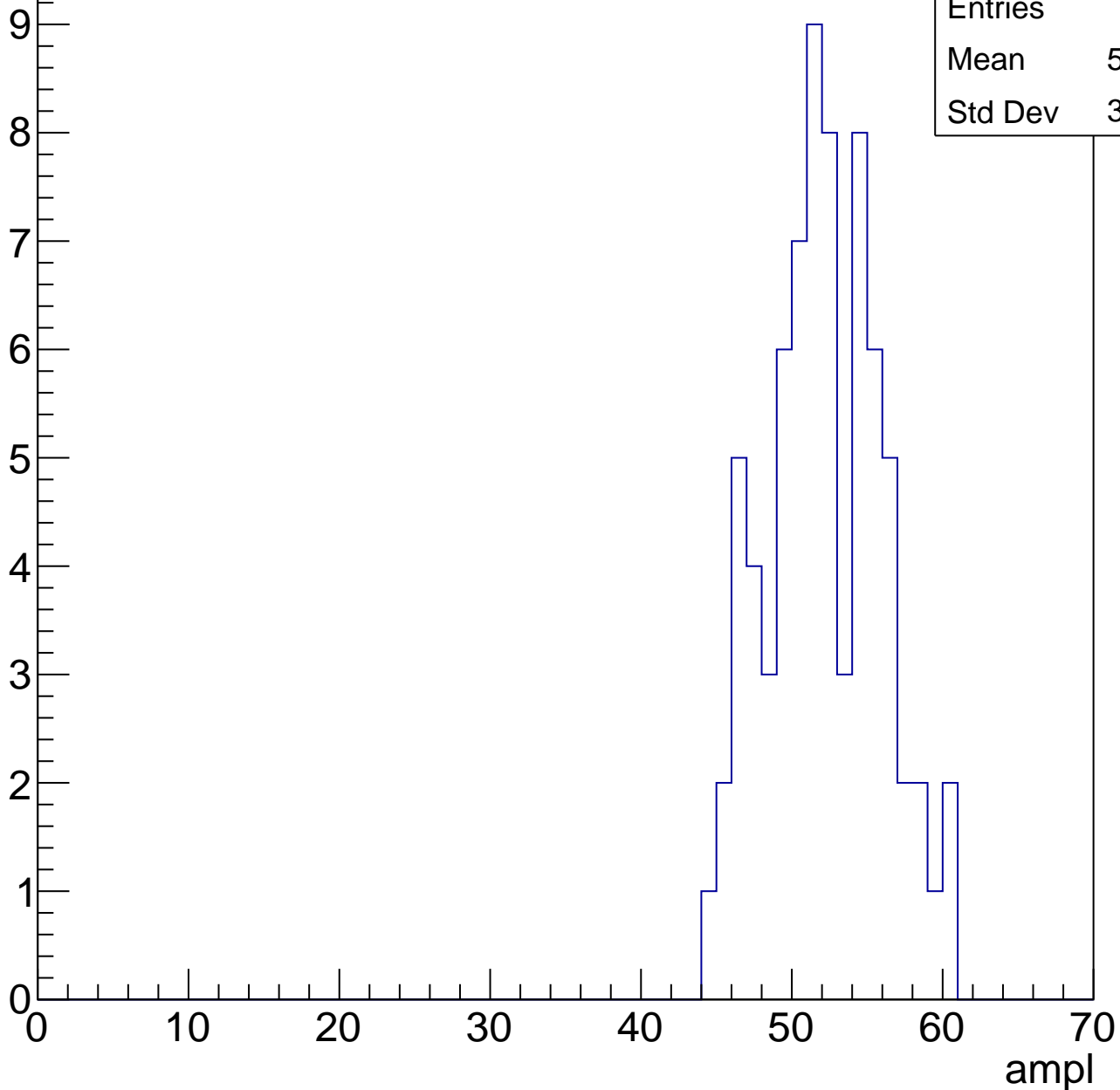


B0L001S, U11-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	51.69
Std Dev	3.752

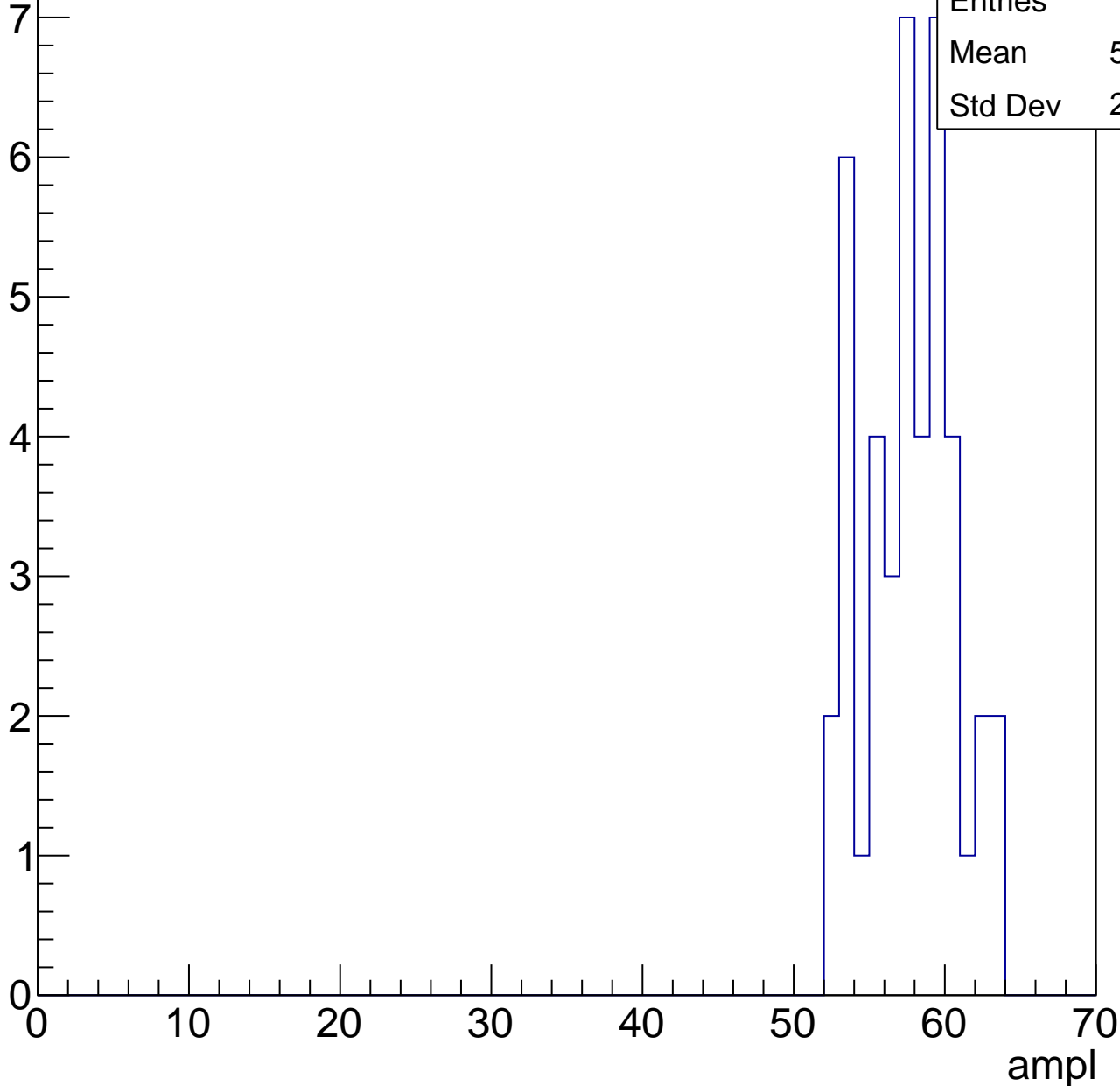


B0L001S, U11-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	57.19
Std Dev	2.967

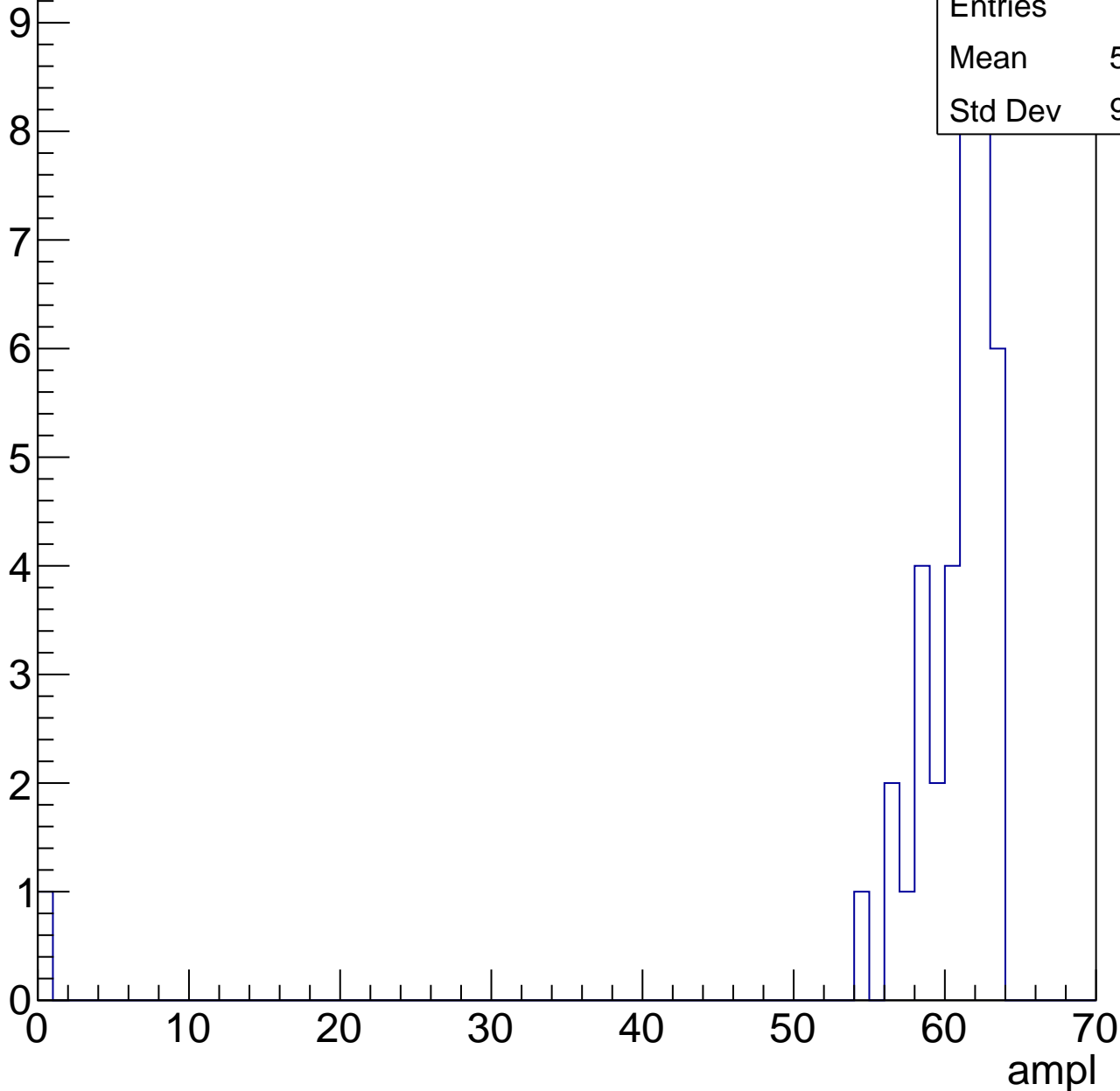


B0L001S, U11-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

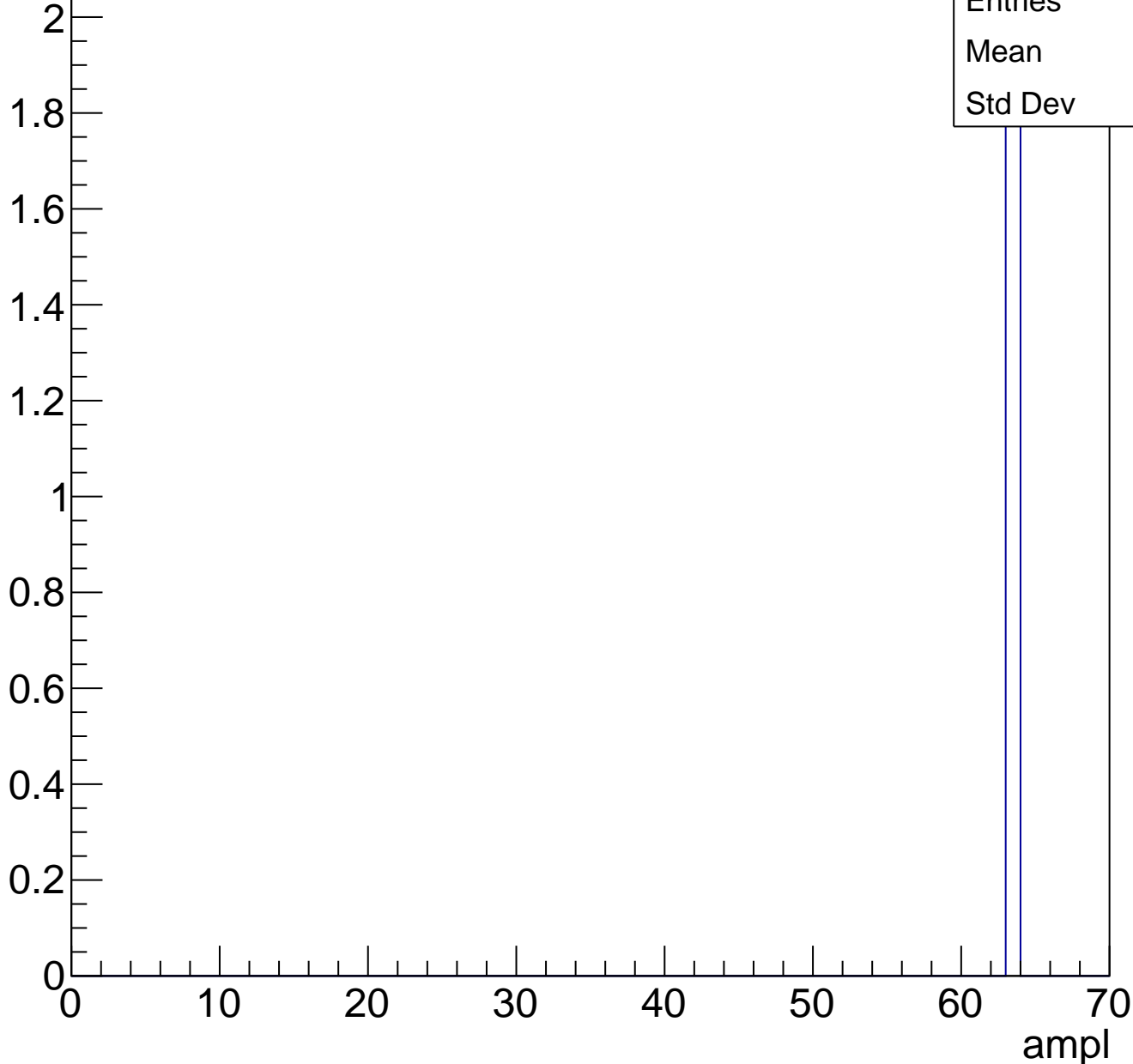
Entries	38
Mean	58.84
Std Dev	9.919



B0L001S, U11-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch4, adc0

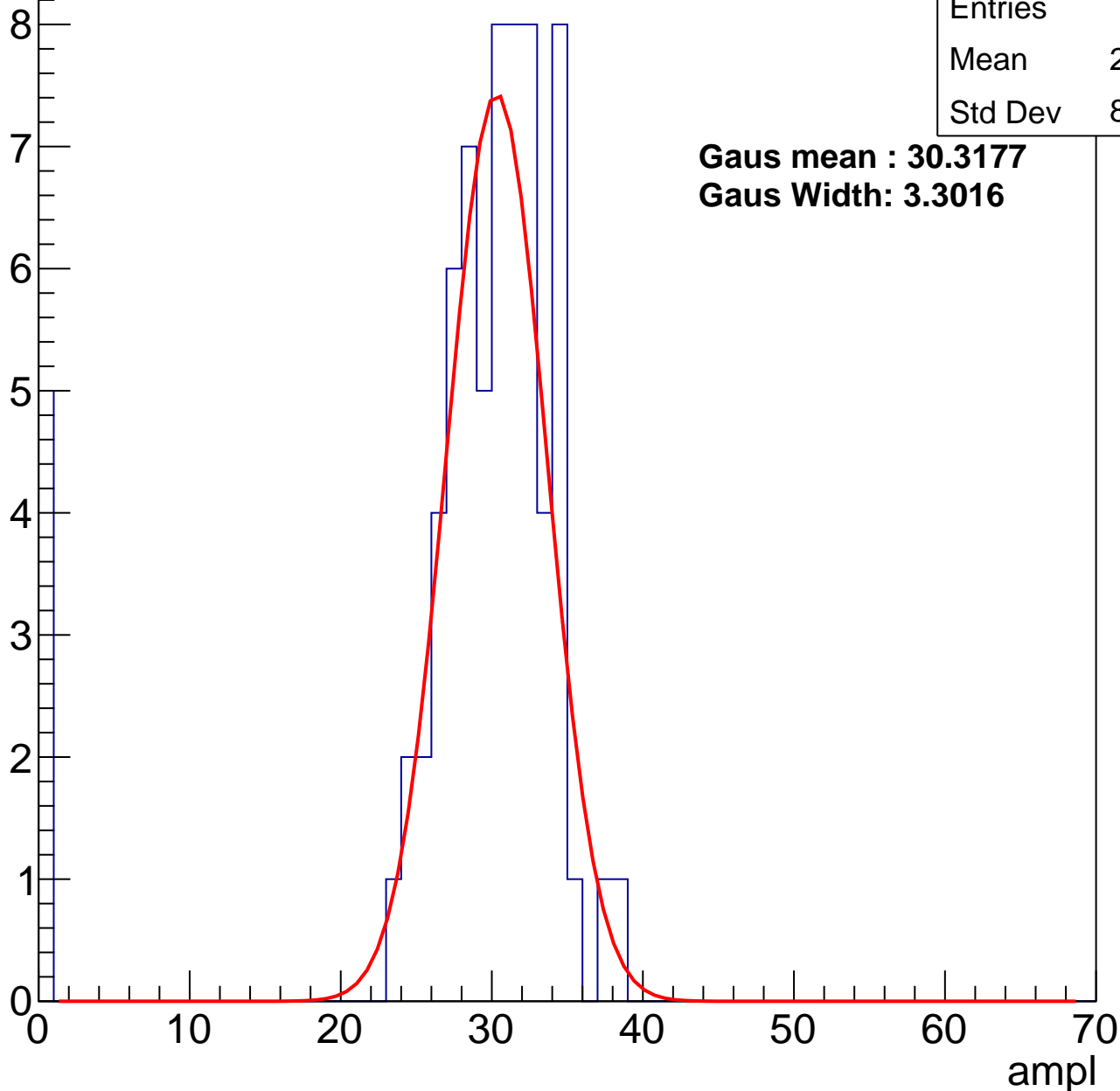
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	27.97
Std Dev	8.284

Gaus mean : 30.3177

Gaus Width: 3.3016



B0L001S, U11-ch4, adc1

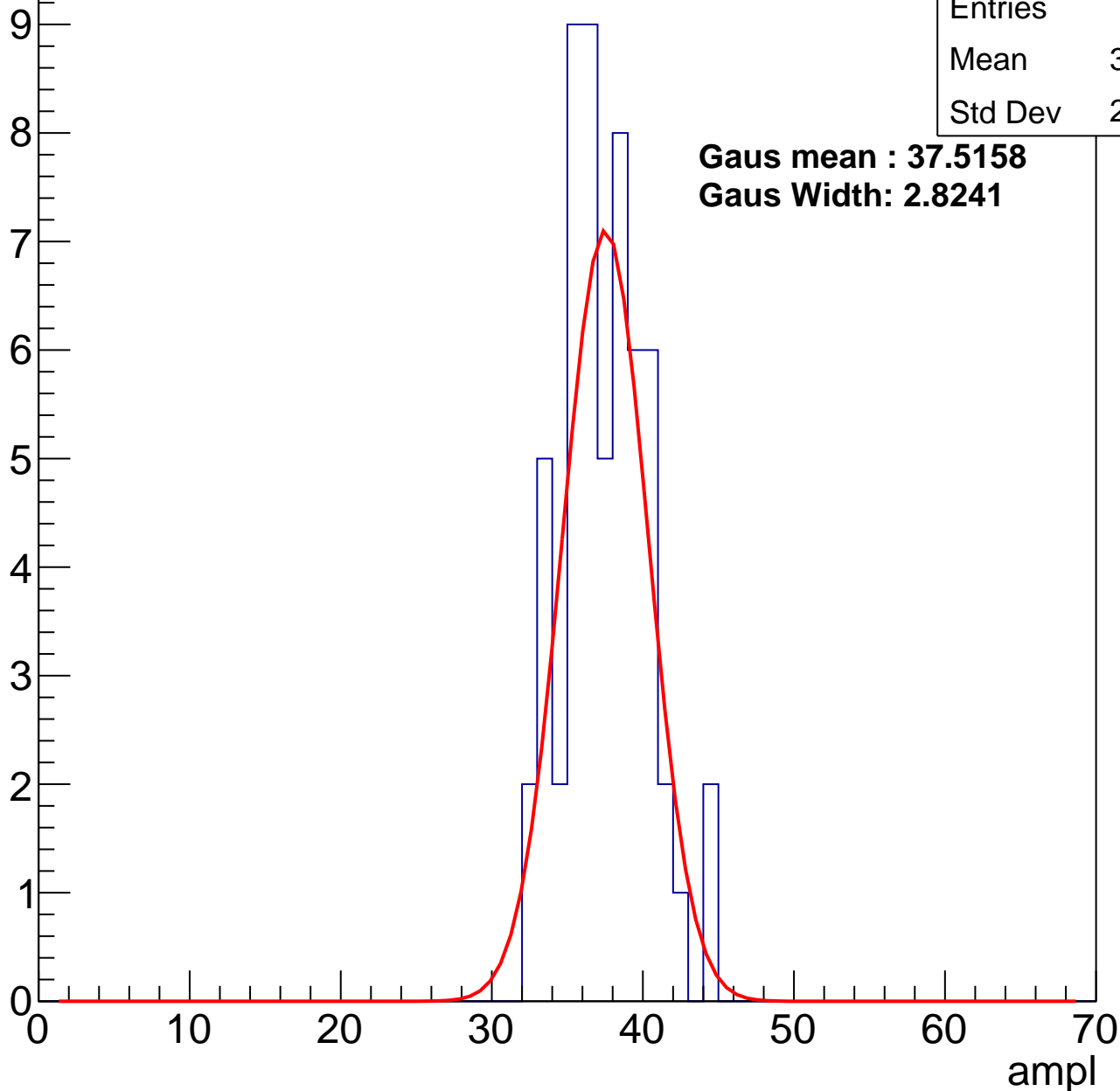
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.04
Std Dev	2.759

Gaus mean : 37.5158

Gaus Width: 2.8241



B0L001S, U11-ch4, adc2

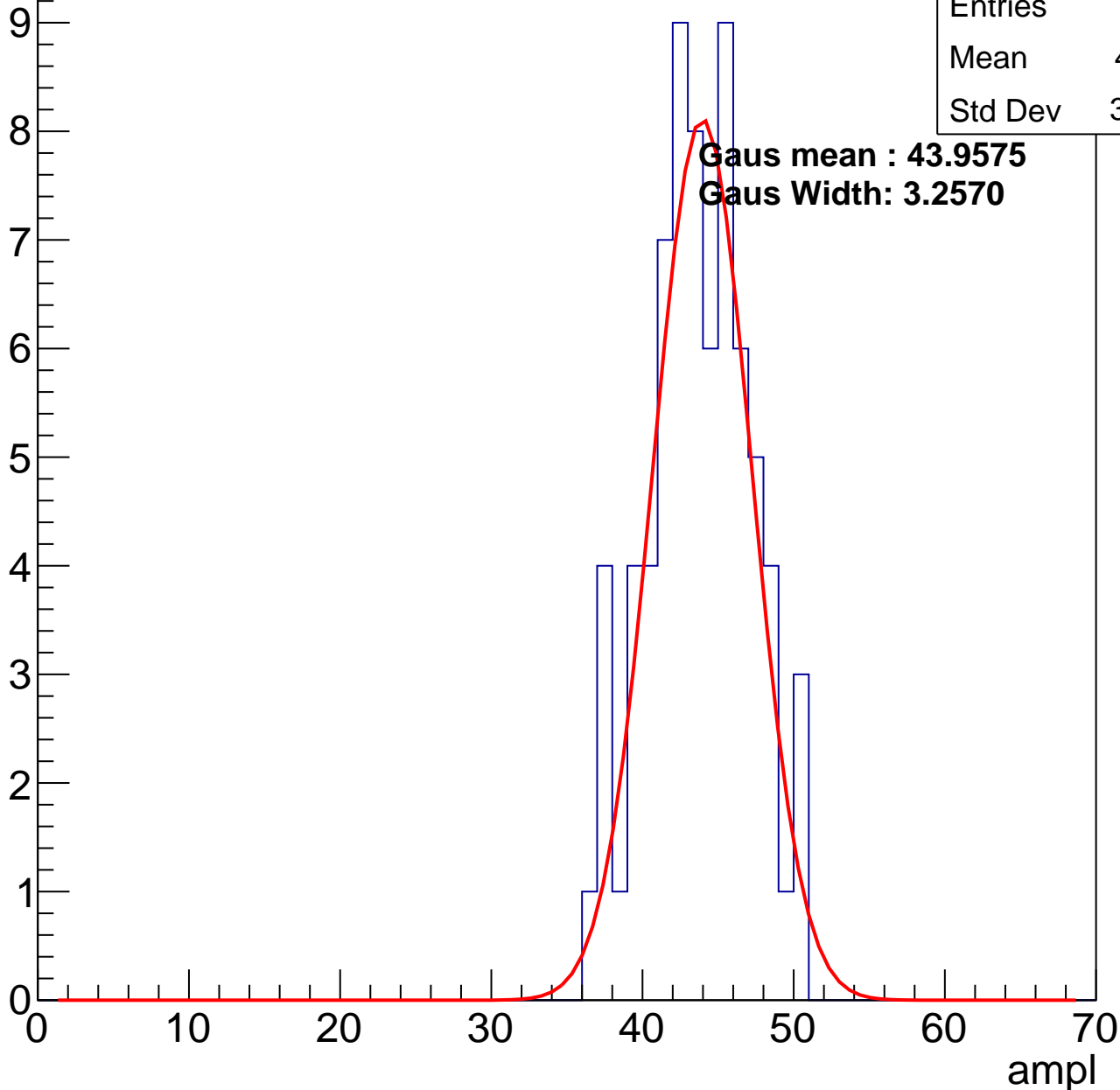
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.31
Std Dev	3.348

Gaus mean : 43.9575

Gaus Width: 3.2570

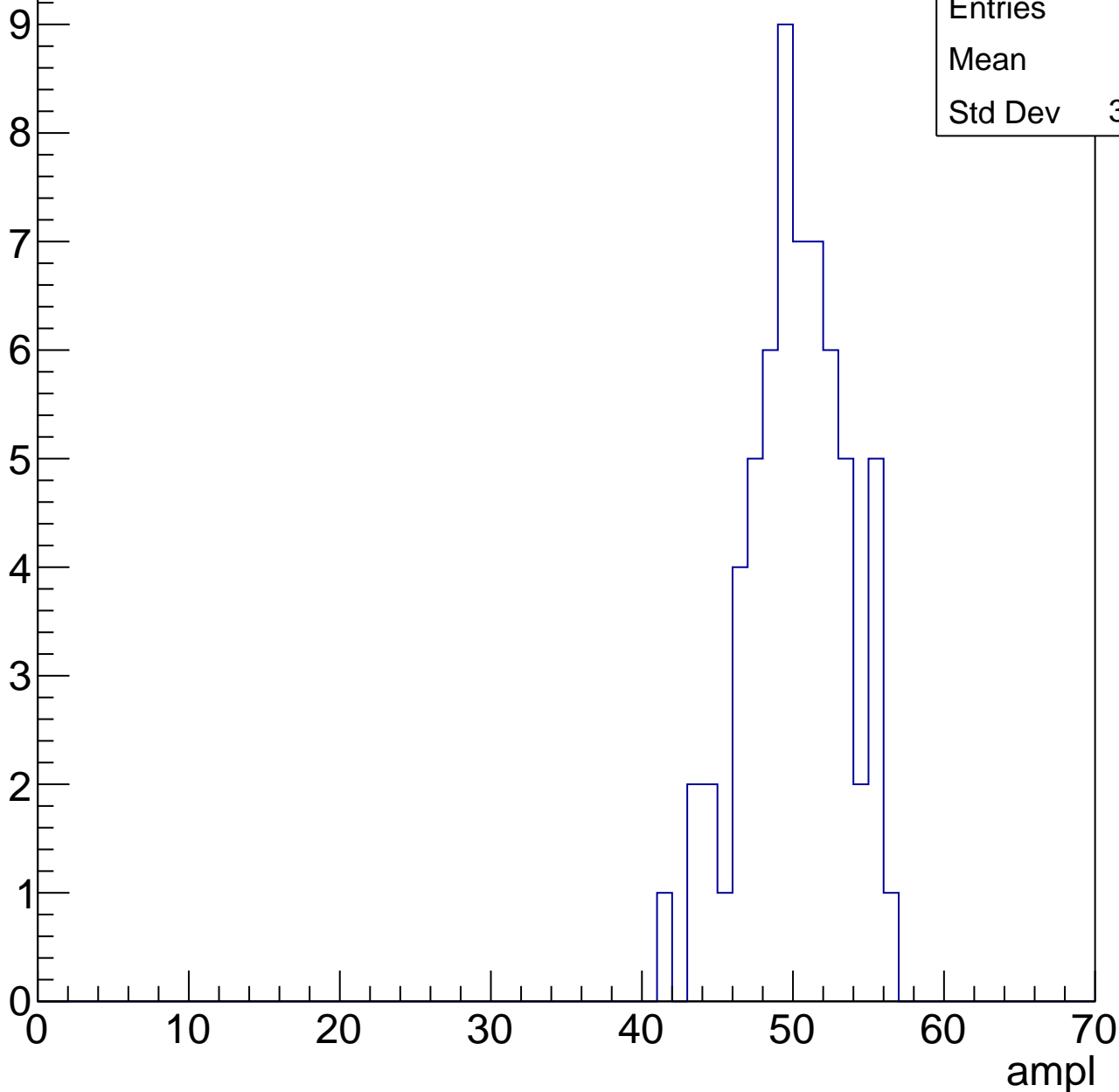


B0L001S, U11-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	49.7
Std Dev	3.288

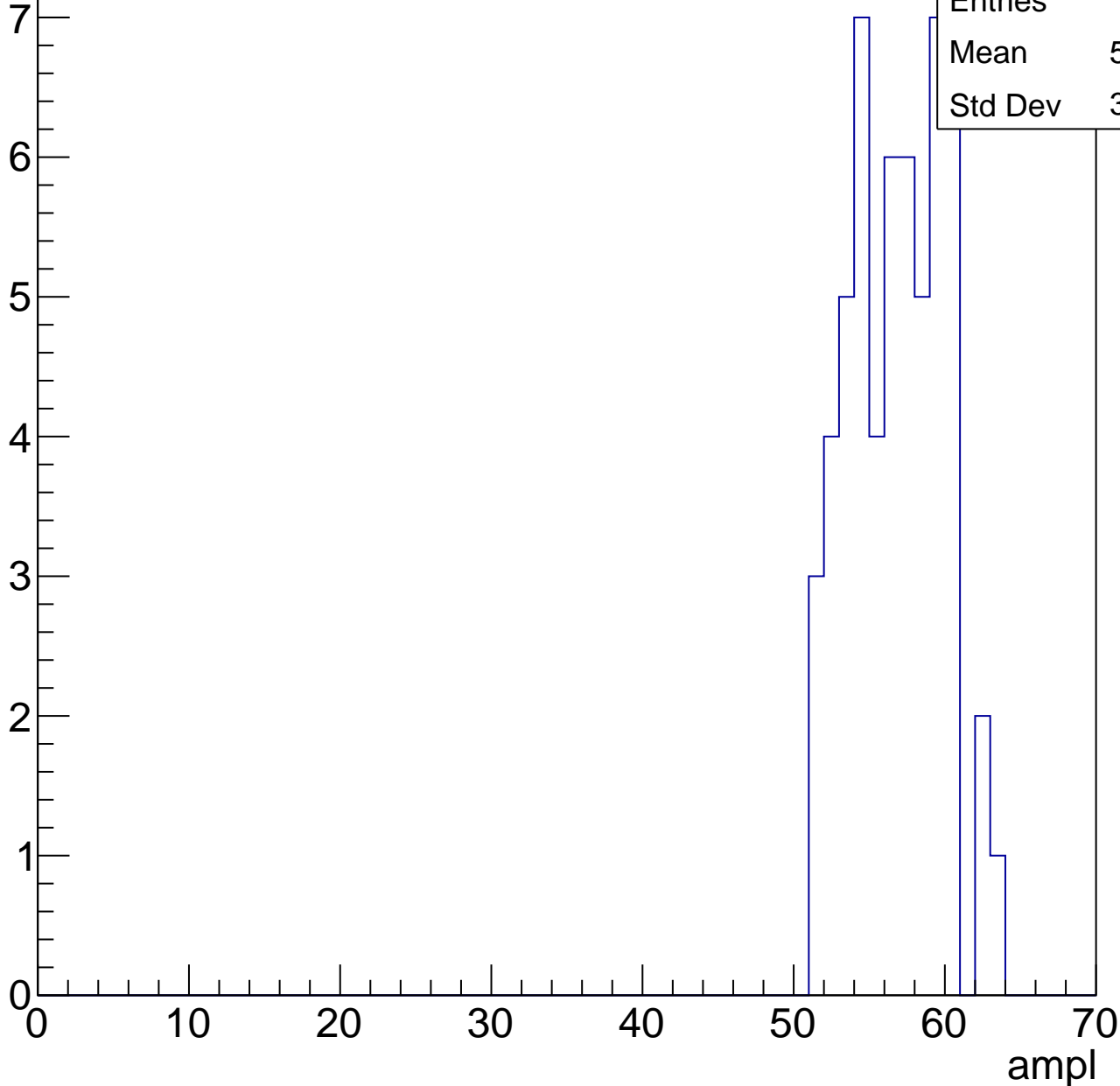


B0L001S, U11-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.35
Std Dev	3.052

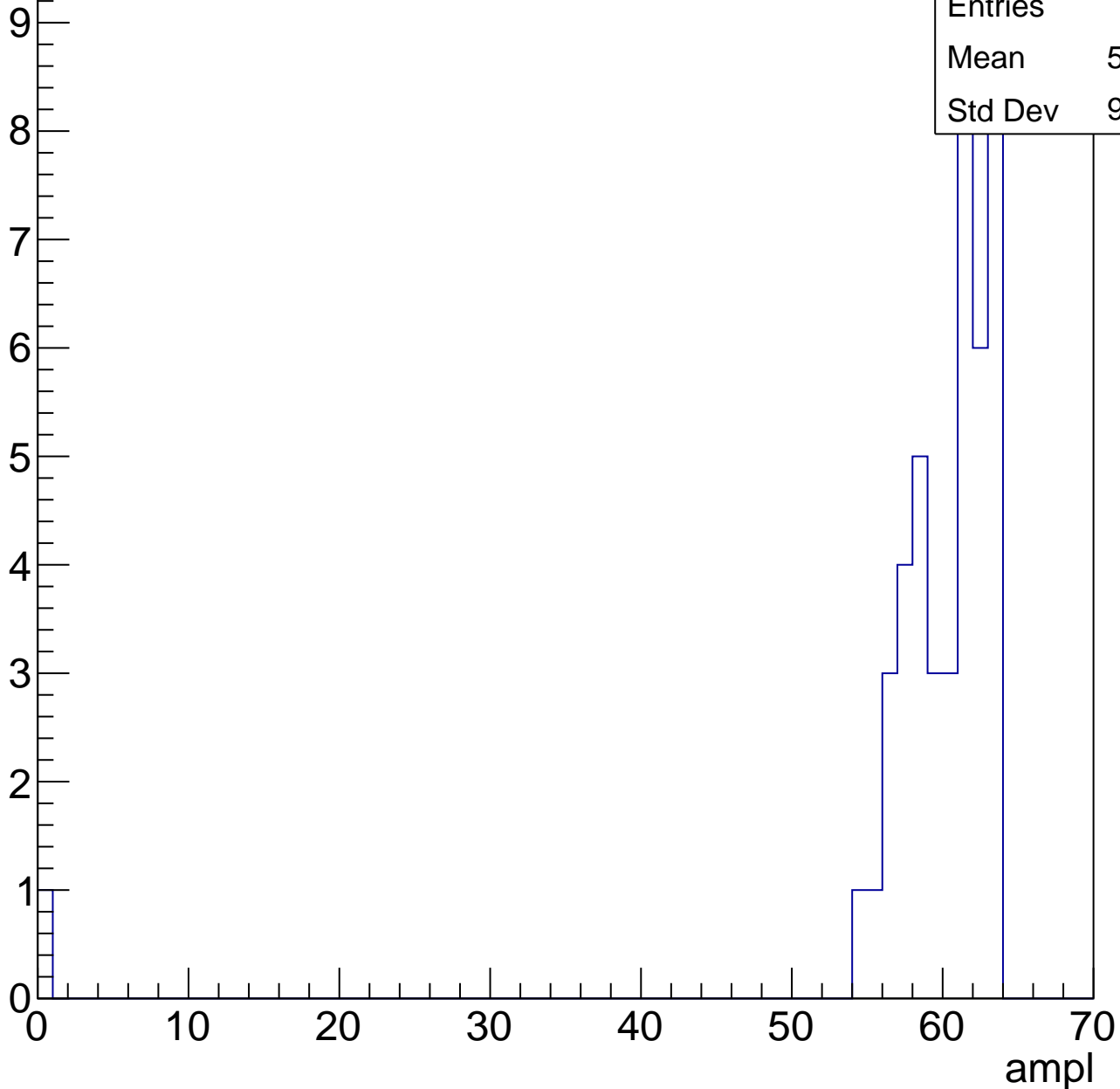


B0L001S, U11-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

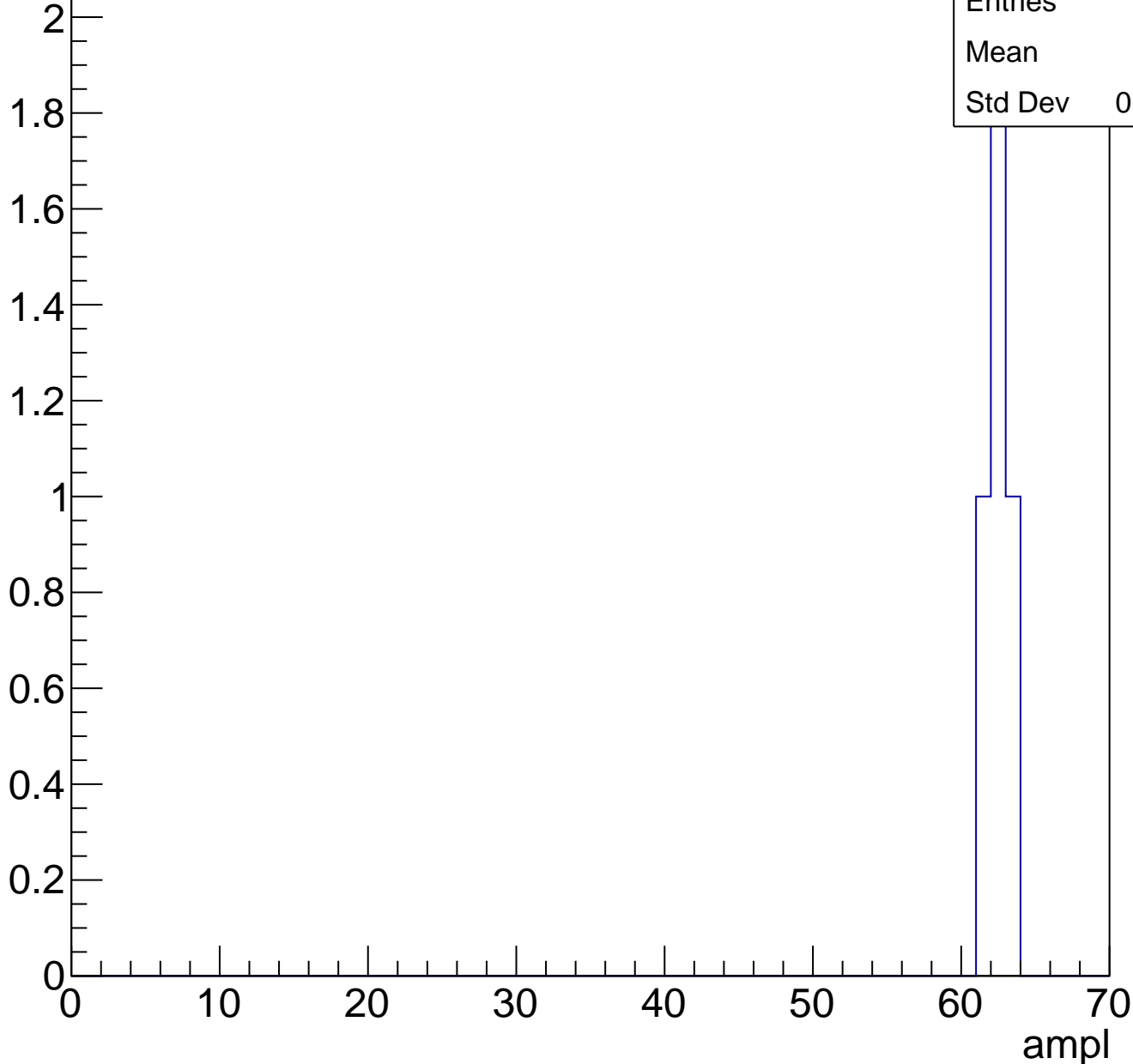
Entries	44
Mean	58.57
Std Dev	9.272



B0L001S, U11-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch5, adc0

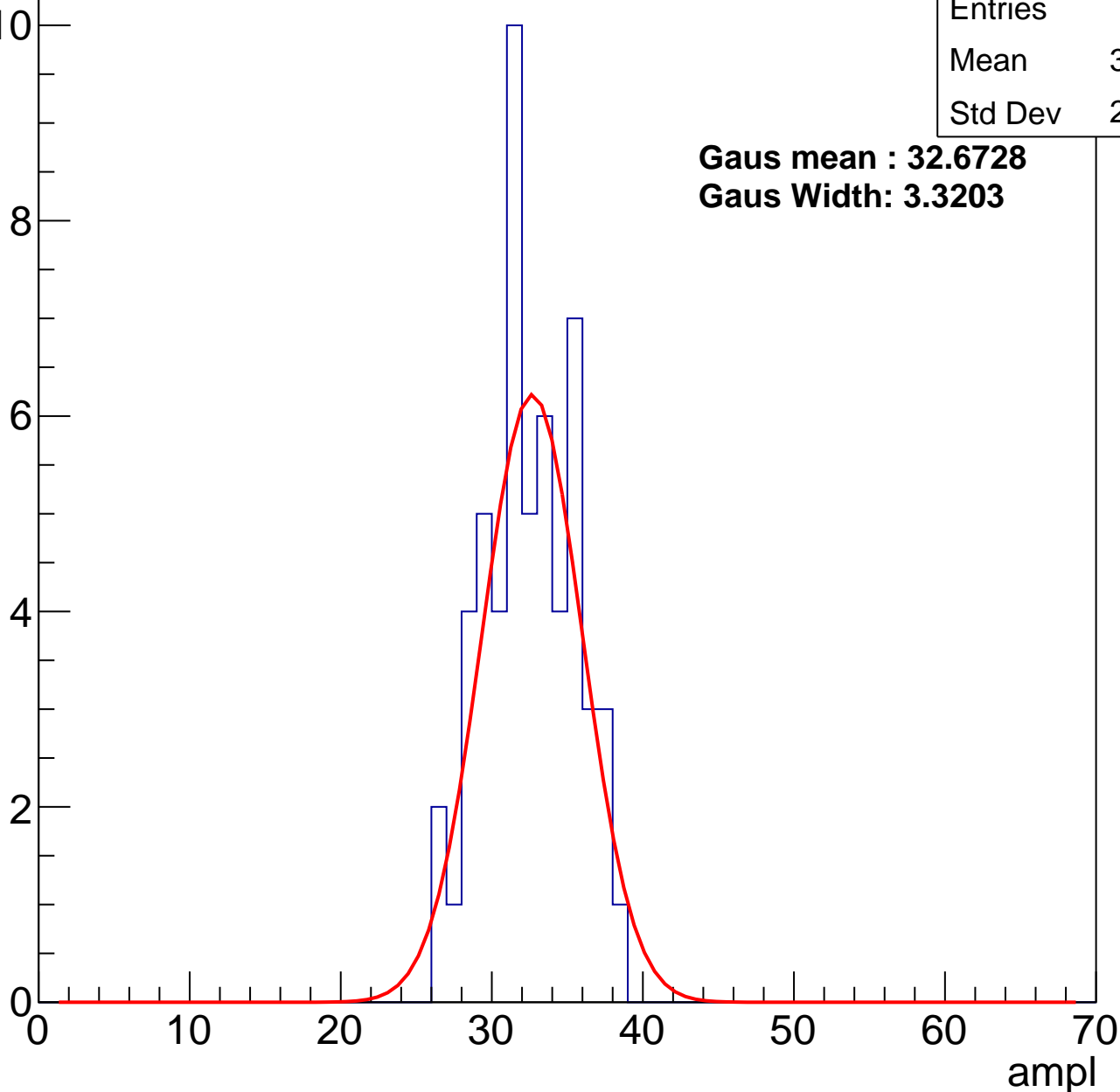
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	32.04
Std Dev	2.942

Gaus mean : 32.6728

Gaus Width: 3.3203



B0L001S, U11-ch5, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	39.13
Std Dev	3.591

Gaus mean : 39.5294

Gaus Width: 4.1286

10

8

6

4

2

0

0

10

20

30

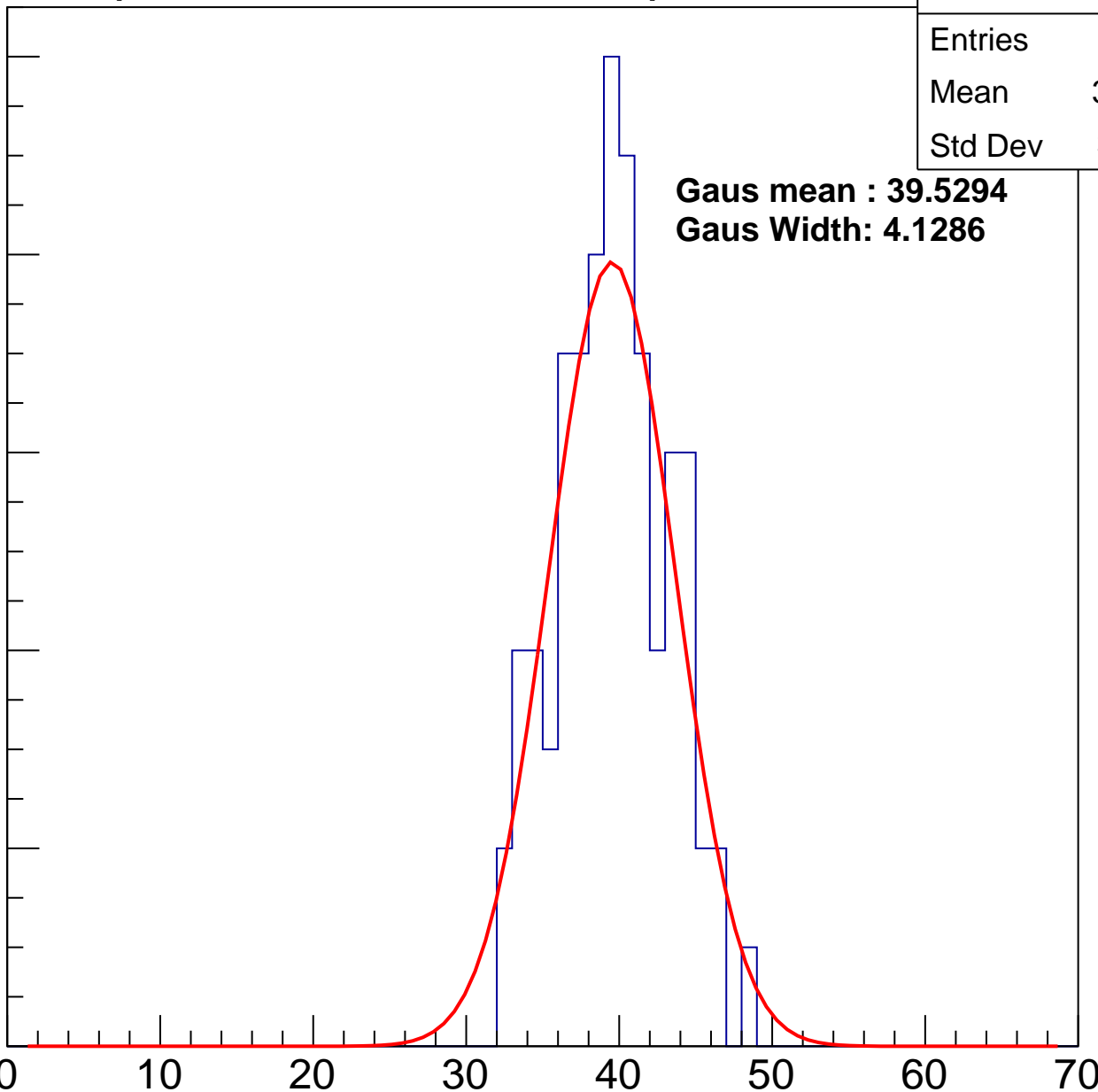
40

50

60

70

ampl



B0L001S, U11-ch5, adc2

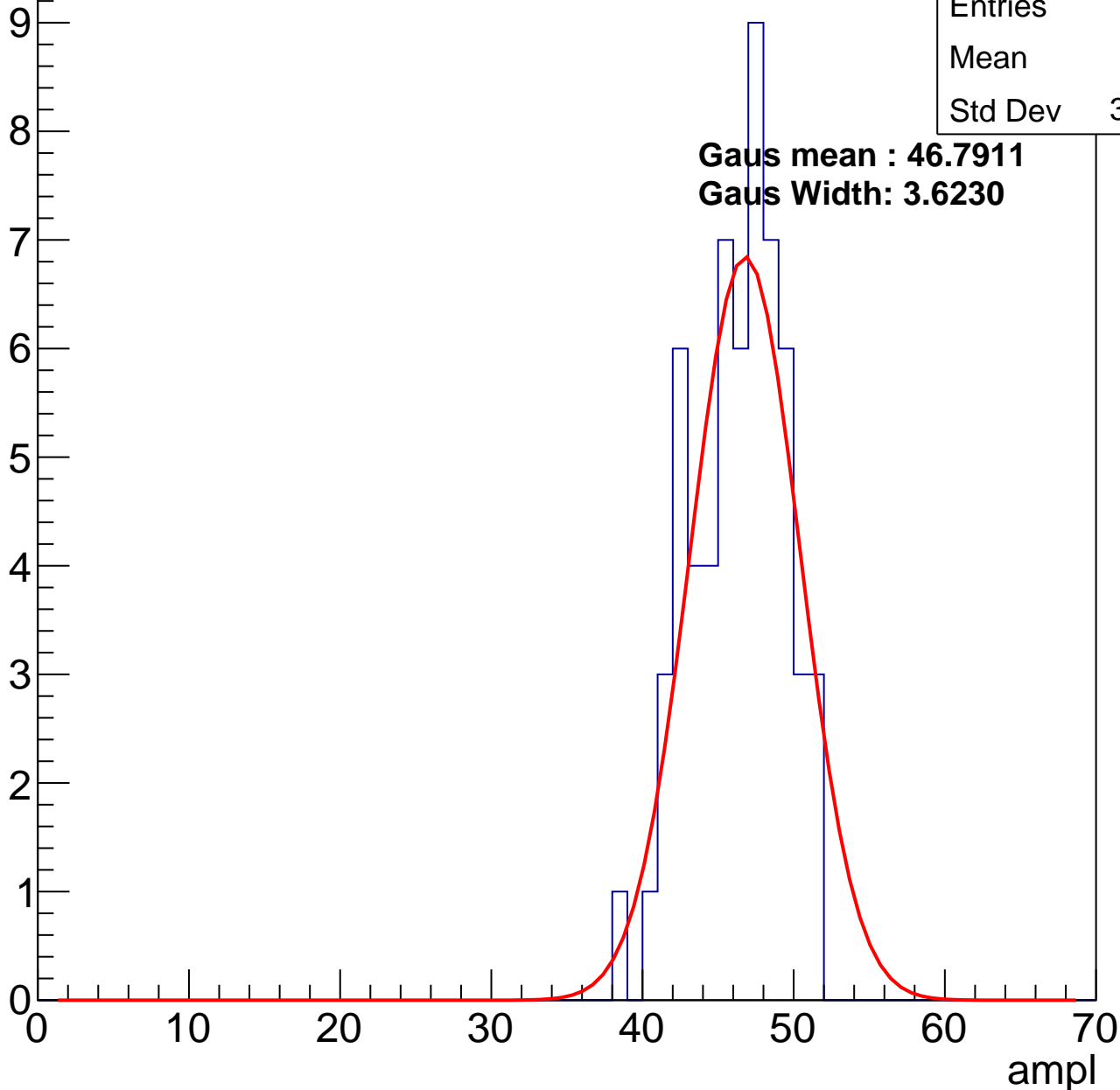
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45.8
Std Dev	3.004

Gaus mean : 46.7911

Gaus Width: 3.6230

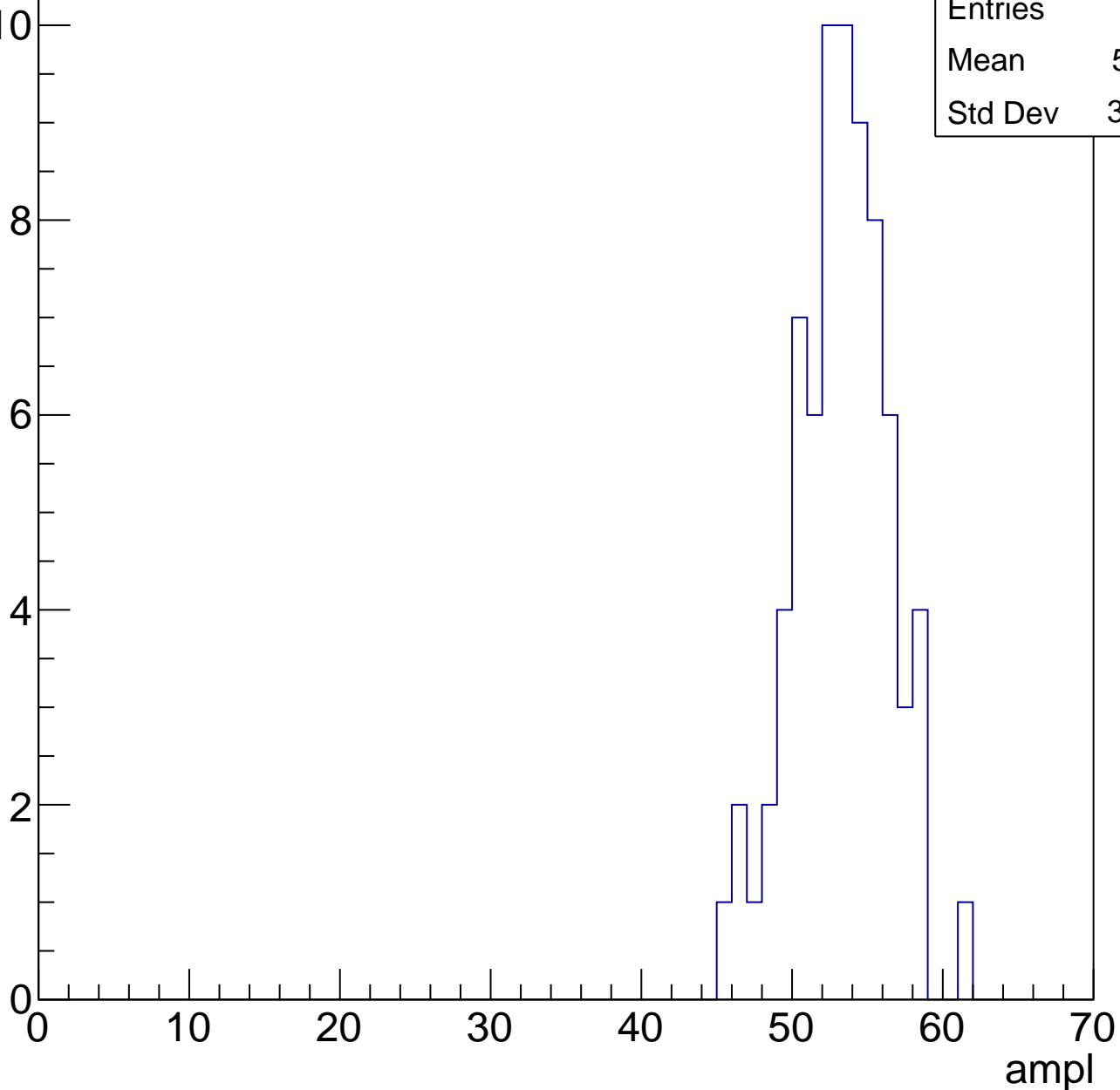


B0L001S, U11-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	52.81
Std Dev	3.105

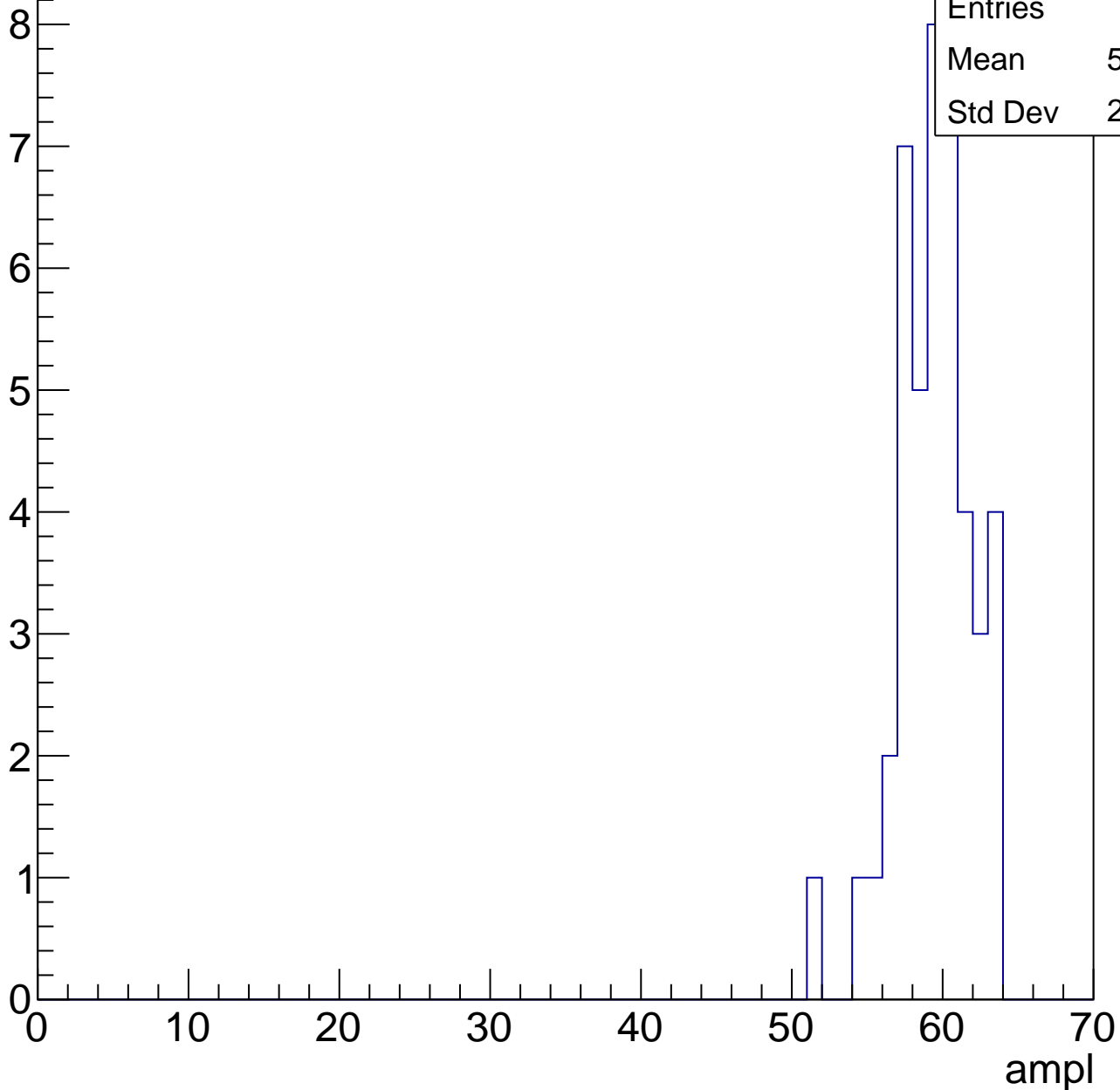


B0L001S, U11-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.98
Std Dev	2.482

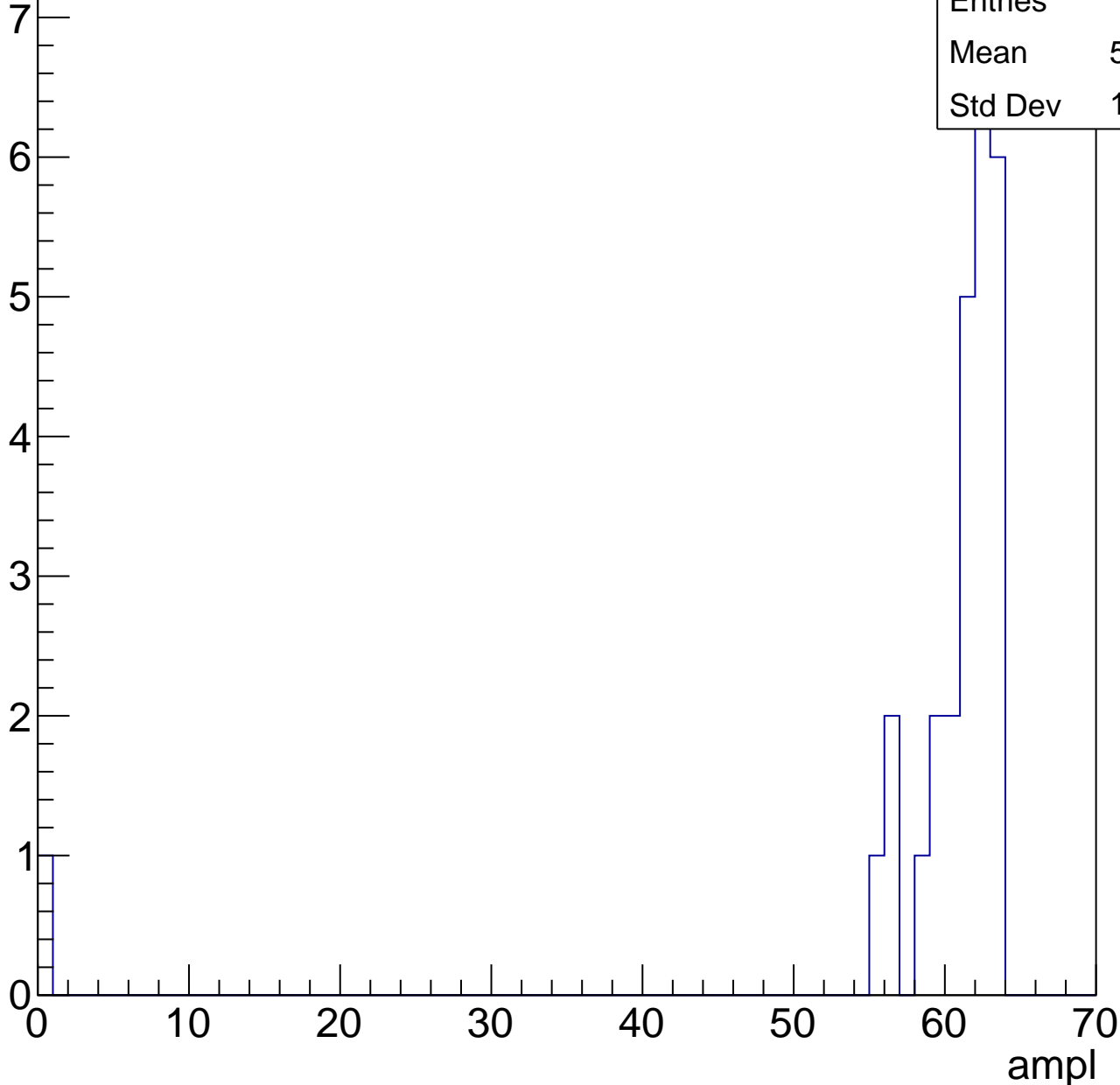


B0L001S, U11-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.52
Std Dev	11.69



B0L001S, U11-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch6, adc0

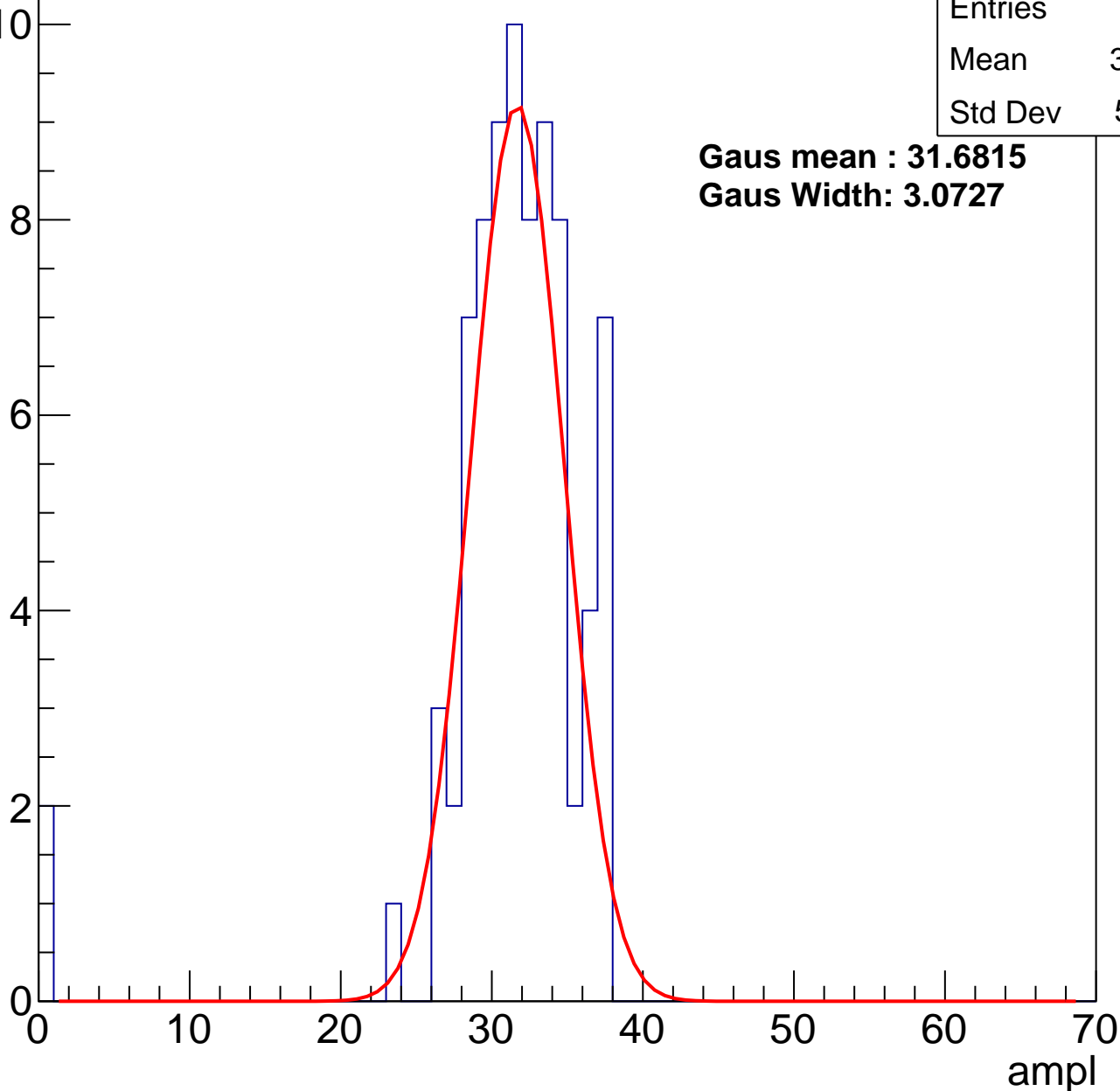
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.76
Std Dev	5.801

Gaus mean : 31.6815

Gaus Width: 3.0727



B0L001S, U11-ch6, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	38.99
Std Dev	3.096

Gaus mean : 39.8052

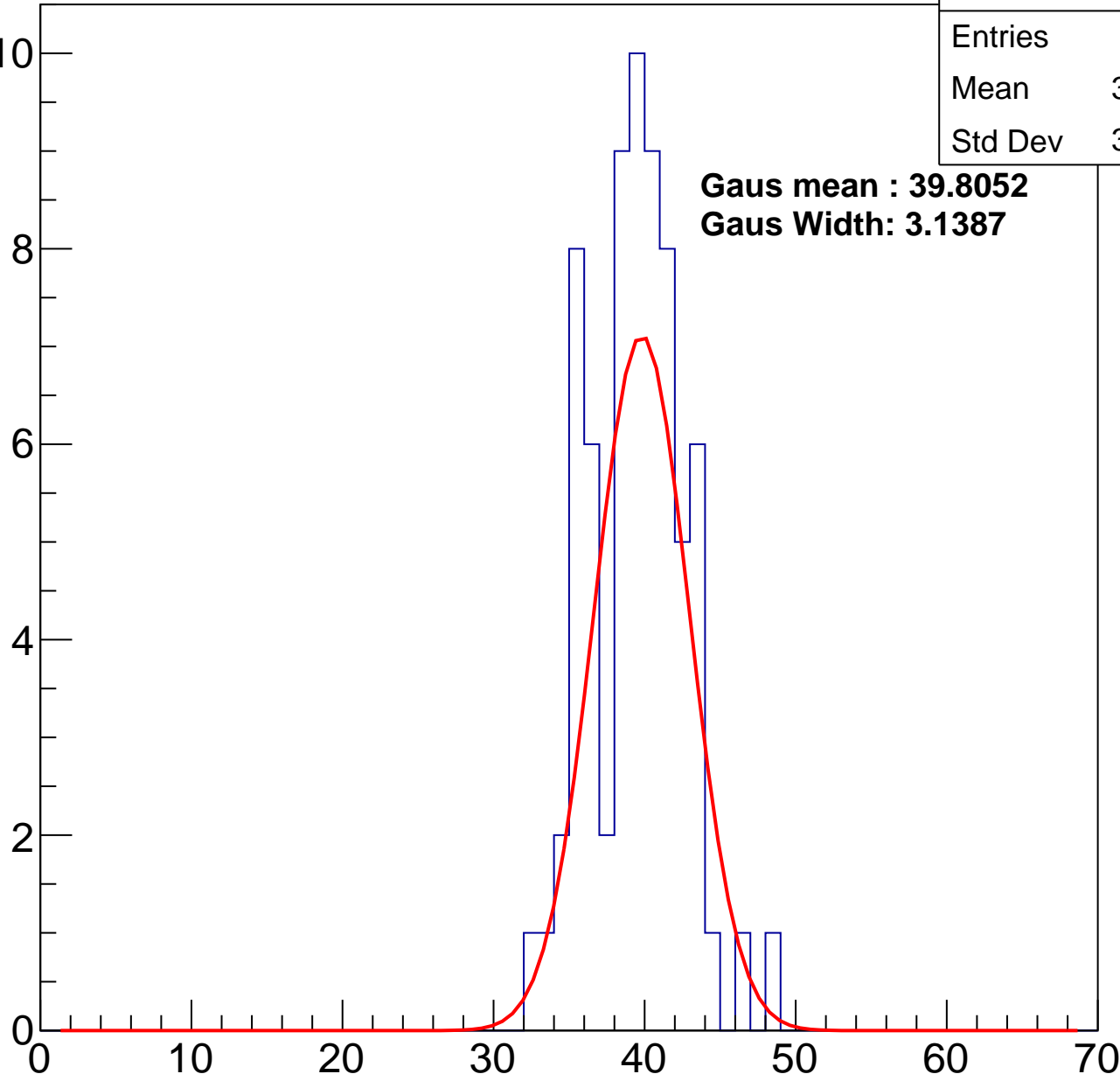
Gaus Width: 3.1387

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch6, adc2

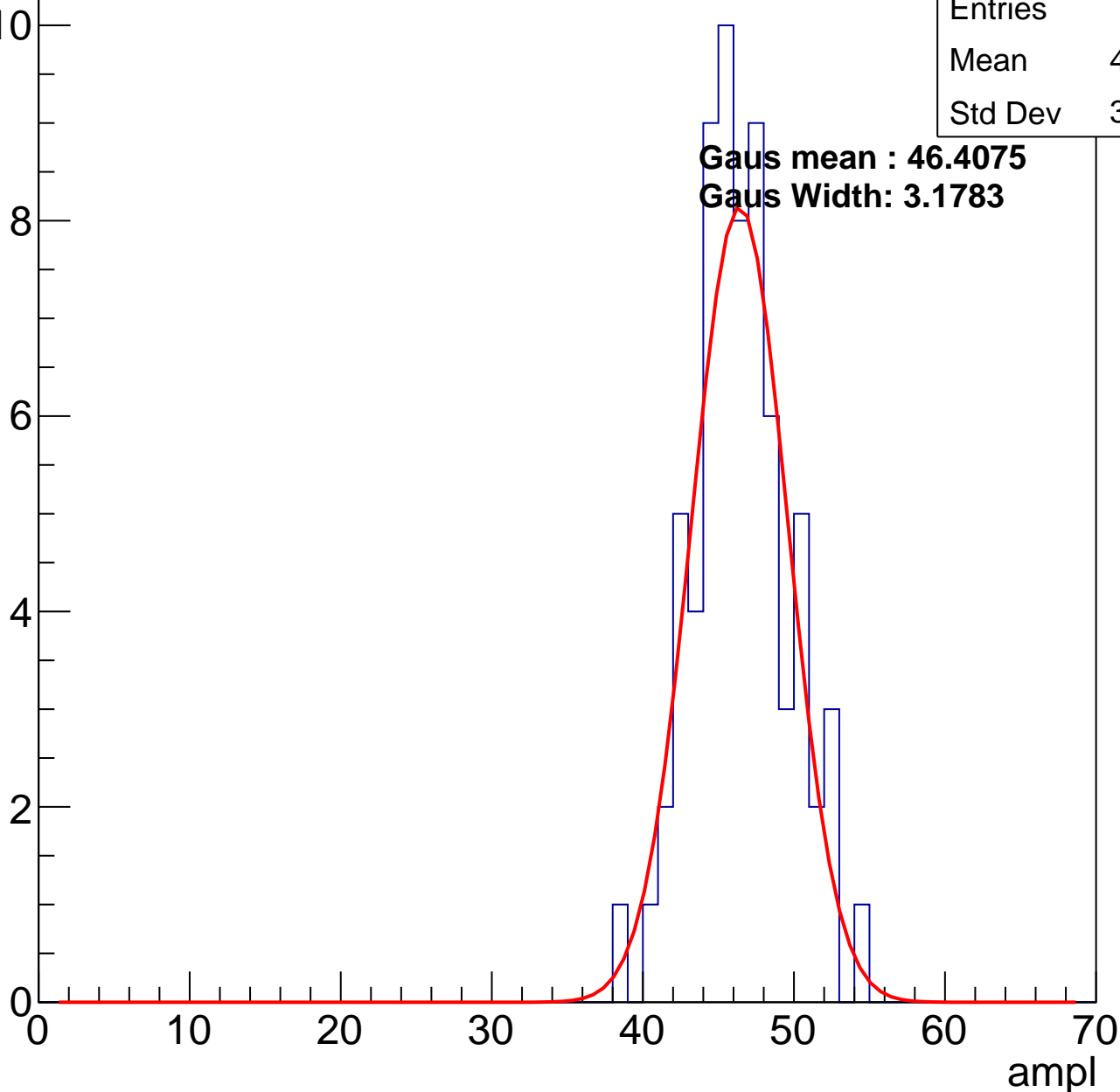
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	46.03
Std Dev	3.125

Gaus mean : 46.4075

Gaus Width: 3.1783

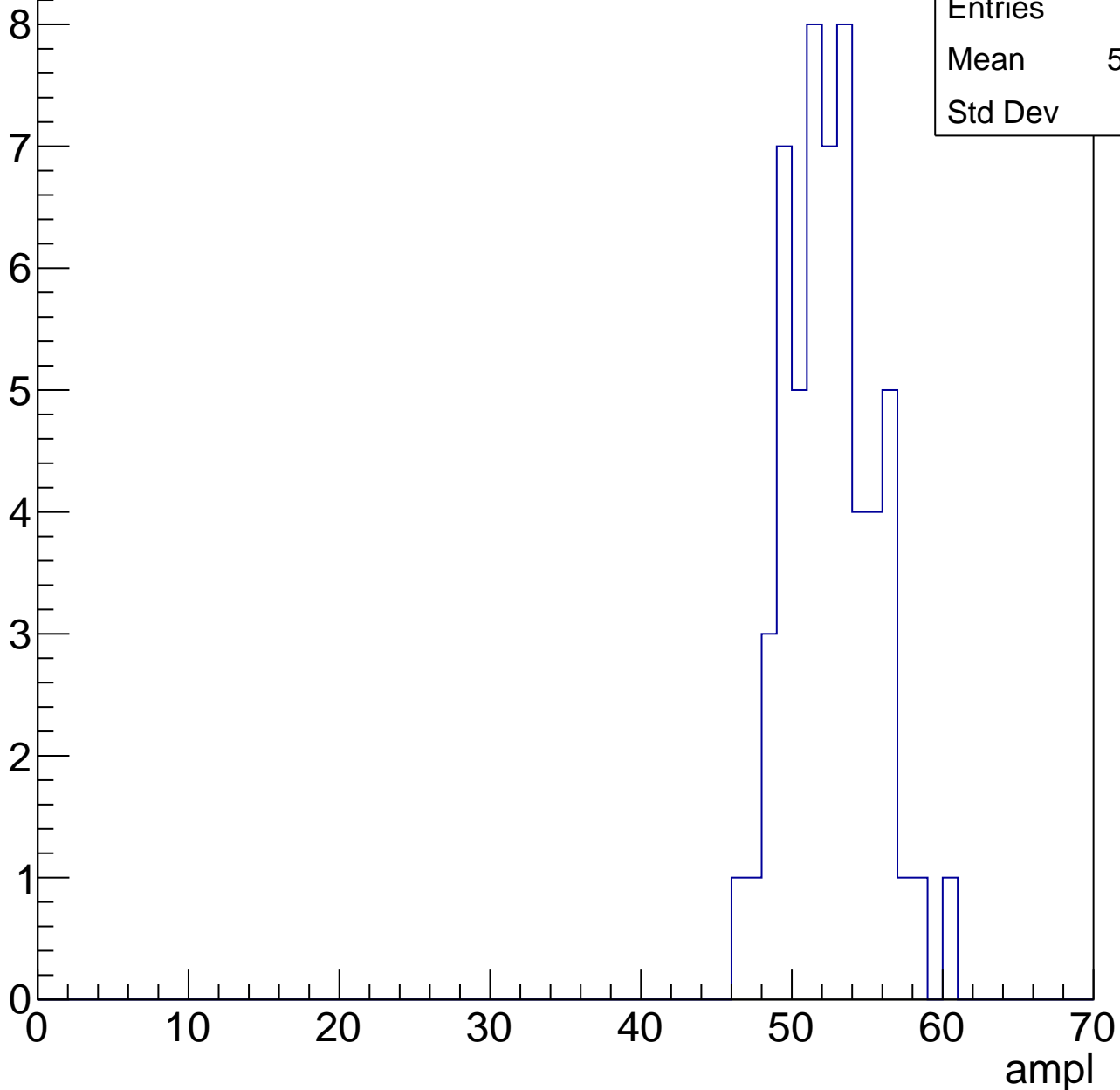


B0L001S, U11-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

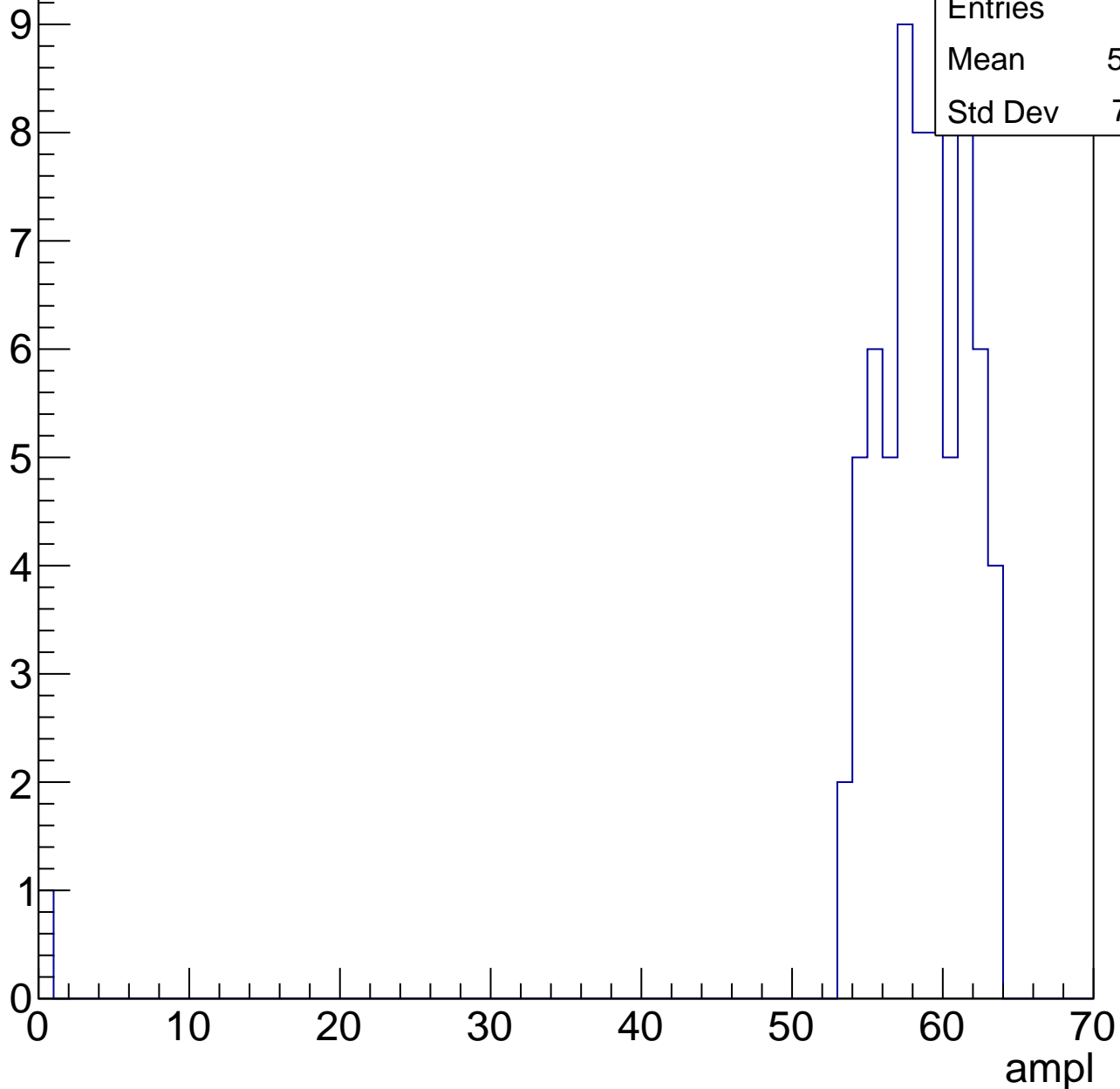
Entries	56
Mean	52.09
Std Dev	2.88



B0L001S, U11-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

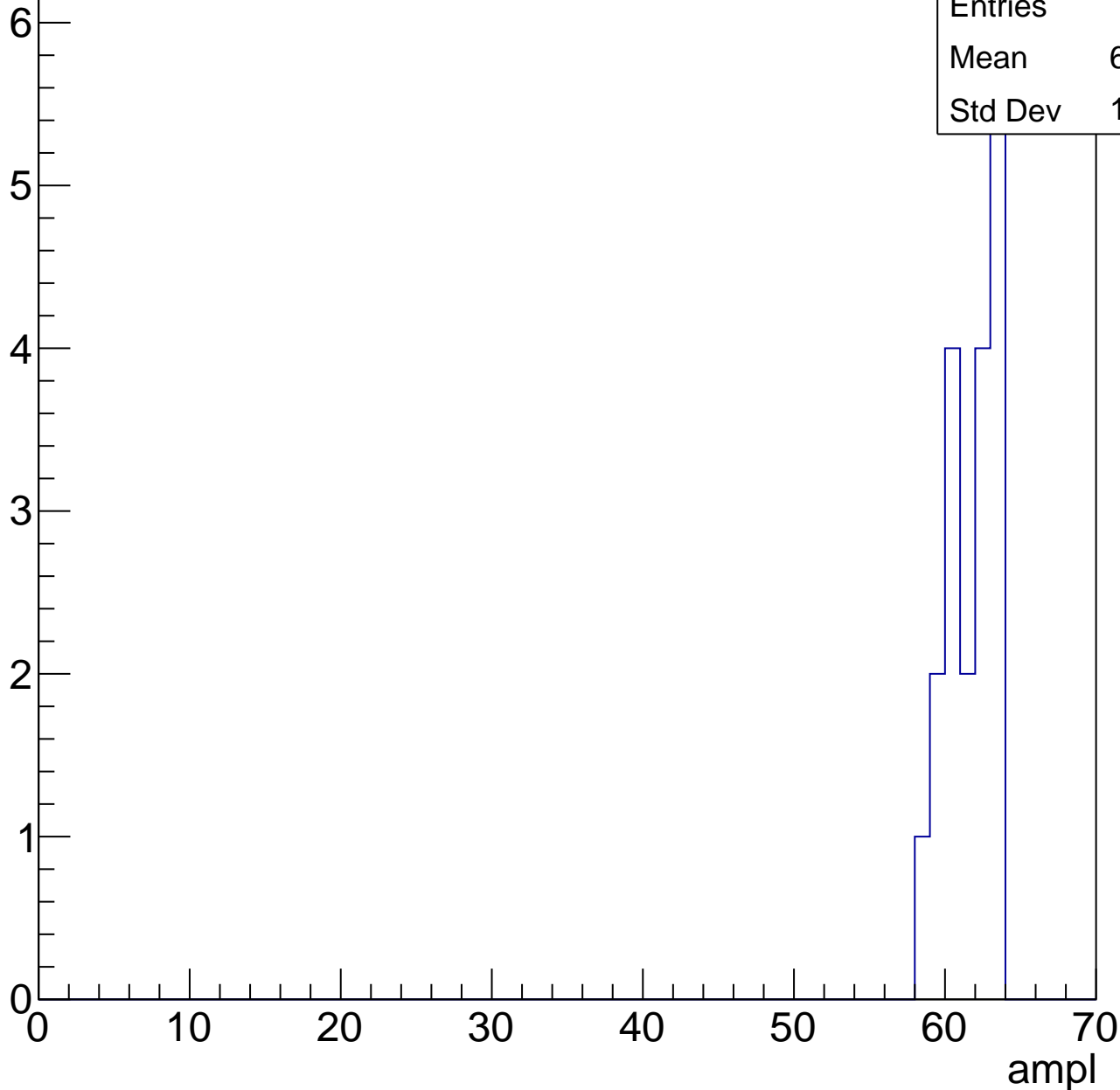


B0L001S, U11-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	61.26
Std Dev	1.584



B0L001S, U11-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch7, adc0

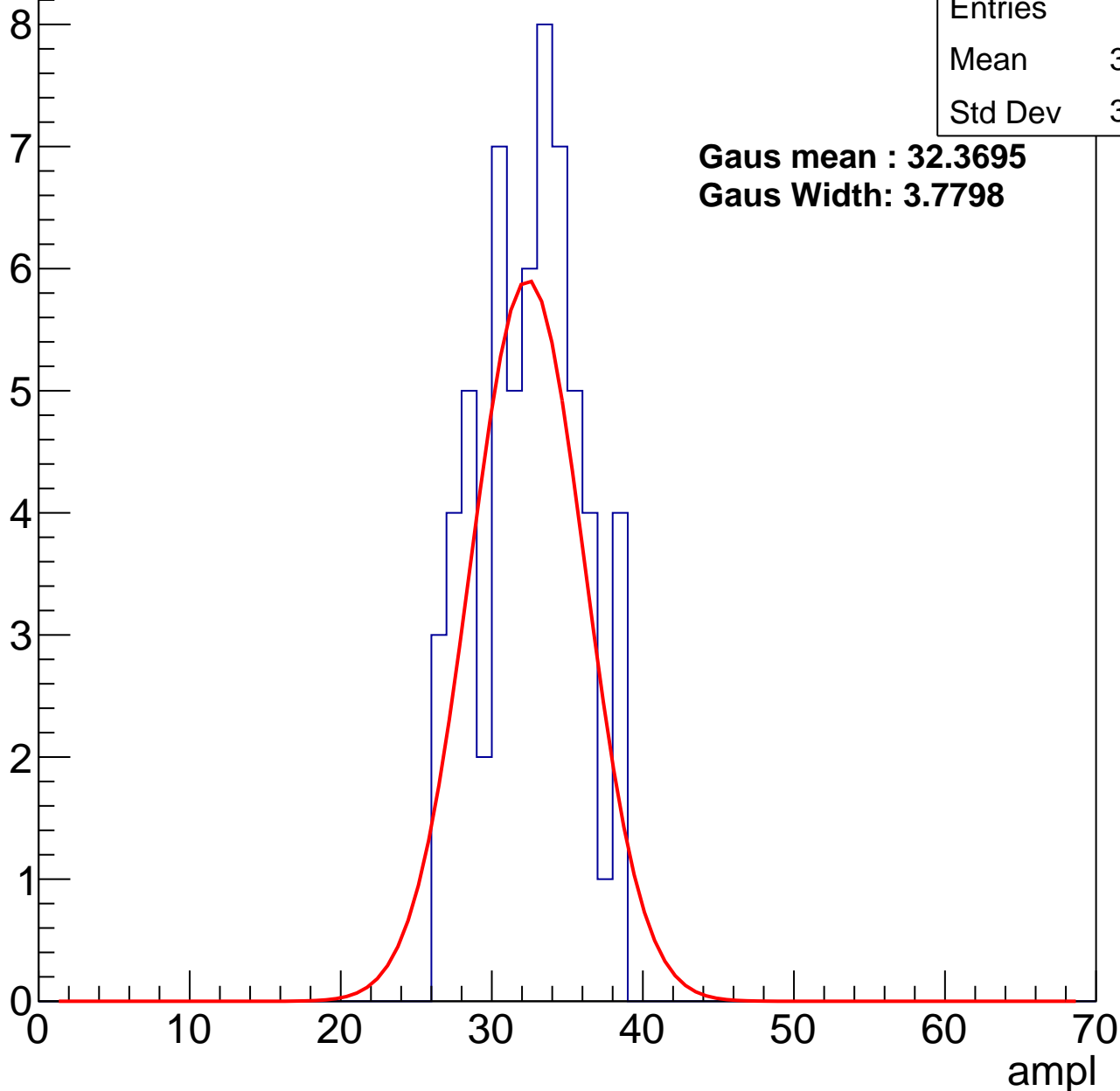
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.98
Std Dev	3.272

Gaus mean : 32.3695

Gaus Width: 3.7798



B0L001S, U11-ch7, adc1

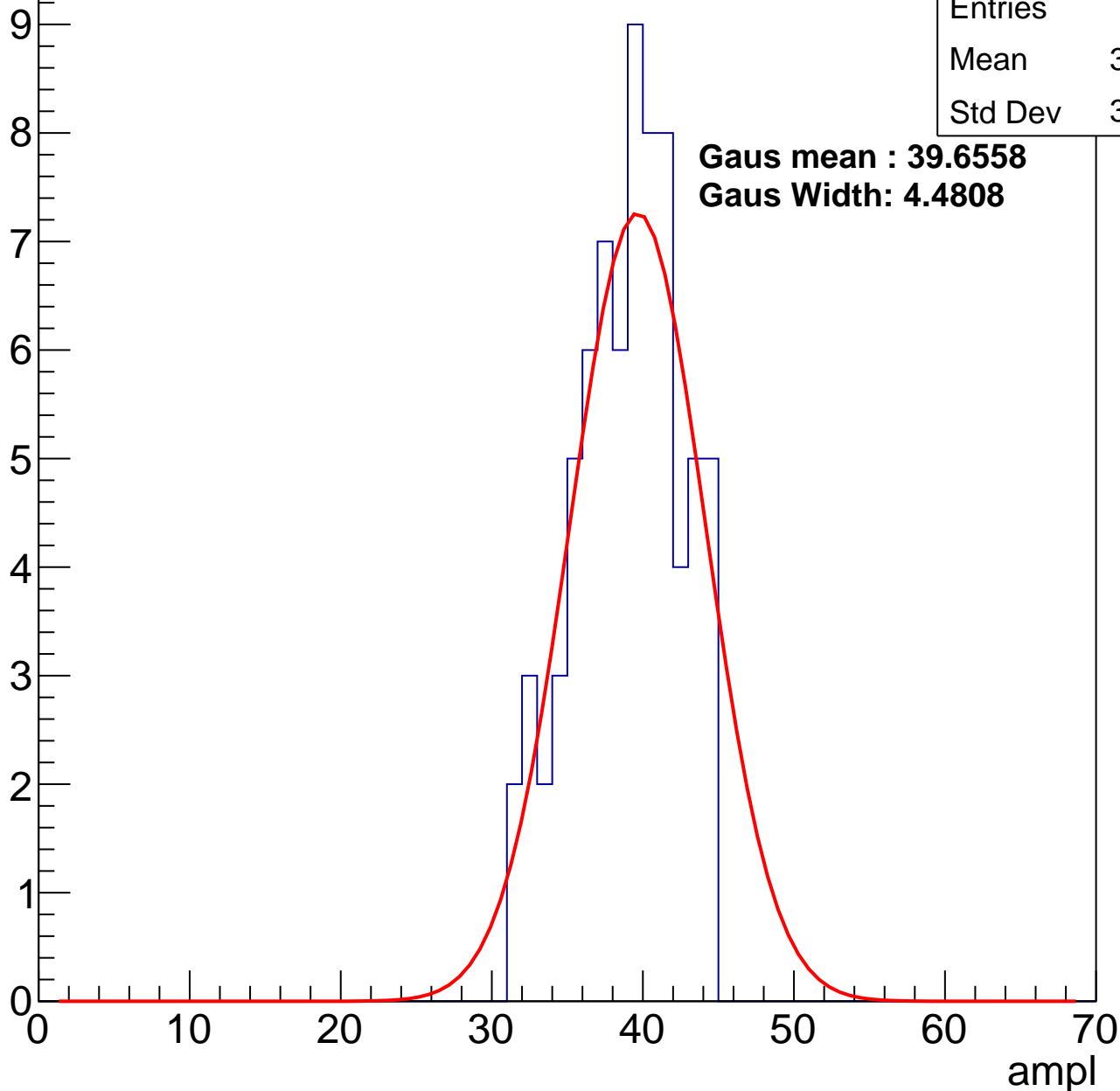
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	38.44
Std Dev	3.408

Gaus mean : 39.6558

Gaus Width: 4.4808



B0L001S, U11-ch7, adc2

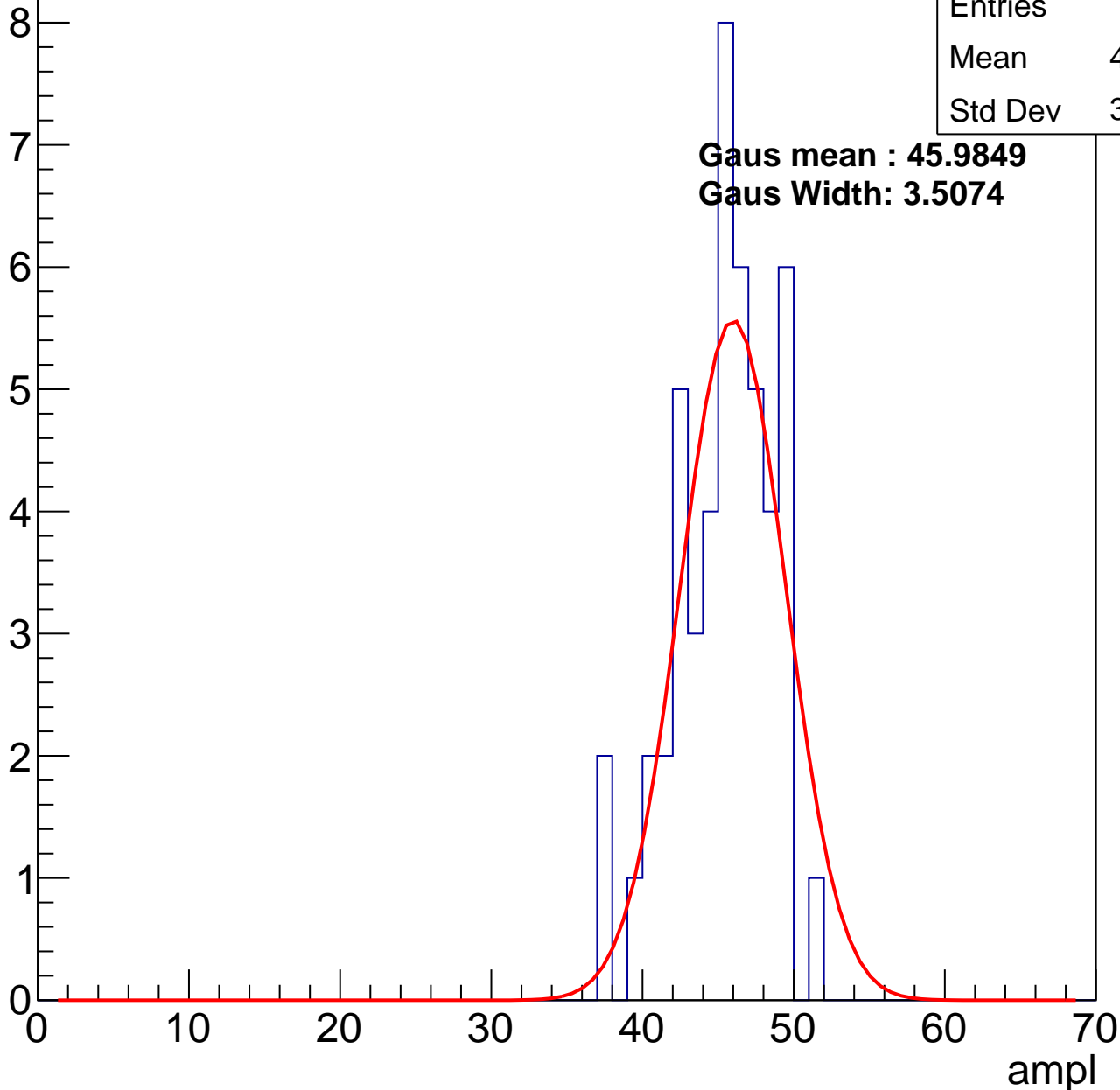
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	44.86
Std Dev	3.194

Gaus mean : 45.9849

Gaus Width: 3.5074

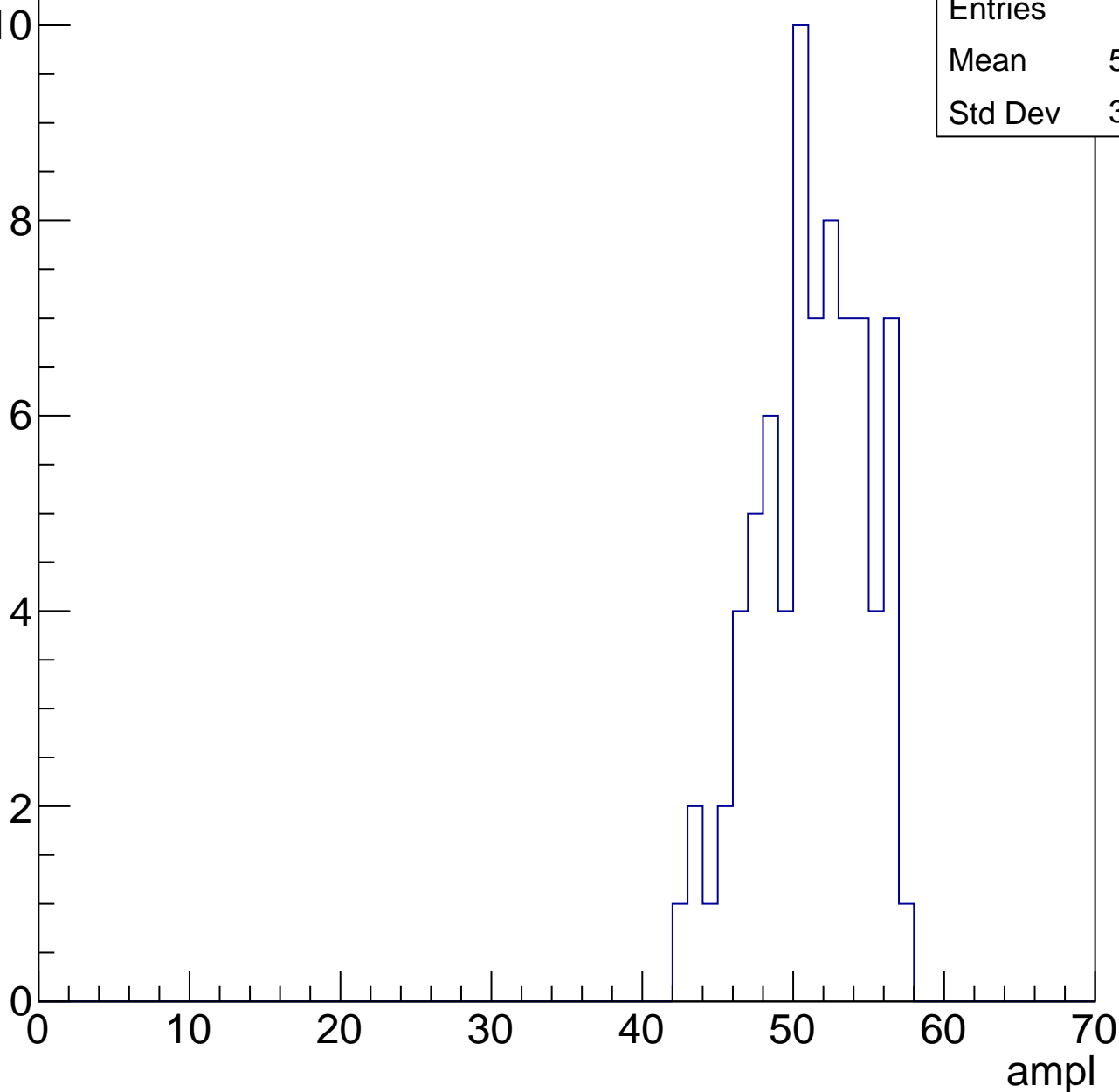


B0L001S, U11-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

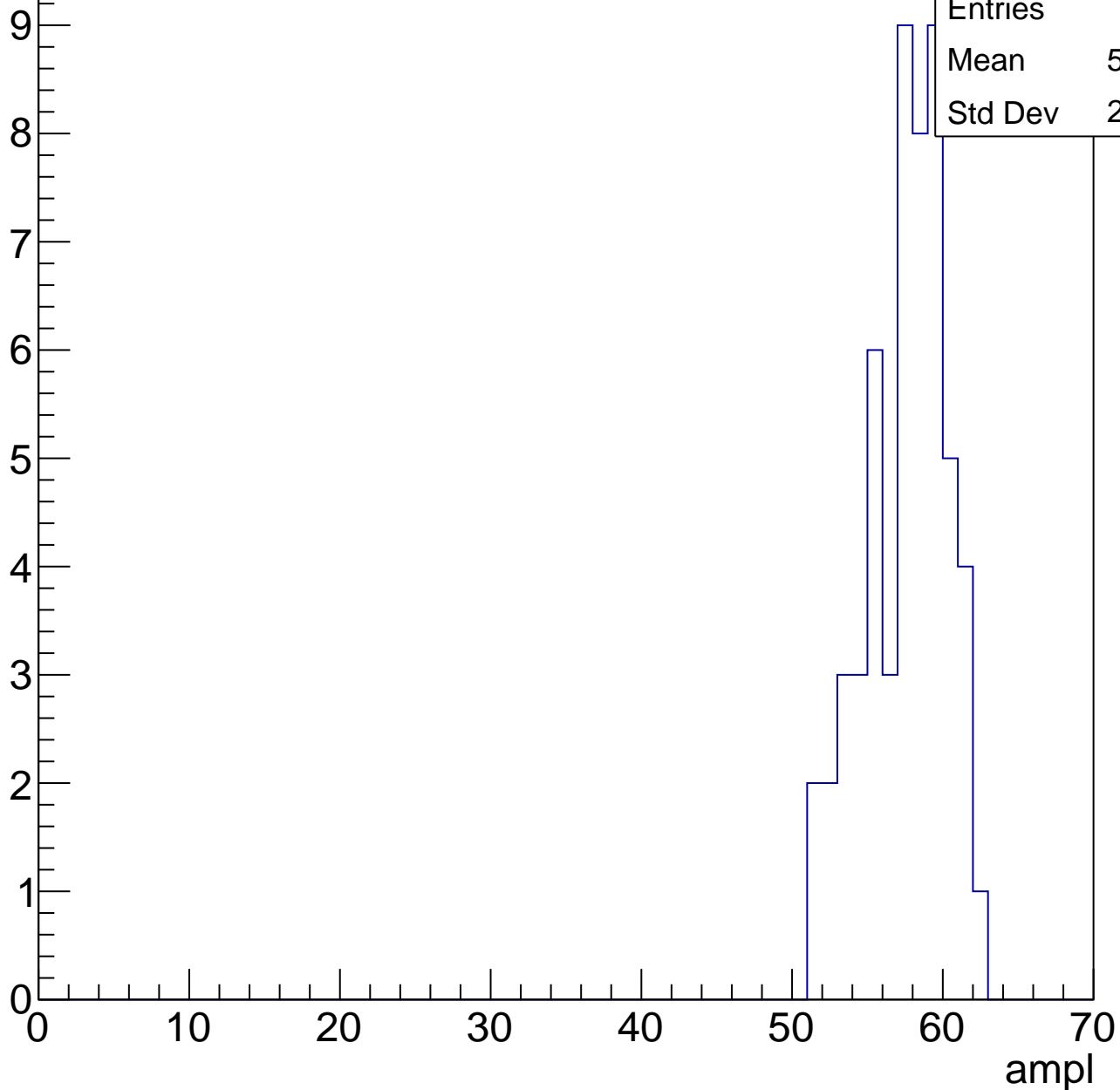
Entries	76
Mean	50.74
Std Dev	3.552



B0L001S, U11-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

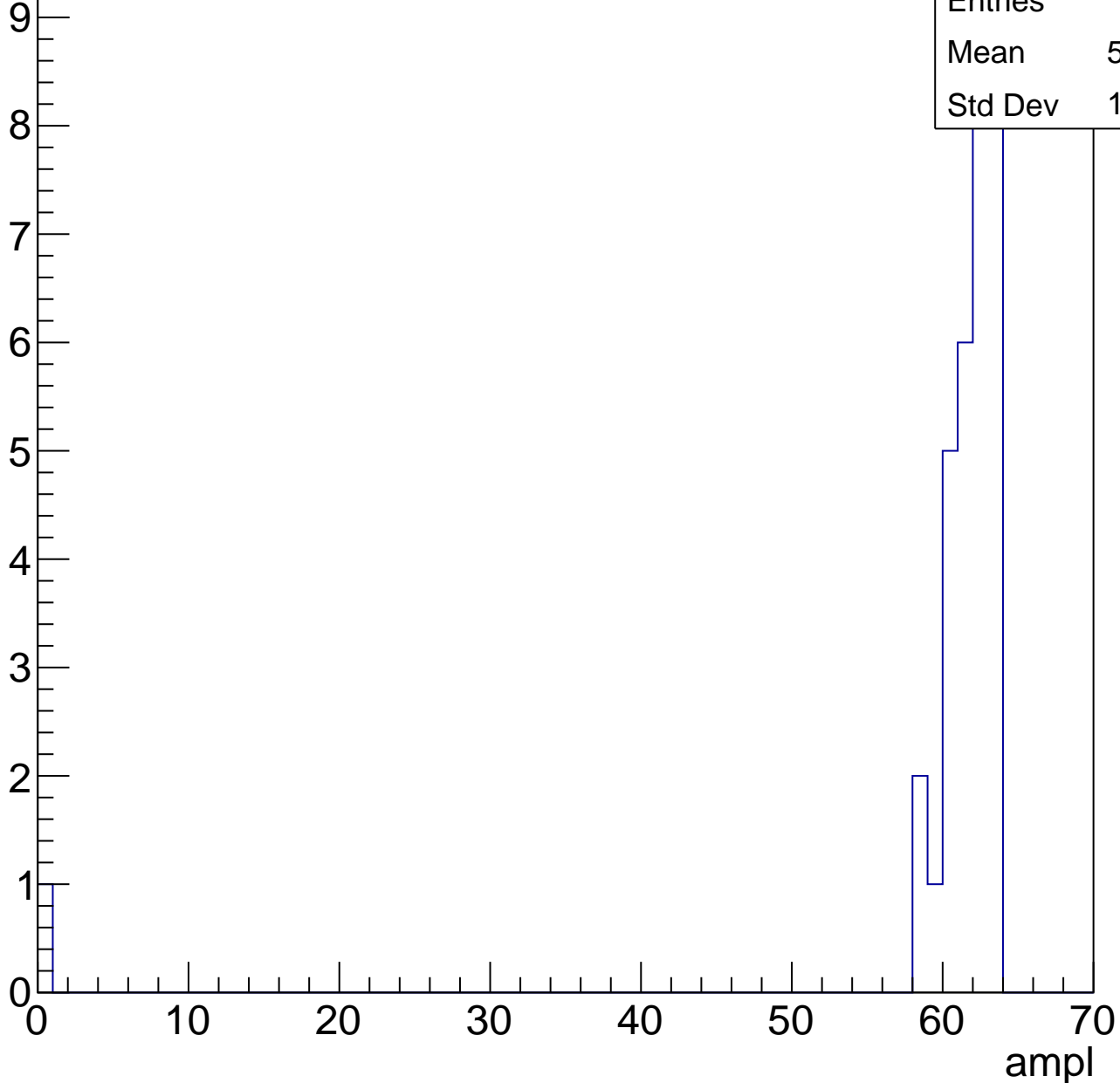
Entry



B0L001S, U11-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch8, adc0

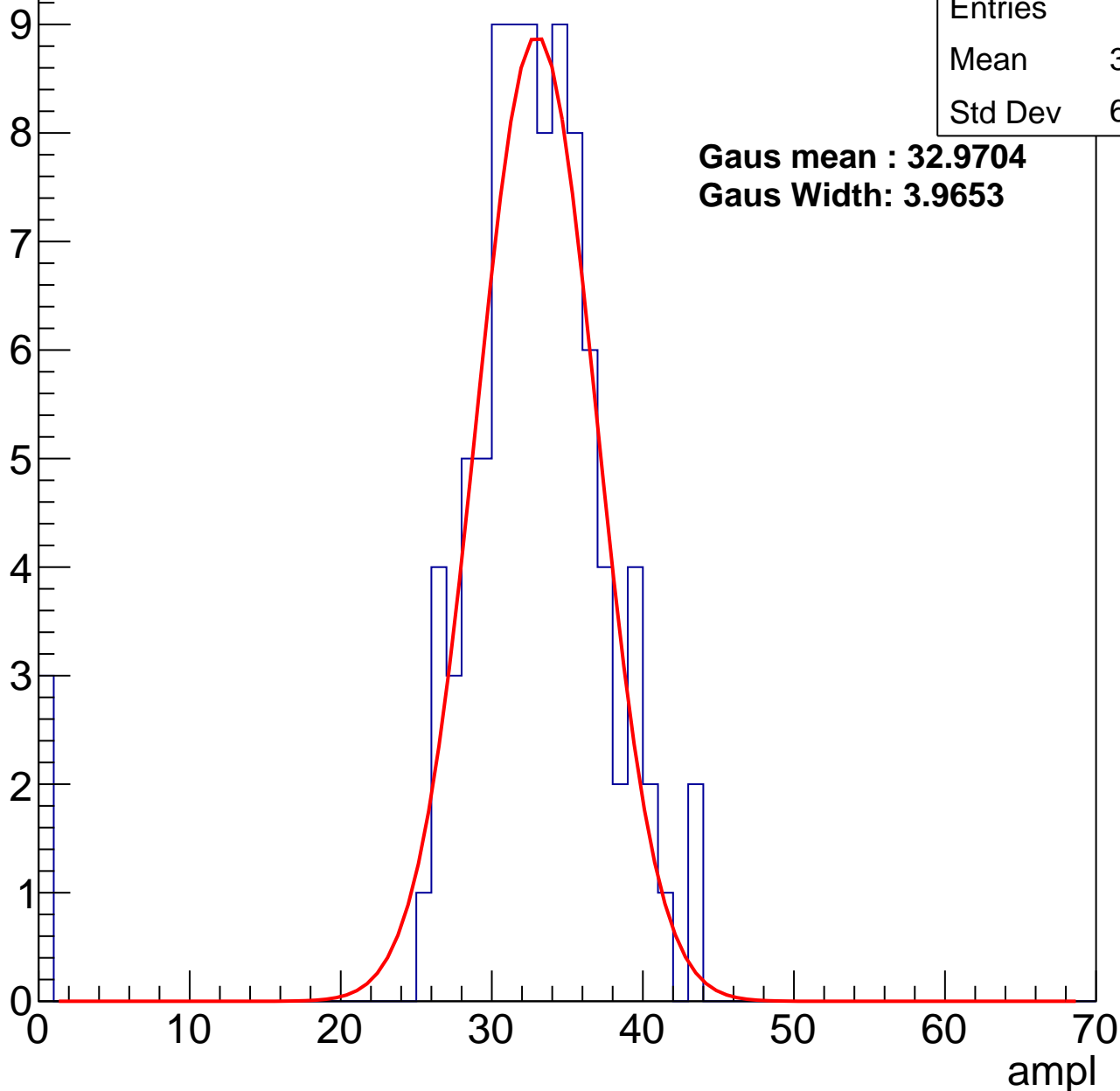
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	94
Mean	31.76
Std Dev	6.946

Gaus mean : 32.9704

Gaus Width: 3.9653



B0L001S, U11-ch8, adc1

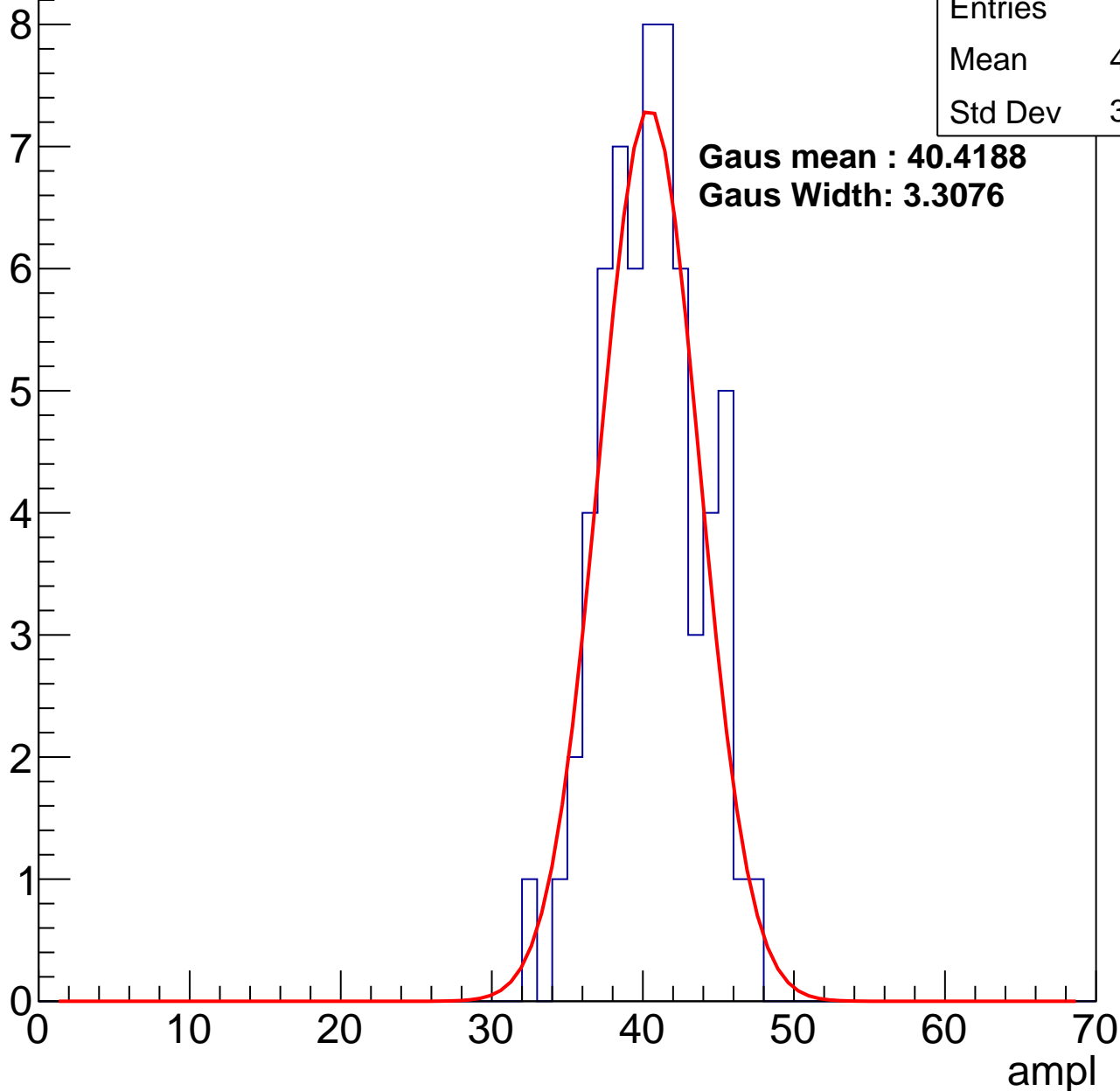
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	40.08
Std Dev	3.174

Gaus mean : 40.4188

Gaus Width: 3.3076



B0L001S, U11-ch8, adc2

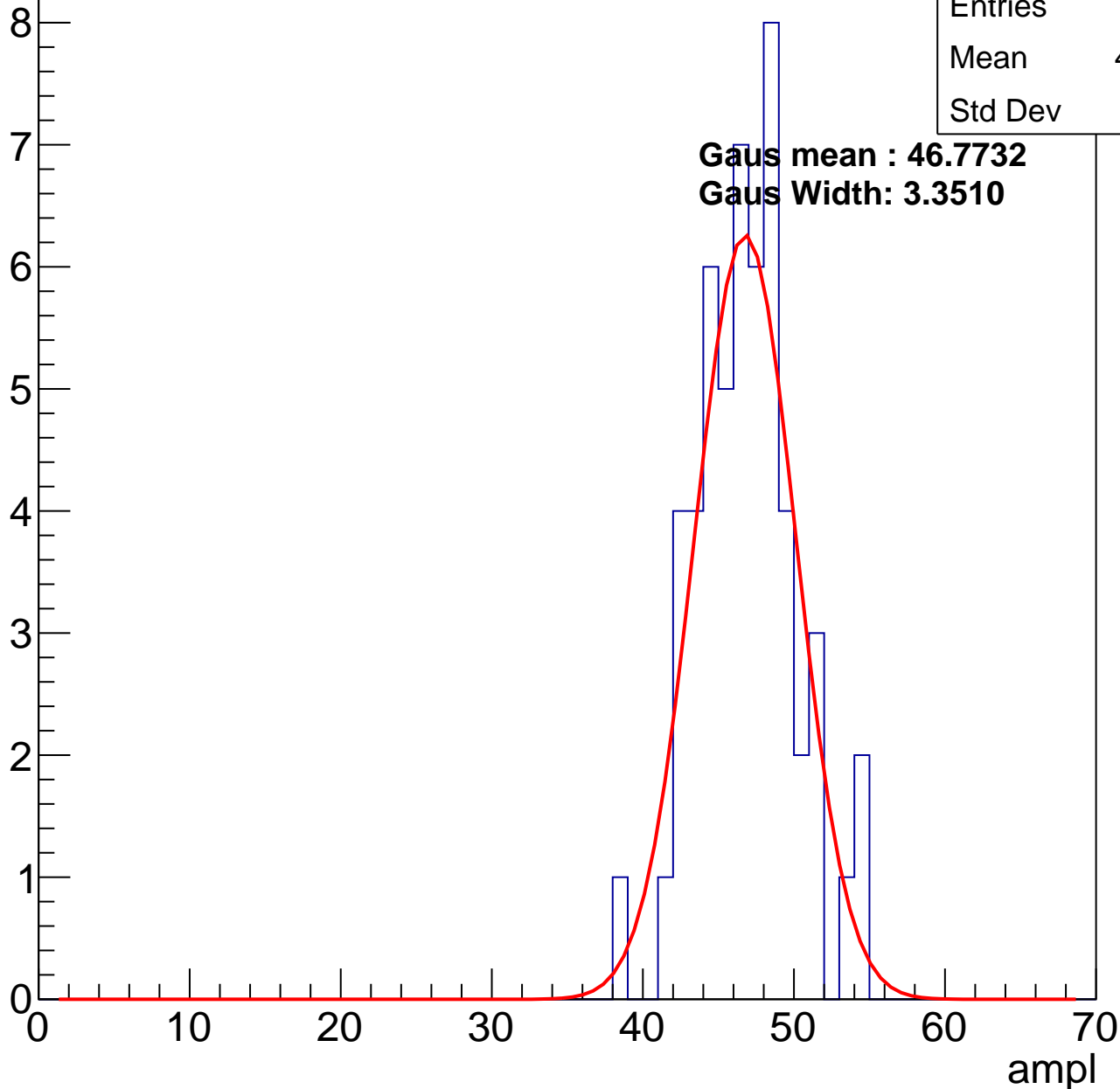
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	46.41
Std Dev	3.24

Gaus mean : 46.7732

Gaus Width: 3.3510



B0L001S, U11-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	53.08
Std Dev	3.708

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

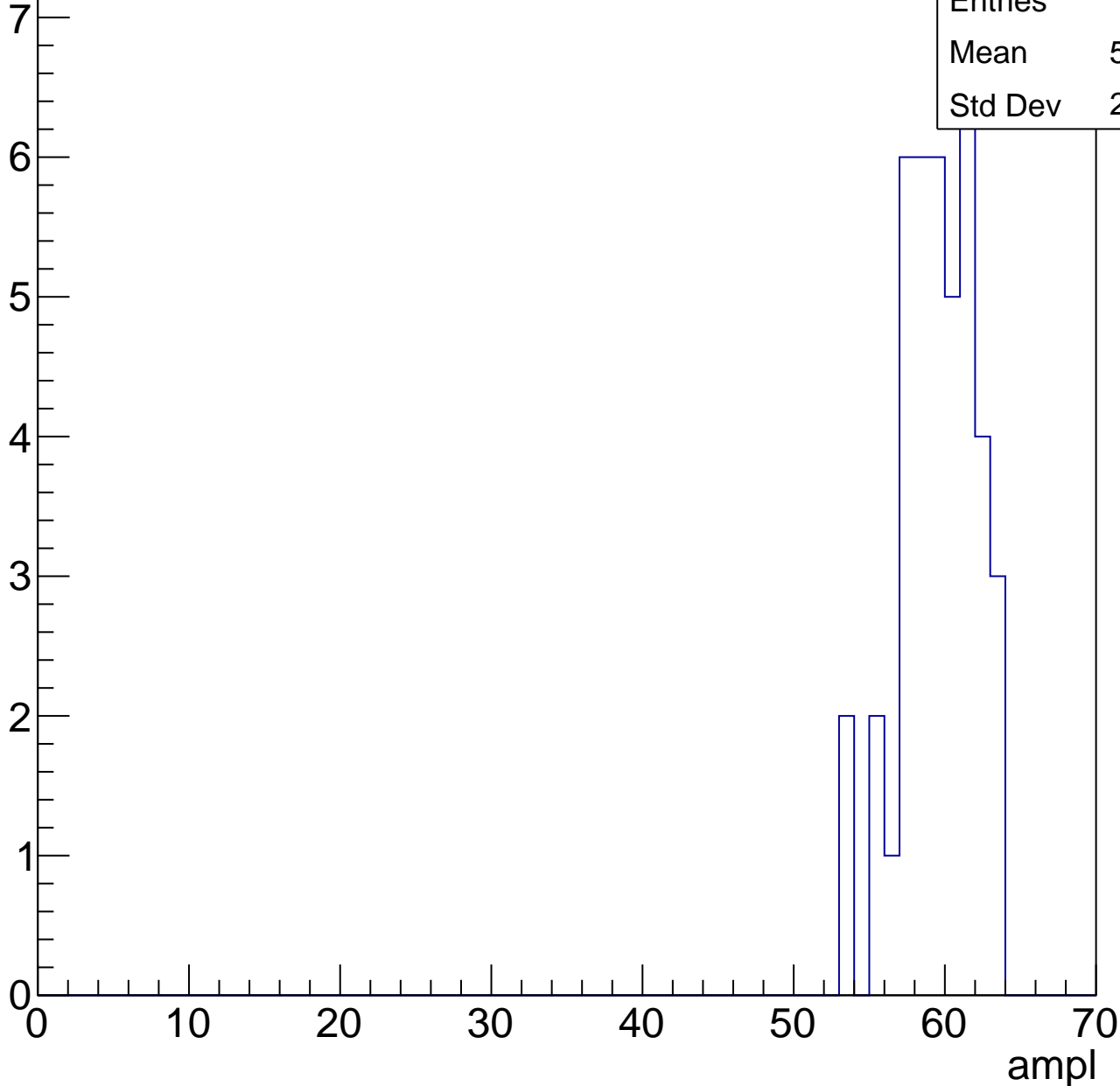
70

B0L001S, U11-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.05
Std Dev	2.488

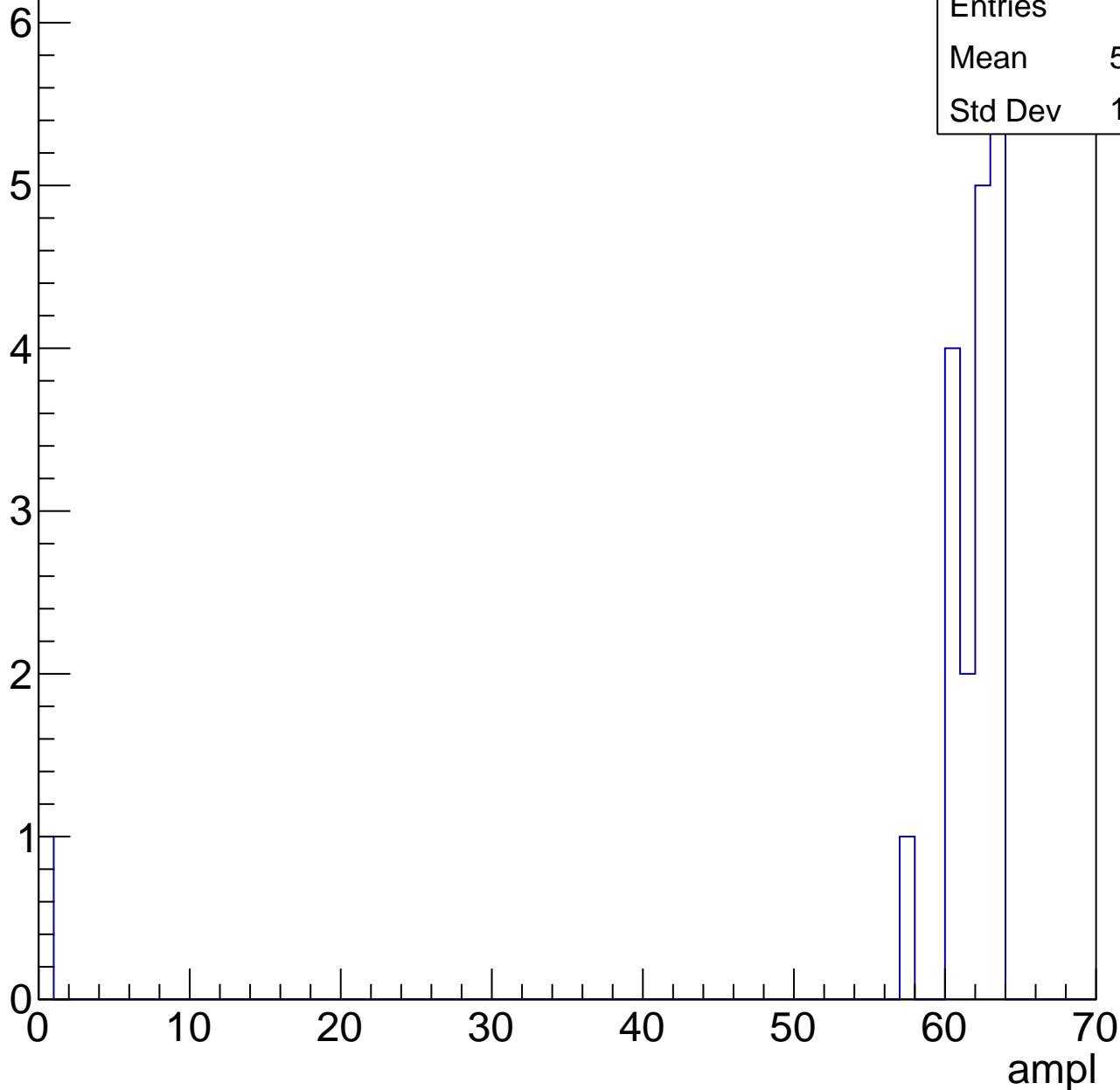


B0L001S, U11-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

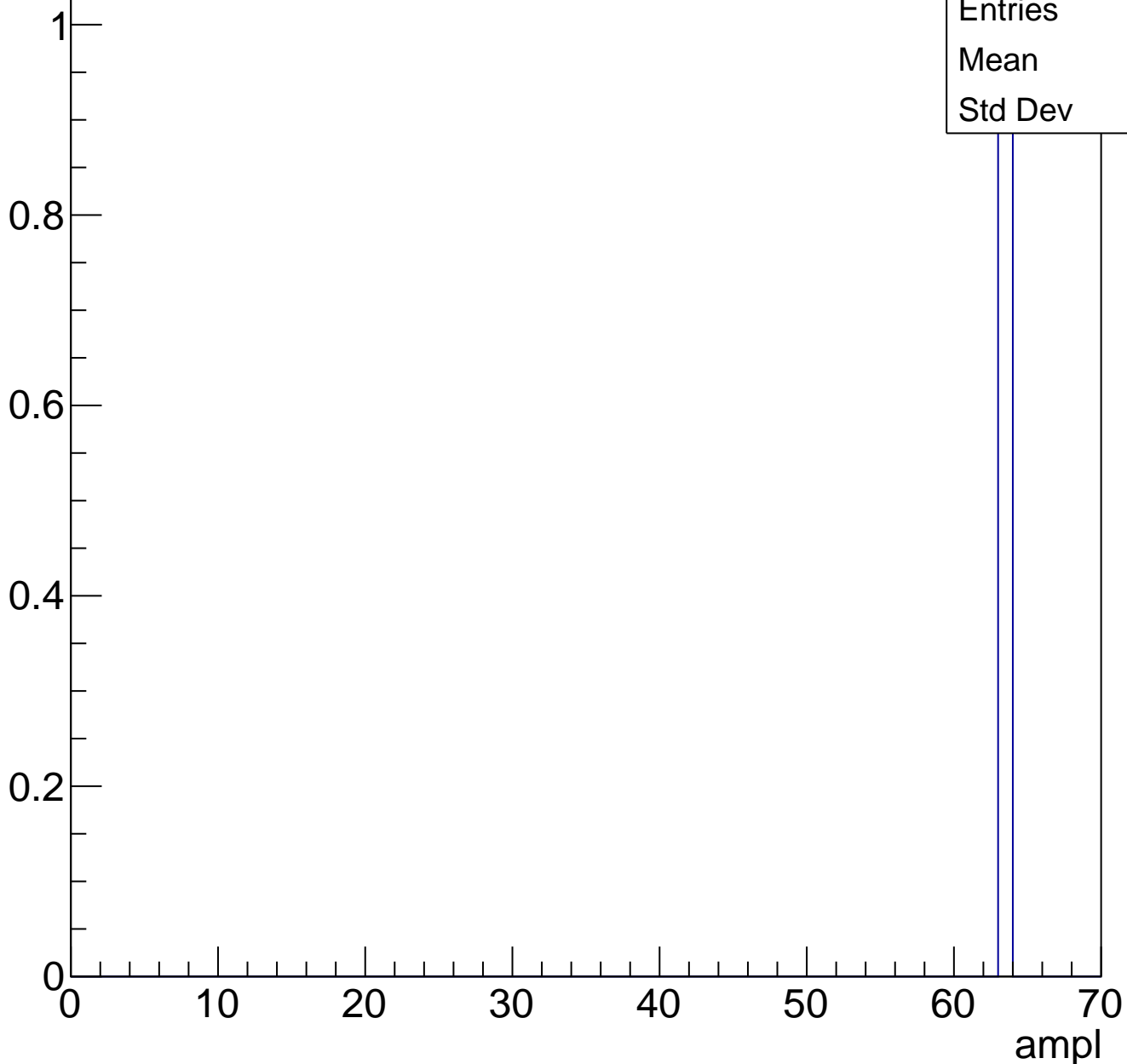
Entries	19
Mean	58.26
Std Dev	13.82



B0L001S, U11-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch9, adc0

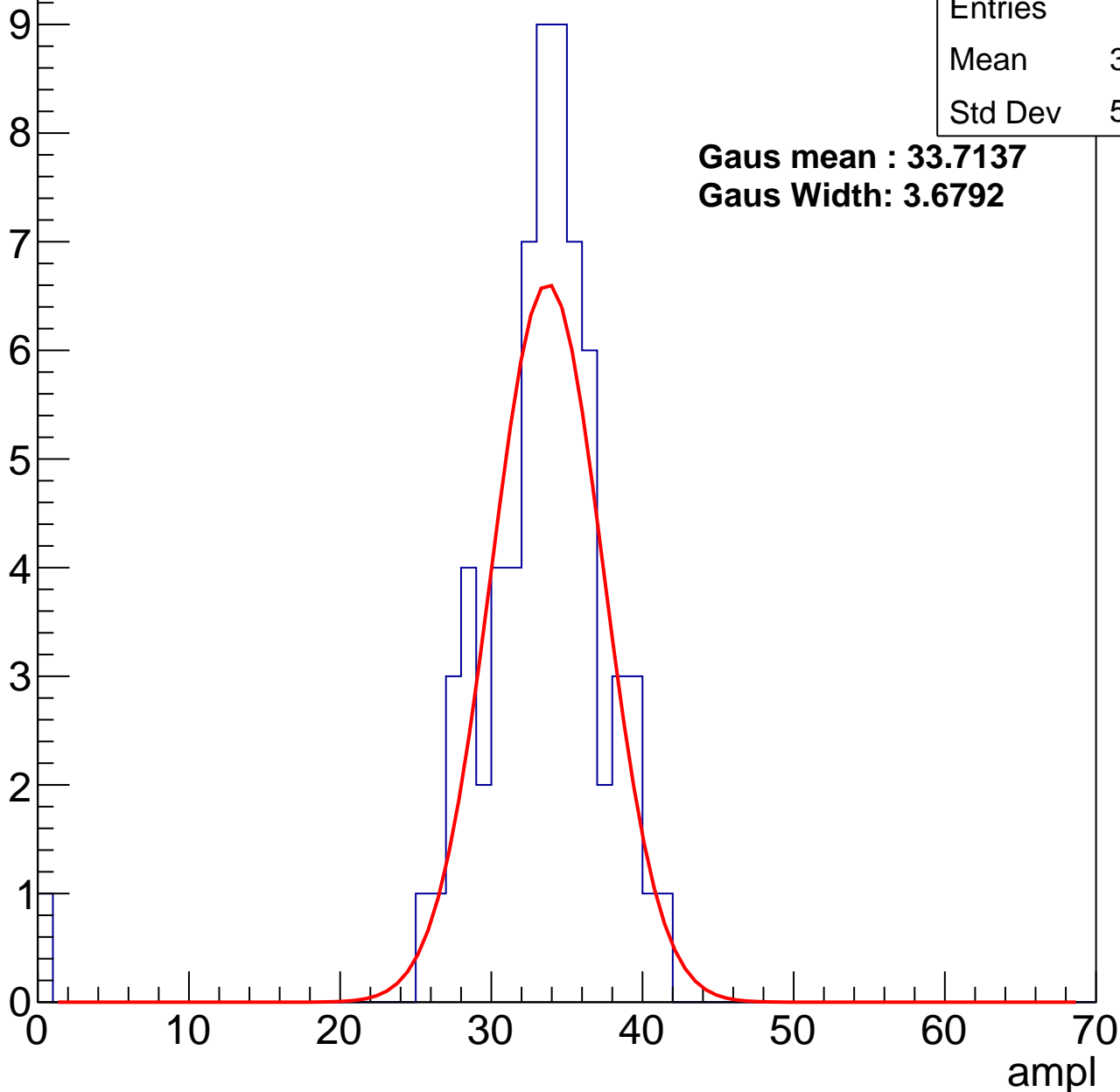
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	32.65
Std Dev	5.302

Gaus mean : 33.7137

Gaus Width: 3.6792



B0L001S, U11-ch9, adc1

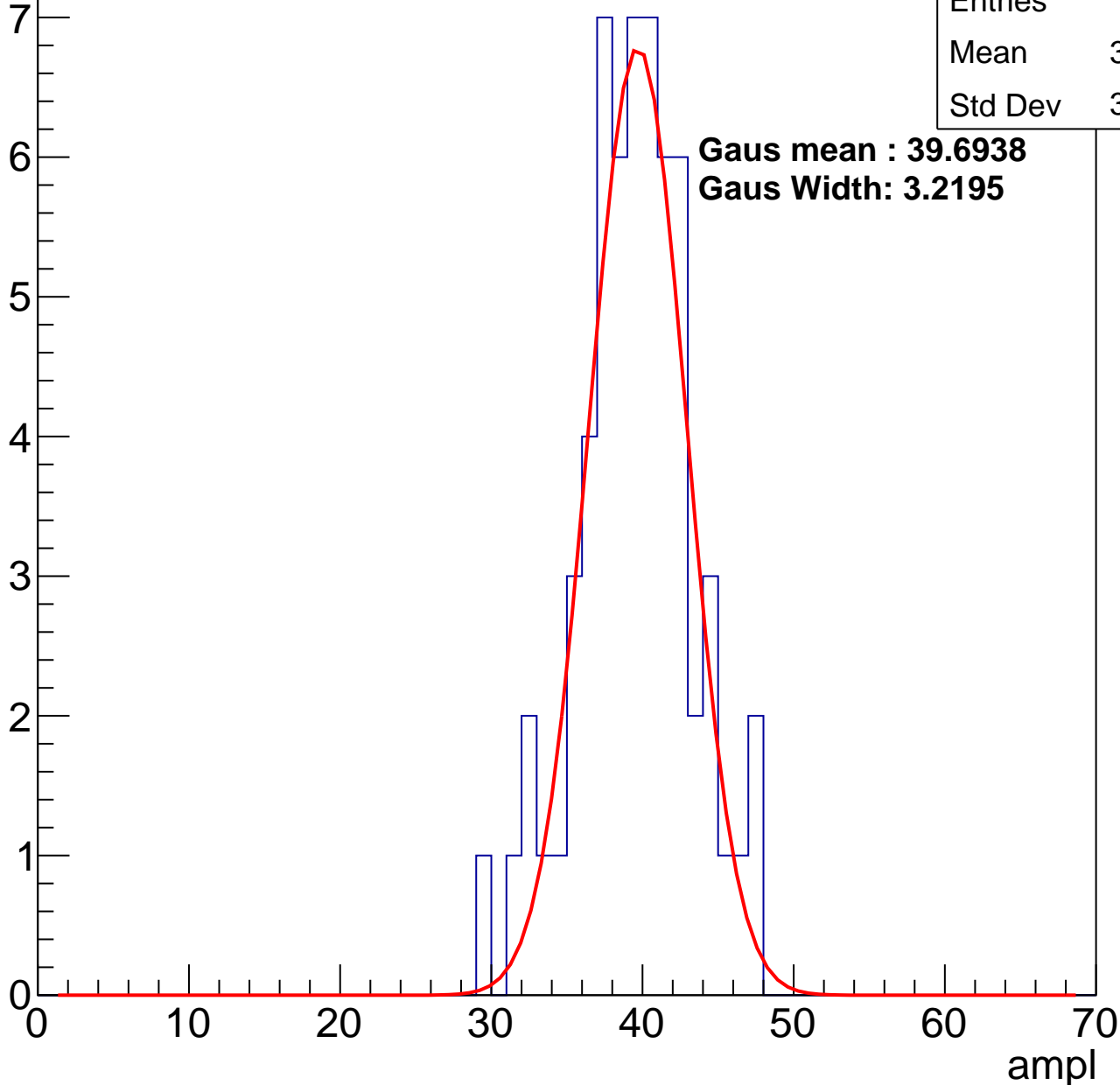
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	39.03
Std Dev	3.724

Gaus mean : 39.6938

Gaus Width: 3.2195



B0L001S, U11-ch9, adc2

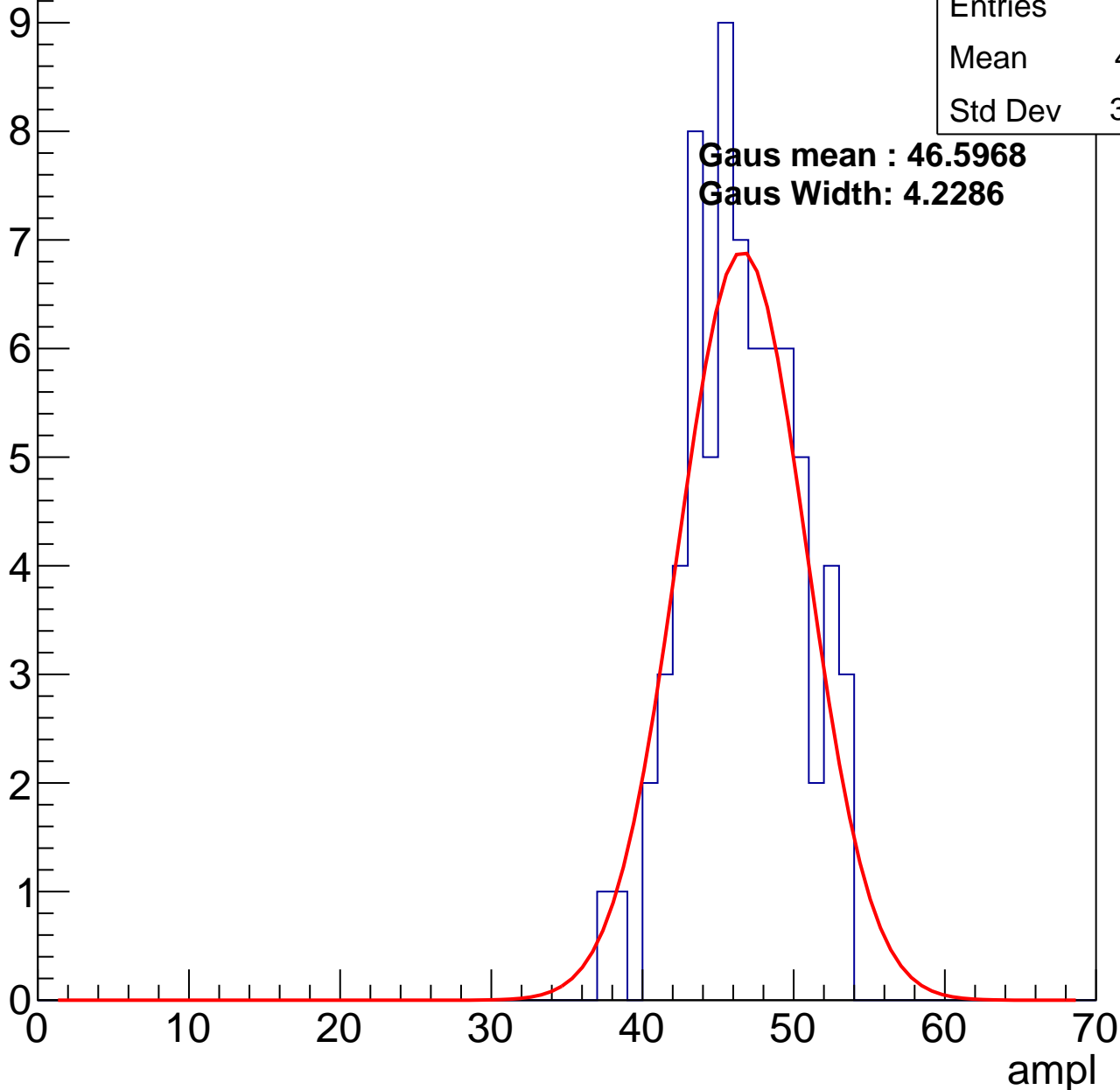
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	46.11
Std Dev	3.654

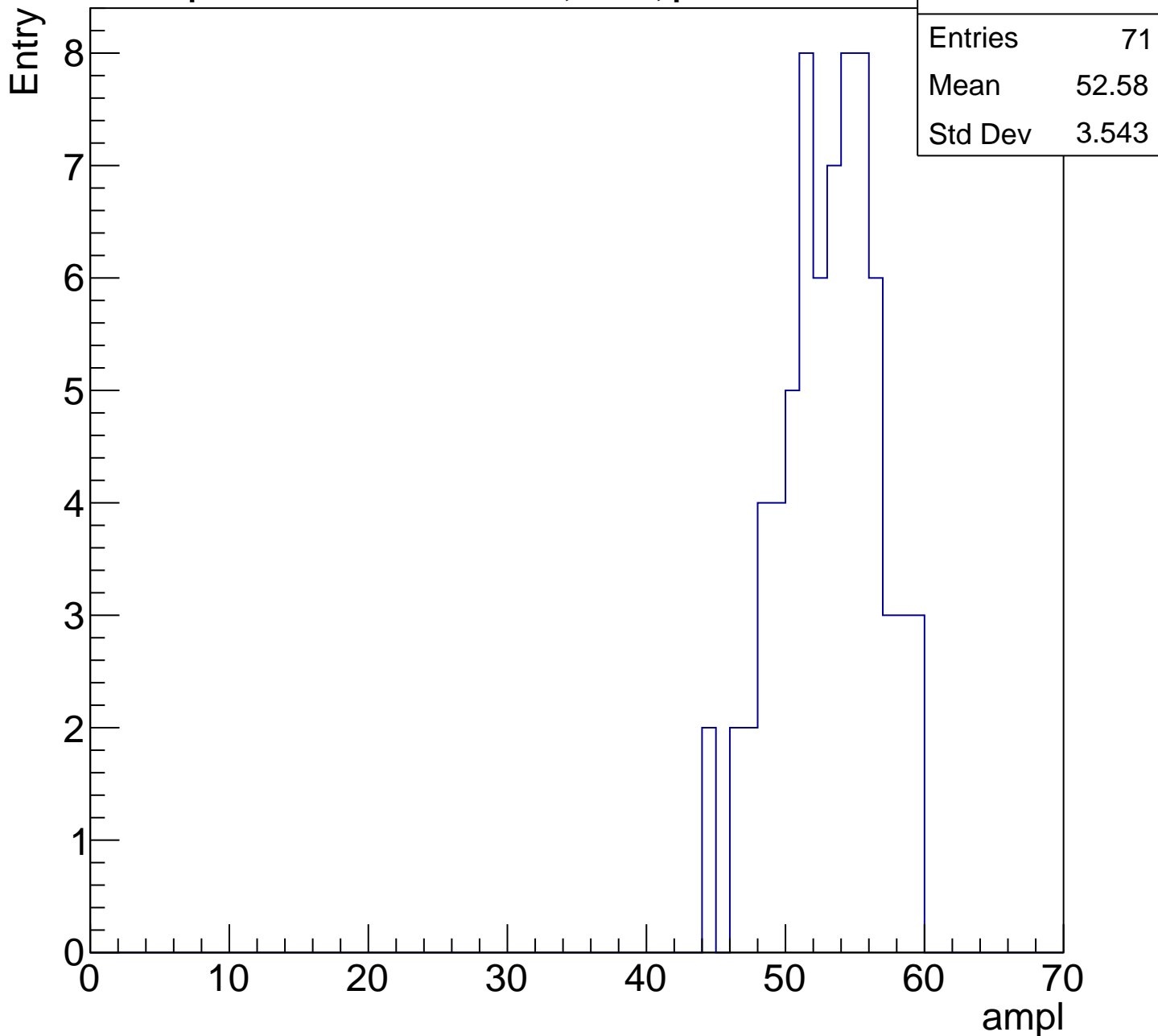
Gaus mean : 46.5968

Gaus Width: 4.2286



B0L001S, U11-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U11-ch9, adc4

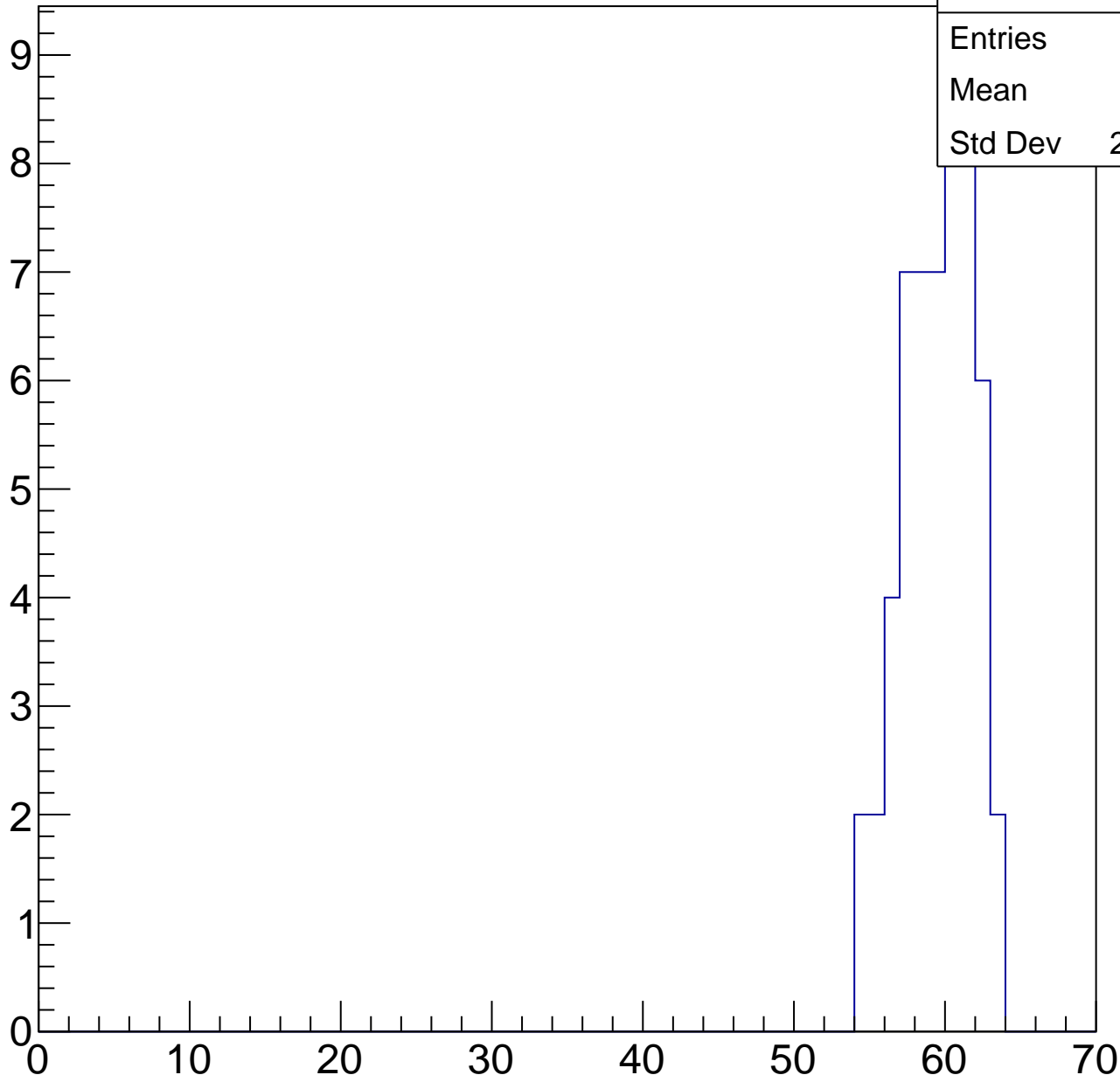
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	59
Std Dev	2.277

ampl

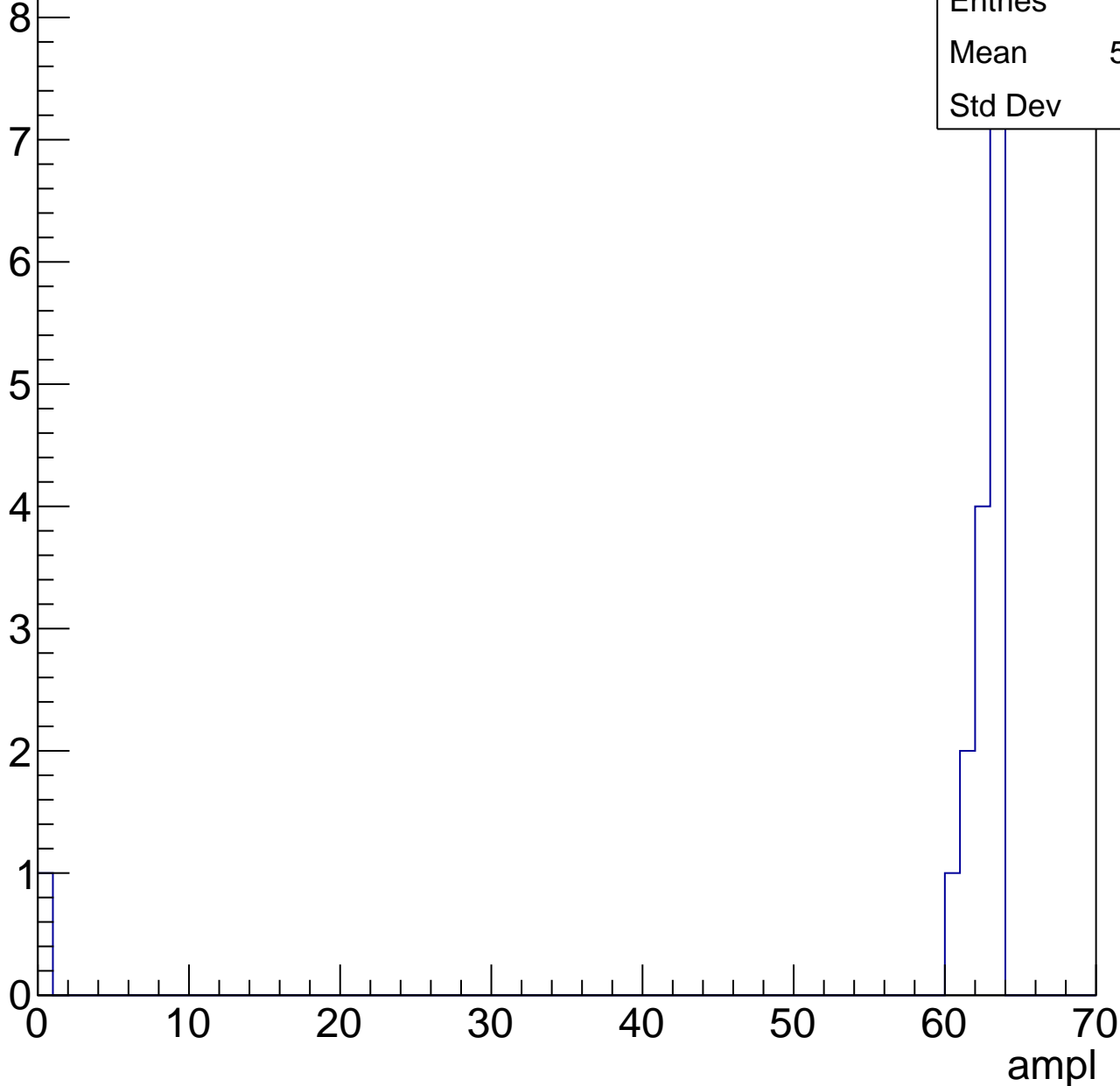


B0L001S, U11-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	58.38
Std Dev	15.1



B0L001S, U11-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch10, adc0

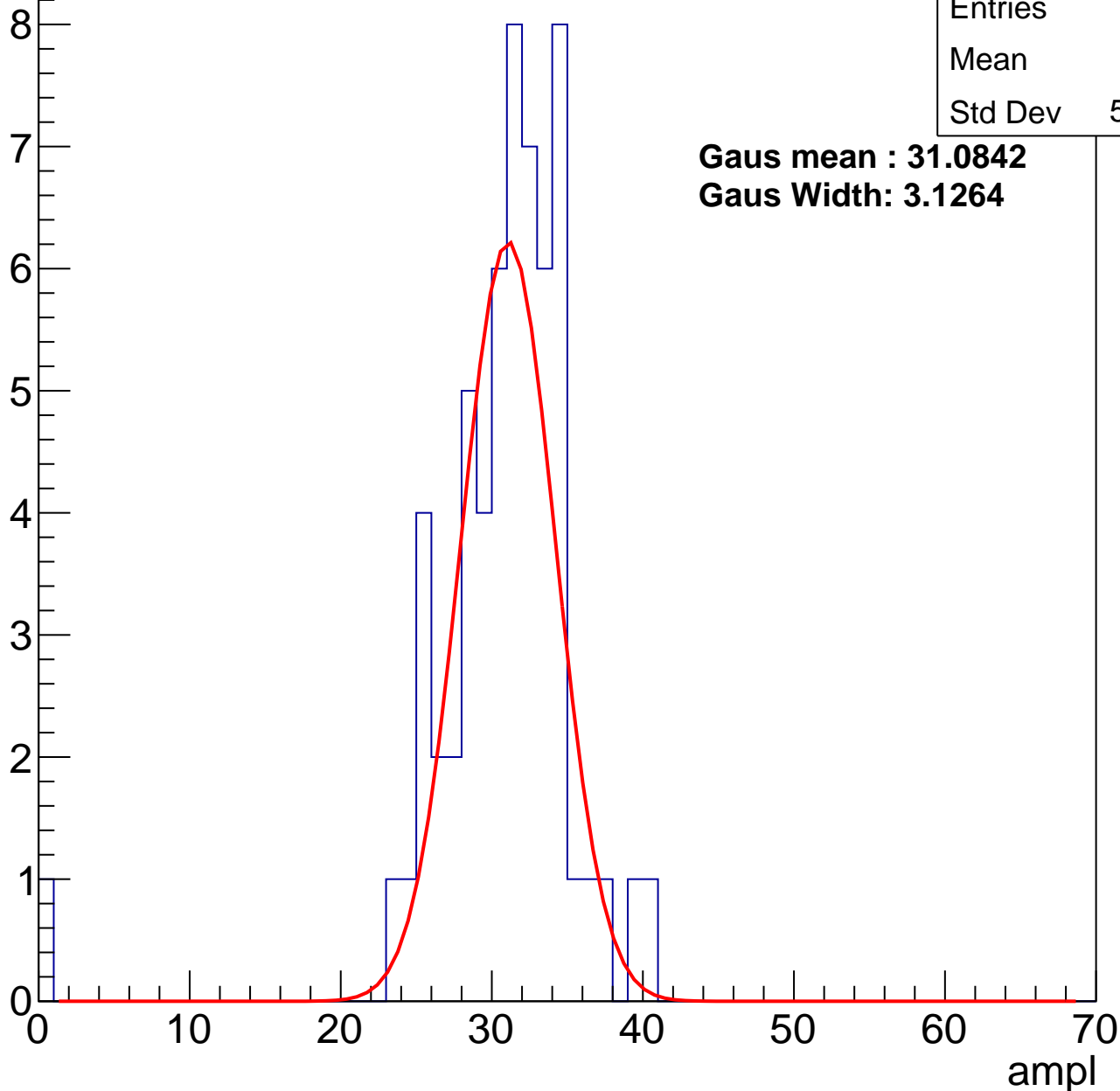
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.3
Std Dev	5.264

Gaus mean : 31.0842

Gaus Width: 3.1264



B0L001S, U11-ch10, adc1

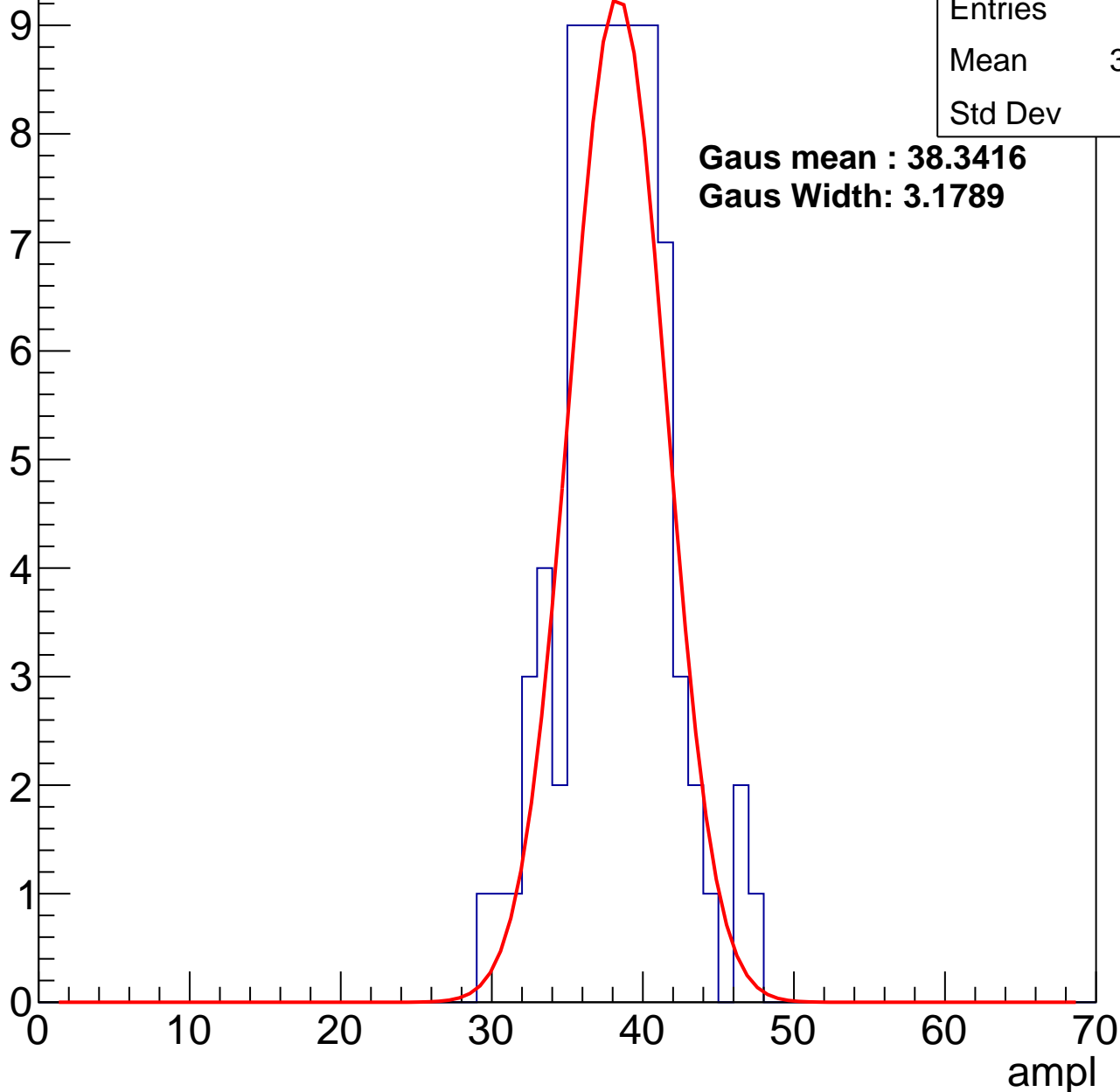
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	37.72
Std Dev	3.5

Gaus mean : 38.3416

Gaus Width: 3.1789



B0L001S, U11-ch10, adc2

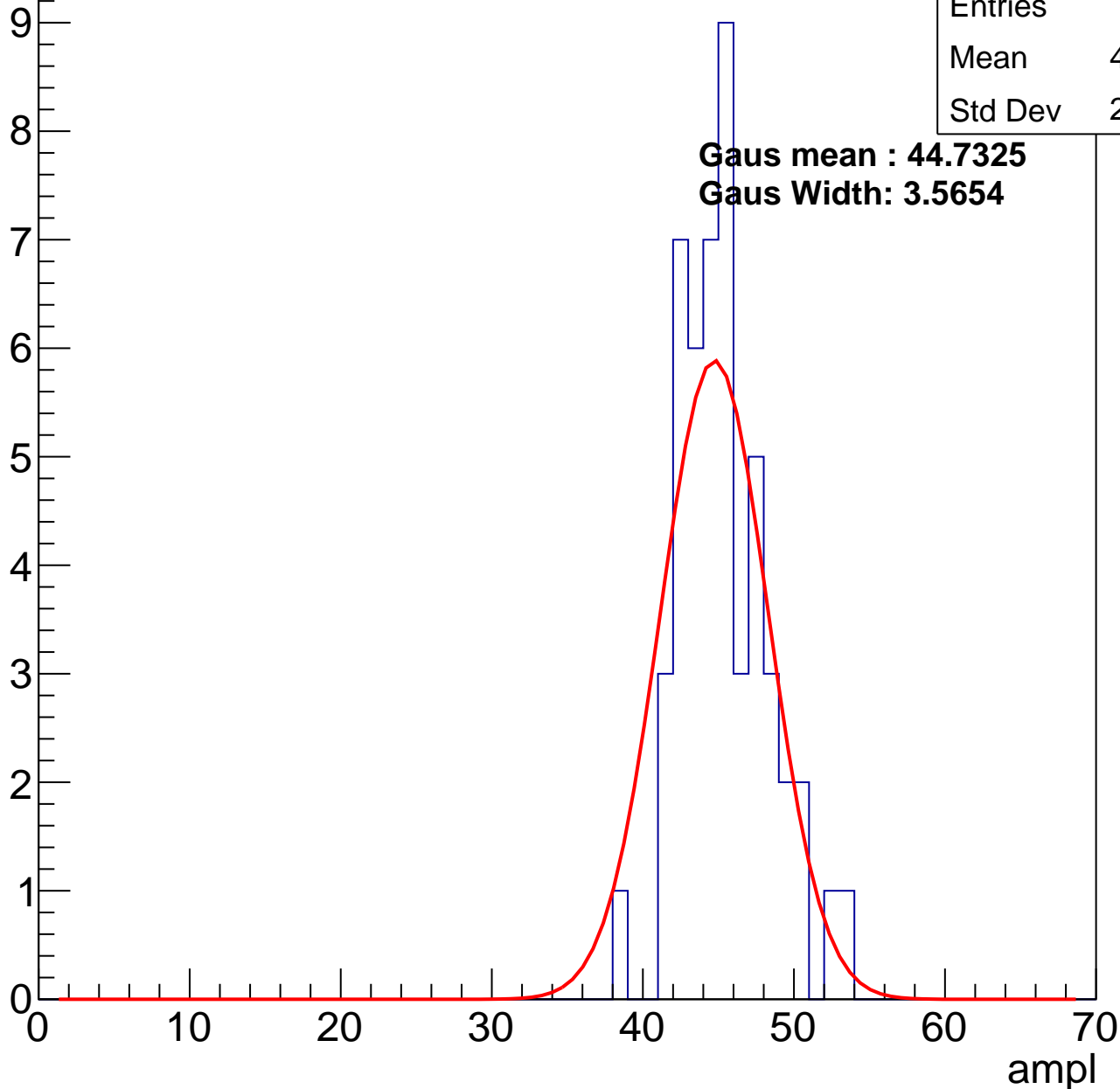
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	44.92
Std Dev	2.952

Gaus mean : 44.7325

Gaus Width: 3.5654

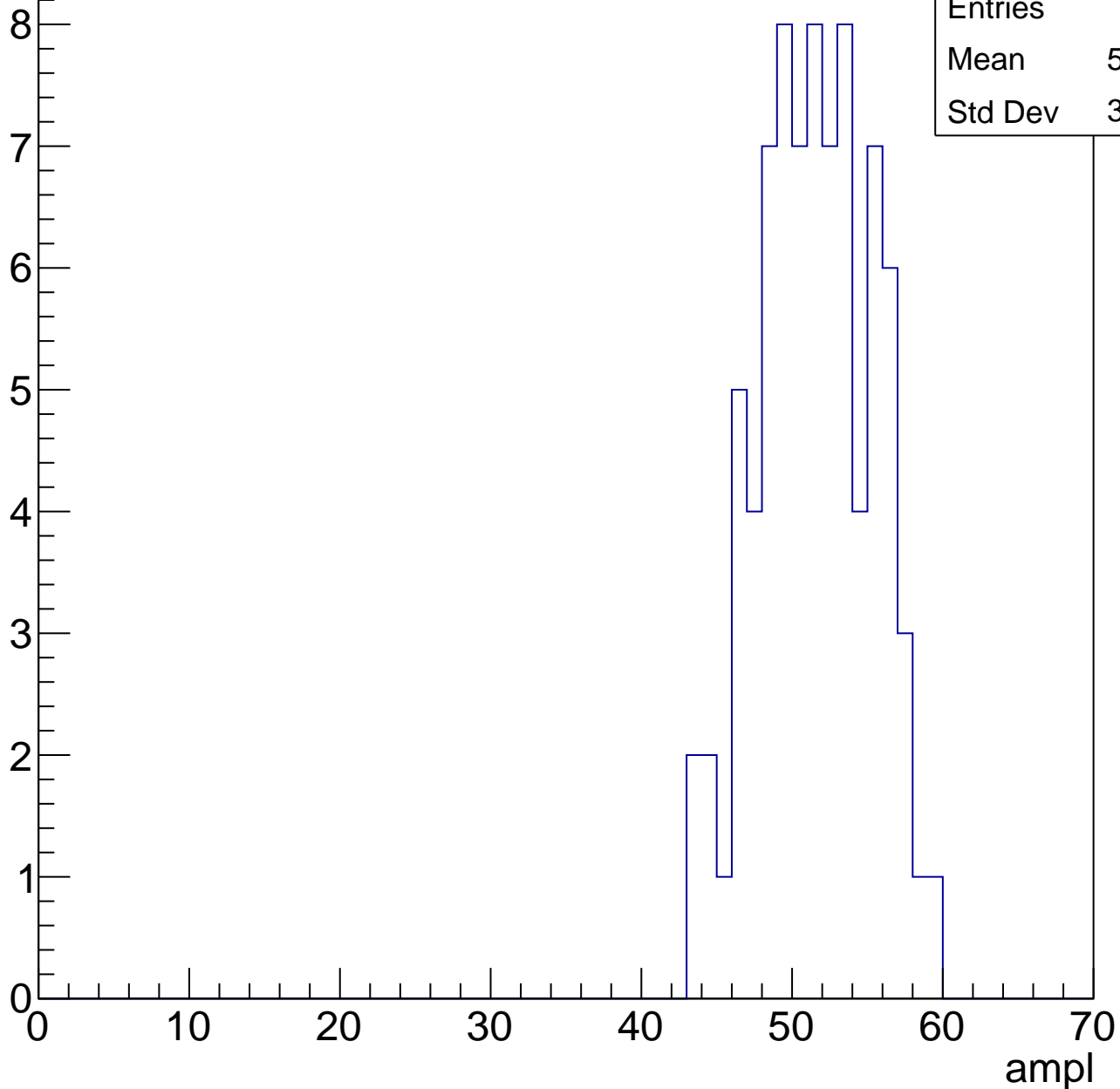


B0L001S, U11-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	51.06
Std Dev	3.703

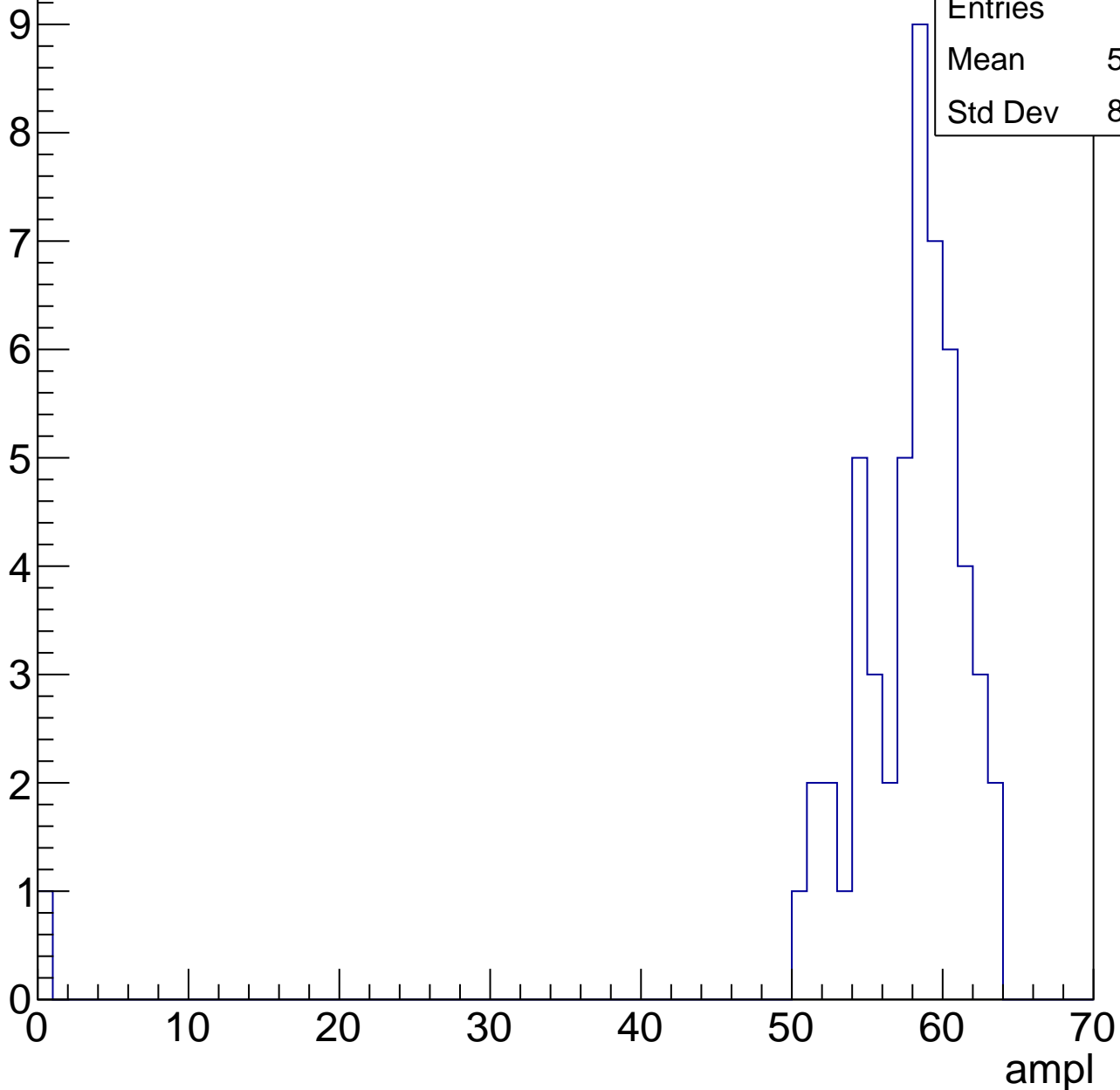


B0L001S, U11-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.45
Std Dev	8.446

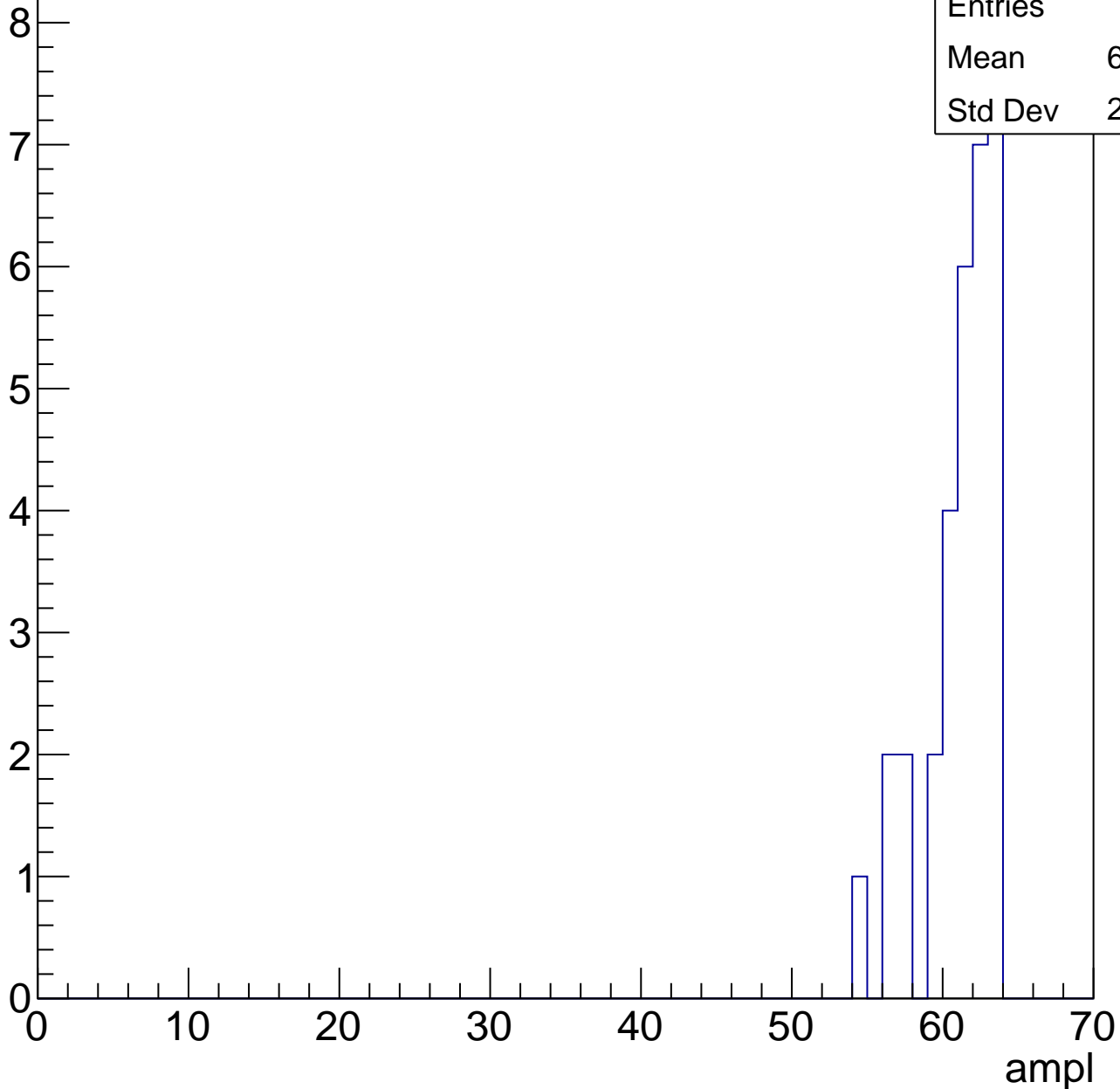


B0L001S, U11-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.69
Std Dev	2.364



B0L001S, U11-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch11, adc0

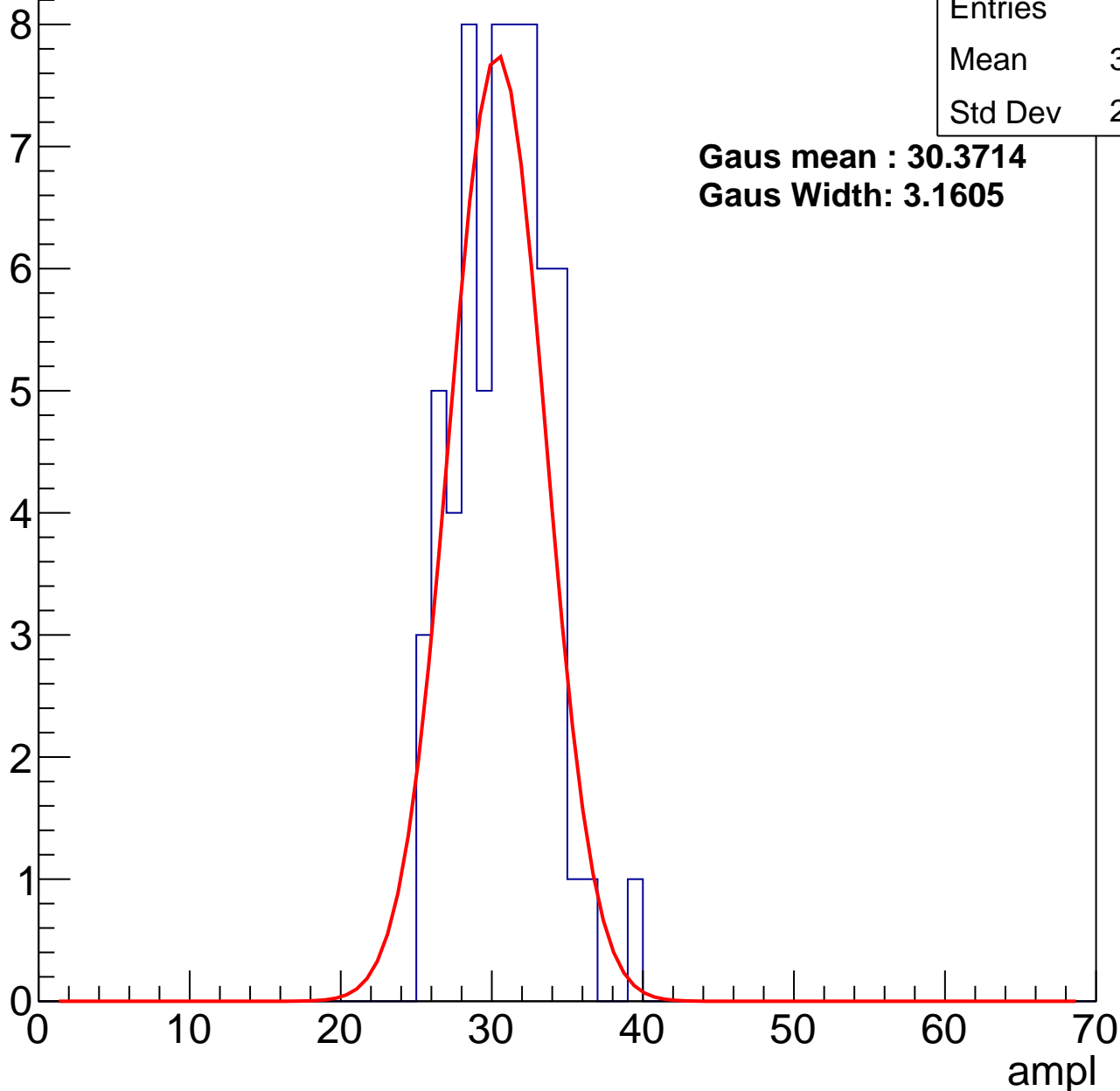
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.28
Std Dev	2.945

Gaus mean : 30.3714

Gaus Width: 3.1605



B0L001S, U11-ch11, adc1

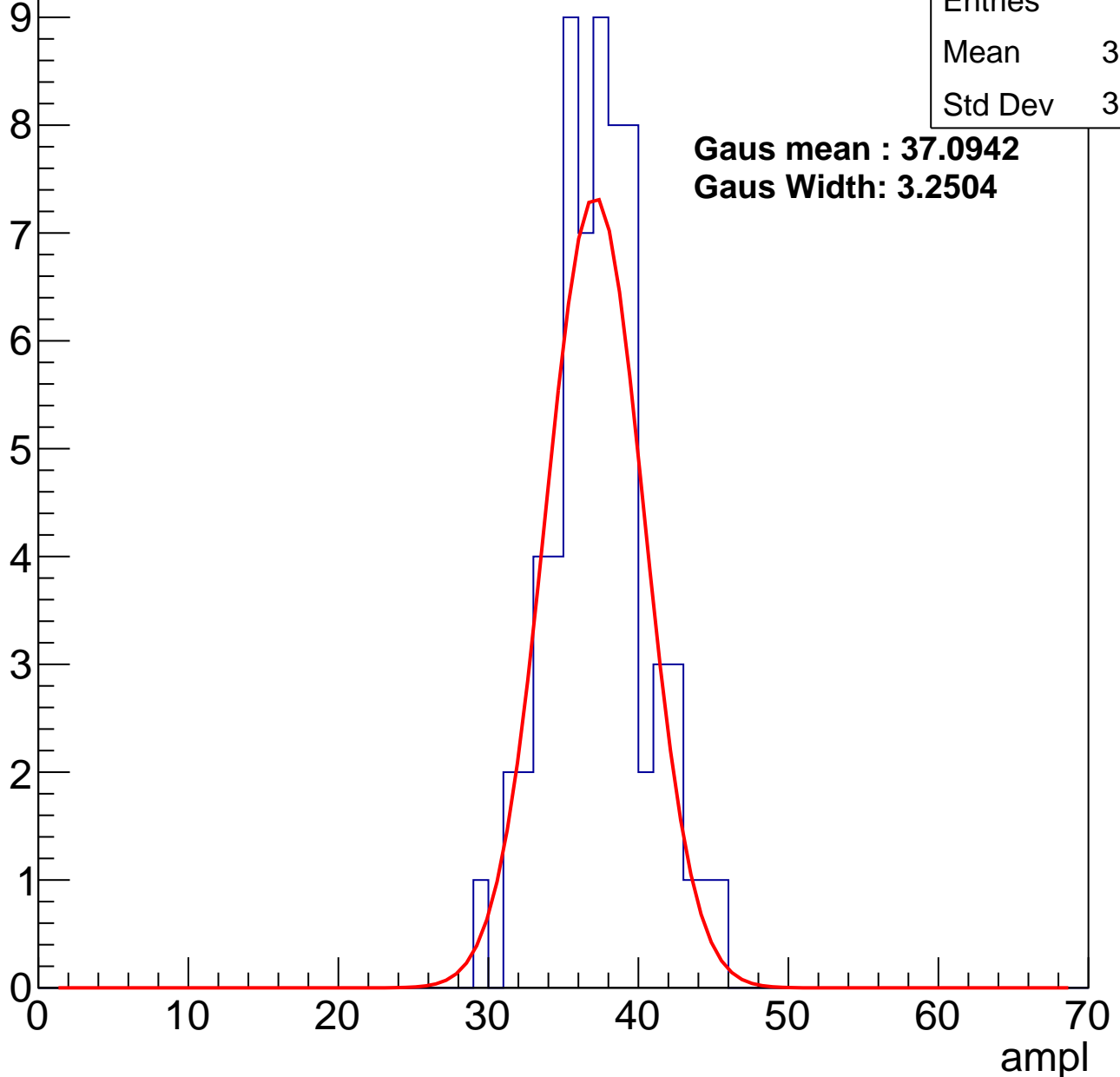
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.92
Std Dev	3.183

Gaus mean : 37.0942

Gaus Width: 3.2504



B0L001S, U11-ch11, adc2

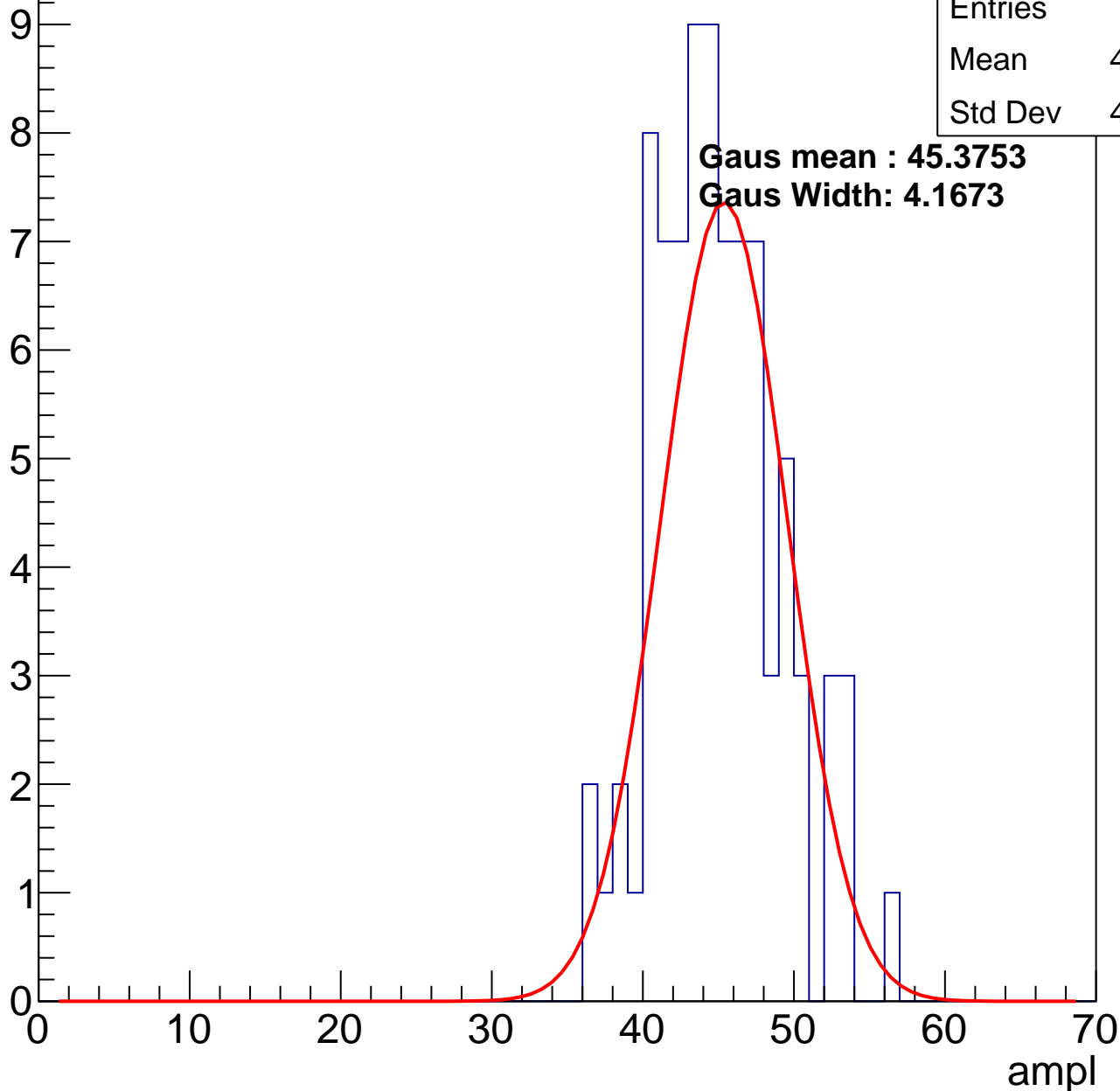
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	44.52
Std Dev	4.112

Gaus mean : 45.3753

Gaus Width: 4.1673



B0L001S, U11-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

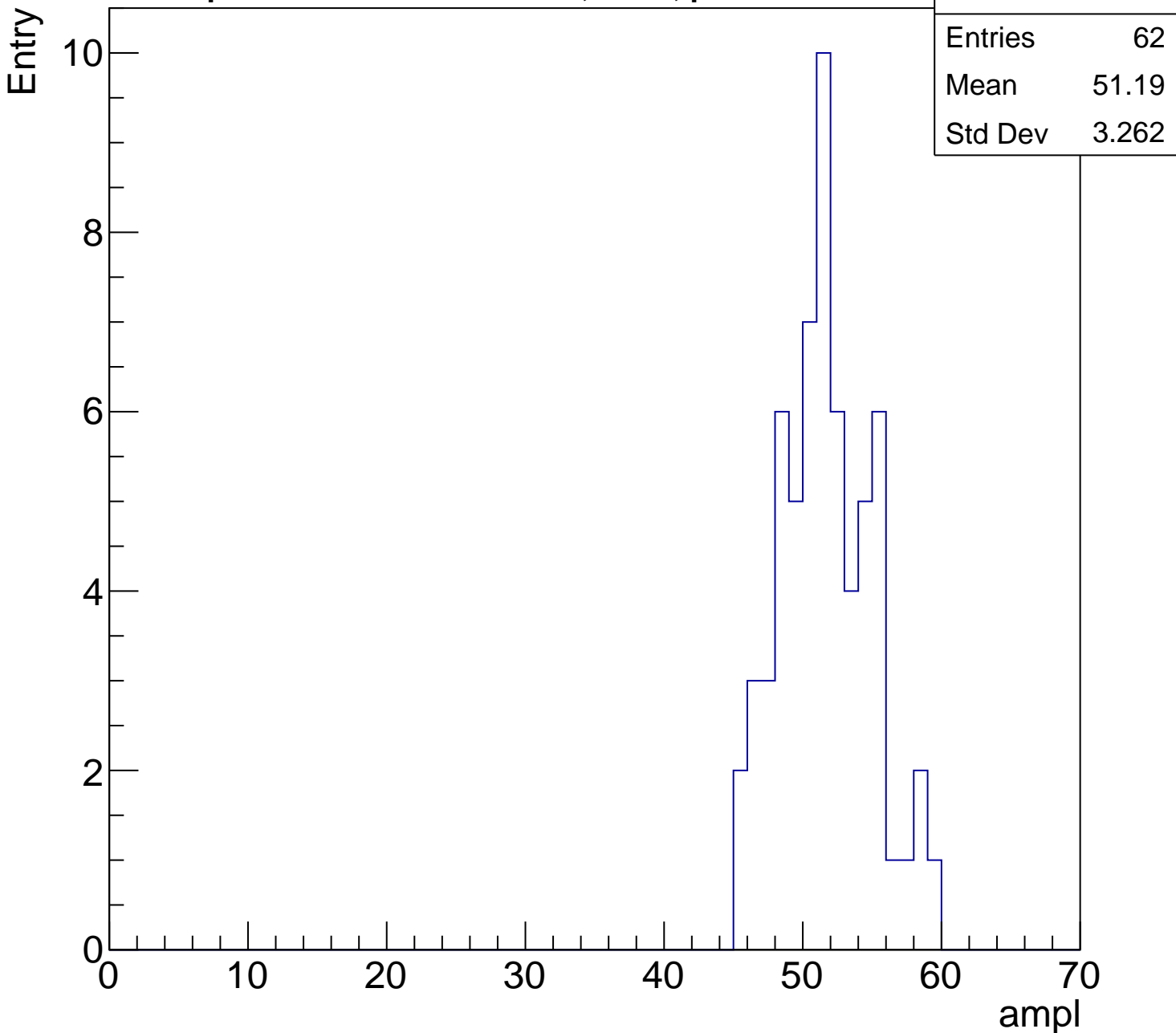
Entries	62
Mean	51.19
Std Dev	3.262

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

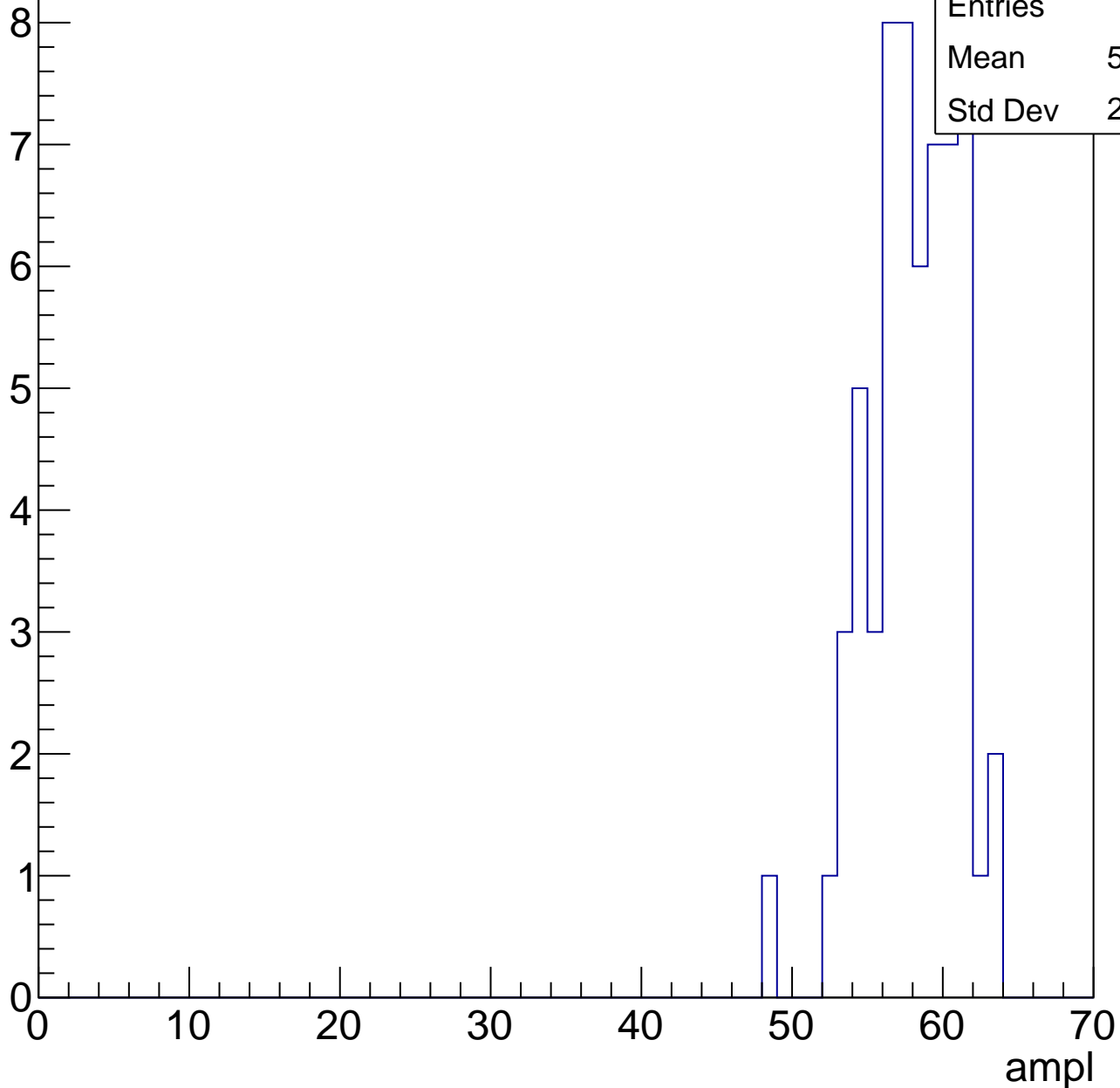


B0L001S, U11-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.58
Std Dev	2.946

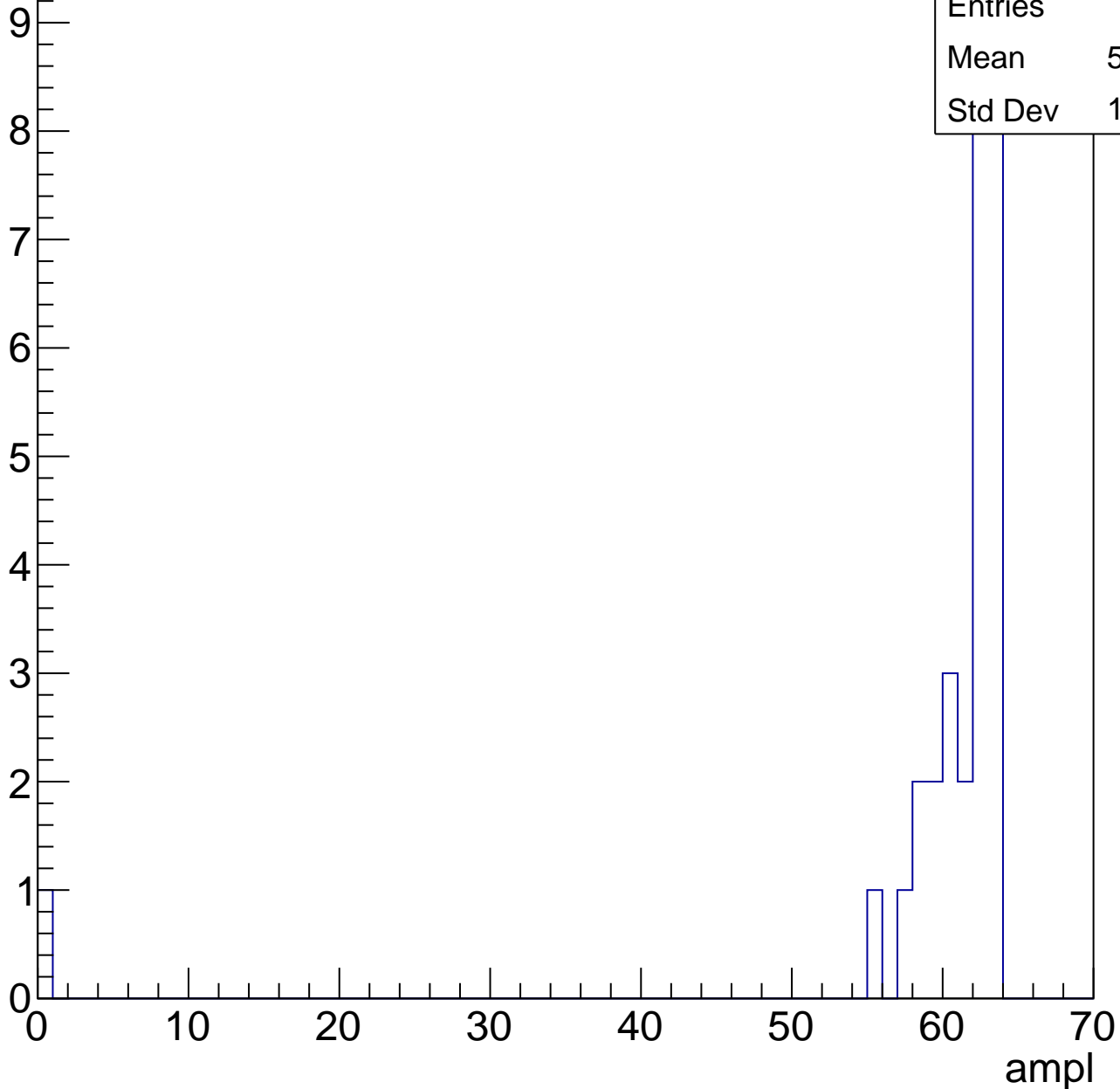


B0L001S, U11-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.97
Std Dev	11.33



B0L001S, U11-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch12, adc0

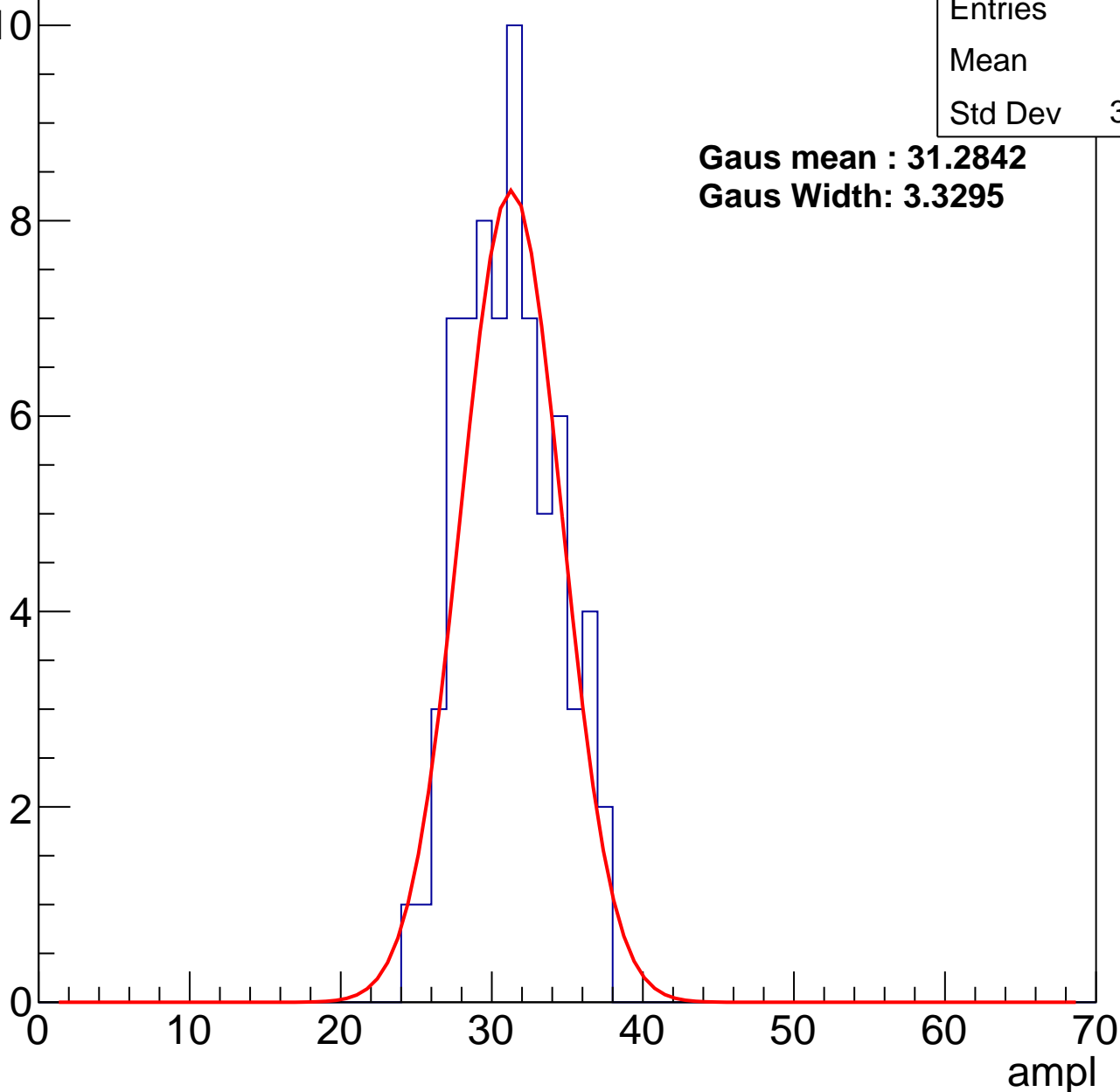
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	30.7
Std Dev	3.069

Gaus mean : 31.2842

Gaus Width: 3.3295



B0L001S, U11-ch12, adc1

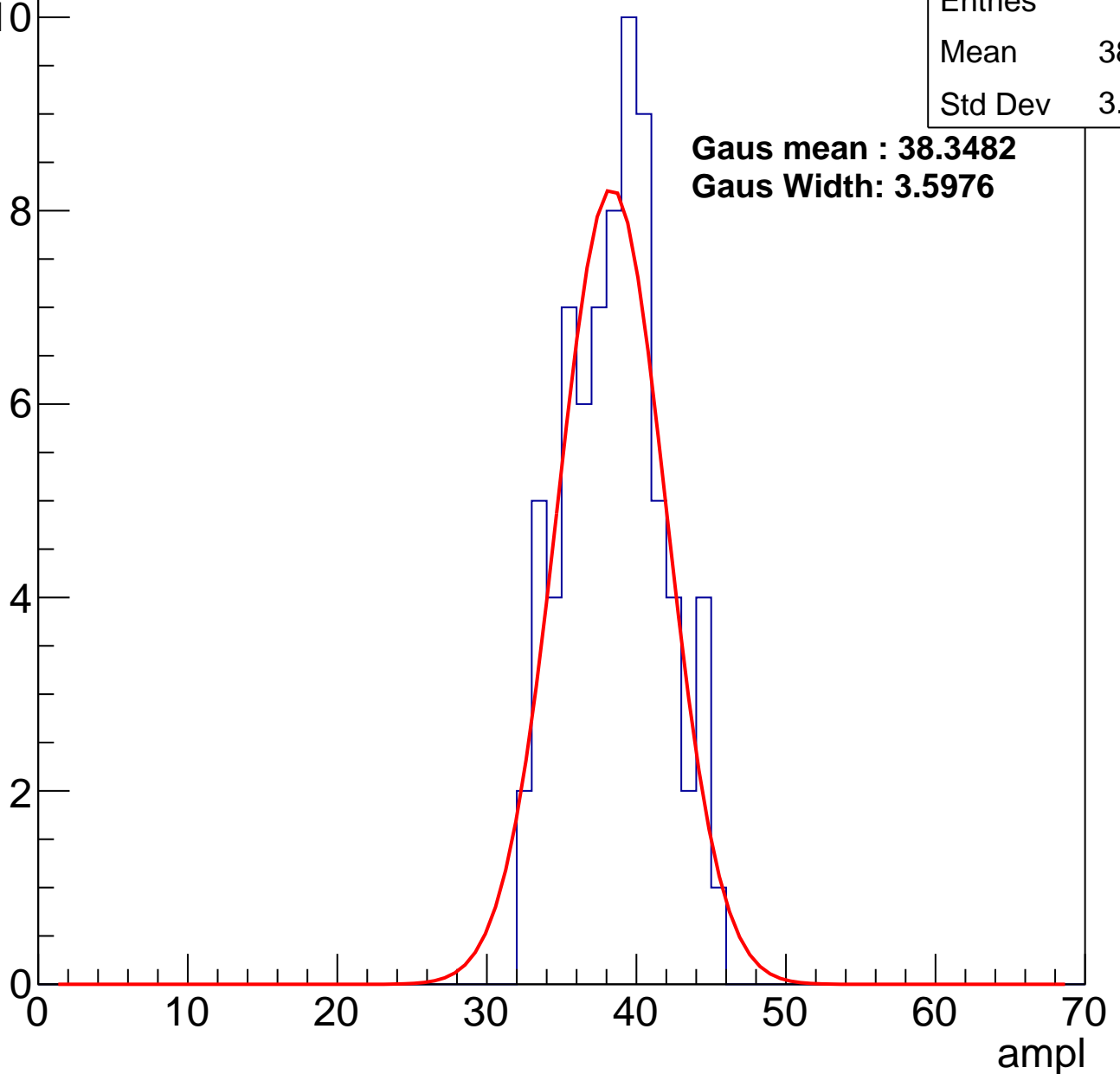
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.09
Std Dev	3.189

Gaus mean : 38.3482

Gaus Width: 3.5976



B0L001S, U11-ch12, adc2

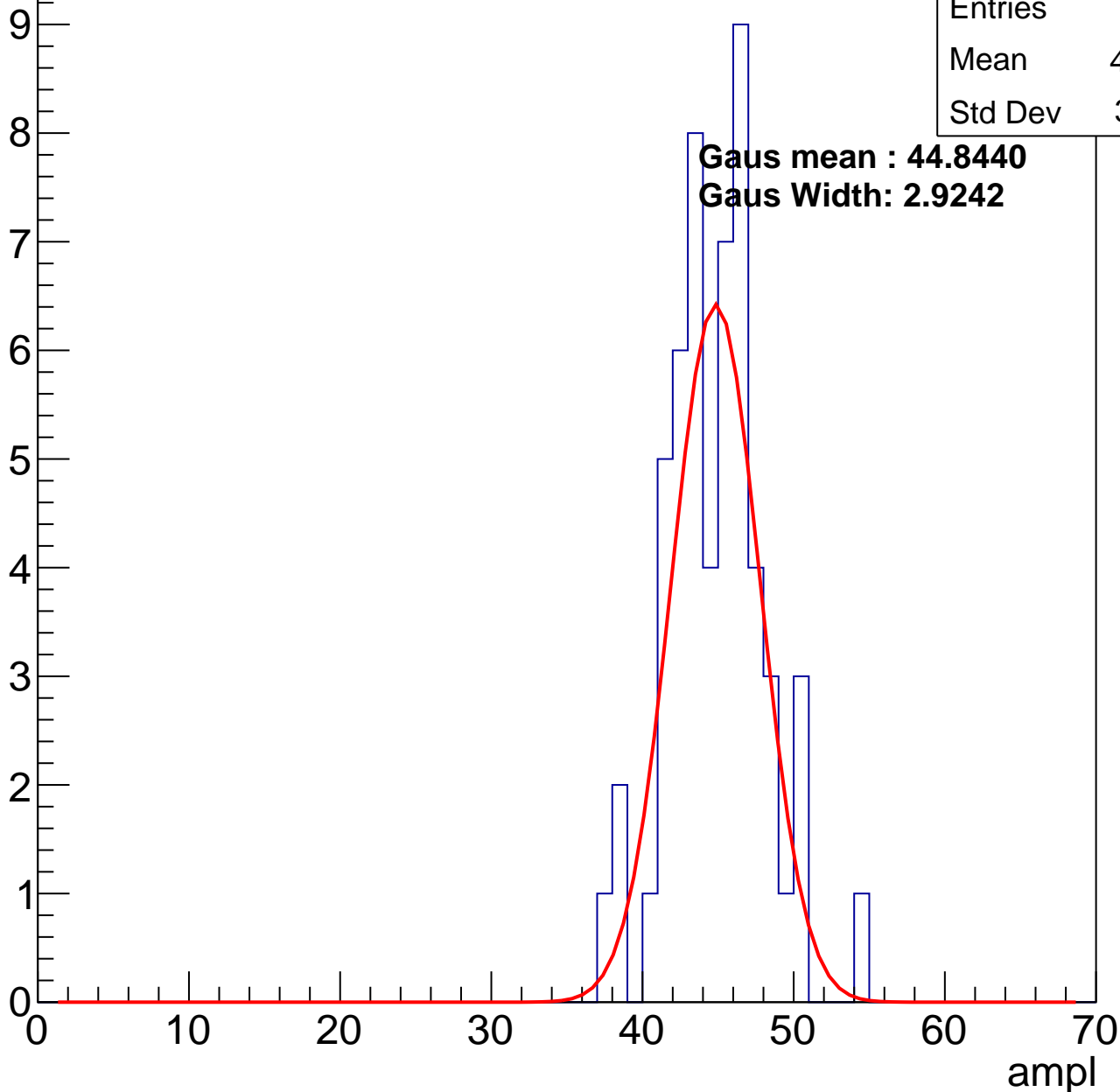
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.44
Std Dev	3.201

Gaus mean : 44.8440

Gaus Width: 2.9242



B0L001S, U11-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	51.01
Std Dev	3.233

Entry

10

8

6

4

2

0

0

10

20

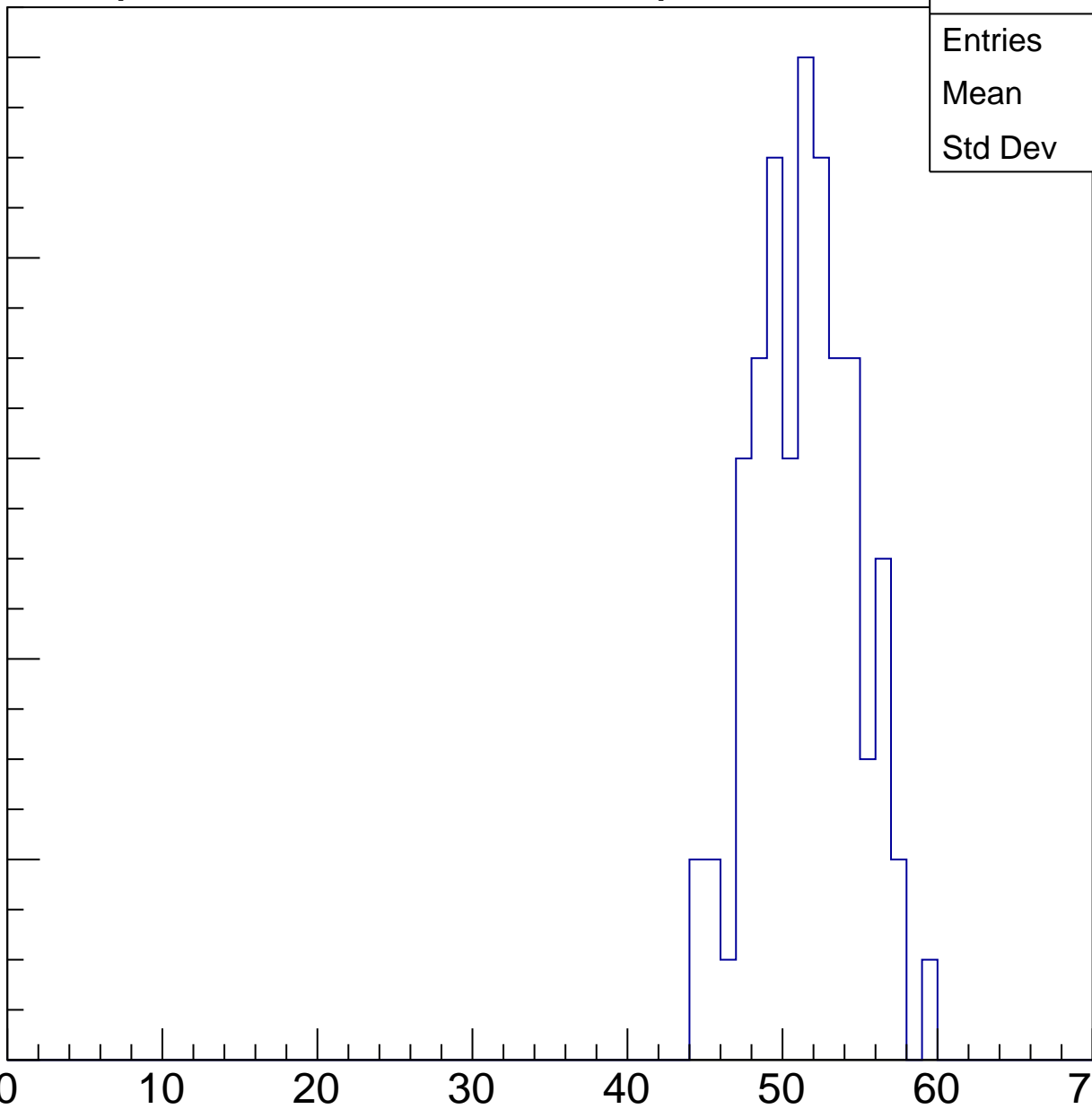
30

40

50

60

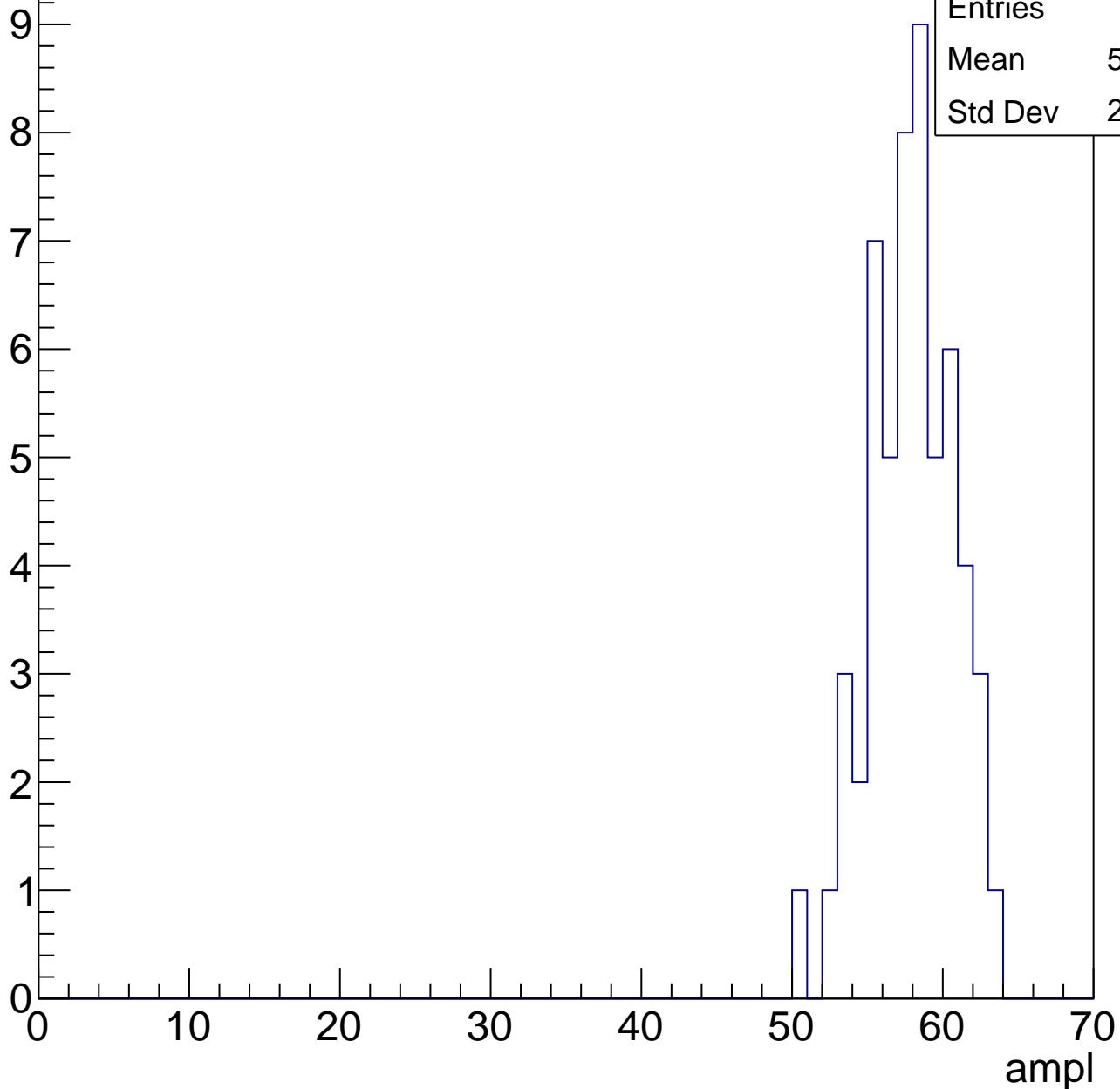
ampl



B0L001S, U11-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

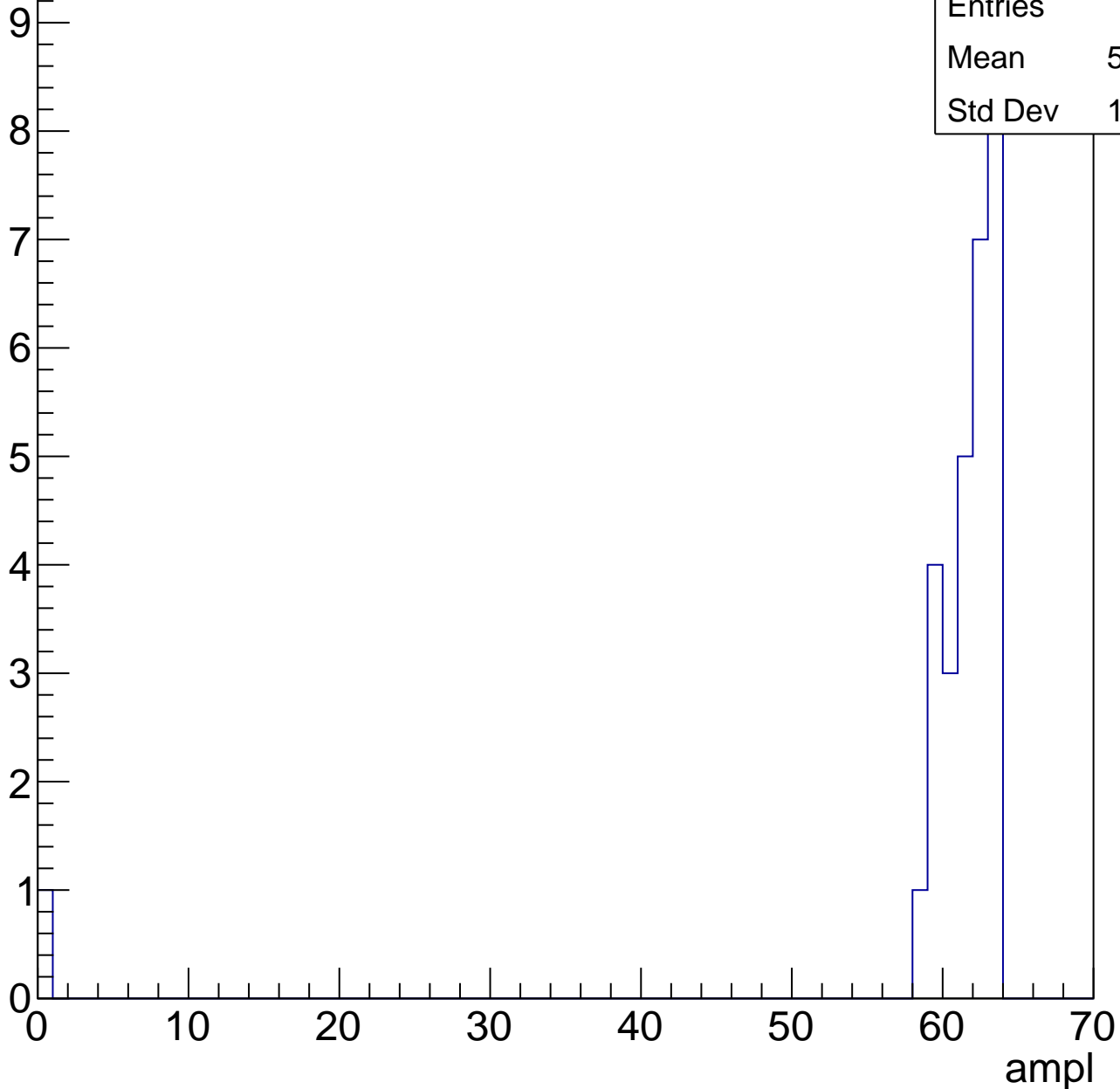


B0L001S, U11-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

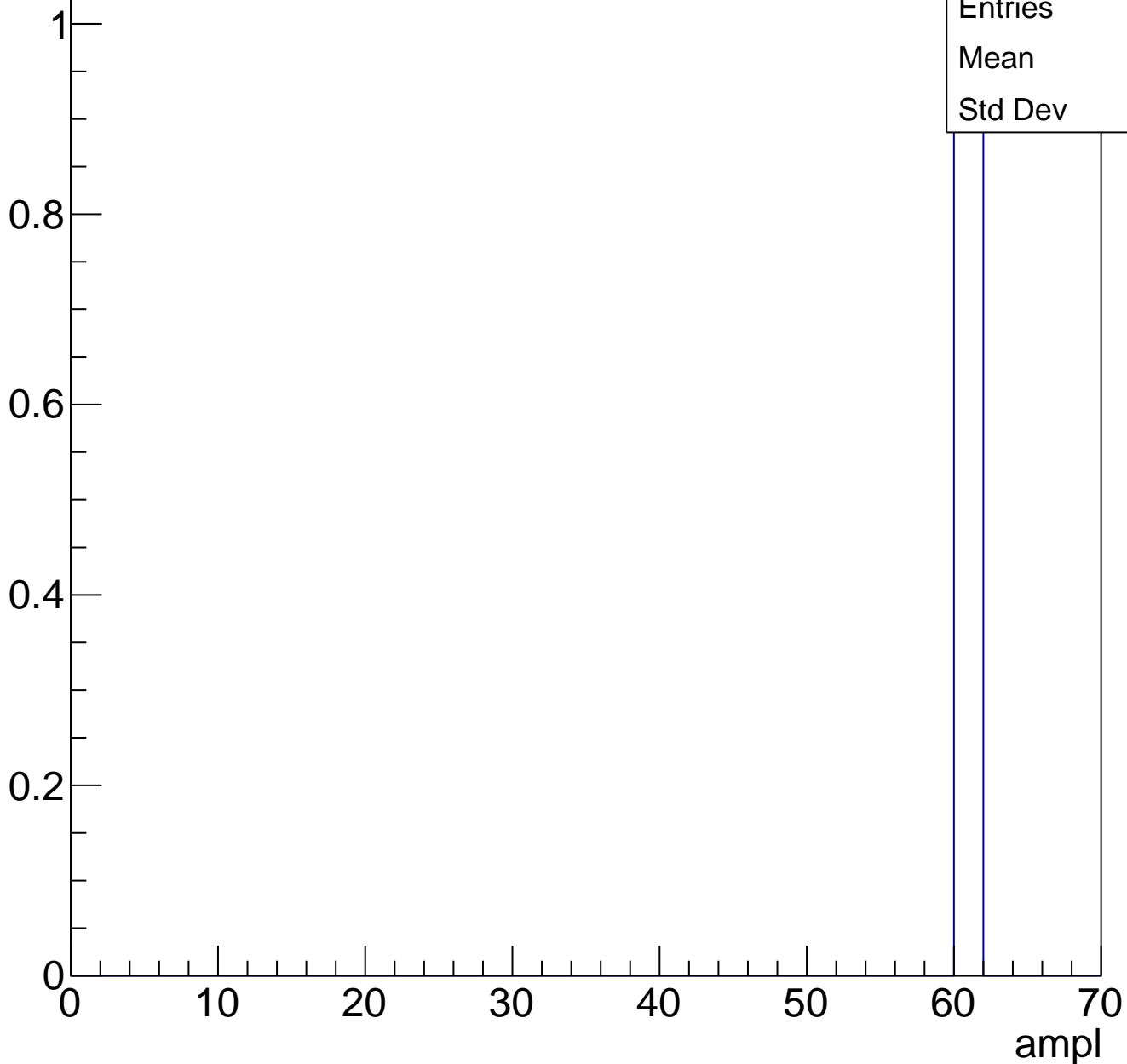
Entries	30
Mean	59.33
Std Dev	11.12



B0L001S, U11-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U11-ch13, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	30.43
Std Dev	7.234

Gaus mean : 32.4140

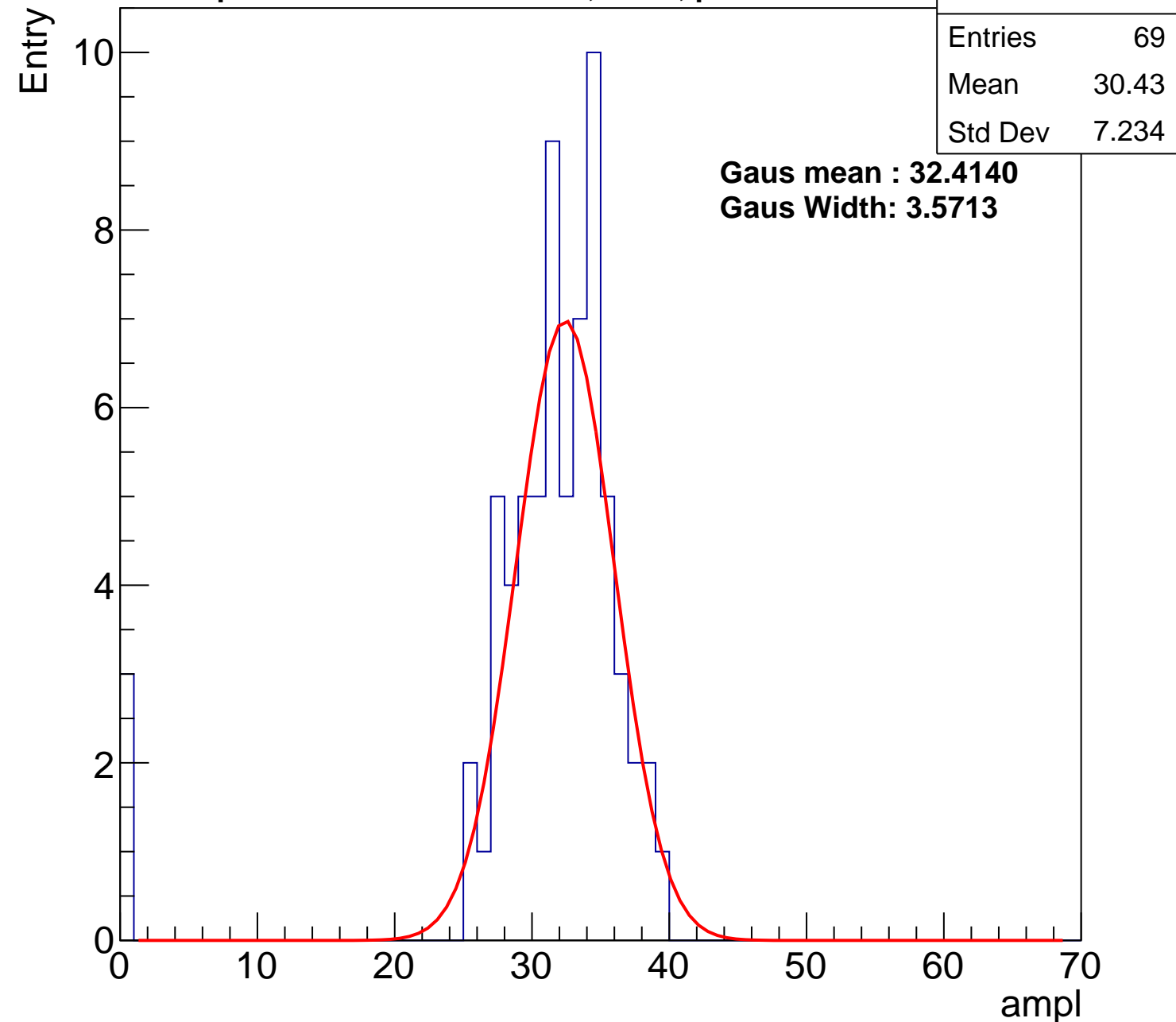
Gaus Width: 3.5713

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch13, adc1

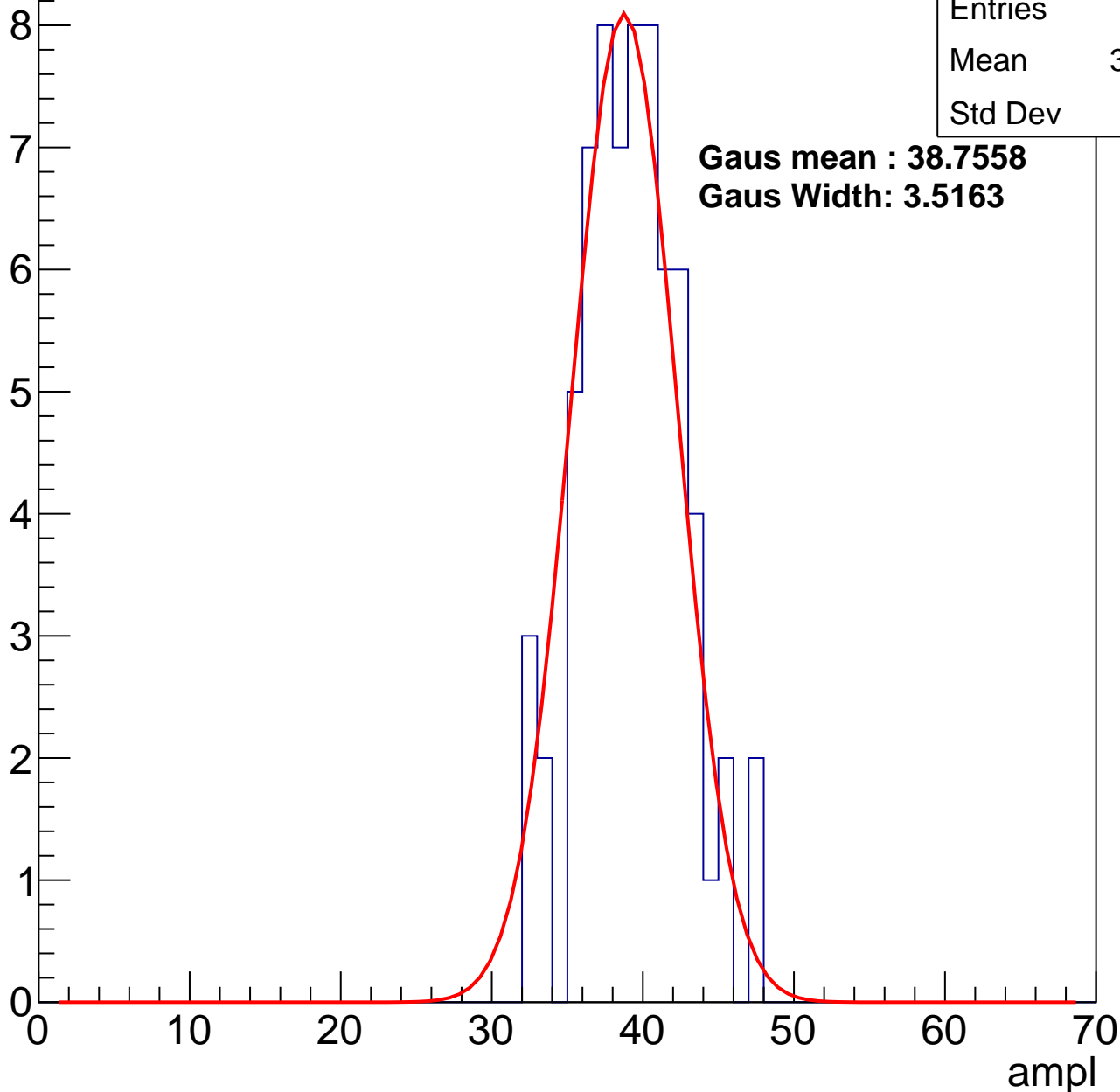
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	38.86
Std Dev	3.35

Gaus mean : 38.7558

Gaus Width: 3.5163



B0L001S, U11-ch13, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	45.64
Std Dev	3.295

Gaus mean : 46.0163

Gaus Width: 3.4375

Entry

10

8

6

4

2

0

0

10

20

30

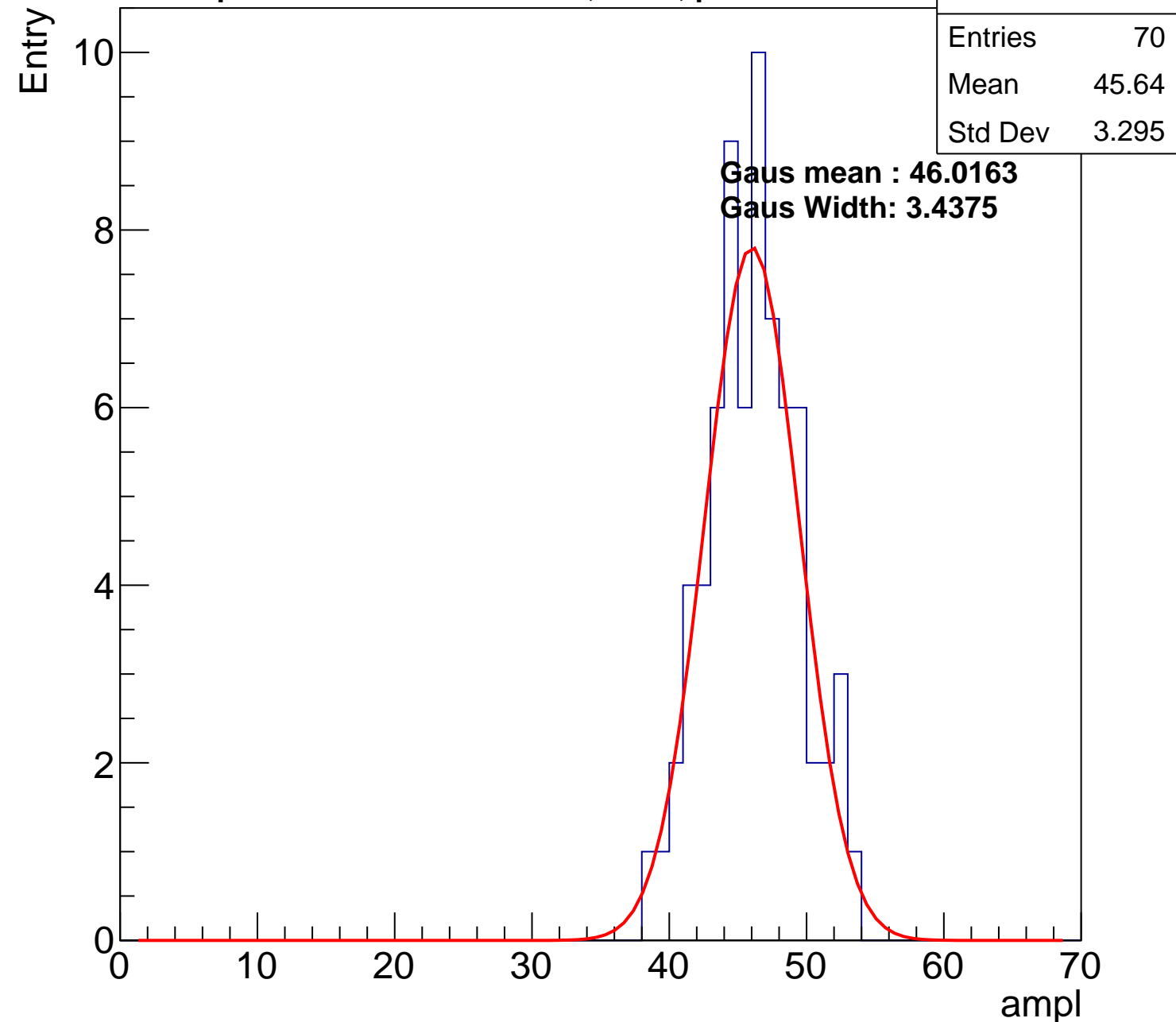
40

50

60

70

ampl



B0L001S, U11-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	52.65
Std Dev	3.338

Entry

10

8

6

4

2

0

0

10

20

30

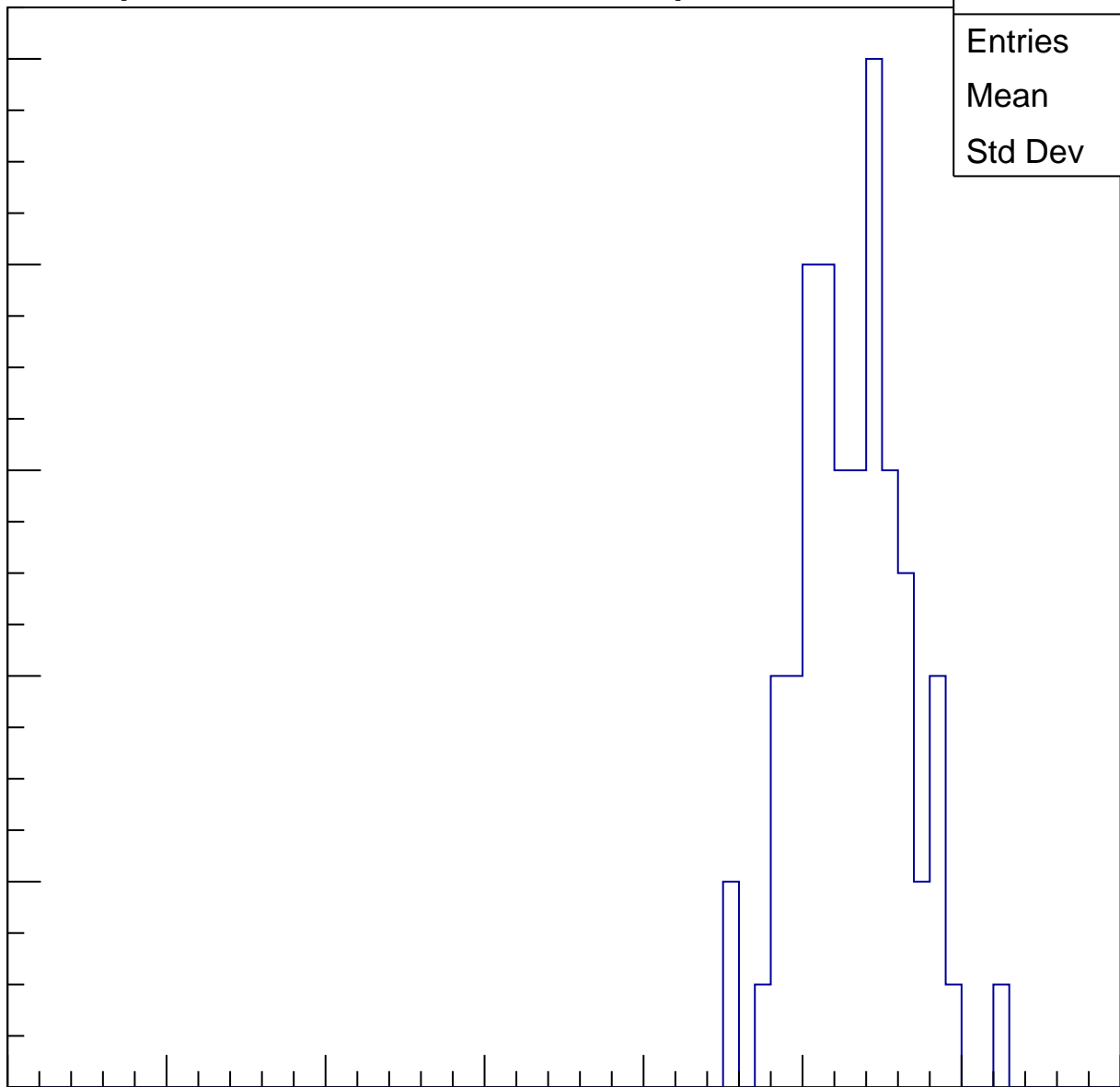
40

50

60

70

ampl

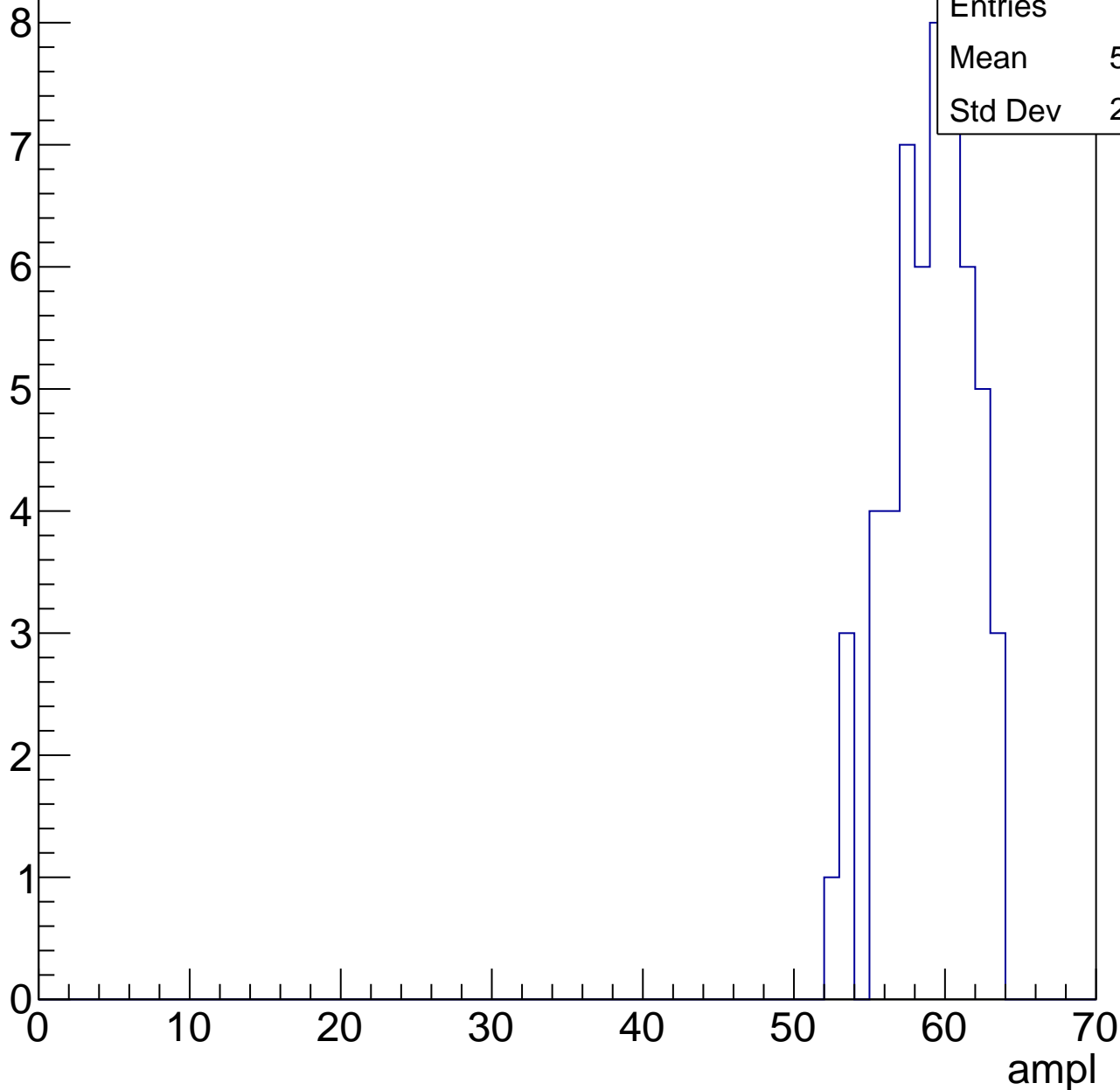


B0L001S, U11-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	58.53
Std Dev	2.709

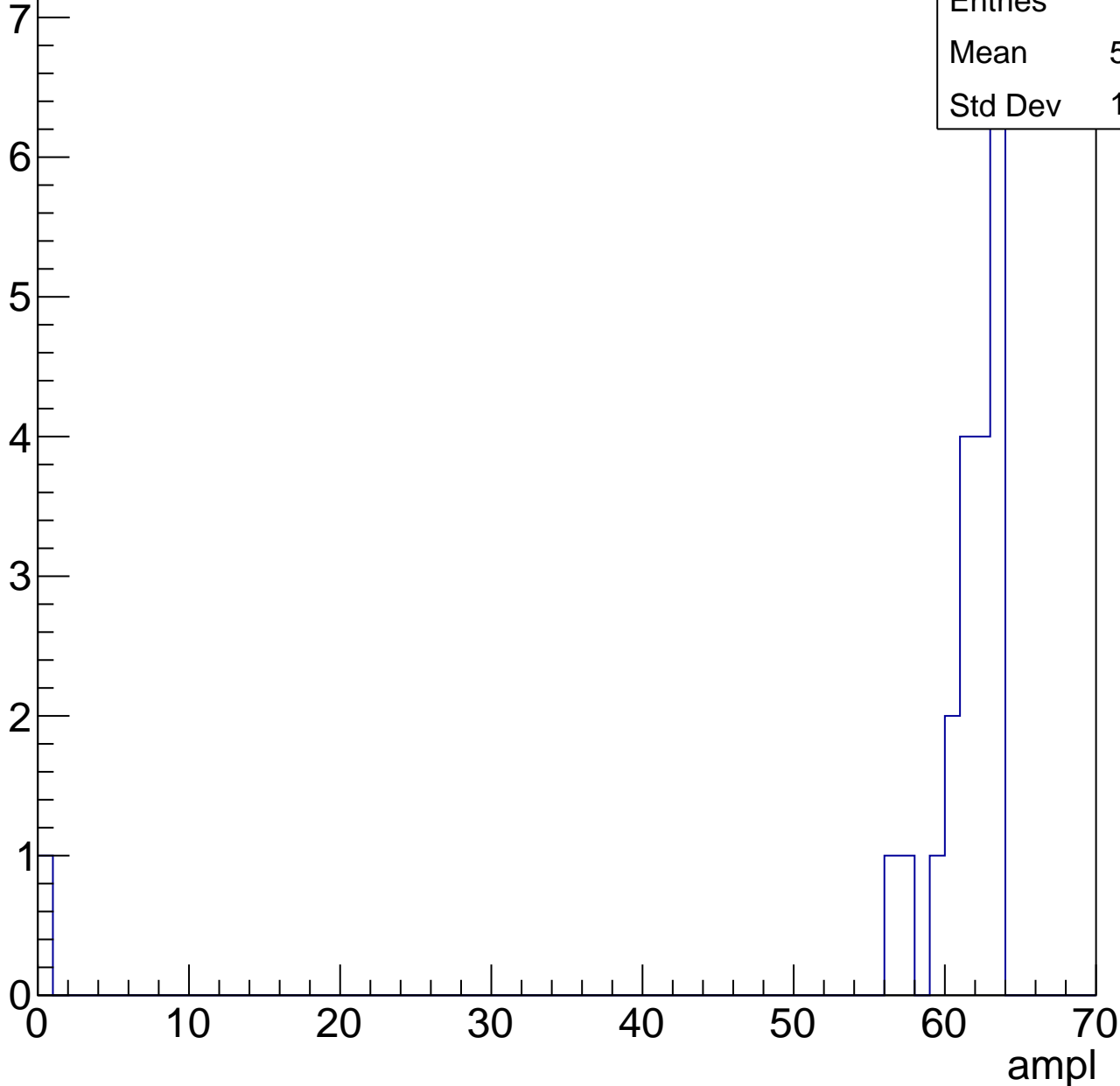


B0L001S, U11-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.33
Std Dev	13.18



B0L001S, U11-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch14, adc0

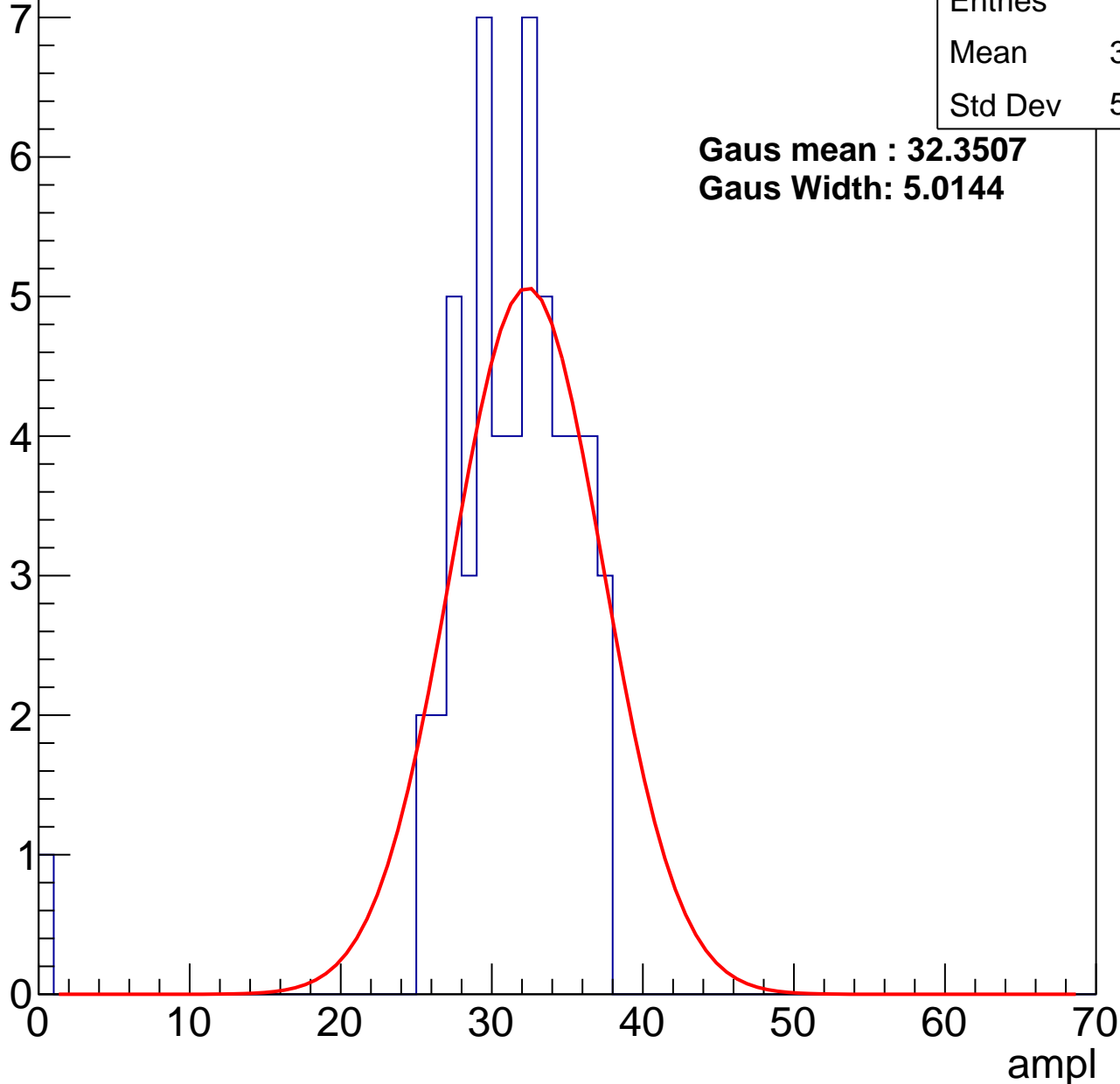
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	30.69
Std Dev	5.312

Gaus mean : 32.3507

Gaus Width: 5.0144



B0L001S, U11-ch14, adc1

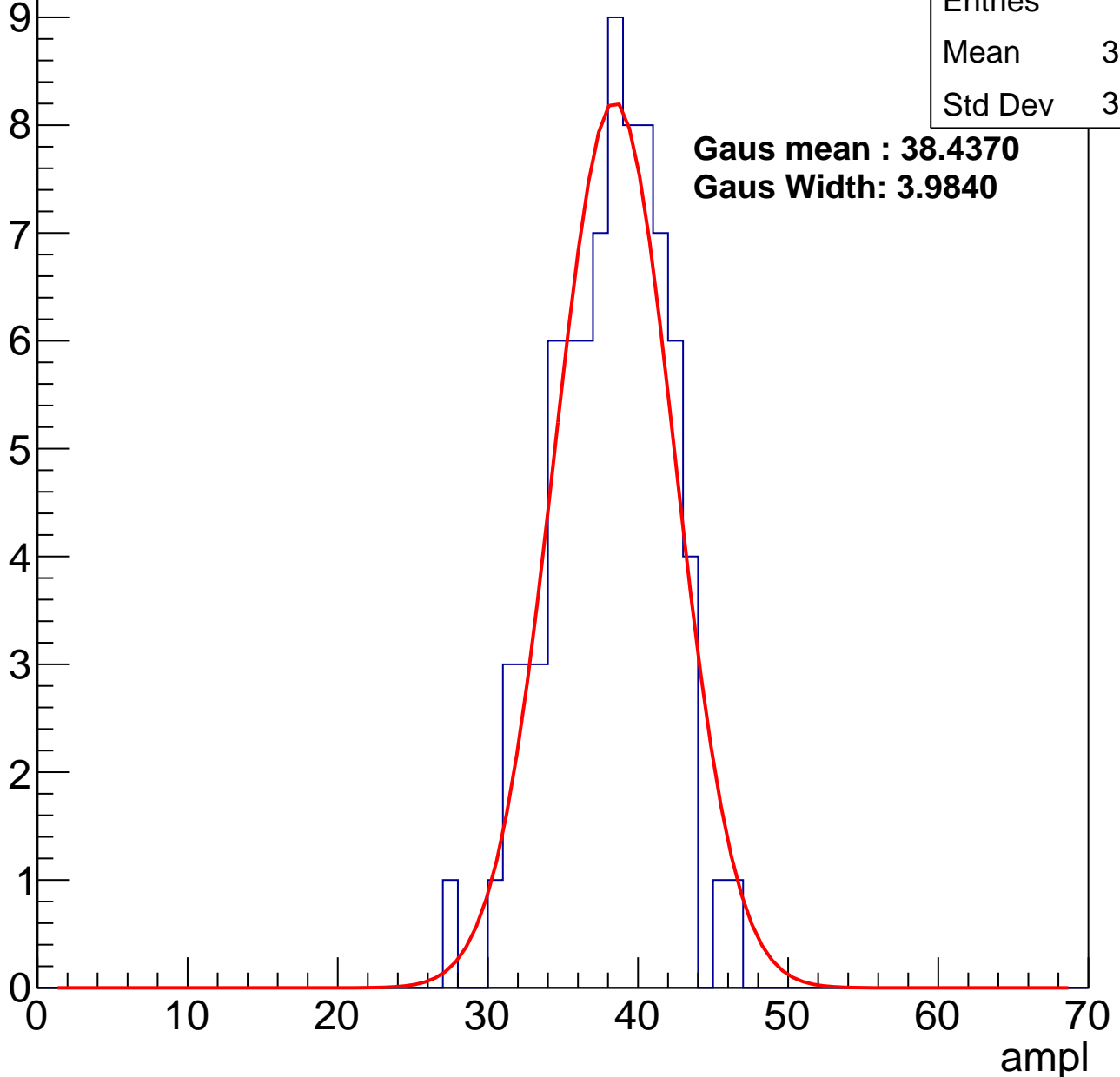
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	37.62
Std Dev	3.703

Gaus mean : 38.4370

Gaus Width: 3.9840



B0L001S, U11-ch14, adc2

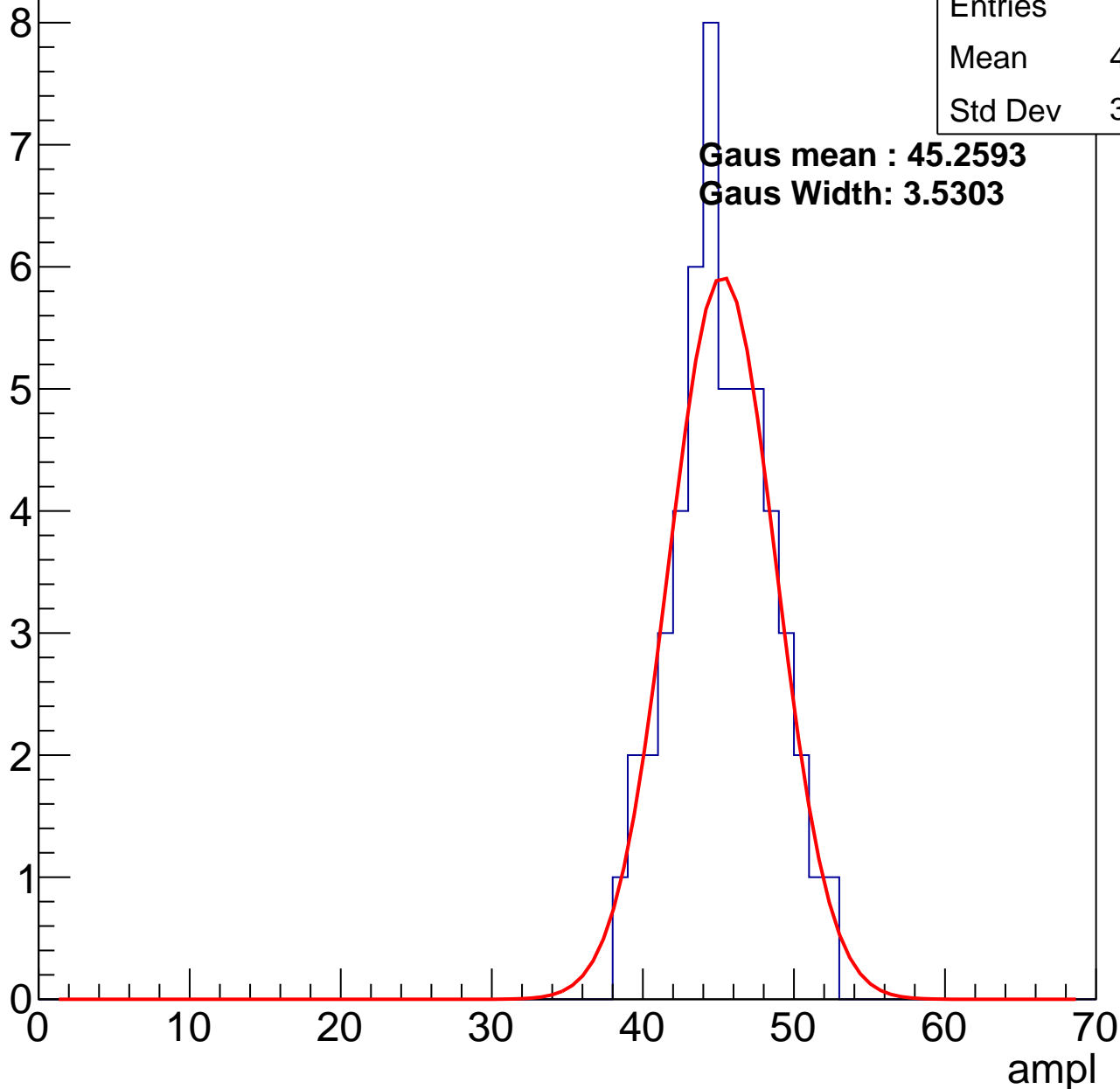
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.79
Std Dev	3.189

Gaus mean : 45.2593

Gaus Width: 3.5303

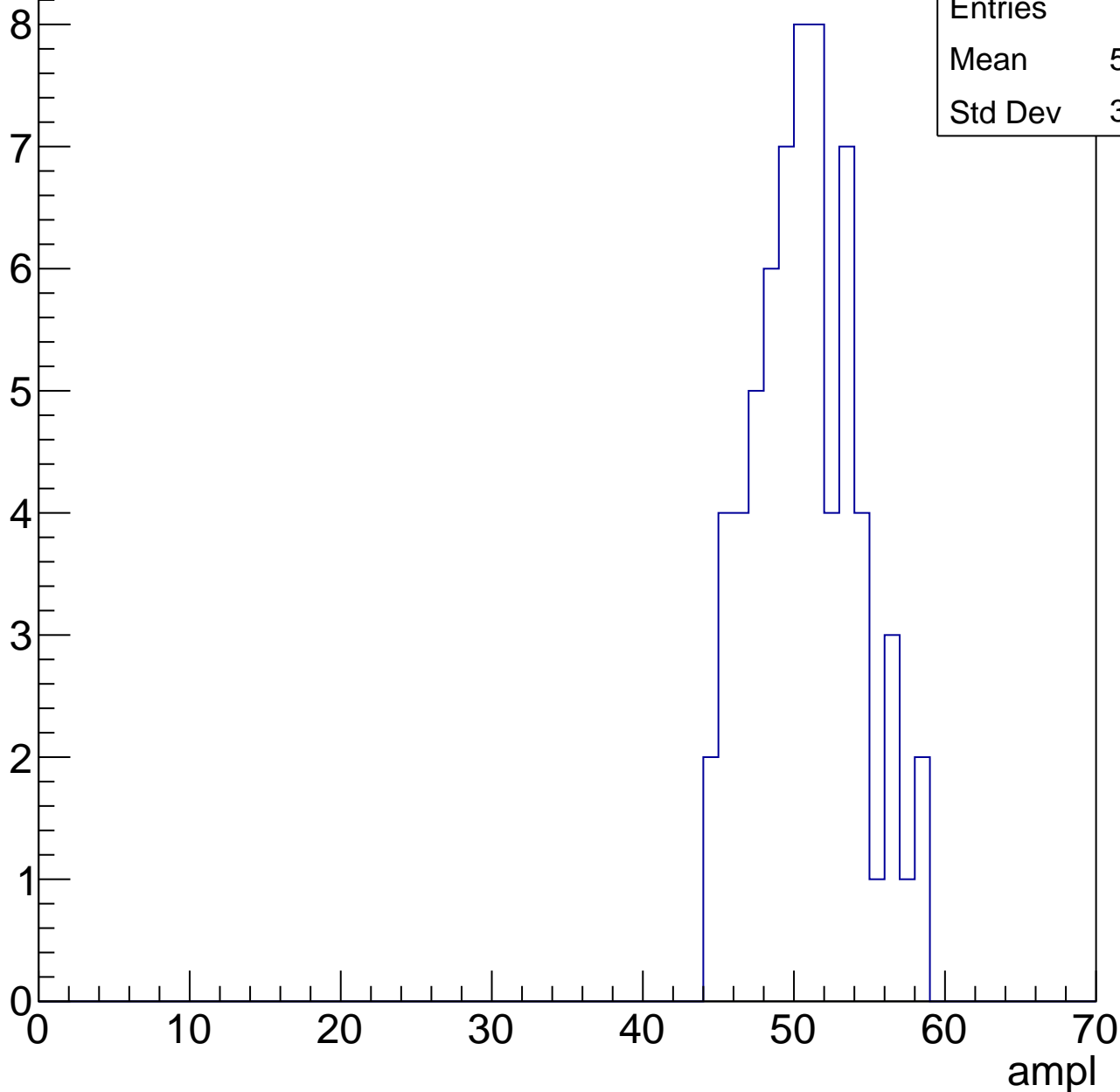


B0L001S, U11-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.26
Std Dev	3.413



B0L001S, U11-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

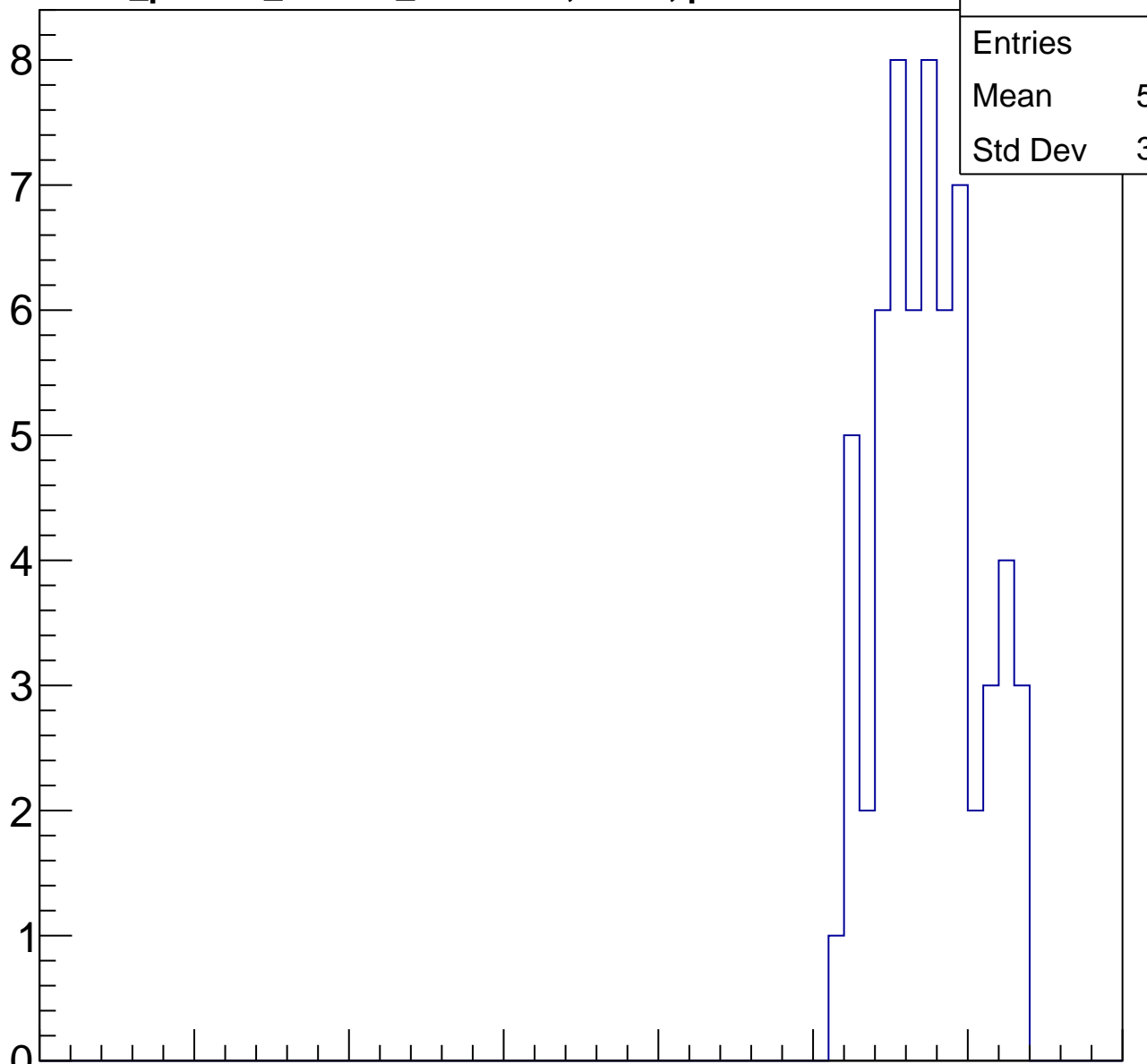
Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	56.95
Std Dev	3.118

ampl

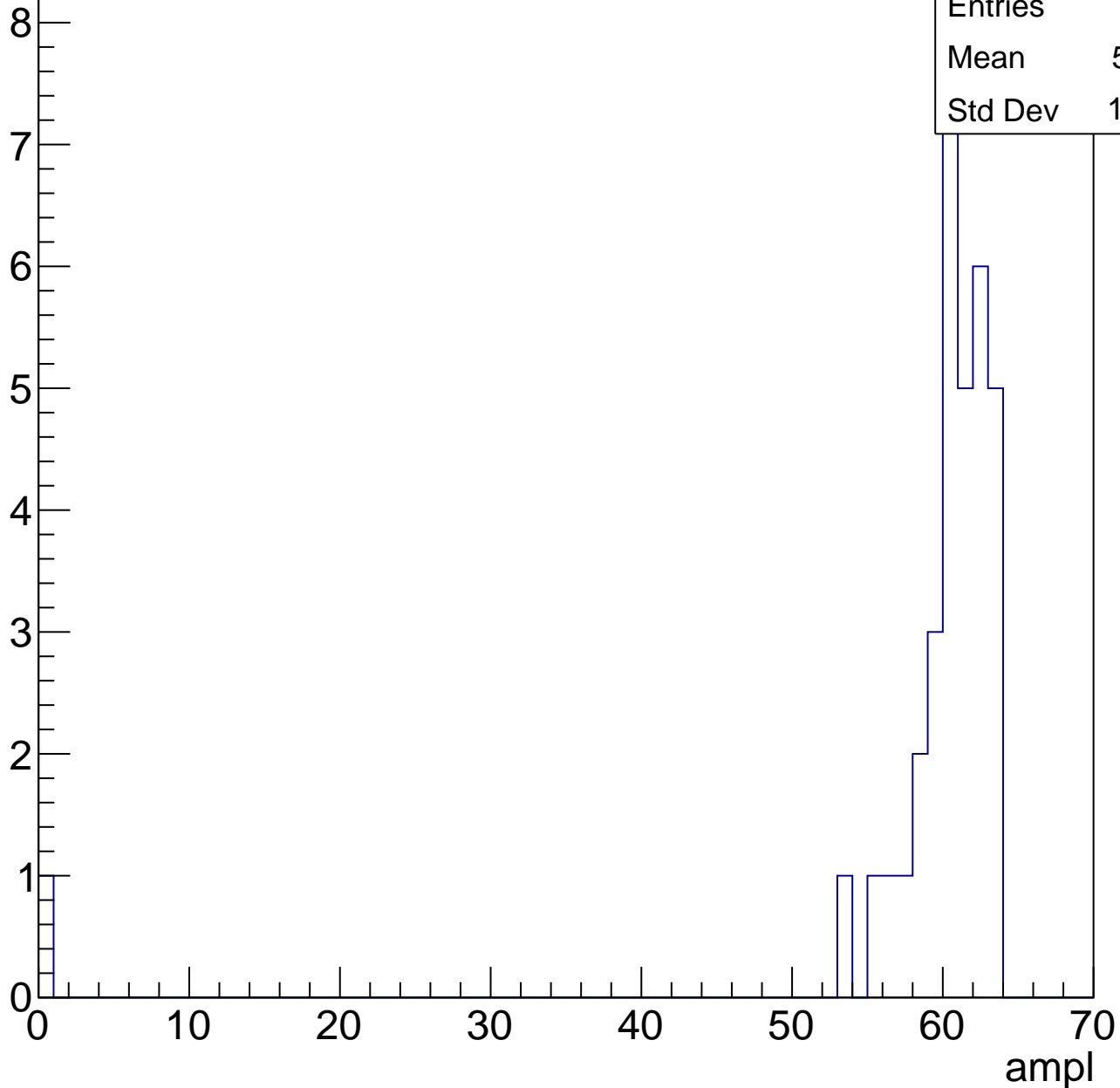
0 10 20 30 40 50 60 70



B0L001S, U11-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

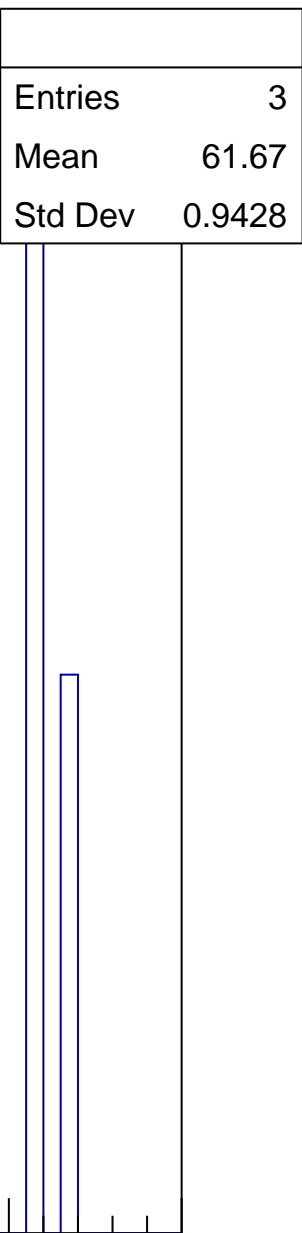
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.9428

0 10 20 30 40 50 60 70

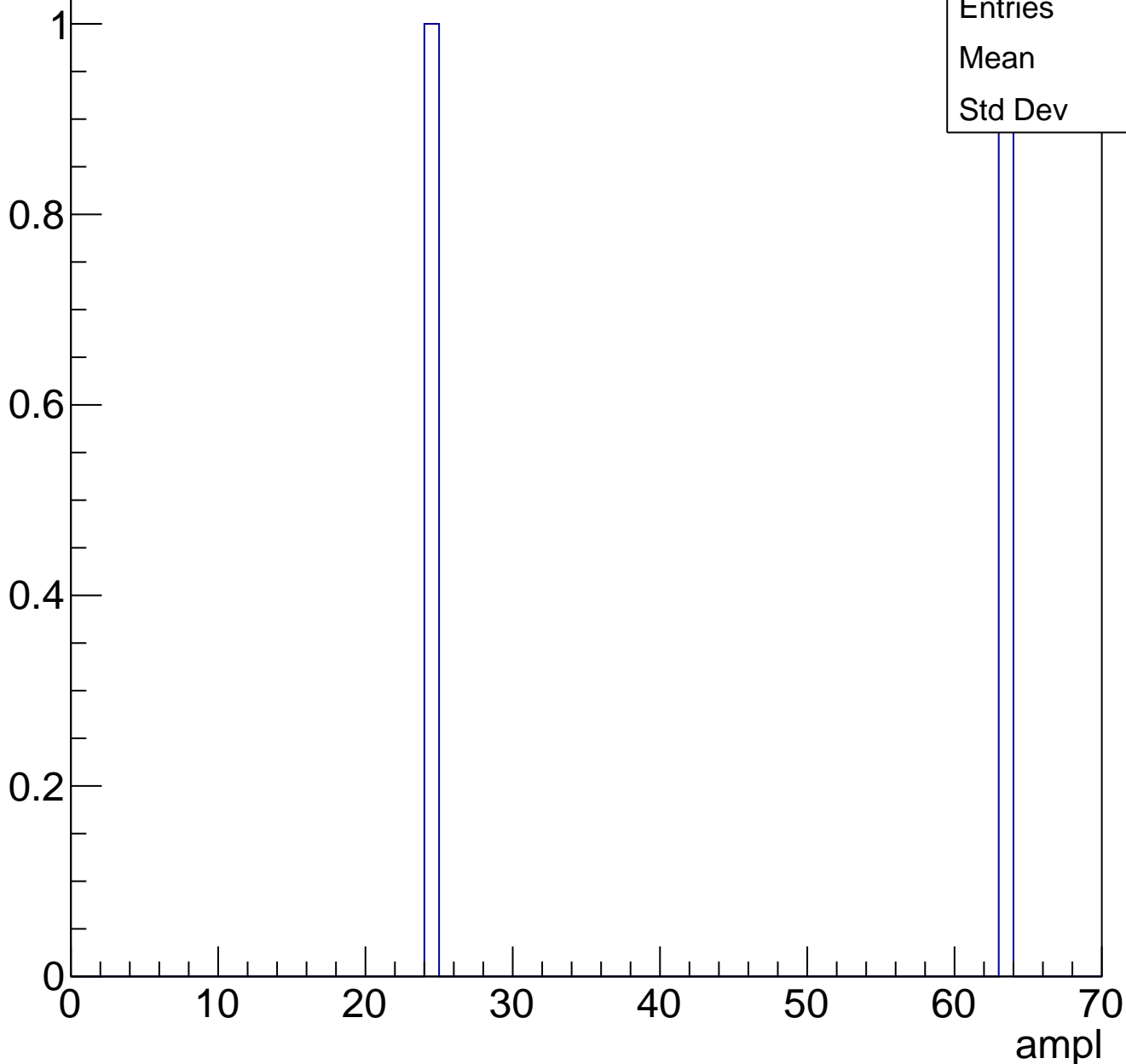
ampl



B0L001S, U11-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch15, adc0

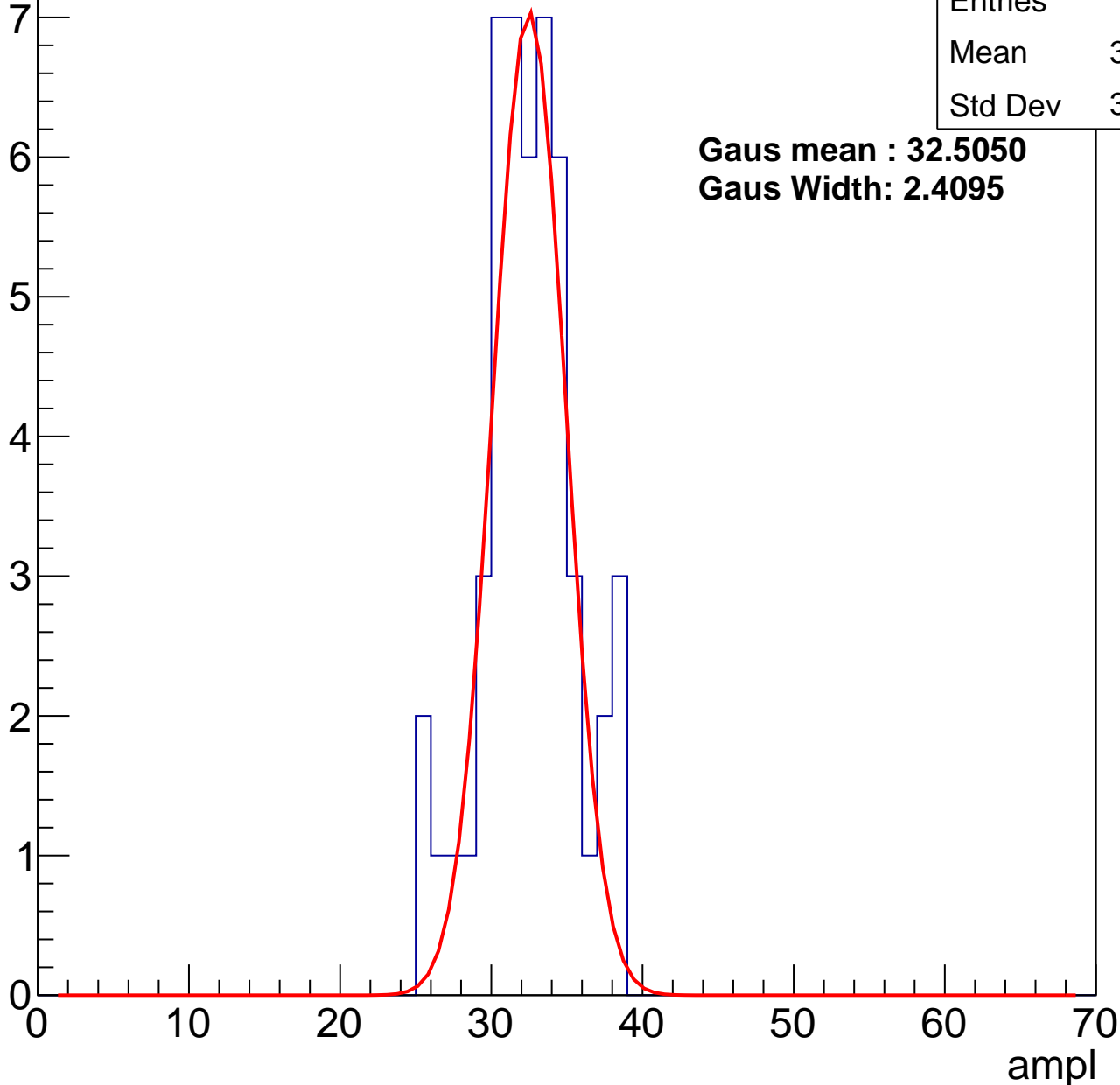
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	32.02
Std Dev	3.063

Gaus mean : 32.5050

Gaus Width: 2.4095



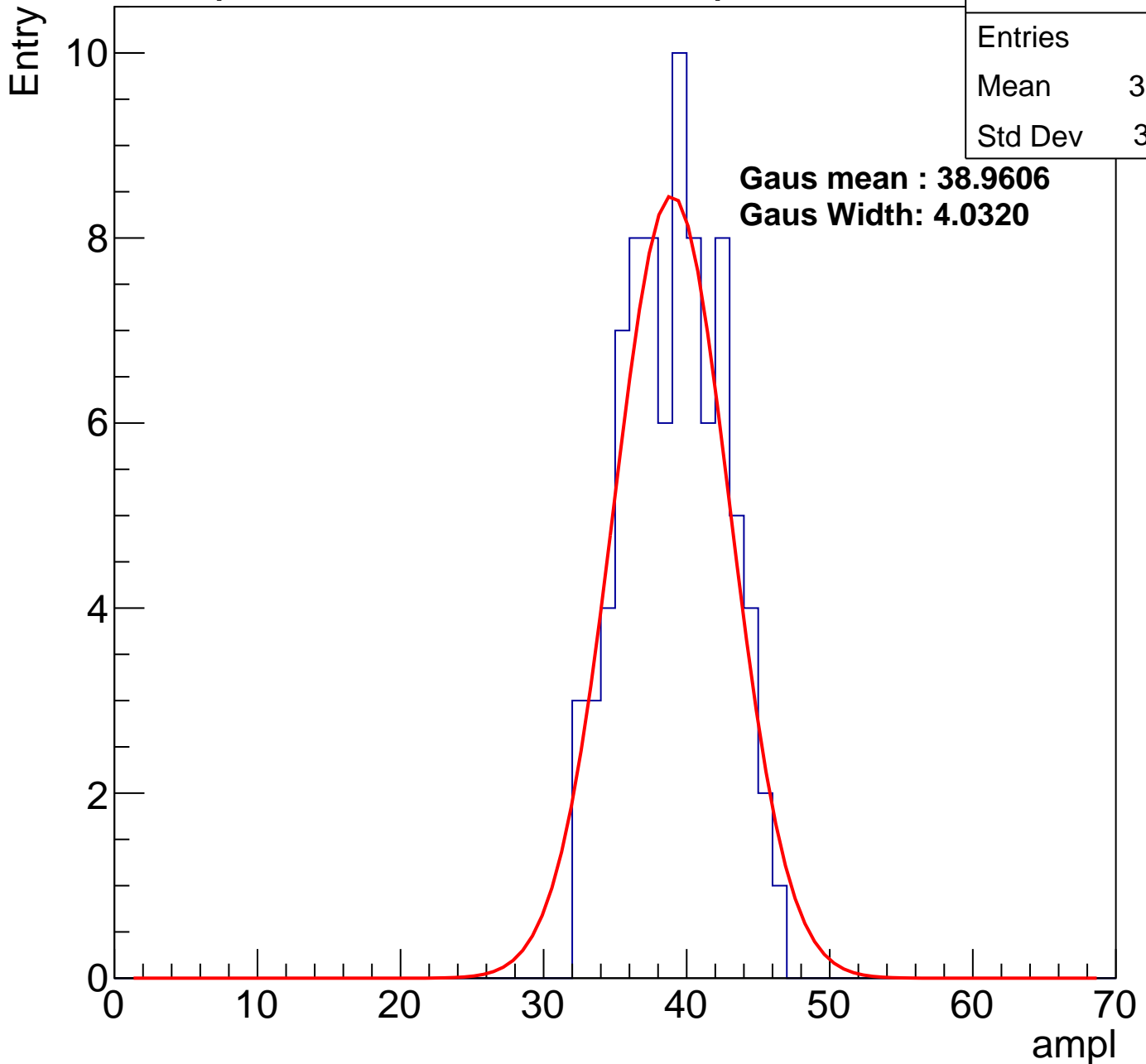
B0L001S, U11-ch15, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	38.64
Std Dev	3.421

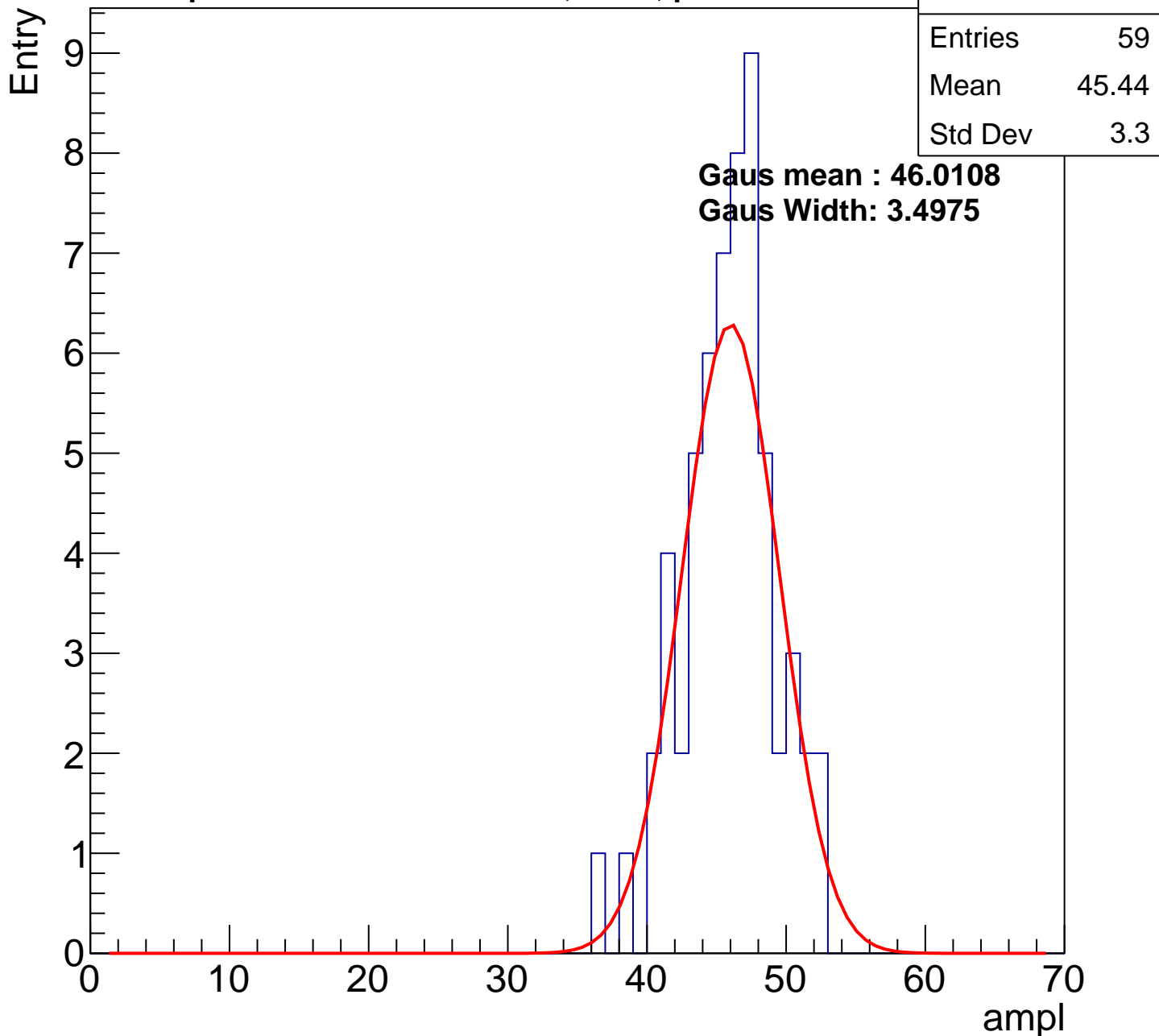
Gaus mean : 38.9606

Gaus Width: 4.0320



B0L001S, U11-ch15, adc2

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U11-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	52.65
Std Dev	3.263

Entry

10

8

6

4

2

0

0

10

20

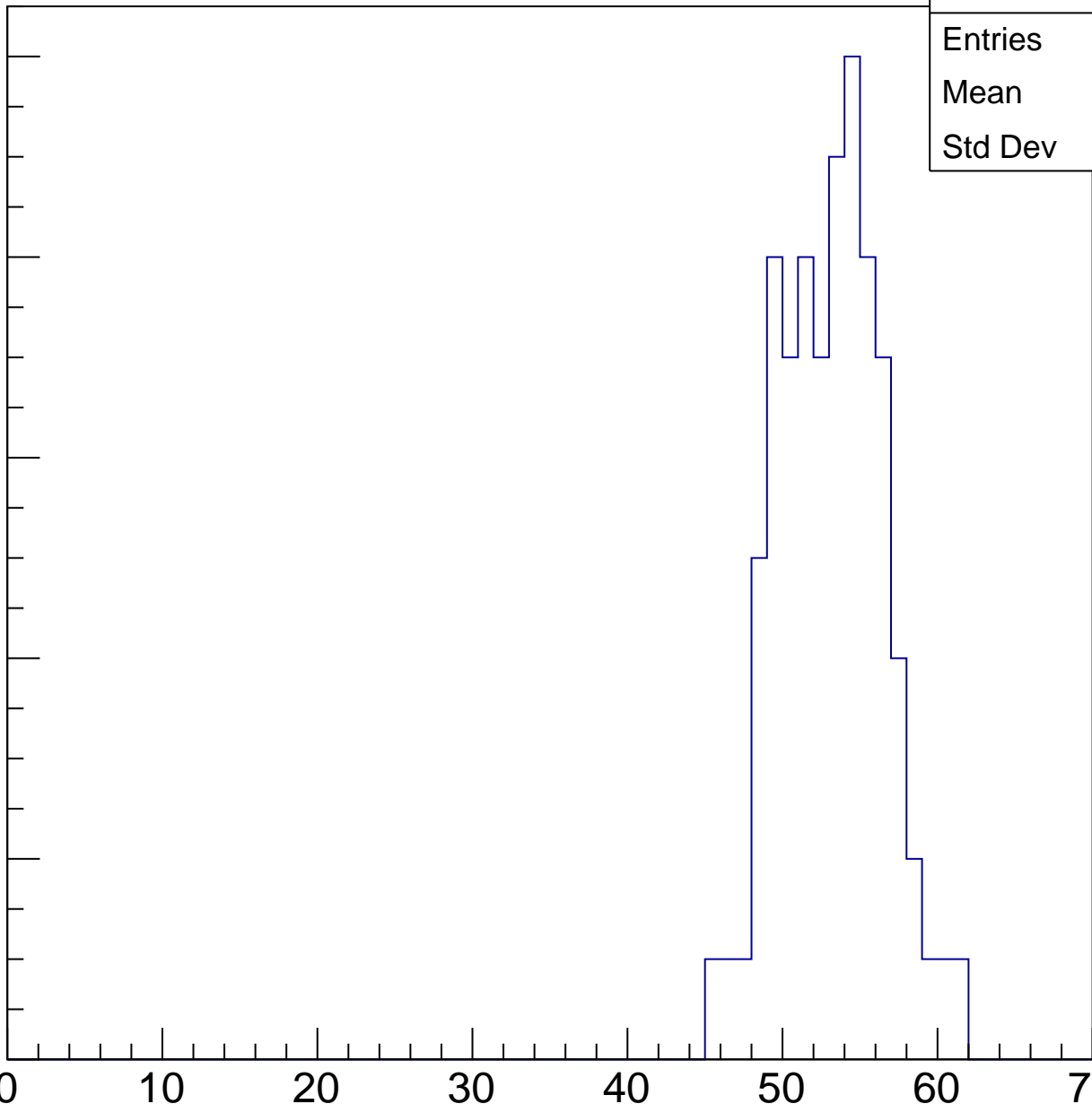
30

40

50

60

ampl

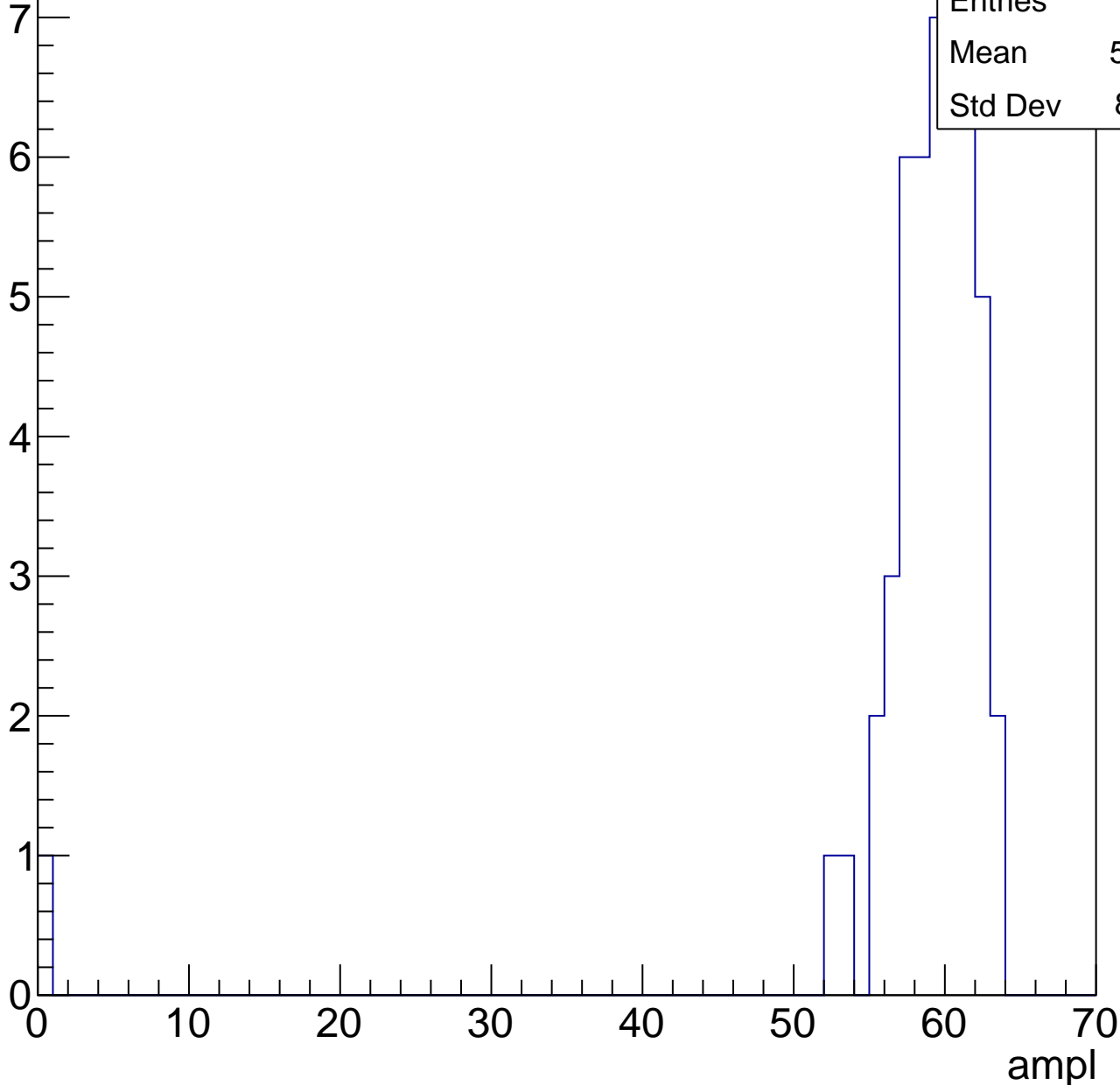


B0L001S, U11-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

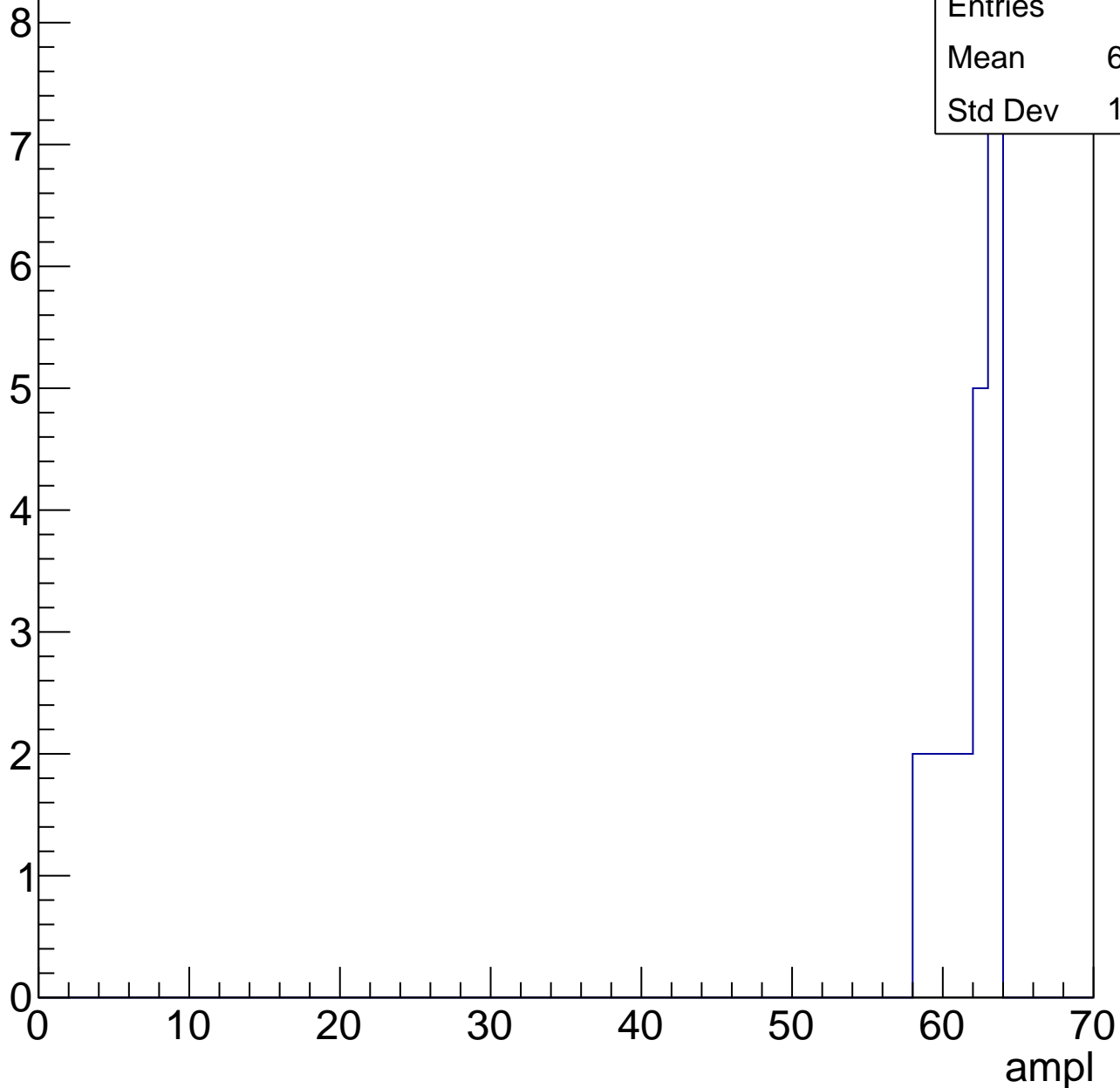
Entries	48
Mean	57.69
Std Dev	8.761



B0L001S, U11-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch16, adc0

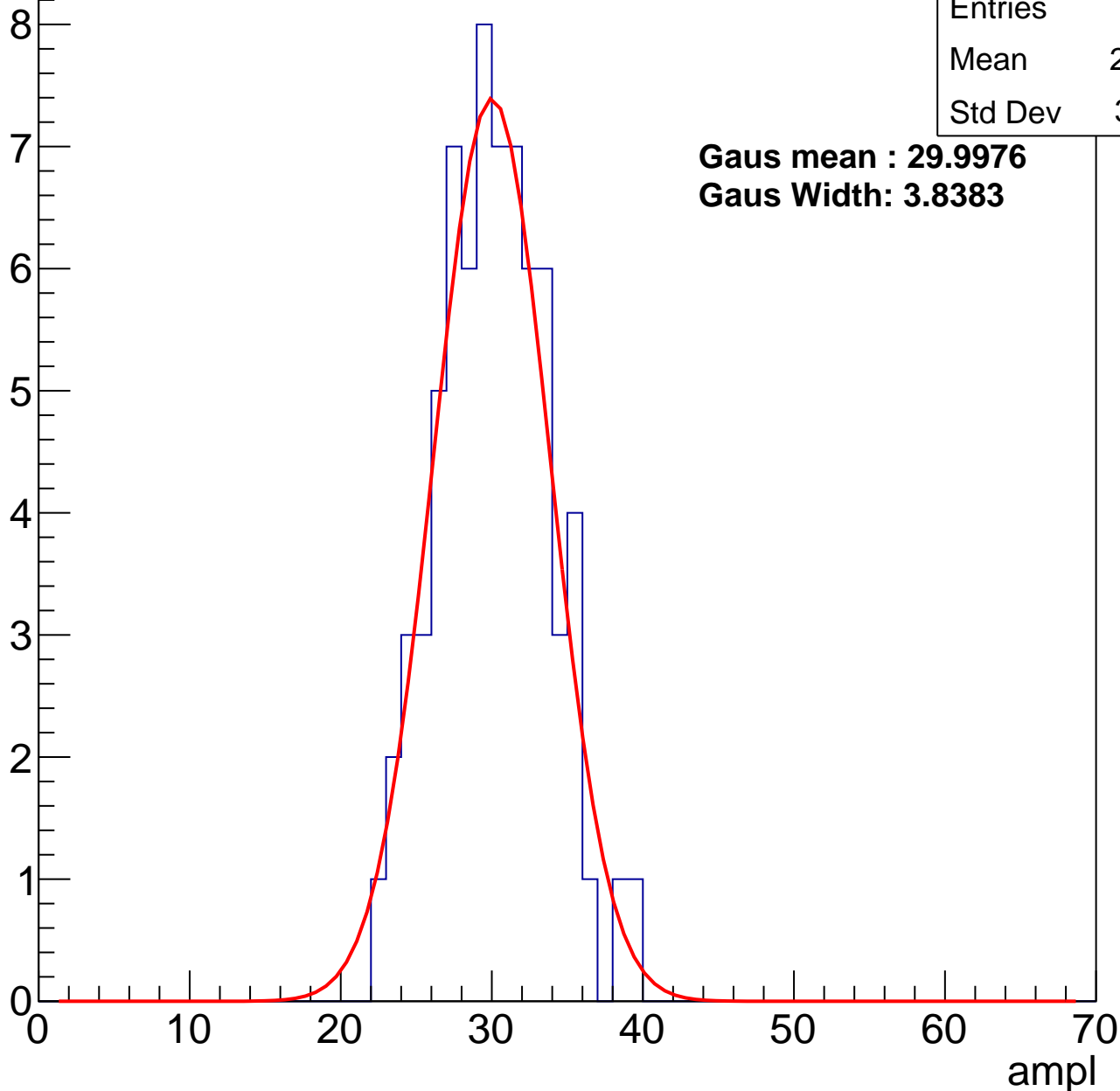
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.66
Std Dev	3.611

Gaus mean : 29.9976

Gaus Width: 3.8383



B0L001S, U11-ch16, adc1

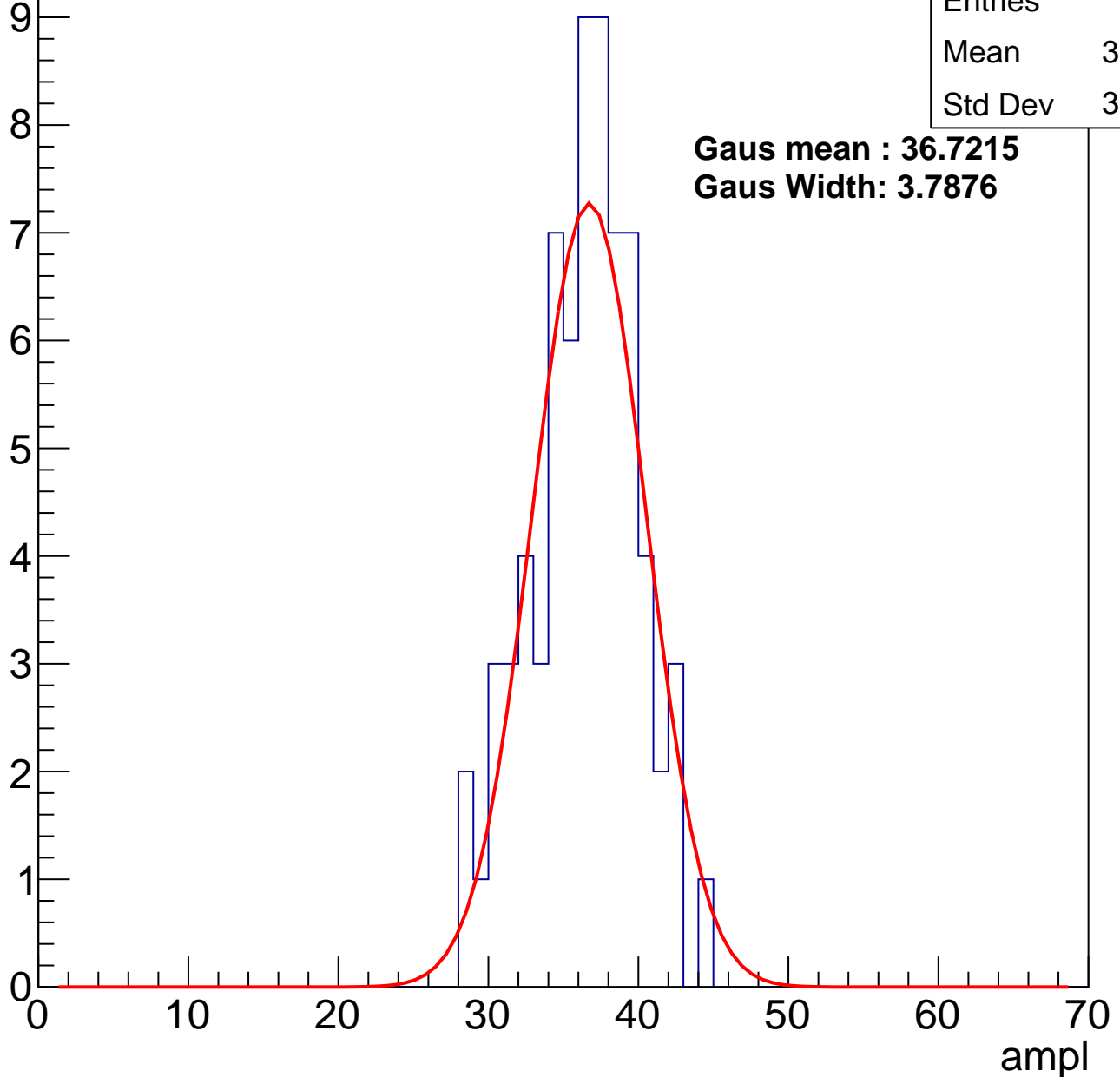
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	35.93
Std Dev	3.502

Gaus mean : 36.7215

Gaus Width: 3.7876



B0L001S, U11-ch16, adc2

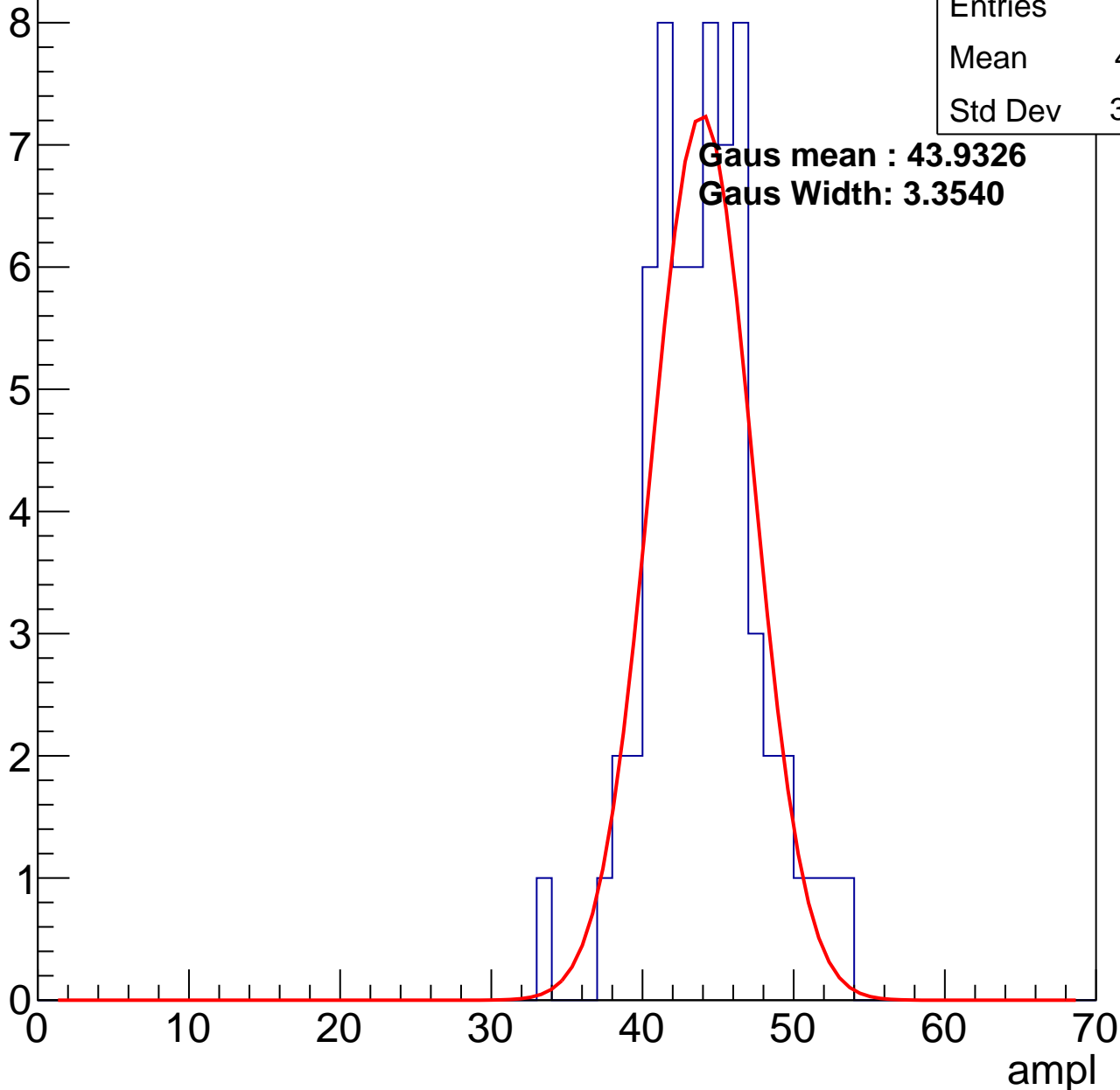
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.61
Std Dev	3.605

Gaus mean : 43.9326

Gaus Width: 3.3540

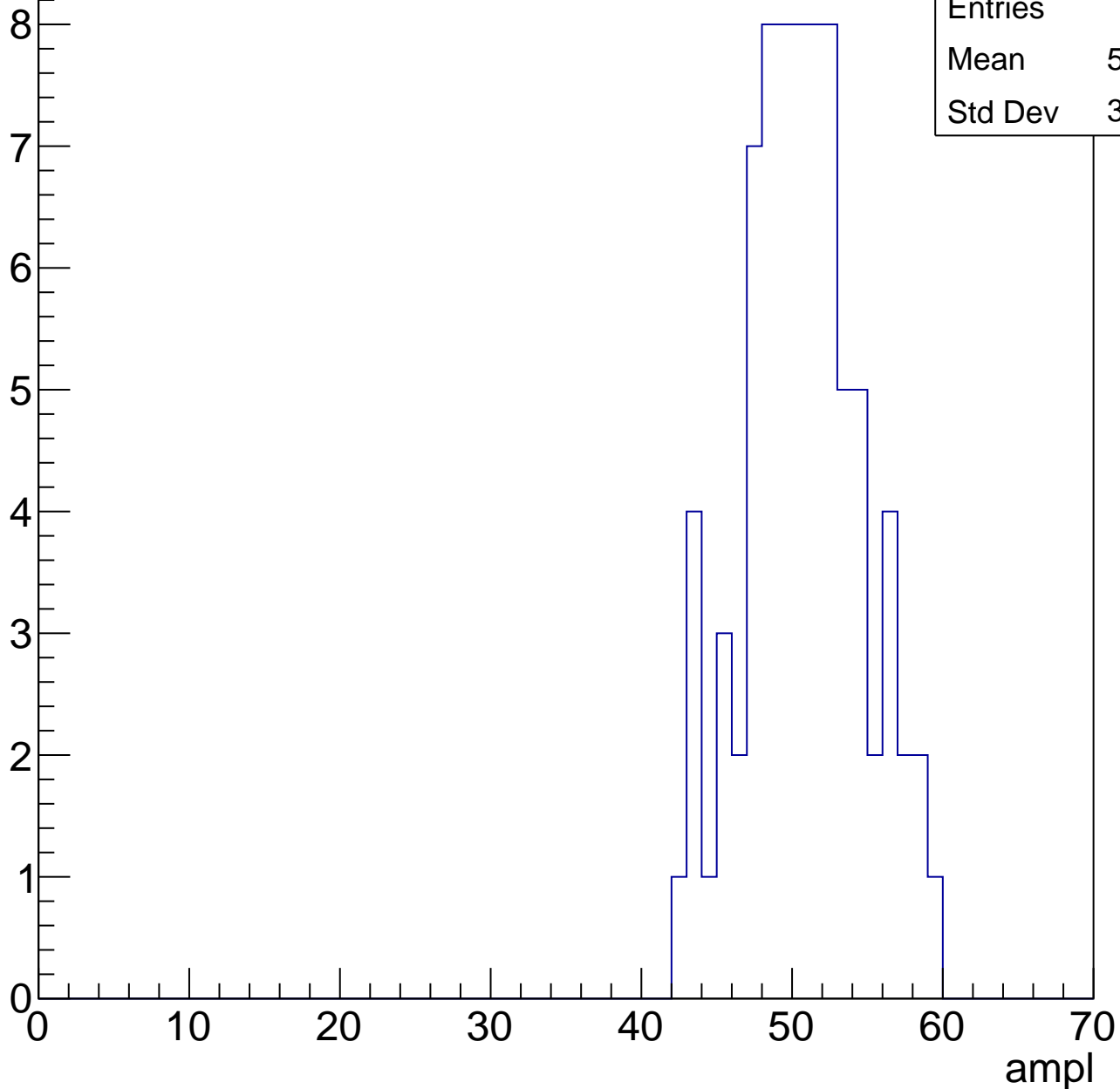


B0L001S, U11-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	50.28
Std Dev	3.842

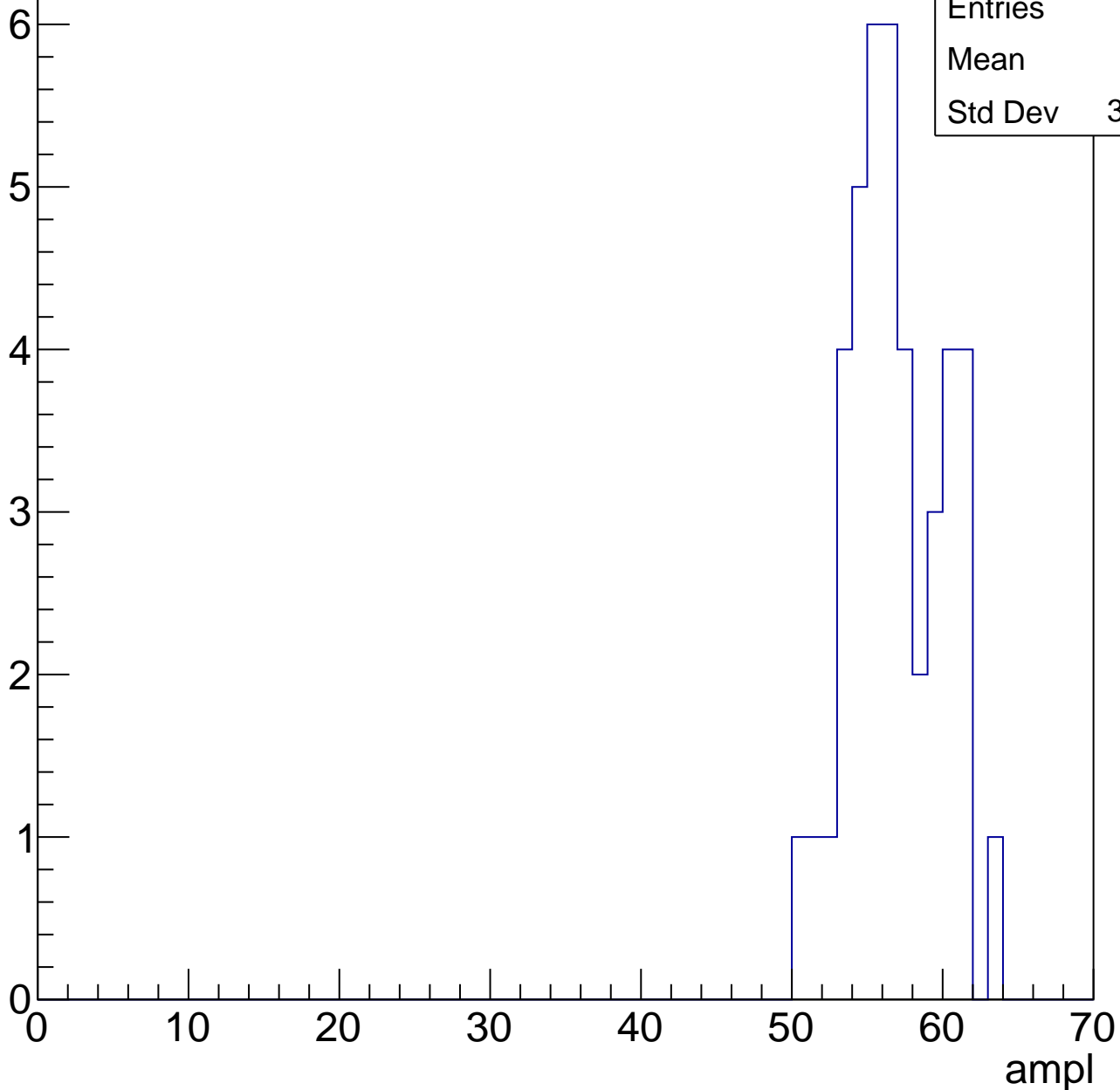


B0L001S, U11-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	56.4
Std Dev	3.024

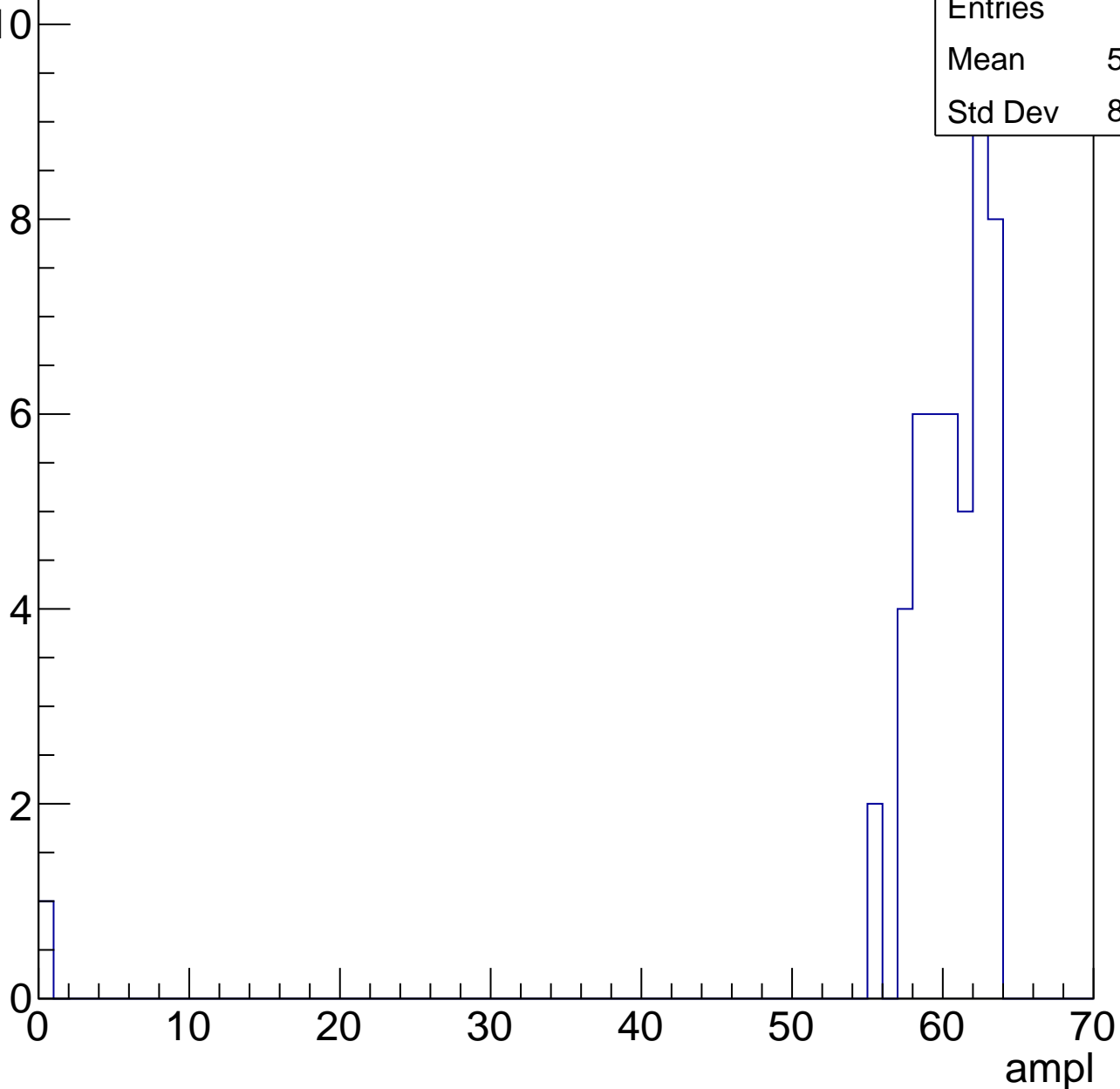


B0L001S, U11-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.94
Std Dev	8.873



B0L001S, U11-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch17, adc0

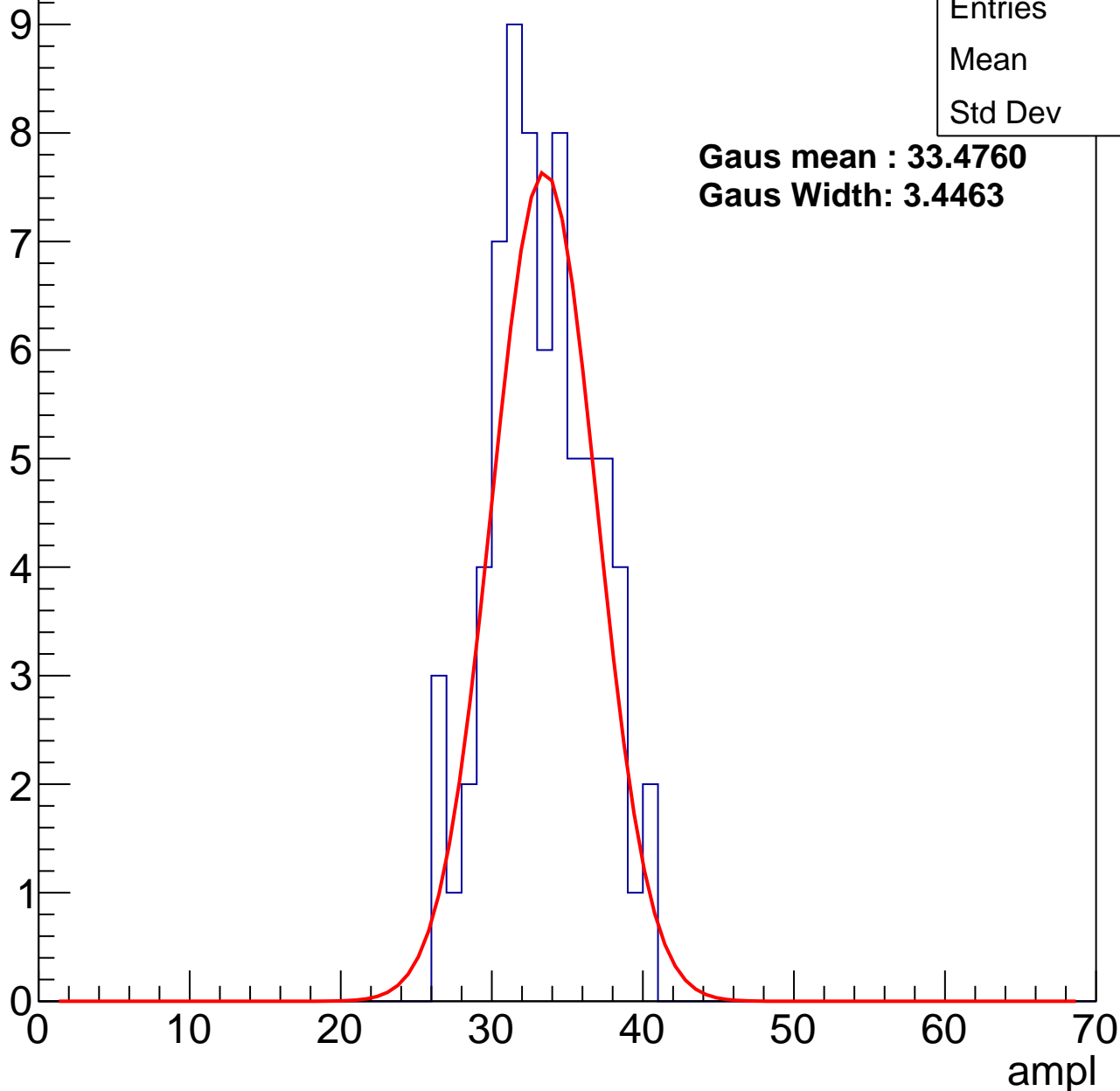
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	32.9
Std Dev	3.36

Gaus mean : 33.4760

Gaus Width: 3.4463



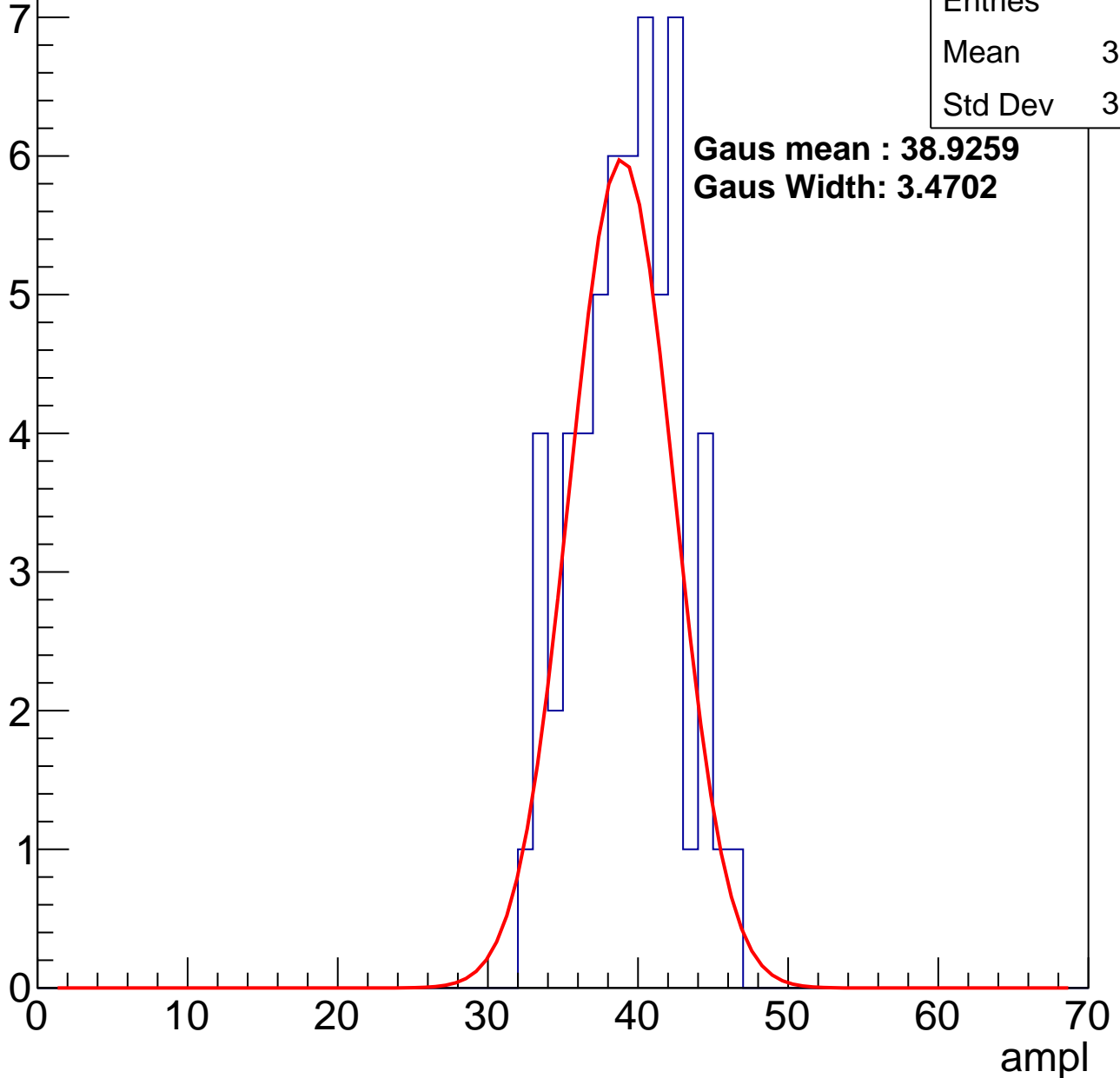
B0L001S, U11-ch17, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	38.83
Std Dev	3.369

Gaus mean : 38.9259
Gaus Width: 3.4702



B0L001S, U11-ch17, adc2

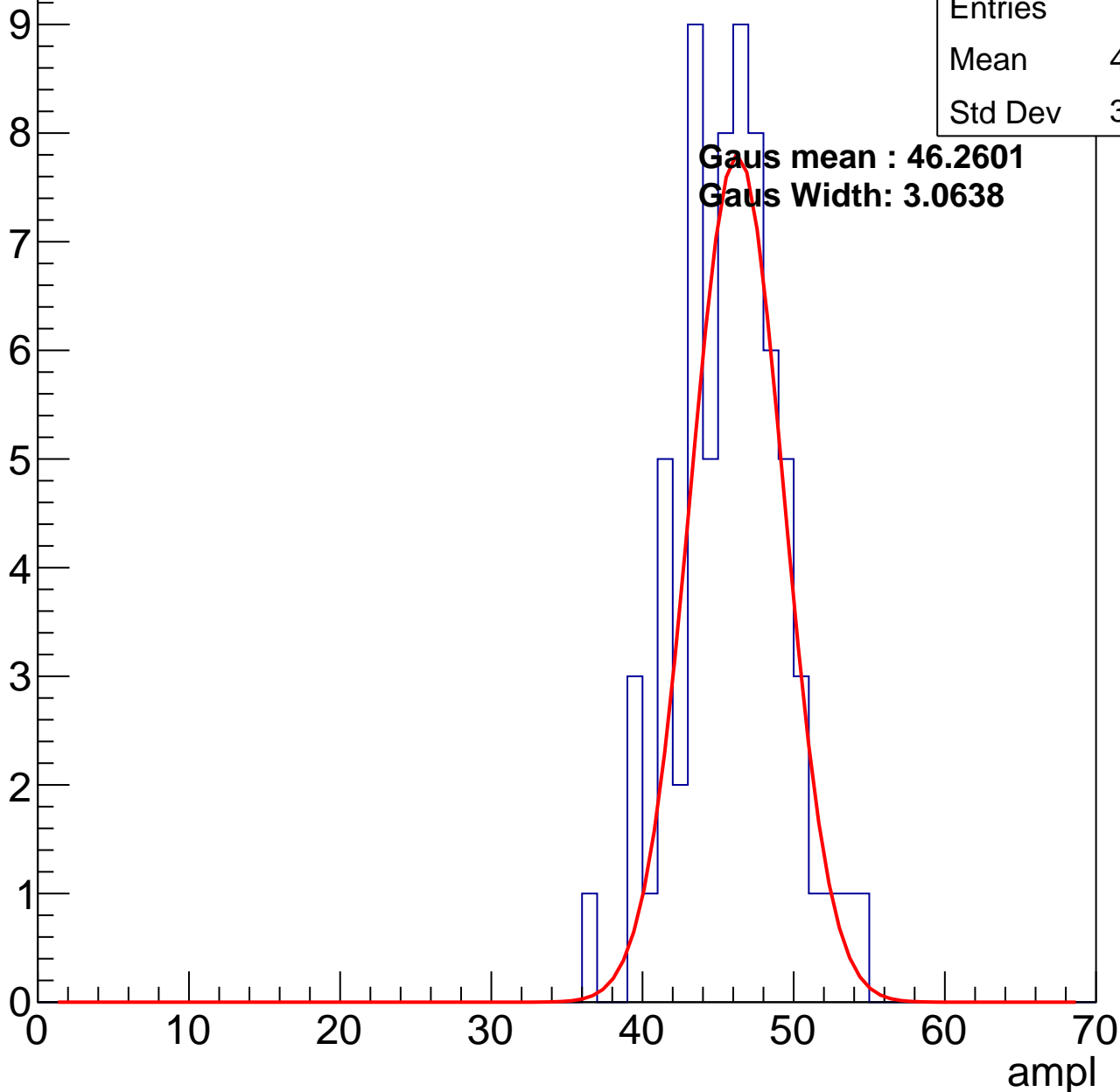
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	45.39
Std Dev	3.452

Gaus mean : 46.2601

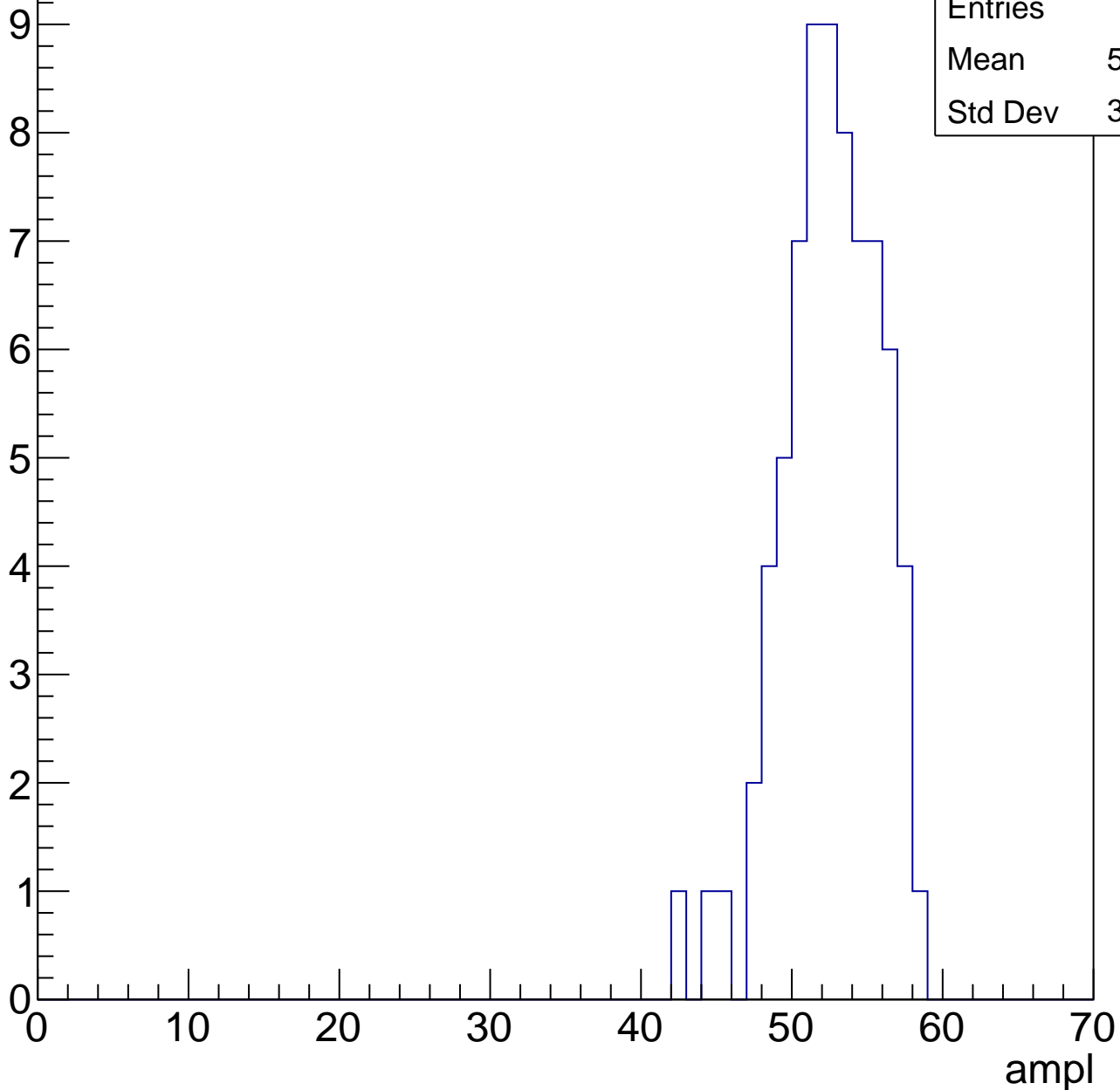
Gaus Width: 3.0638



B0L001S, U11-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

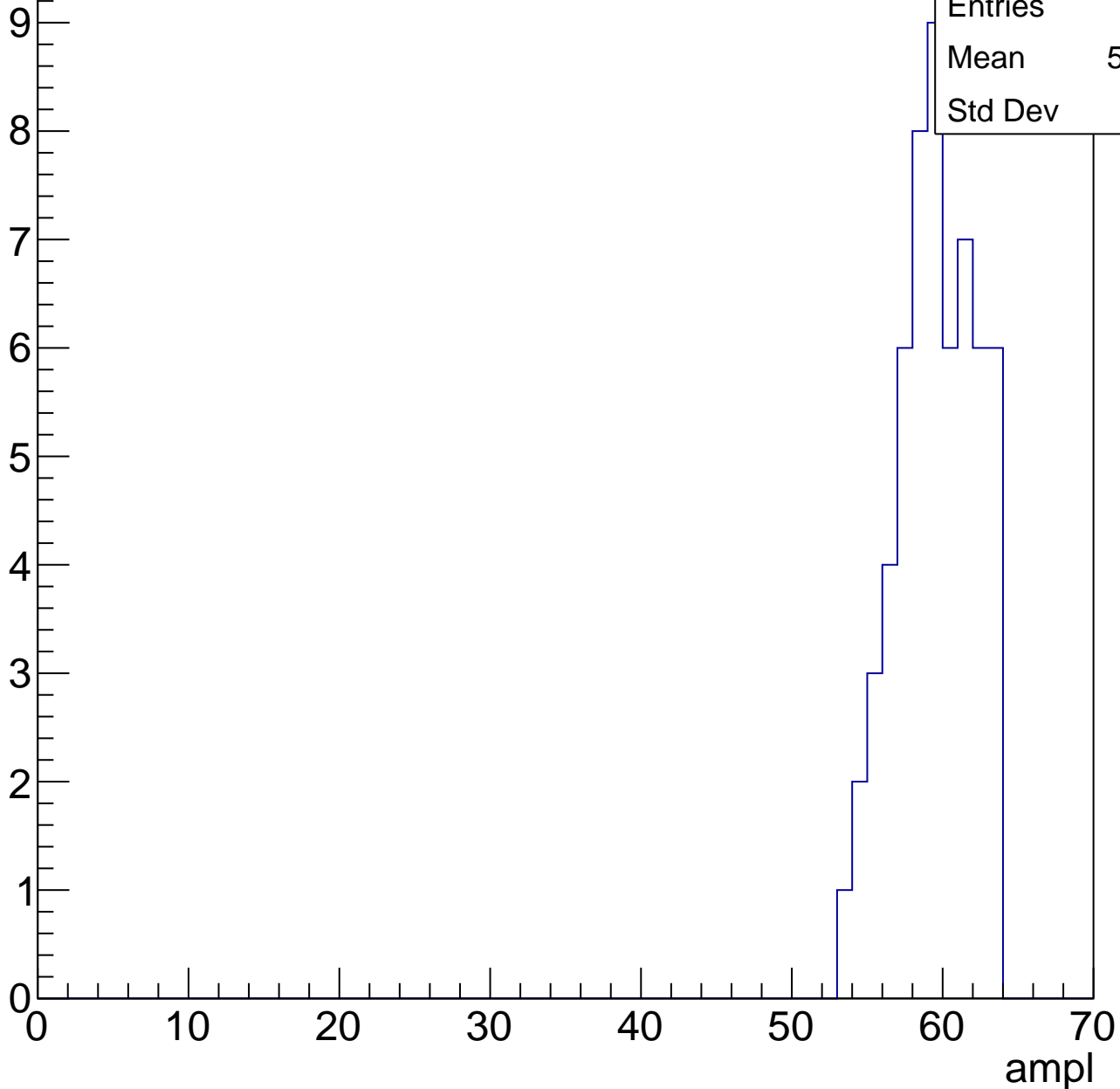
Entry



B0L001S, U11-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

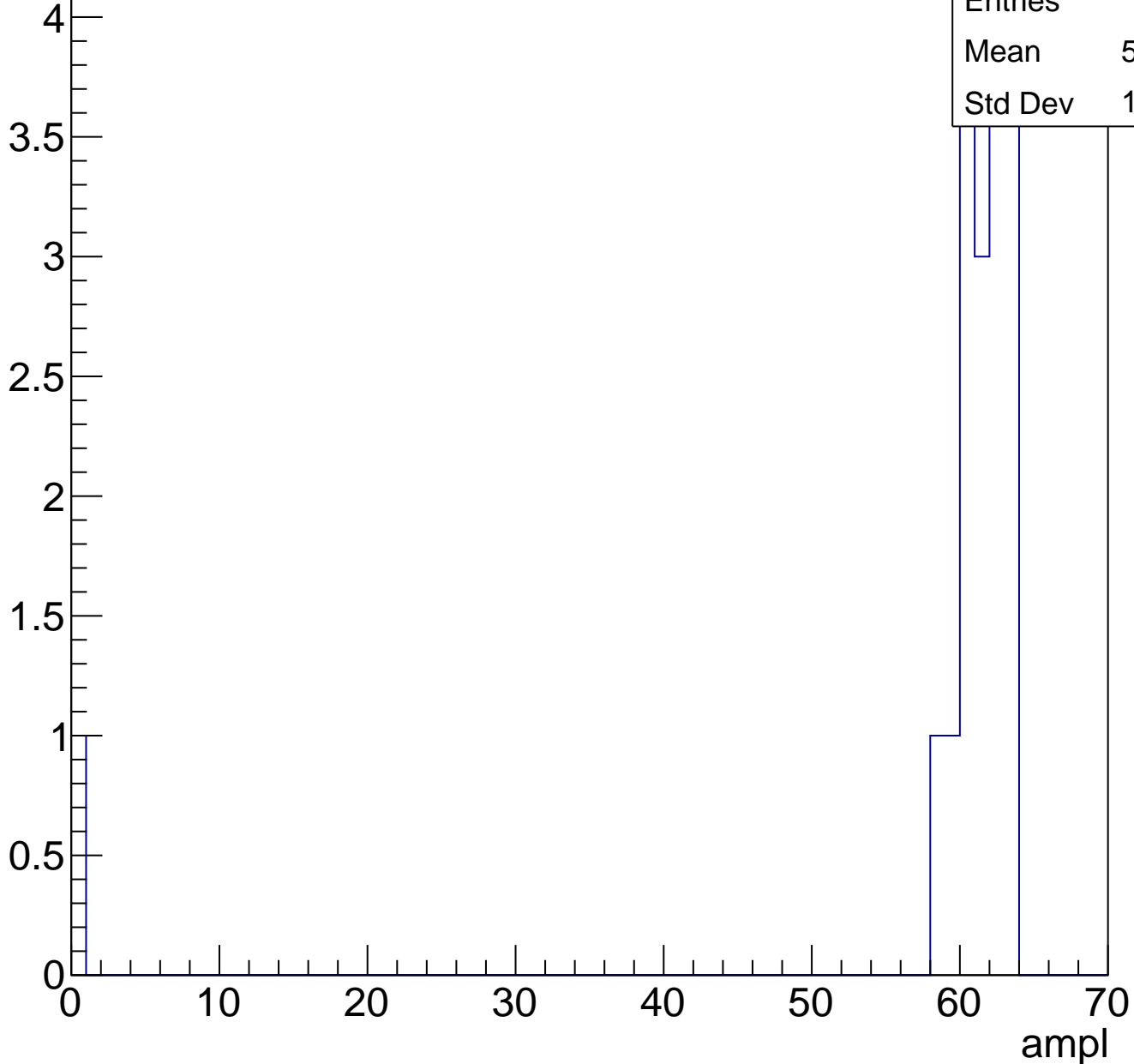
Entry



B0L001S, U11-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch18, adc0

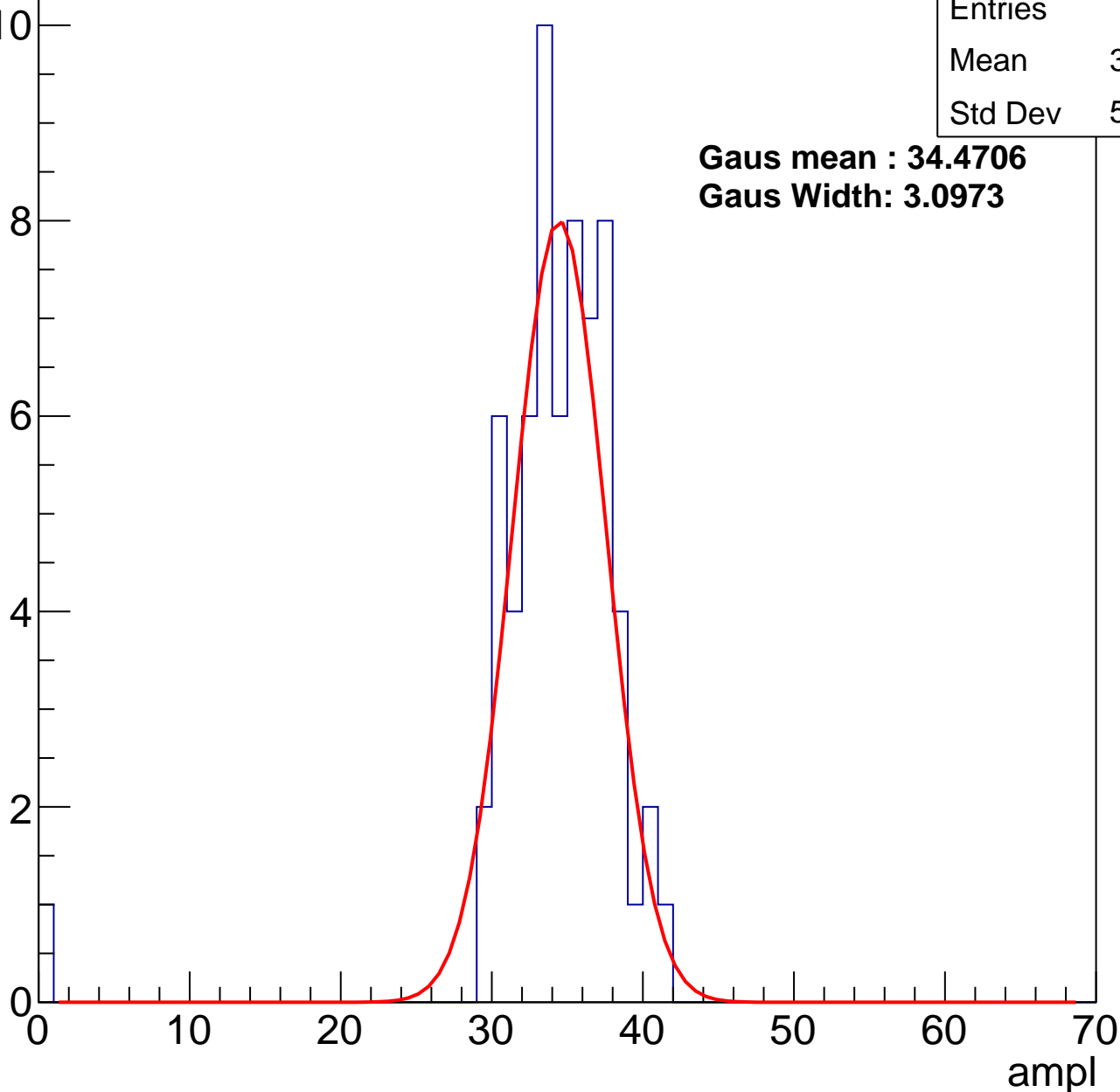
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	33.76
Std Dev	5.054

Gaus mean : 34.4706

Gaus Width: 3.0973



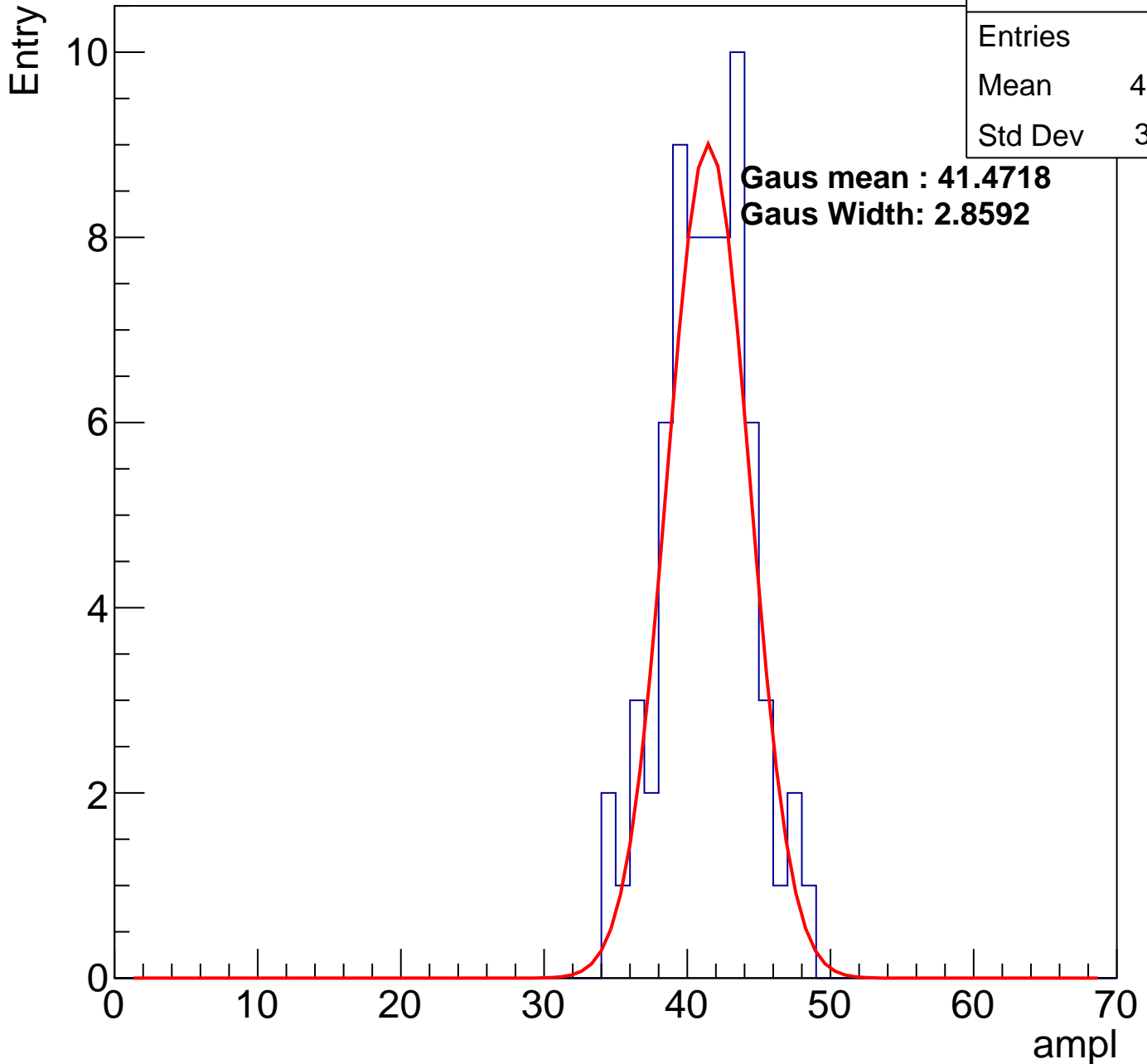
B0L001S, U11-ch18, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	40.93
Std Dev	3.011

Gaus mean : 41.4718

Gaus Width: 2.8592



B0L001S, U11-ch18, adc2

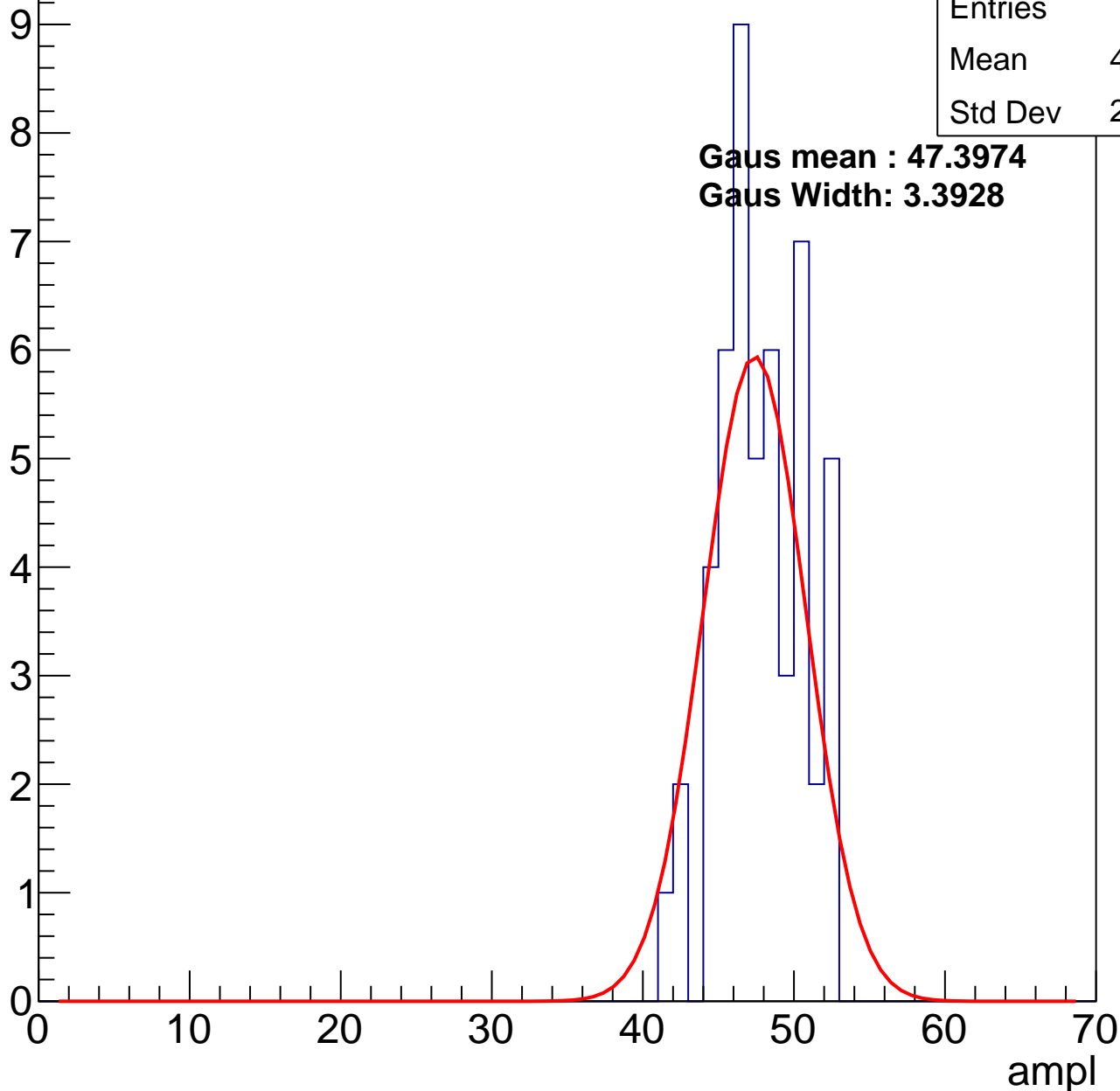
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	47.34
Std Dev	2.783

Gaus mean : 47.3974

Gaus Width: 3.3928

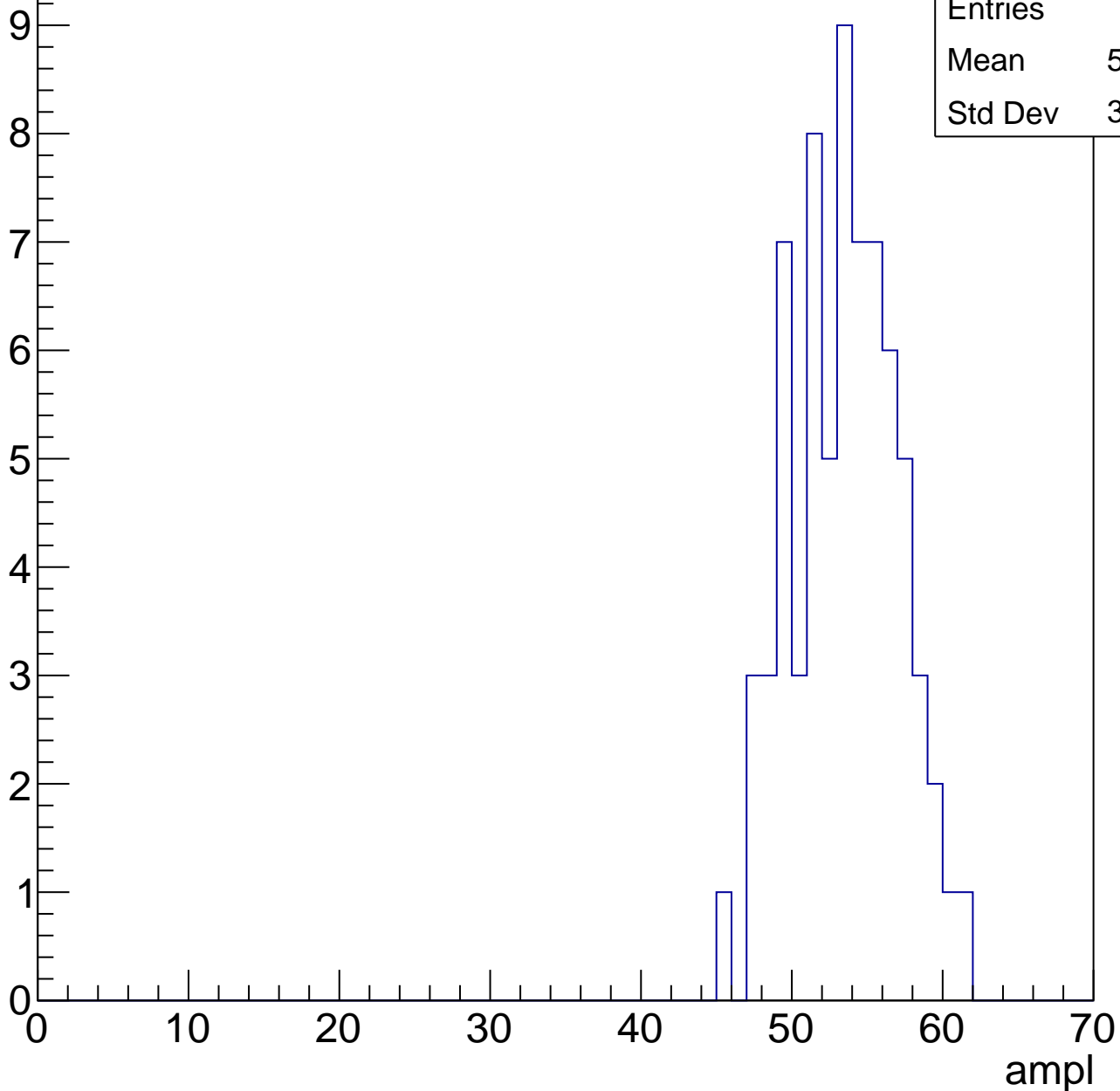


B0L001S, U11-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	53.03
Std Dev	3.464

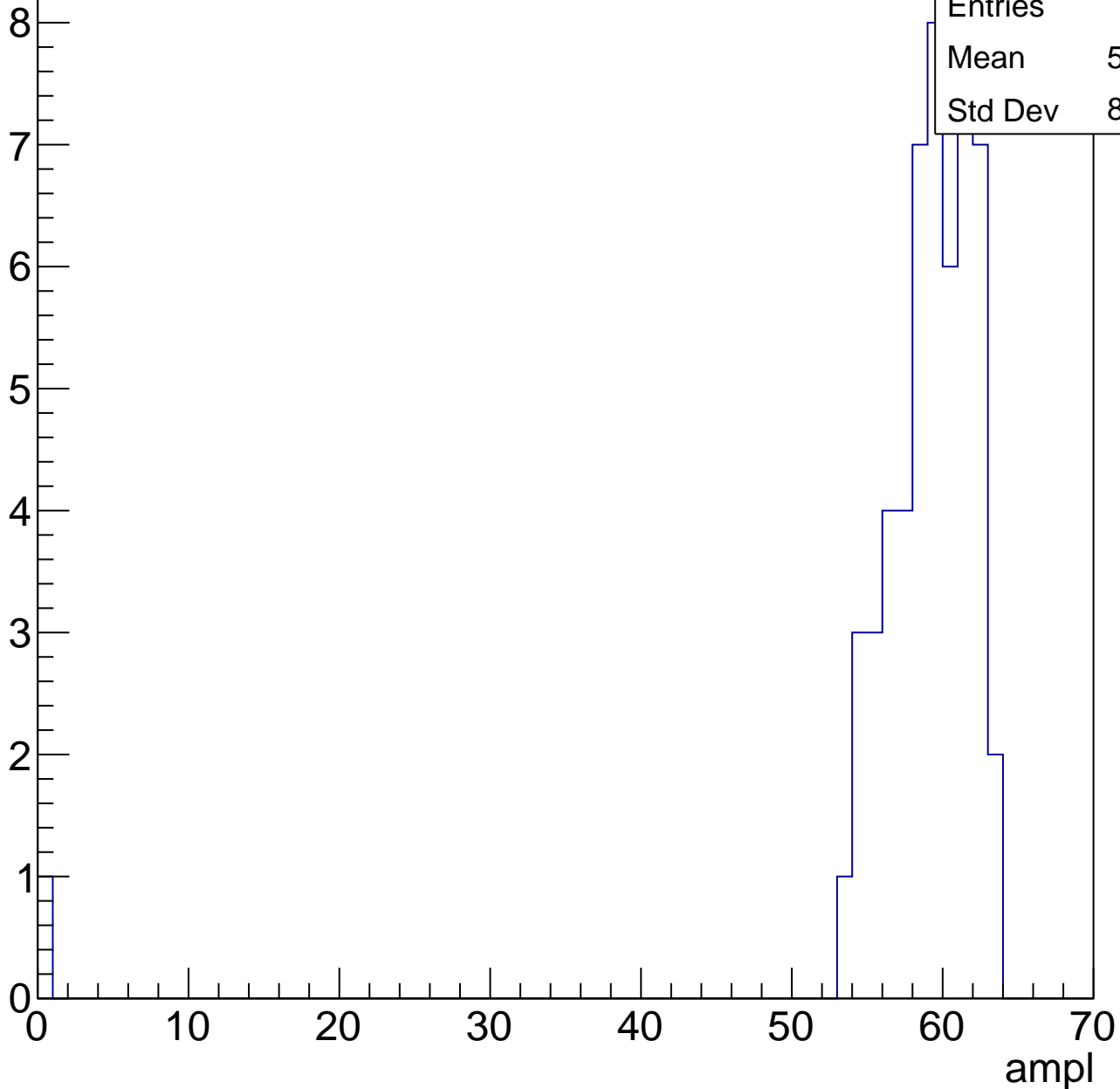


B0L001S, U11-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	57.74
Std Dev	8.329

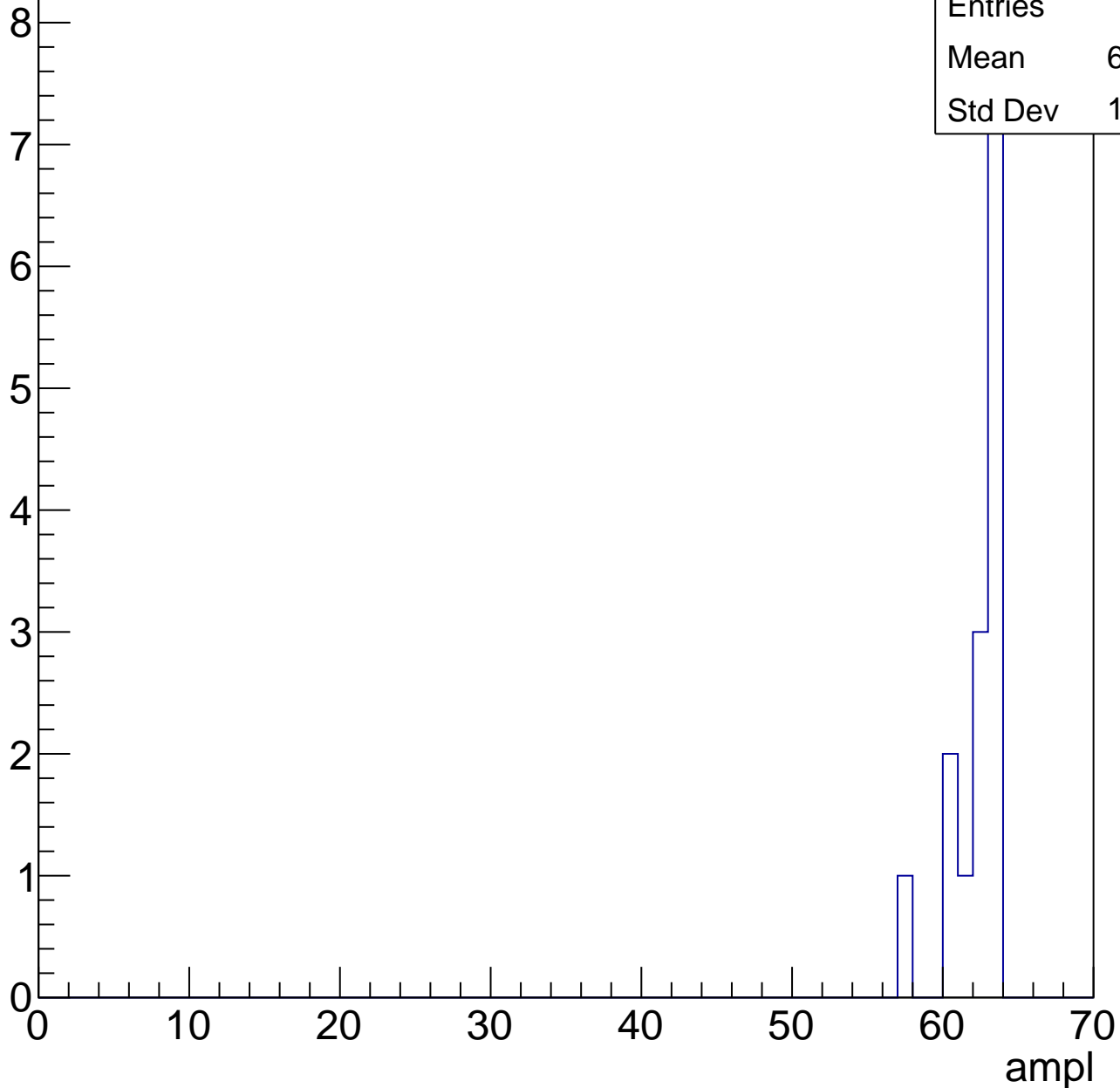


B0L001S, U11-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.87
Std Dev	1.668



B0L001S, U11-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

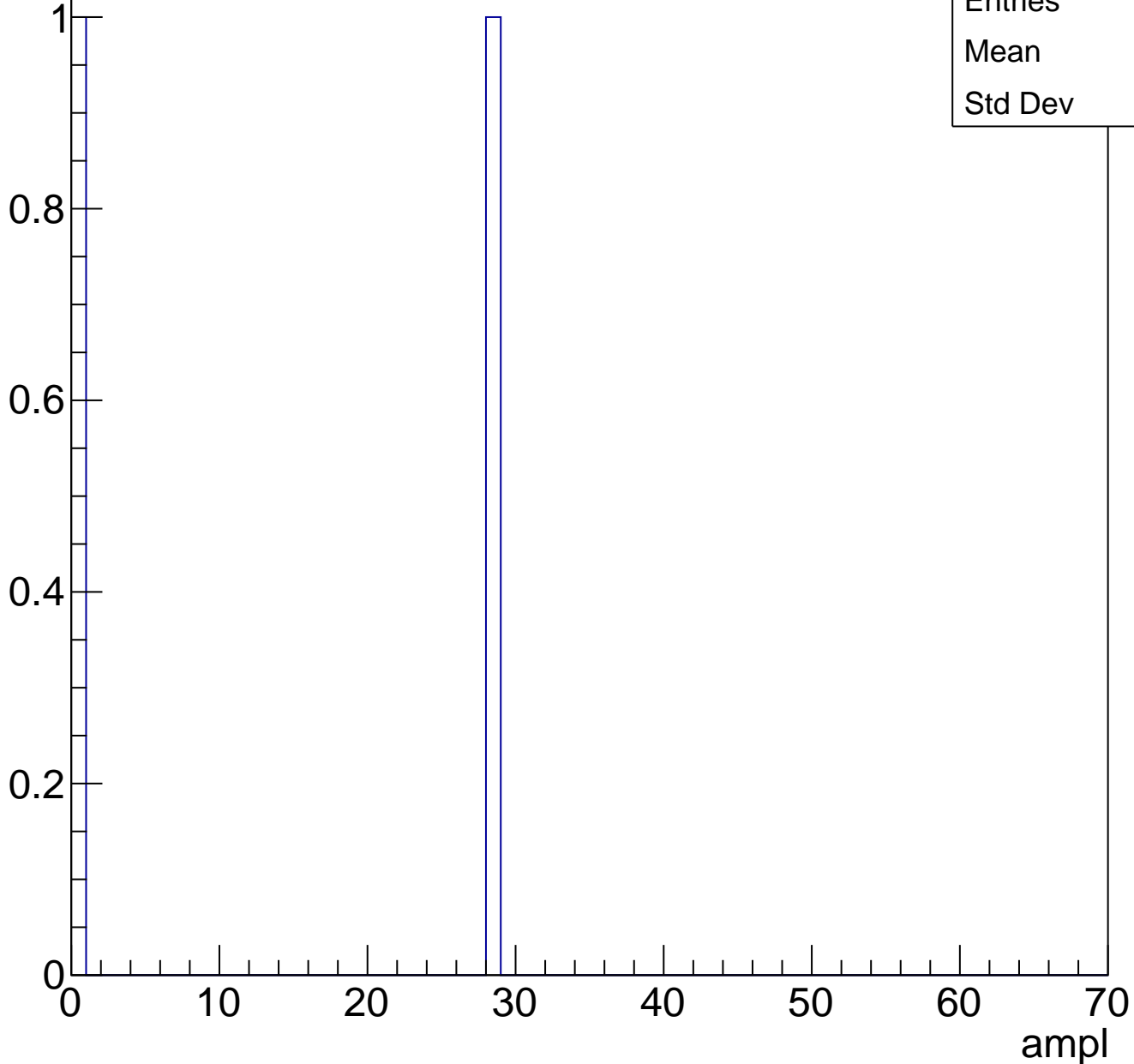
Entry



B0L001S, U11-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	14
Std Dev	14

B0L001S, U11-ch19, adc0

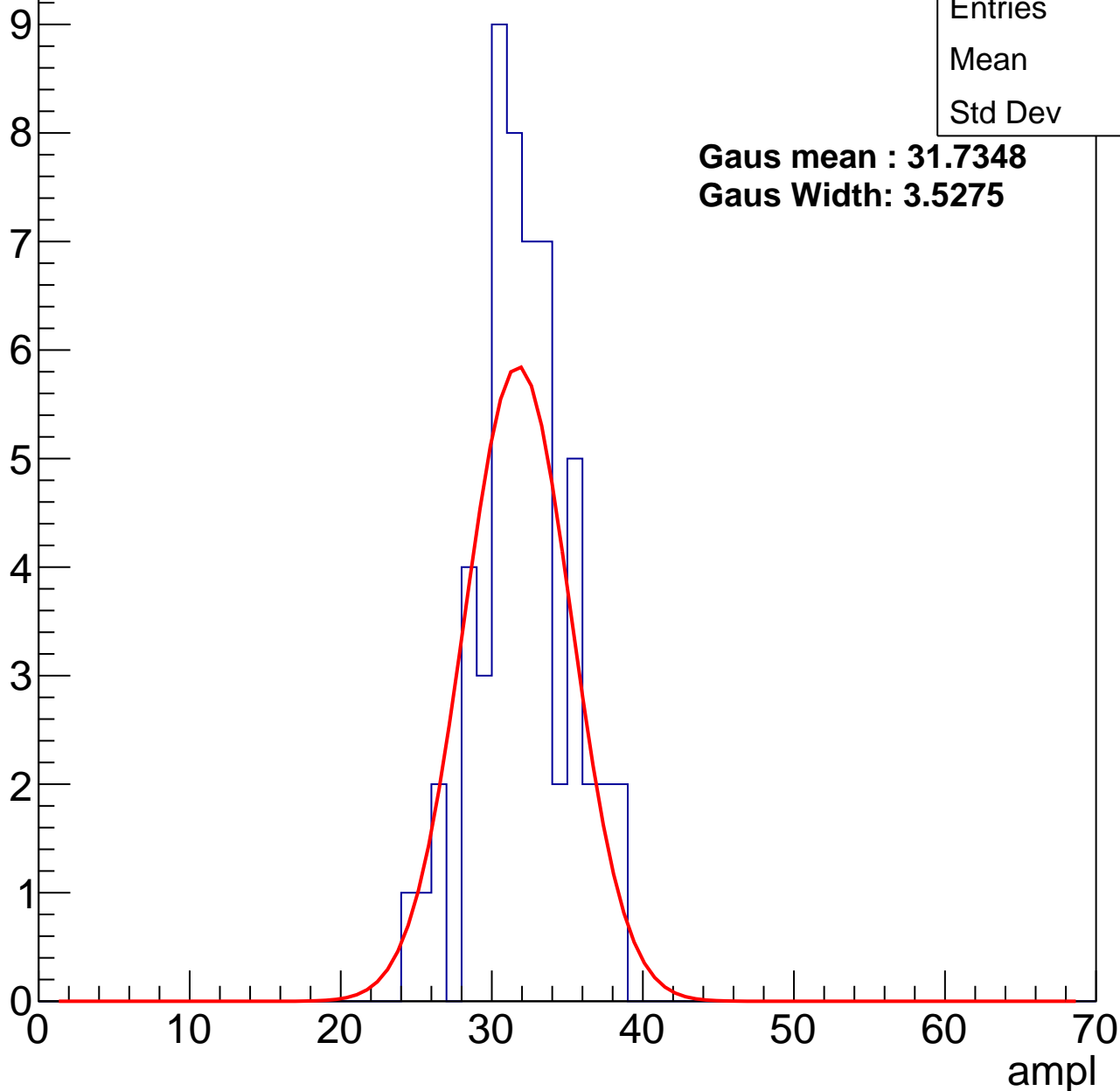
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.6
Std Dev	3.09

Gaus mean : 31.7348

Gaus Width: 3.5275



B0L001S, U11-ch19, adc1

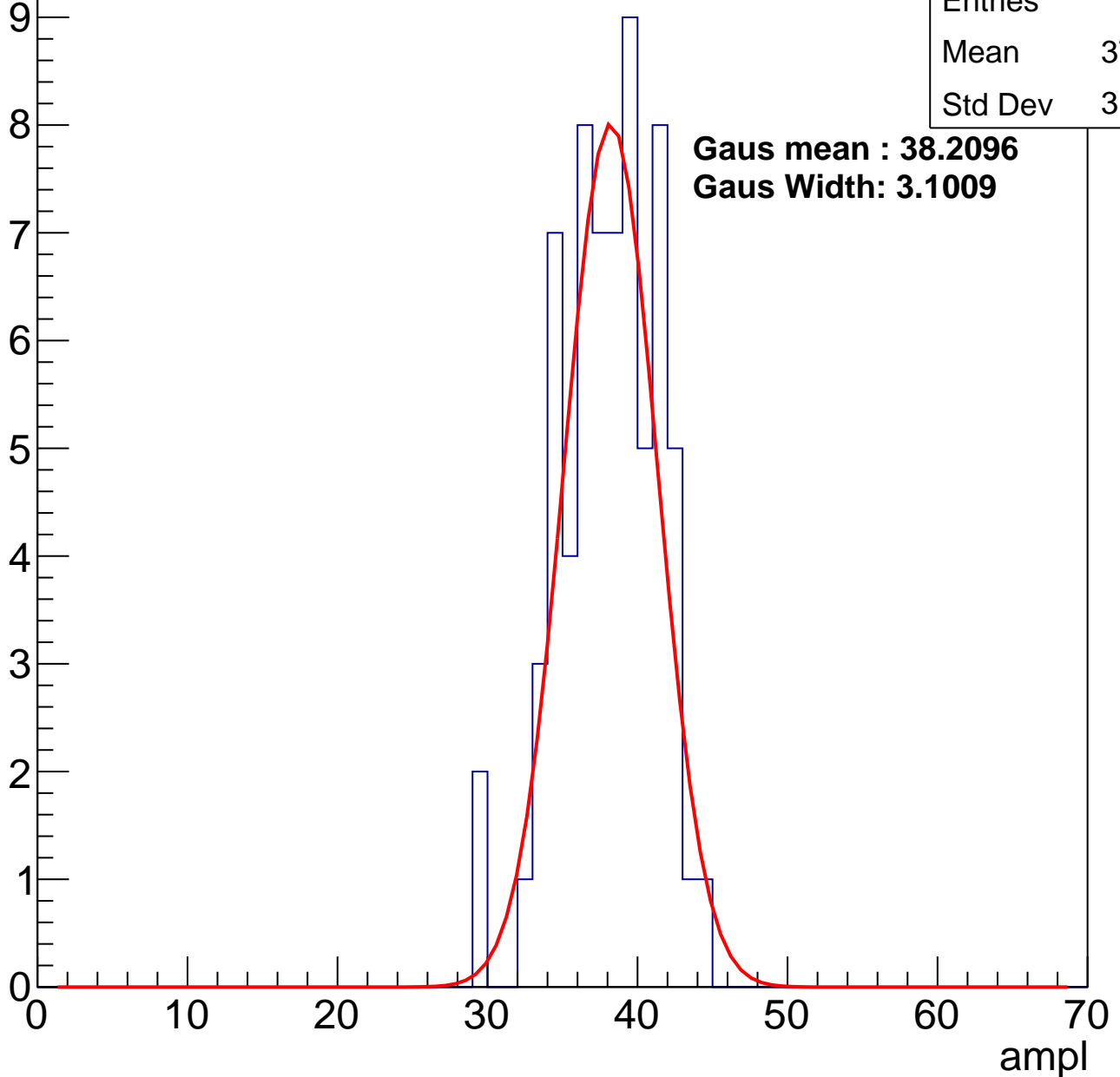
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.59
Std Dev	3.187

Gaus mean : 38.2096

Gaus Width: 3.1009



B0L001S, U11-ch19, adc2

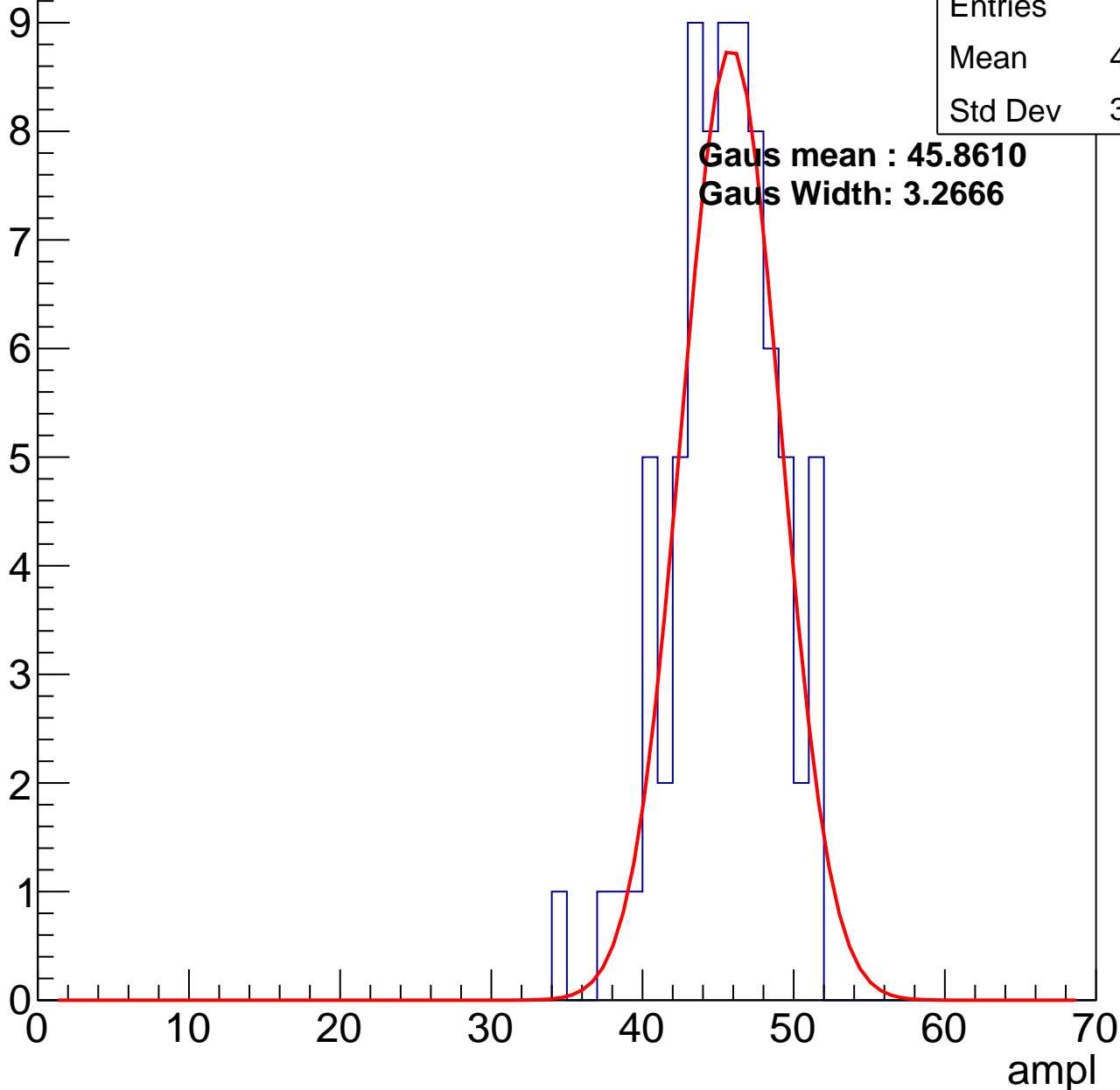
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.96
Std Dev	3.458

Gaus mean : 45.8610

Gaus Width: 3.2666



B0L001S, U11-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	52.19
Std Dev	3.405

Entry

10

8

6

4

2

0

0

10

20

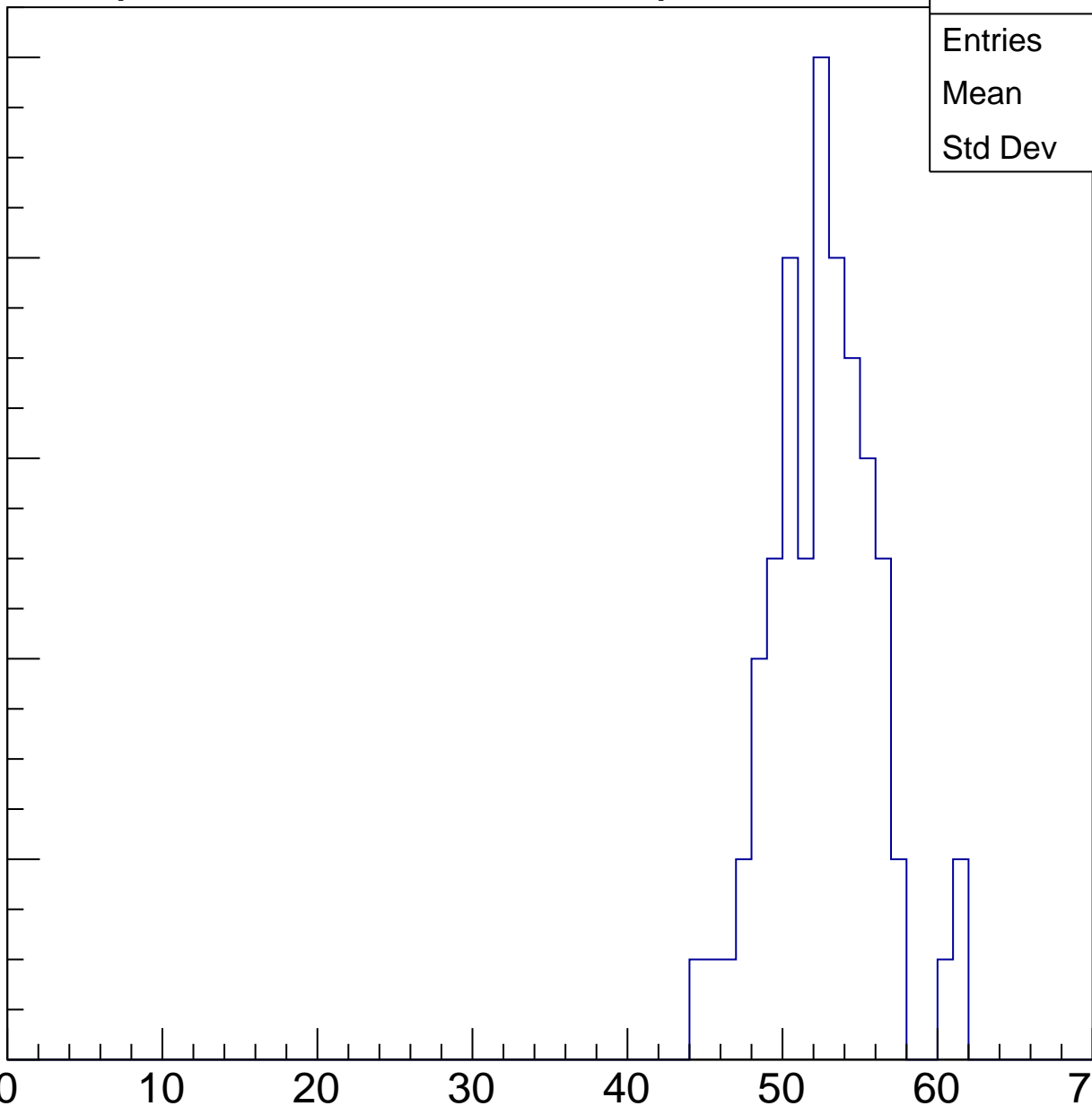
30

40

50

60

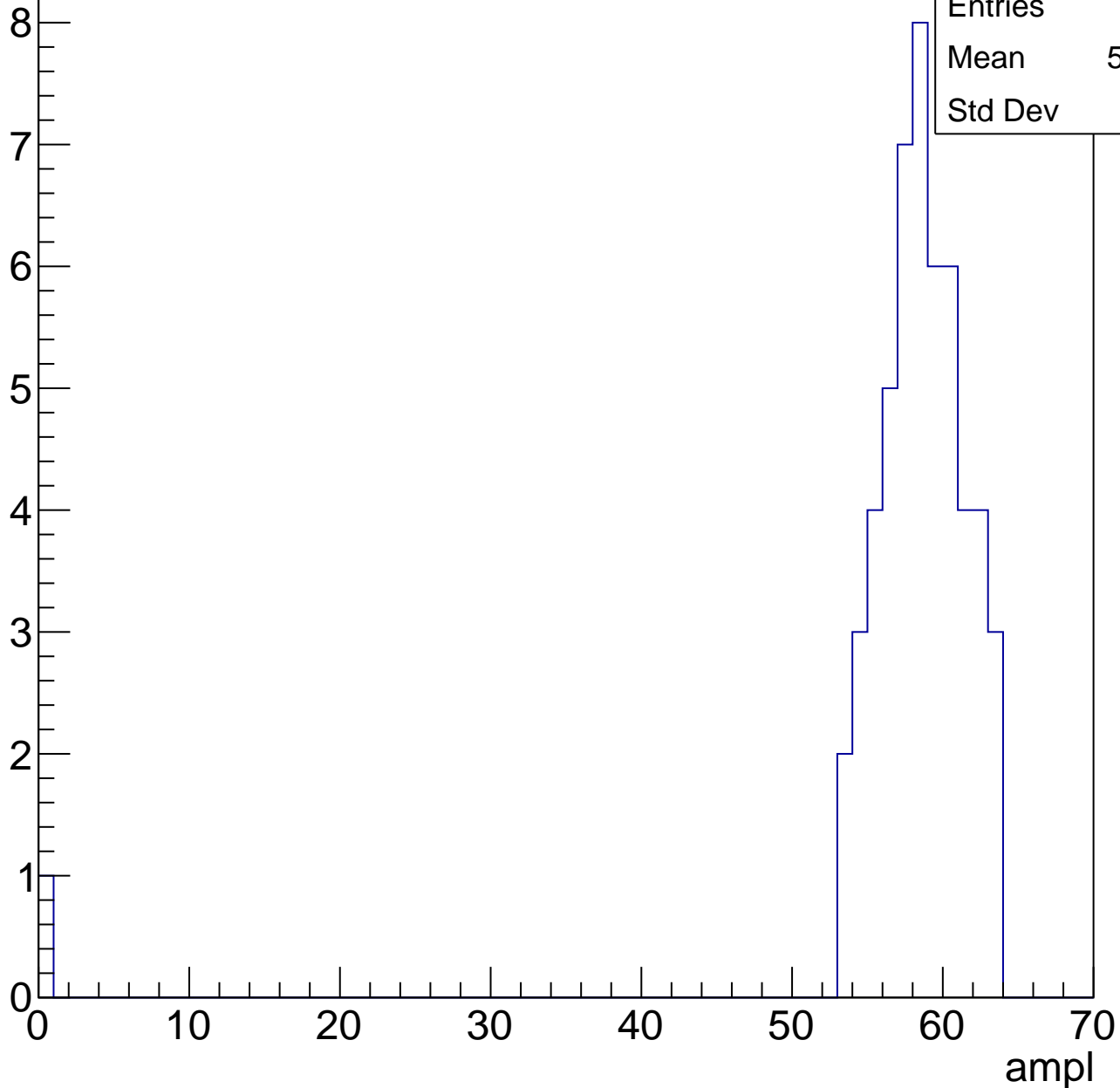
ampl



B0L001S, U11-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

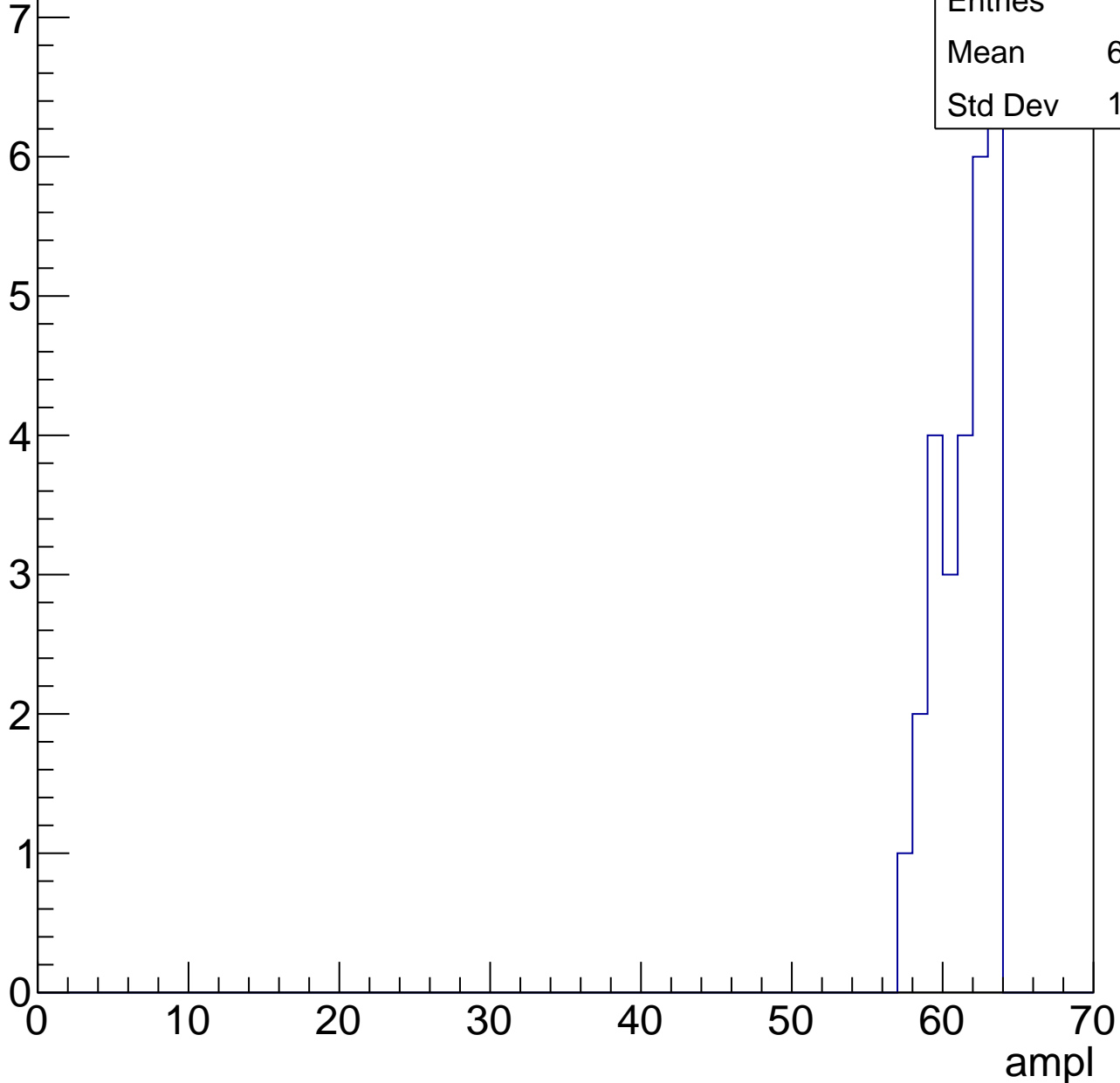


B0L001S, U11-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	60.96
Std Dev	1.795



B0L001S, U11-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch20, adc0

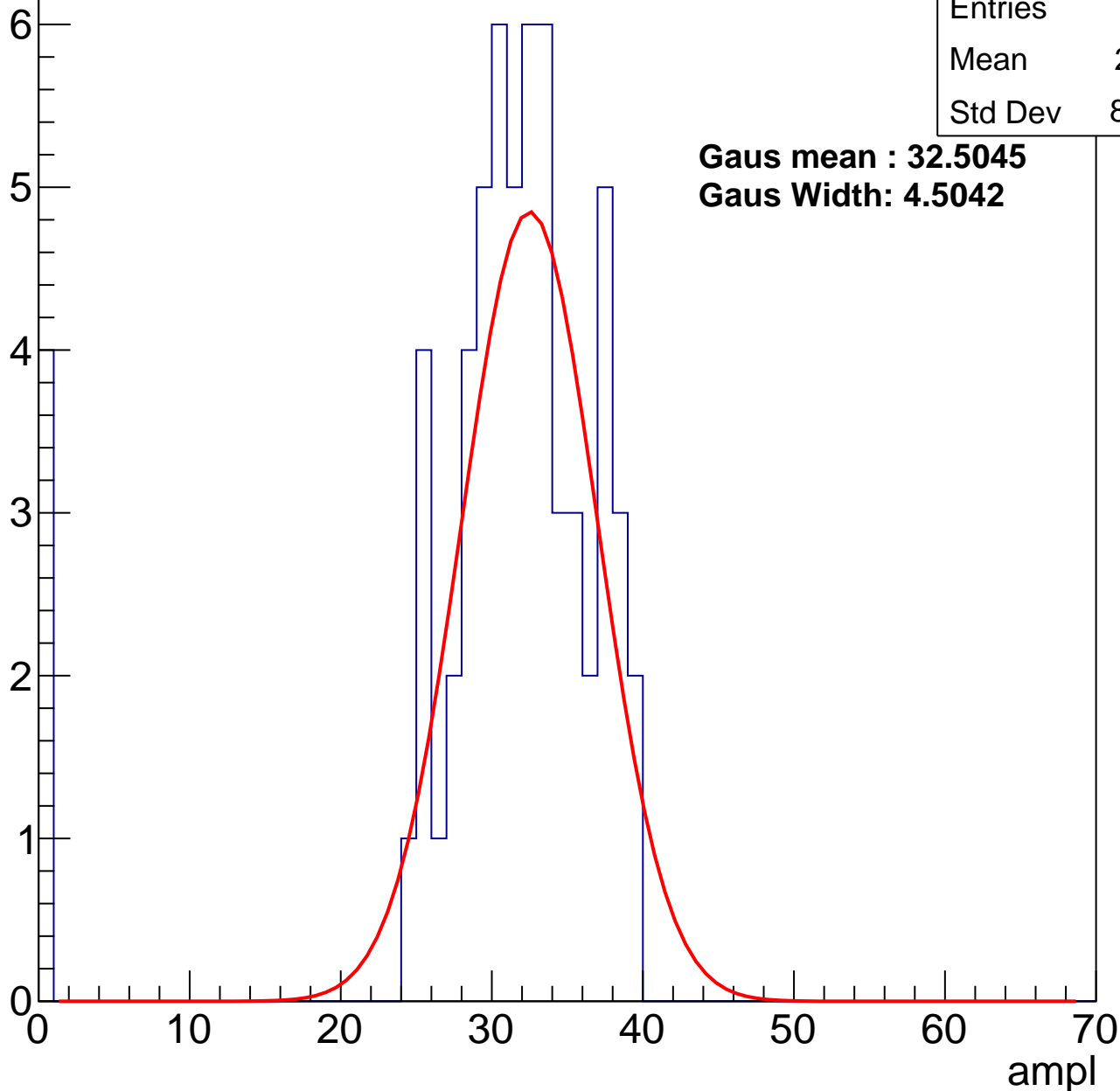
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.71
Std Dev	8.672

Gaus mean : 32.5045

Gaus Width: 4.5042



B0L001S, U11-ch20, adc1

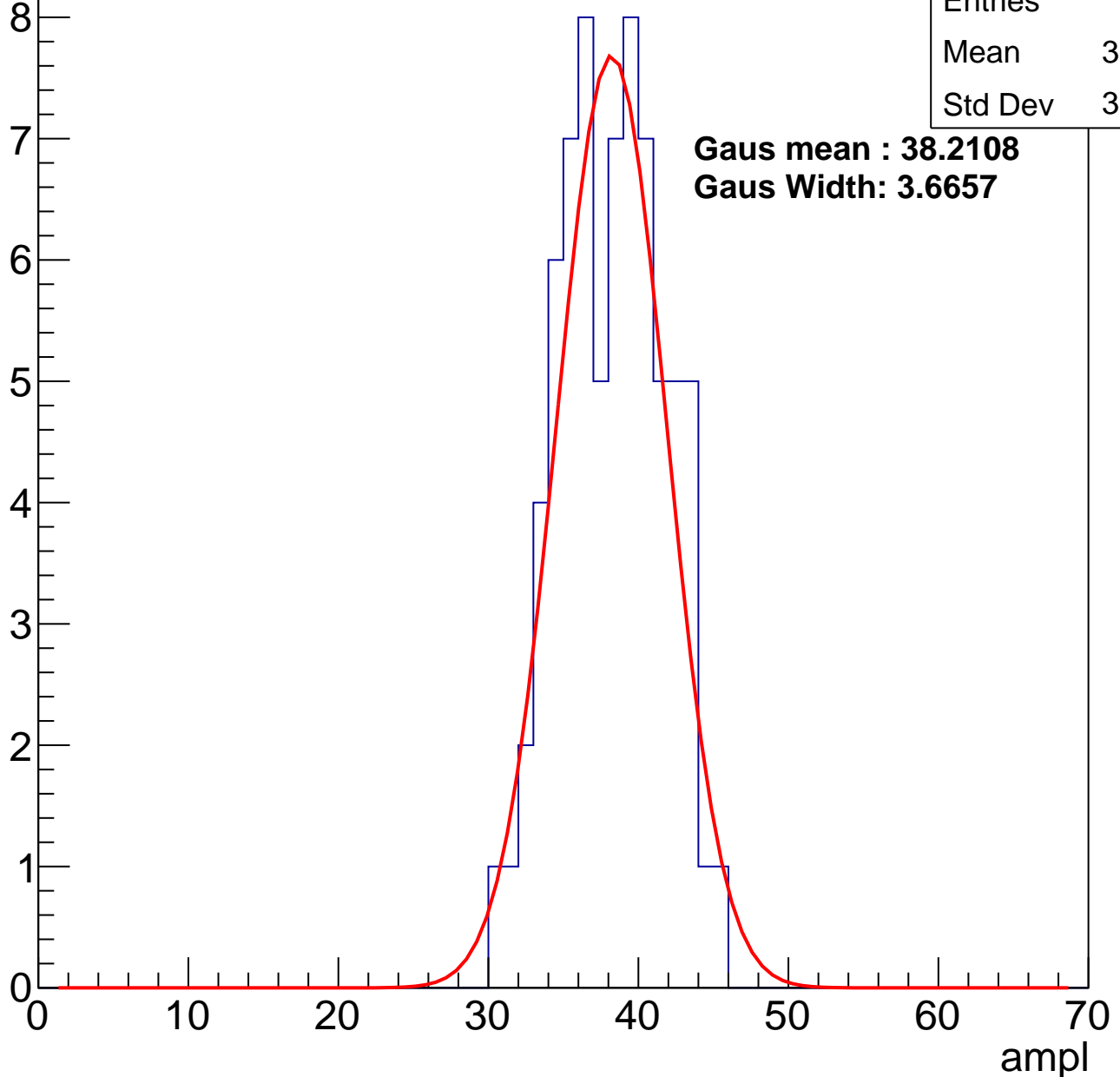
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.75
Std Dev	3.407

Gaus mean : 38.2108

Gaus Width: 3.6657



B0L001S, U11-ch20, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	45.45
Std Dev	3.082

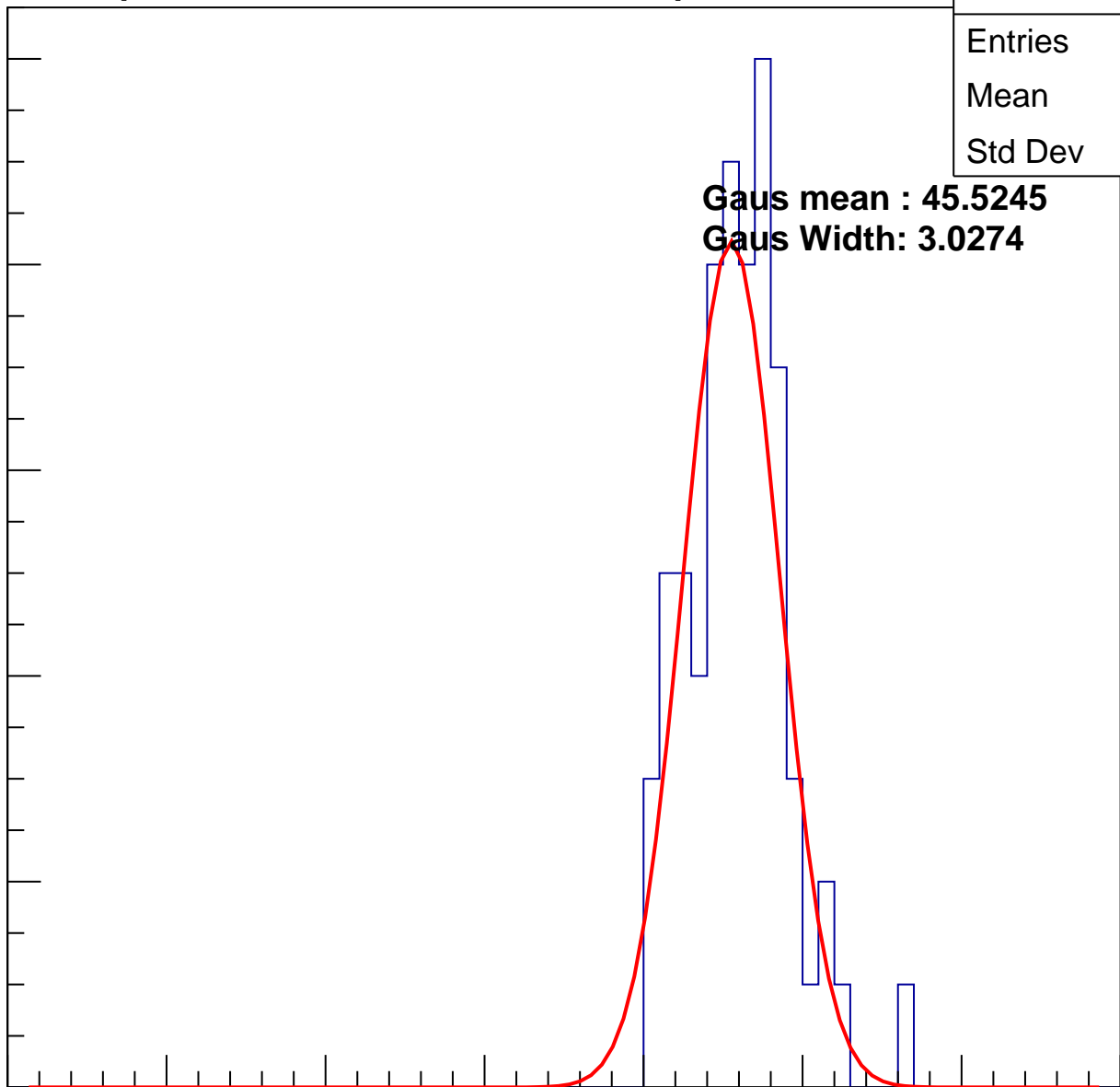
Gaus mean : 45.5245

Gaus Width: 3.0274

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

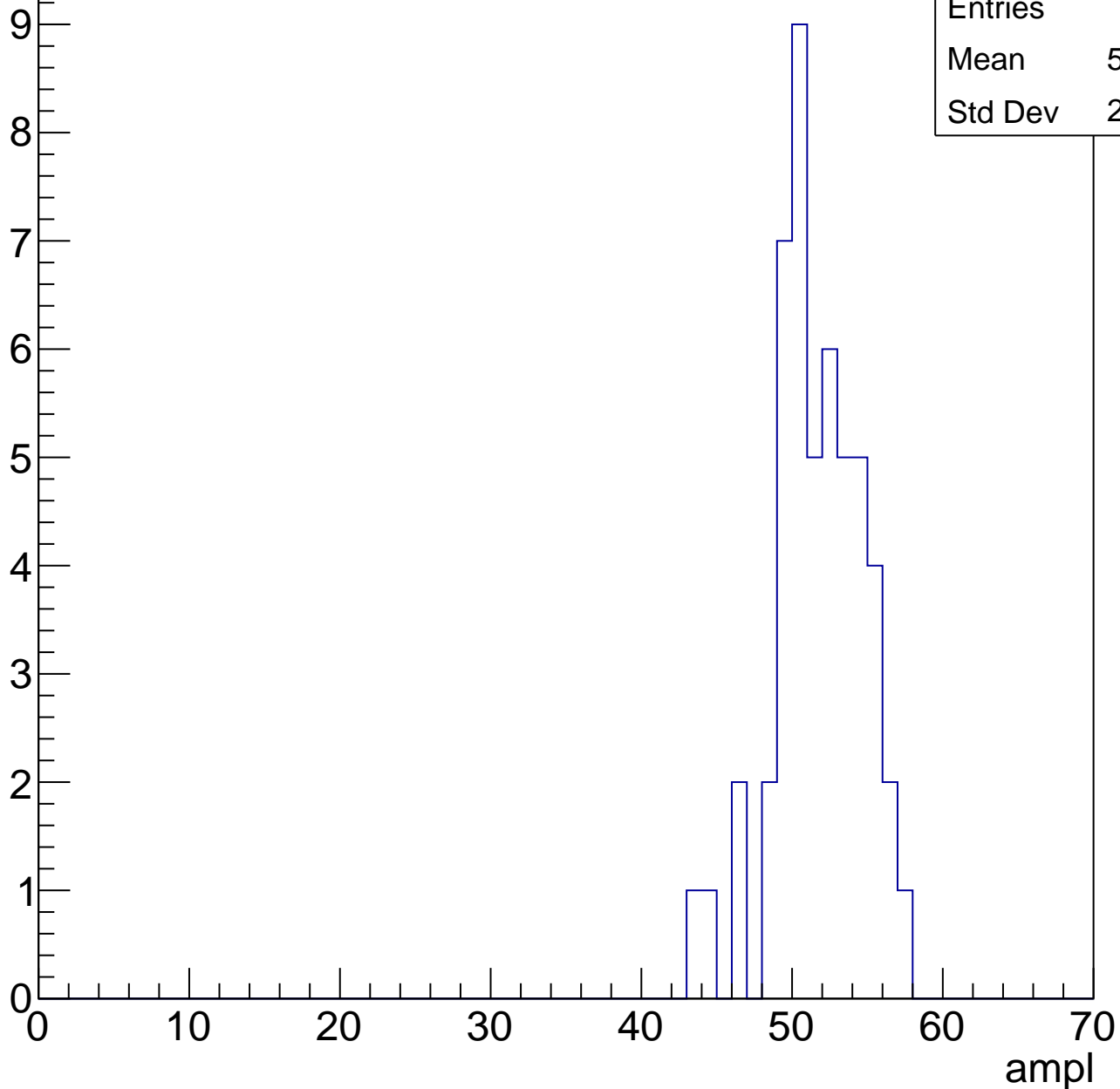


B0L001S, U11-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	51.18
Std Dev	2.958

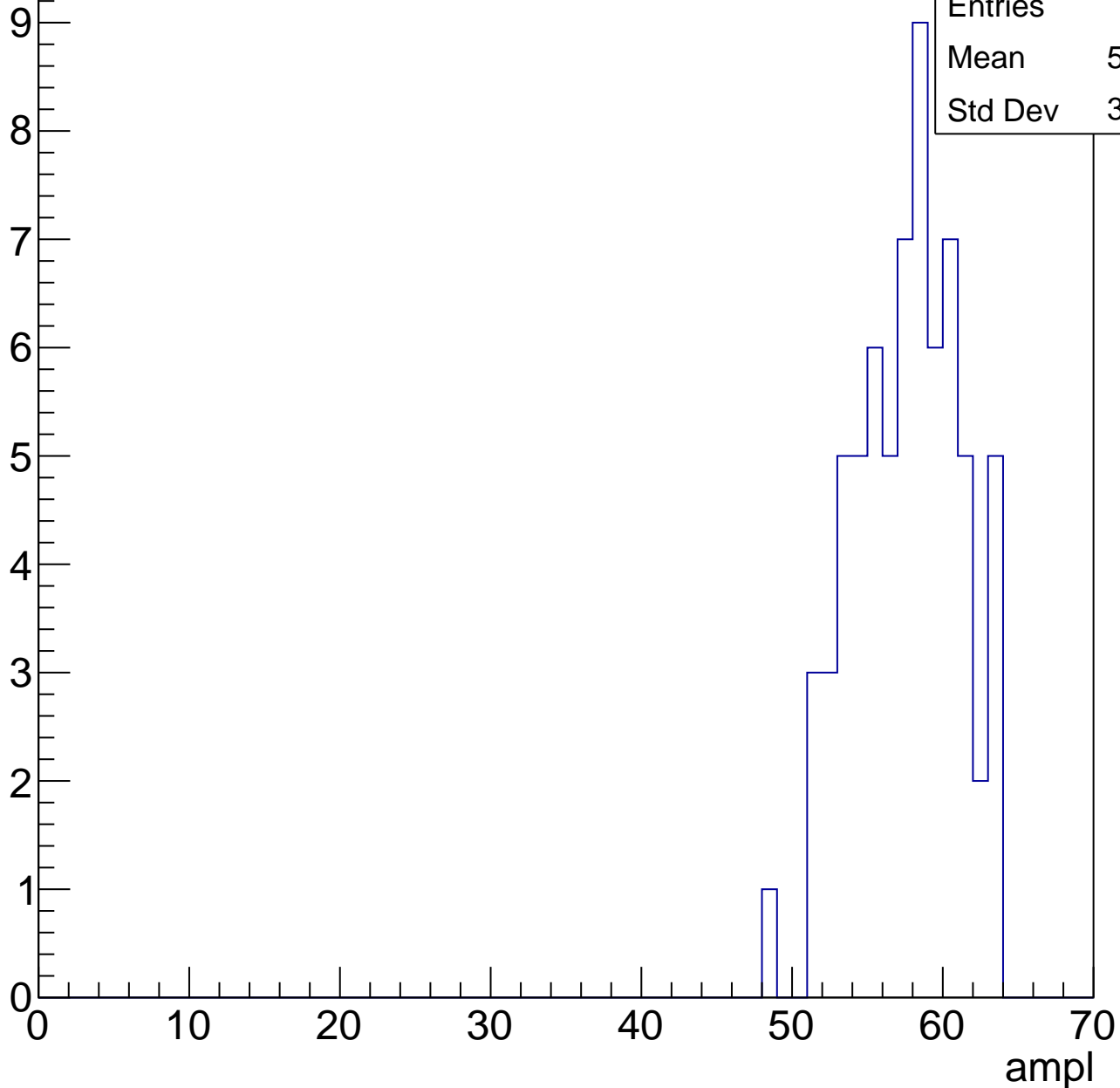


B0L001S, U11-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	57.12
Std Dev	3.454

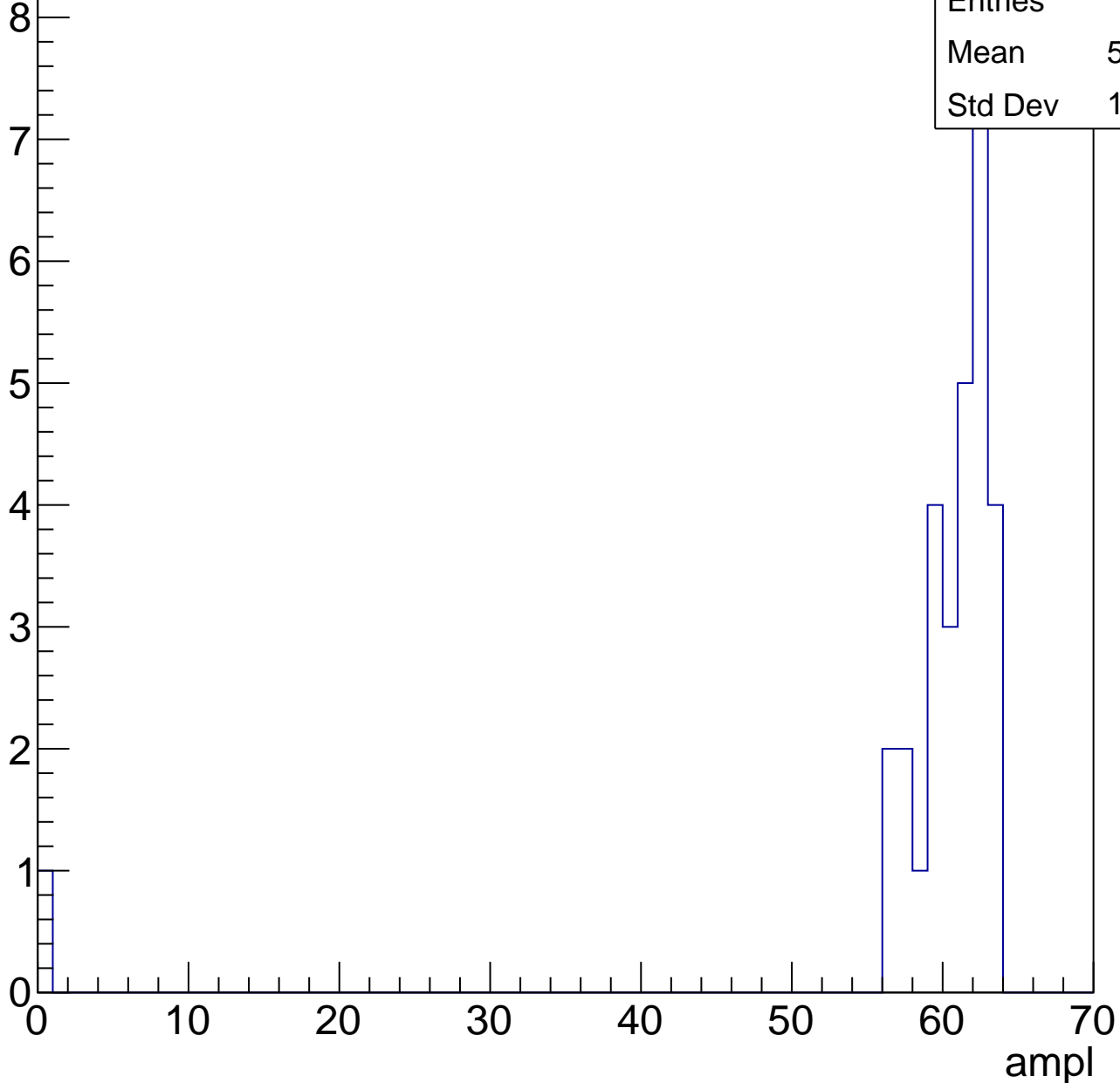


B0L001S, U11-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.43
Std Dev	11.04



B0L001S, U11-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch21, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	30.44
Std Dev	3.488

Gaus mean : 31.0742

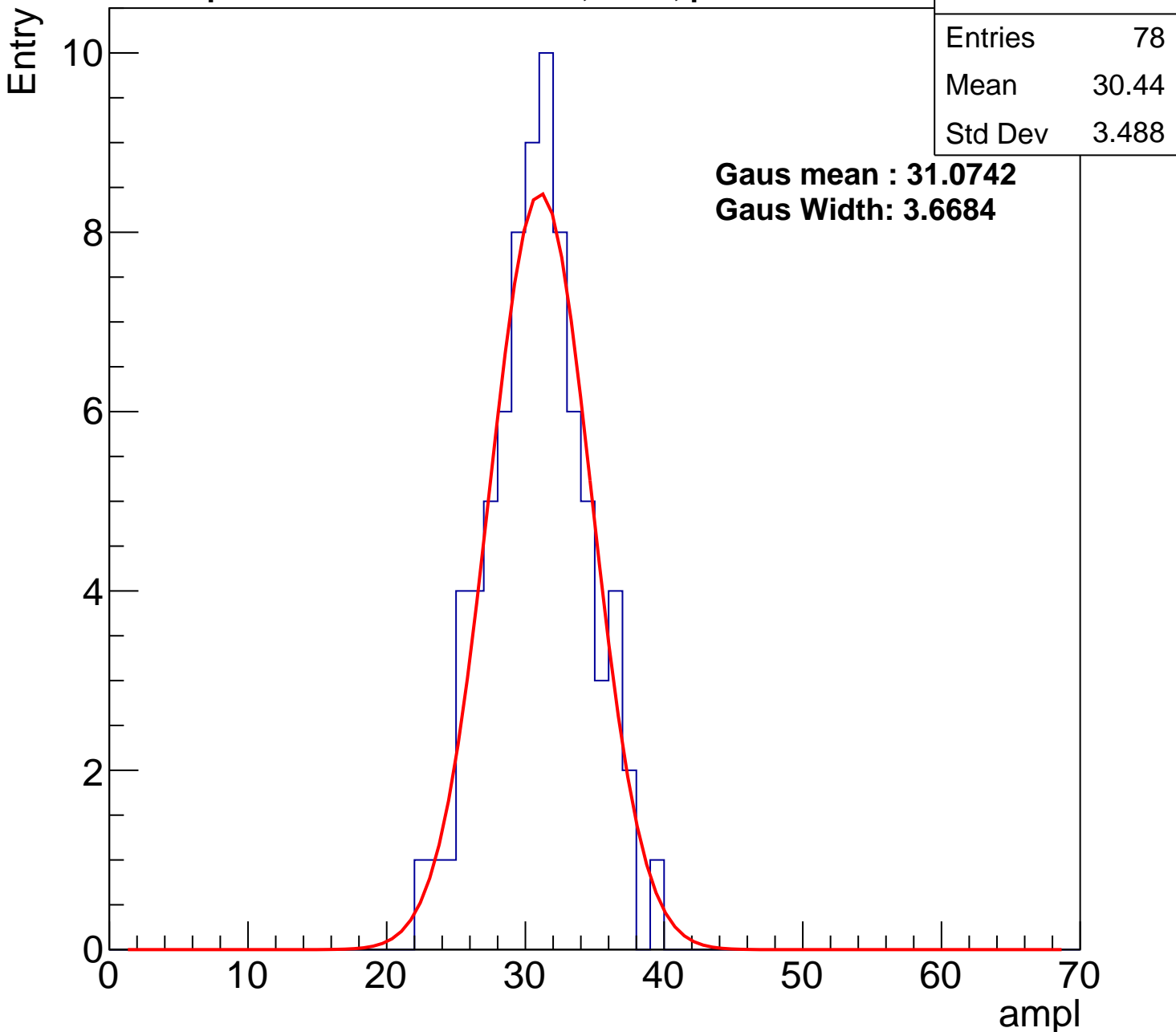
Gaus Width: 3.6684

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



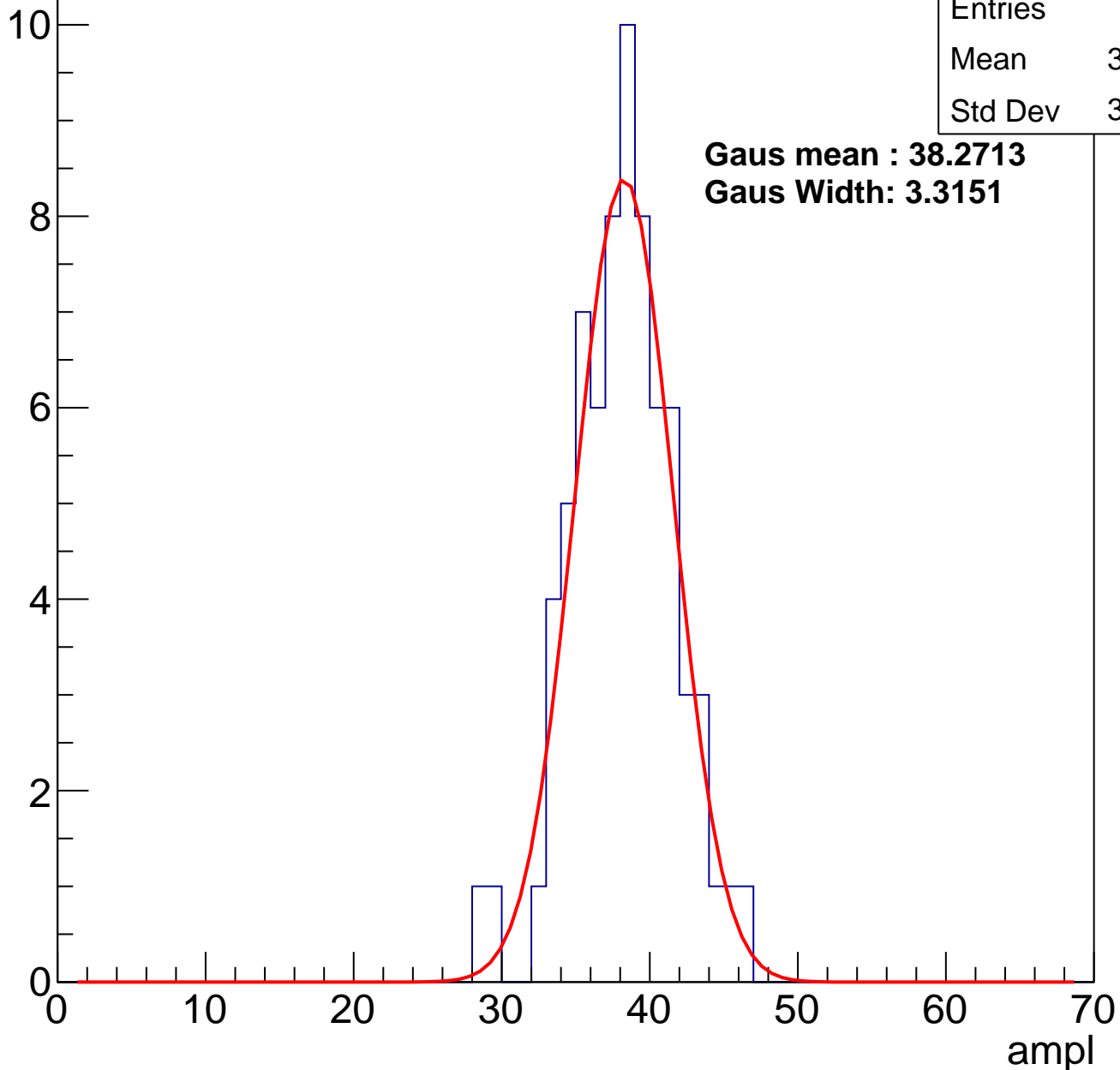
B0L001S, U11-ch21, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	37.72
Std Dev	3.425

Gaus mean : 38.2713
Gaus Width: 3.3151

Entry



B0L001S, U11-ch21, adc2

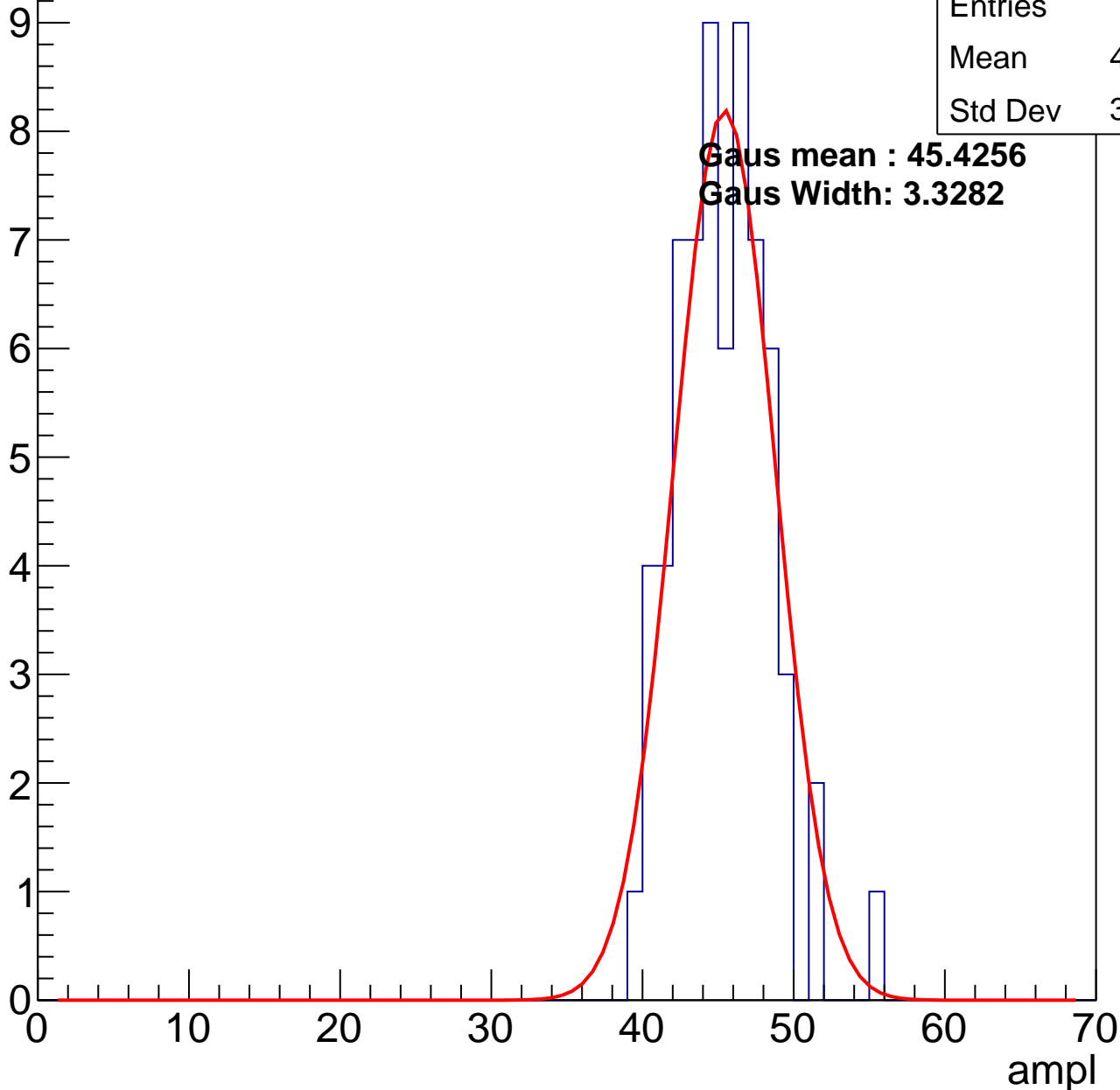
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.83
Std Dev	3.038

Gaus mean : 45.4256

Gaus Width: 3.3282

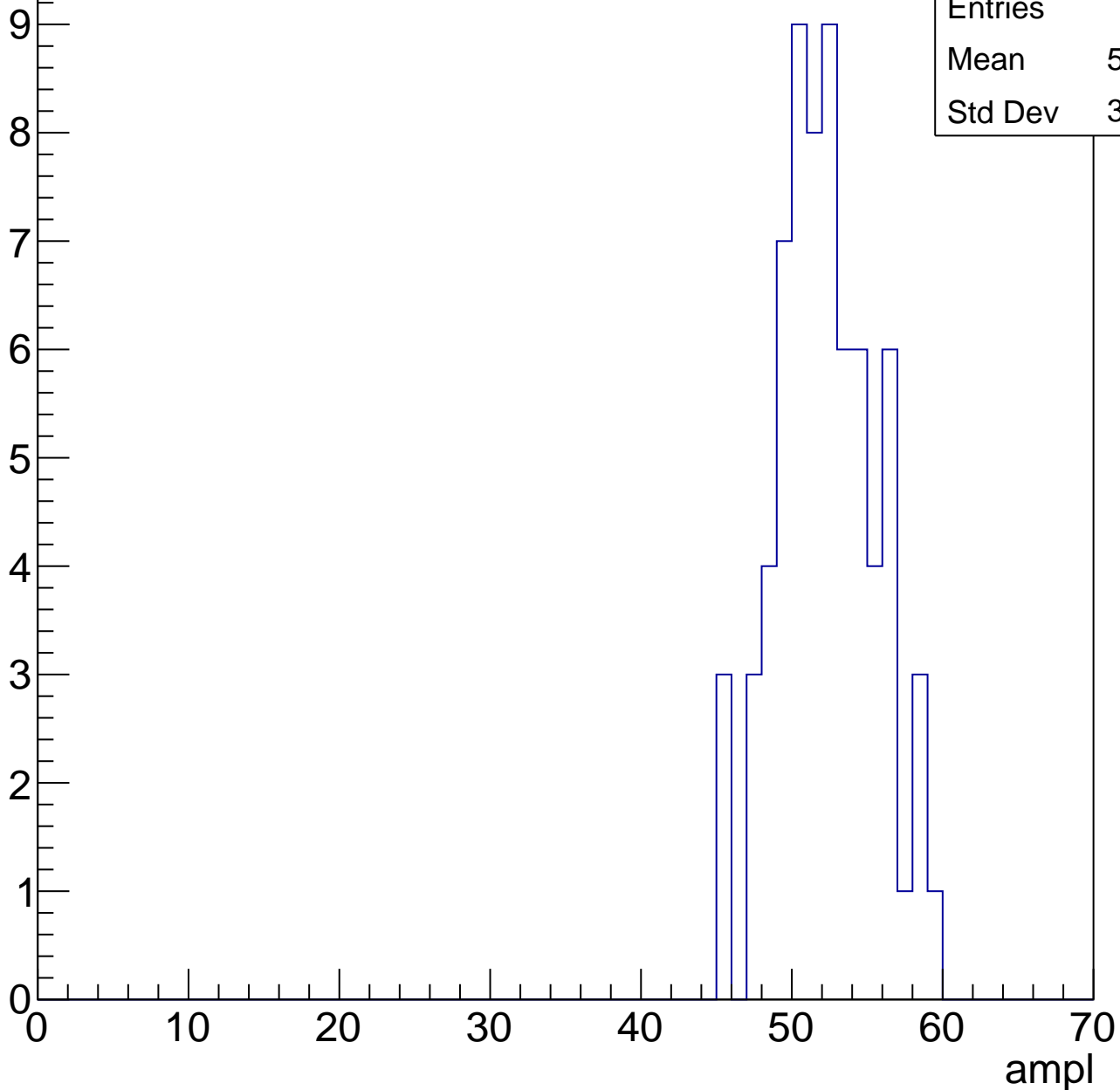


B0L001S, U11-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	51.79
Std Dev	3.238

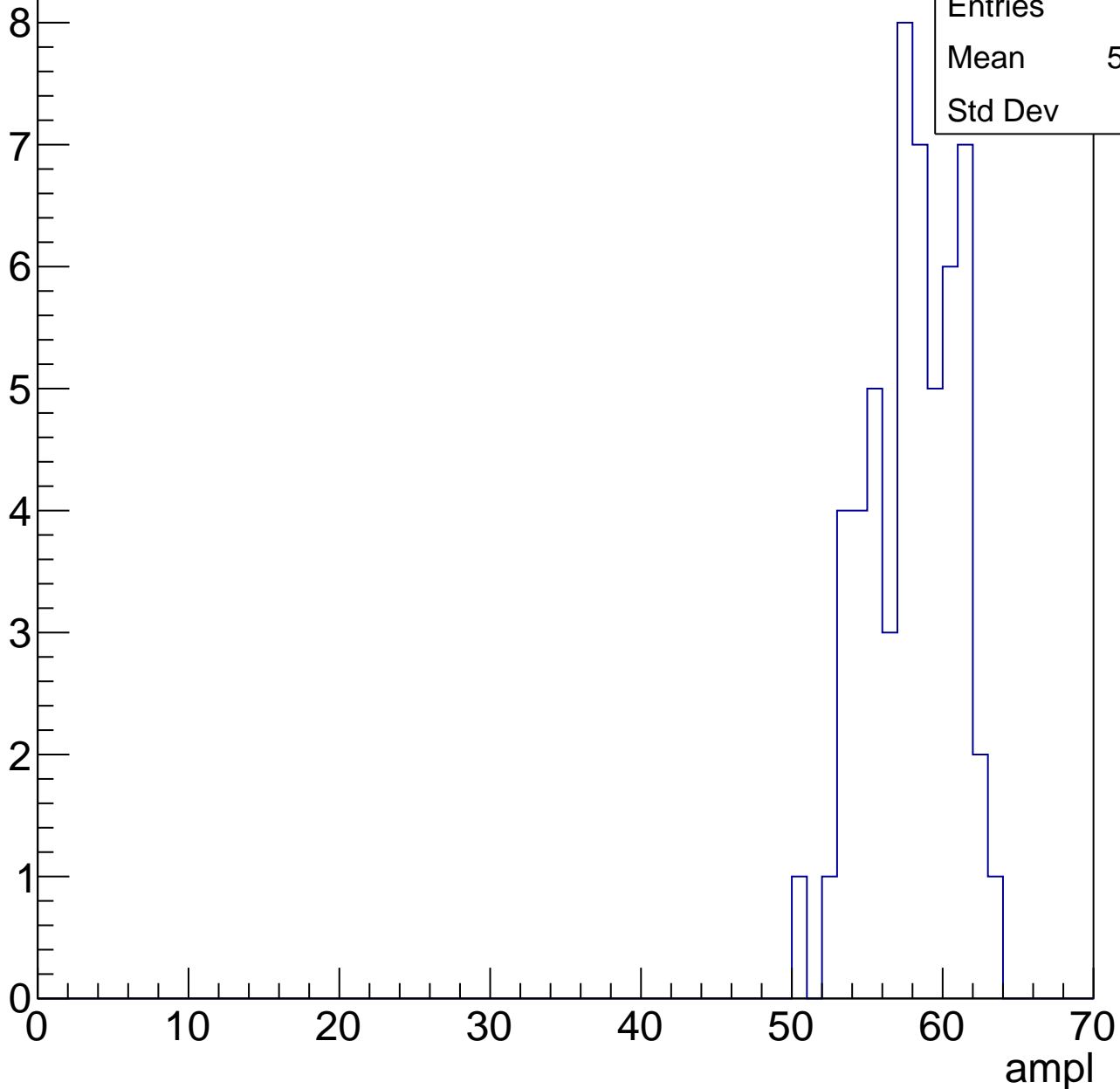


B0L001S, U11-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	57.48
Std Dev	2.93

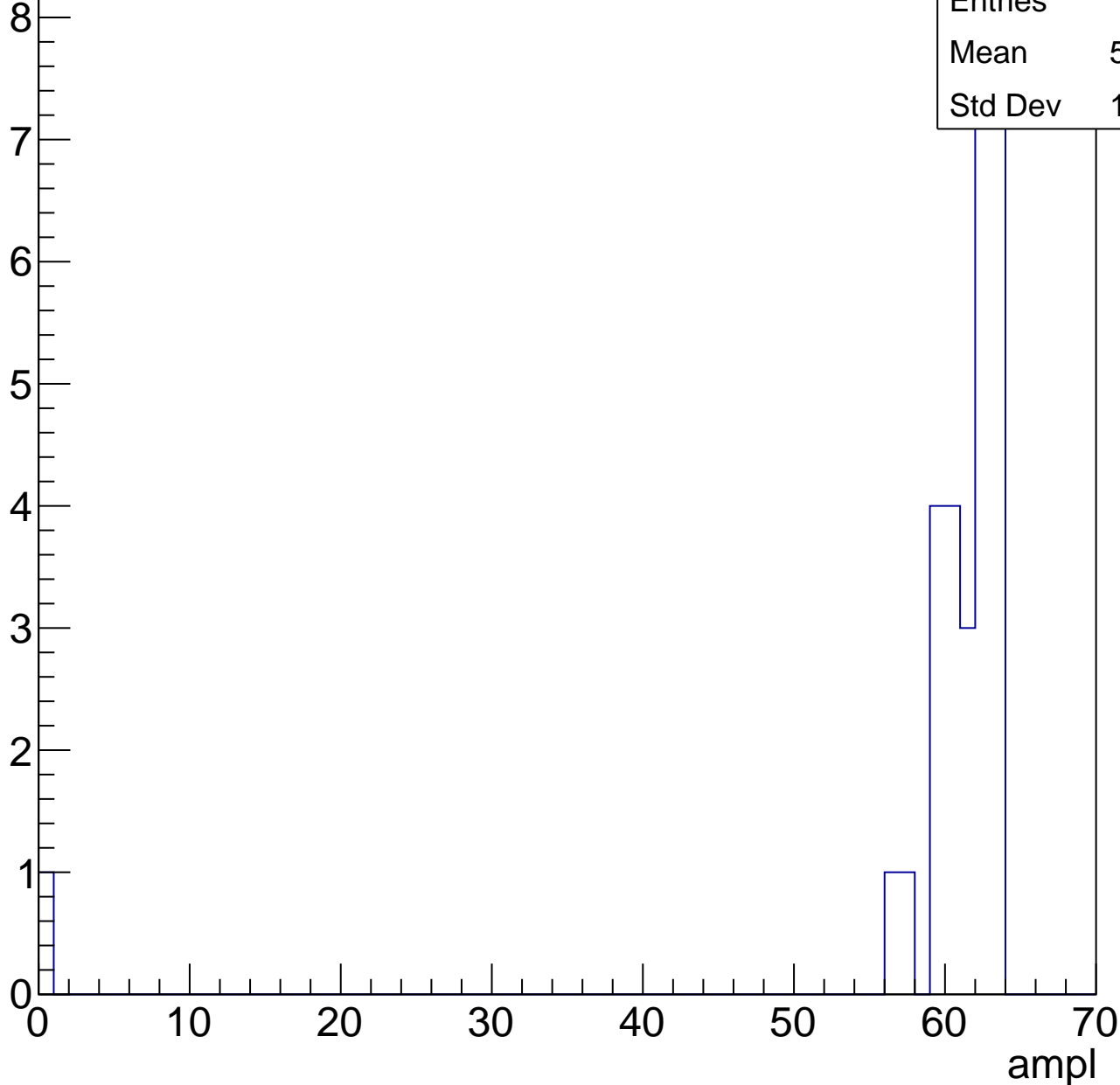


B0L001S, U11-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.07
Std Dev	11.12



B0L001S, U11-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch22, adc0

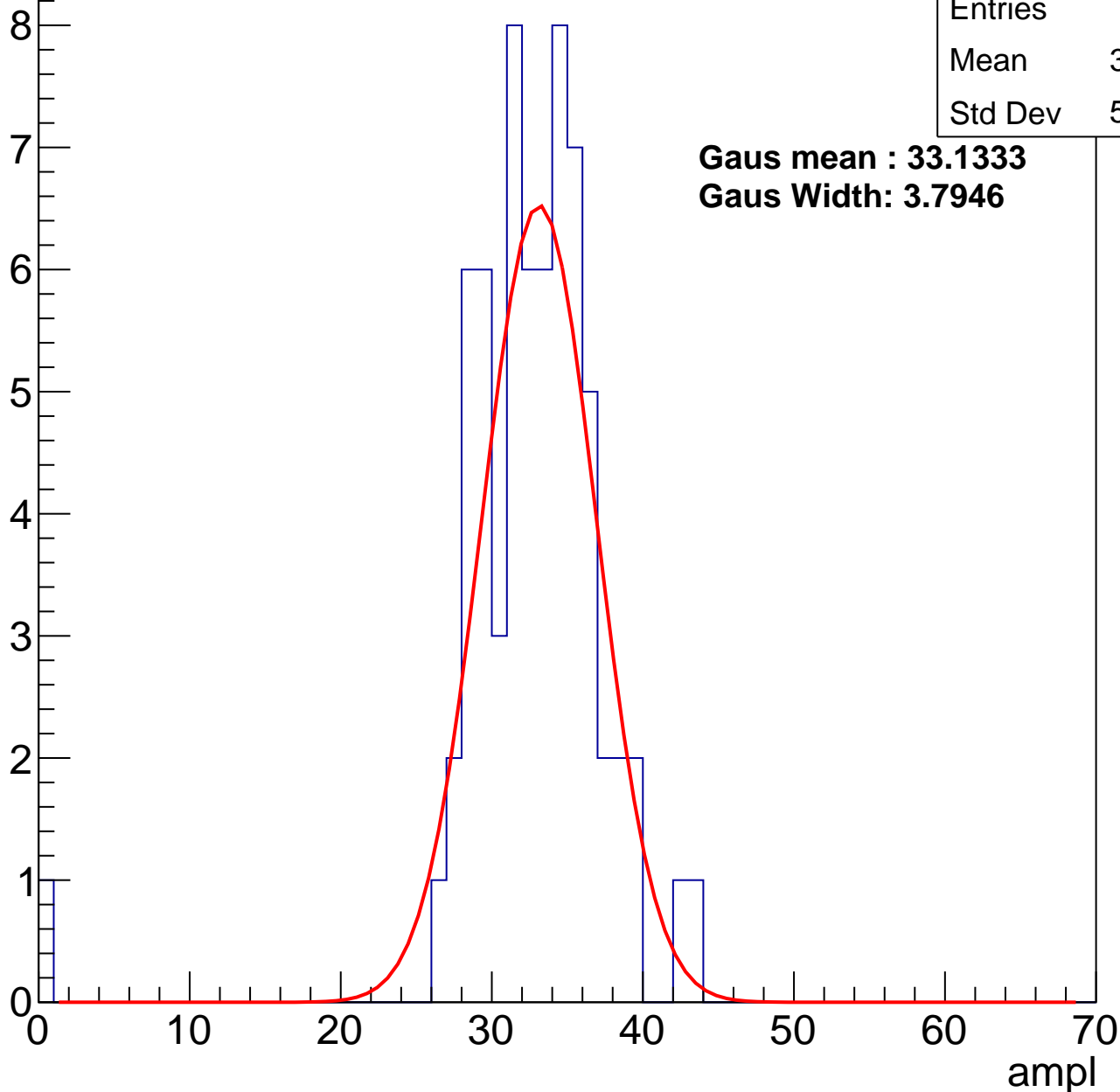
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	32.24
Std Dev	5.323

Gaus mean : 33.1333

Gaus Width: 3.7946



B0L001S, U11-ch22, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	39.83
Std Dev	3.704

Gaus mean : 39.9621

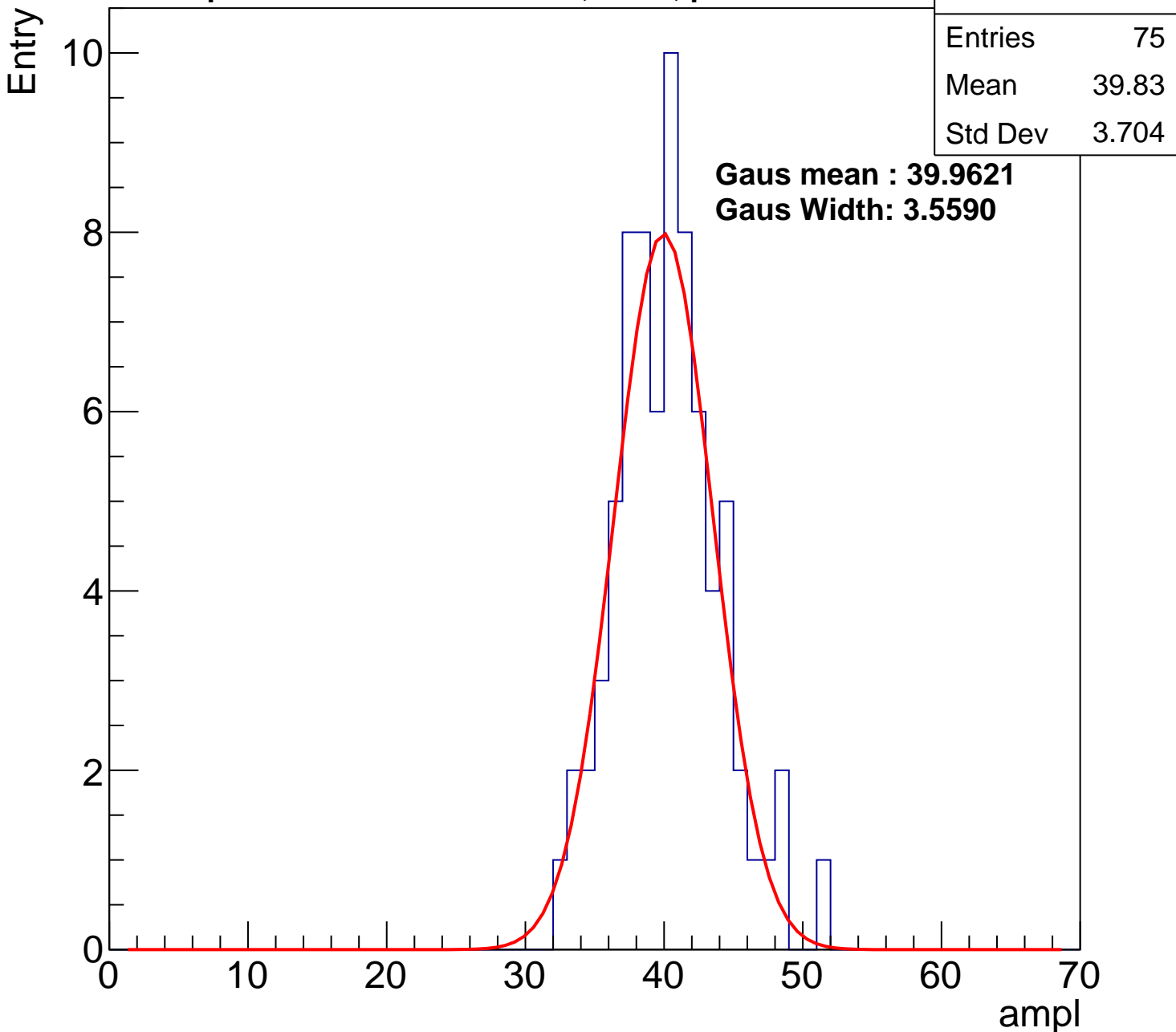
Gaus Width: 3.5590

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch22, adc2

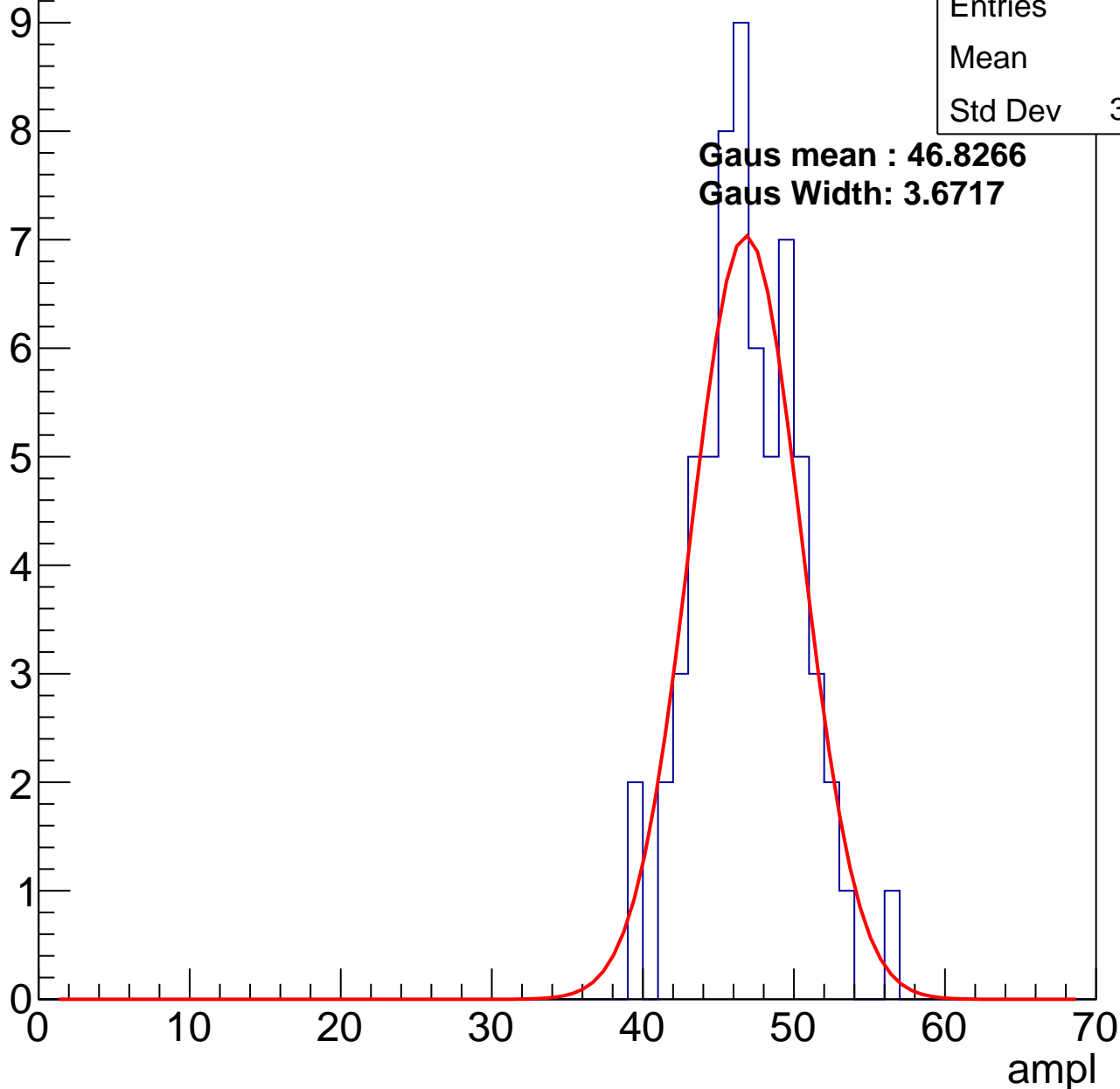
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	46.5
Std Dev	3.349

Gaus mean : 46.8266

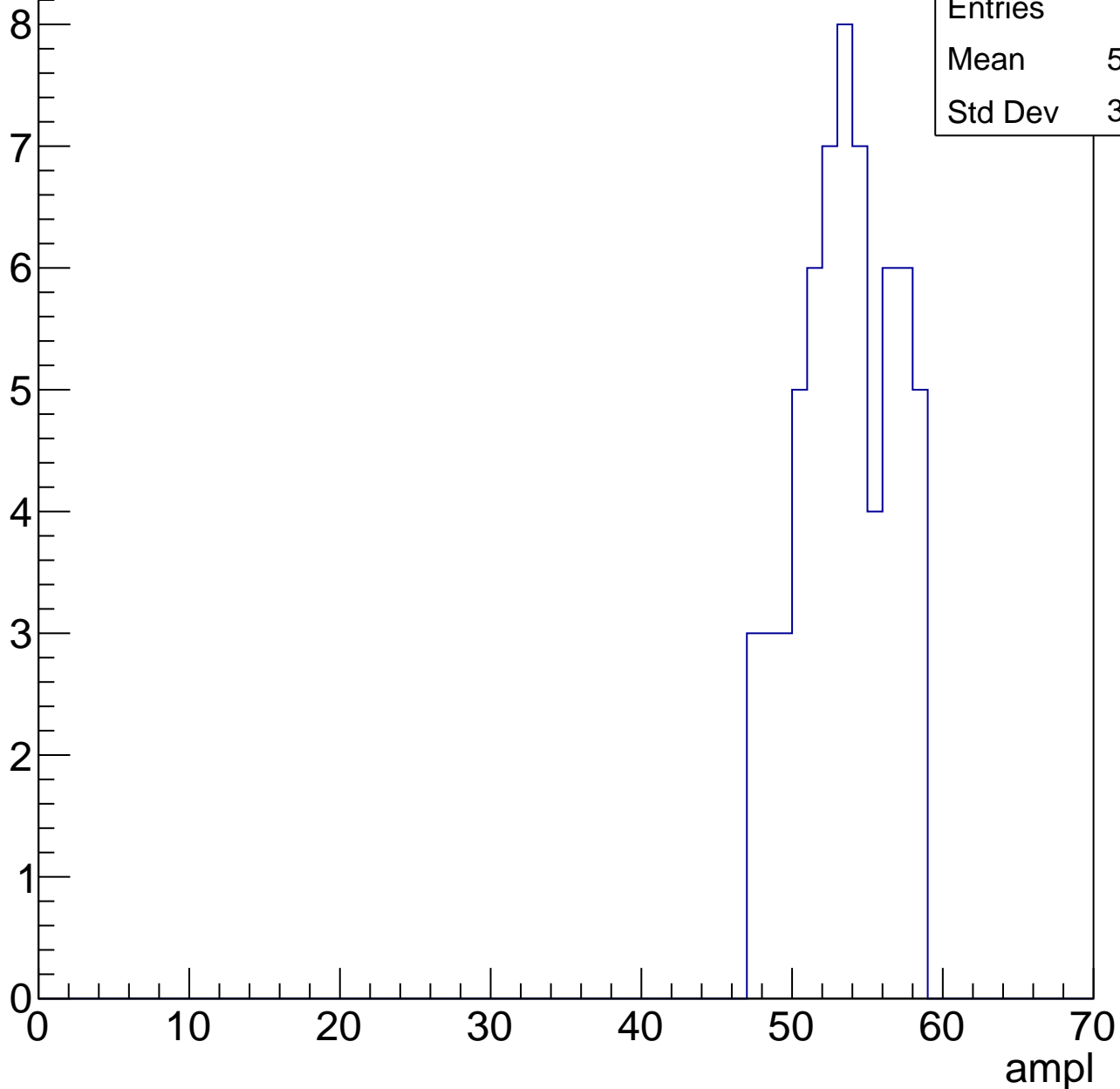
Gaus Width: 3.6717



B0L001S, U11-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

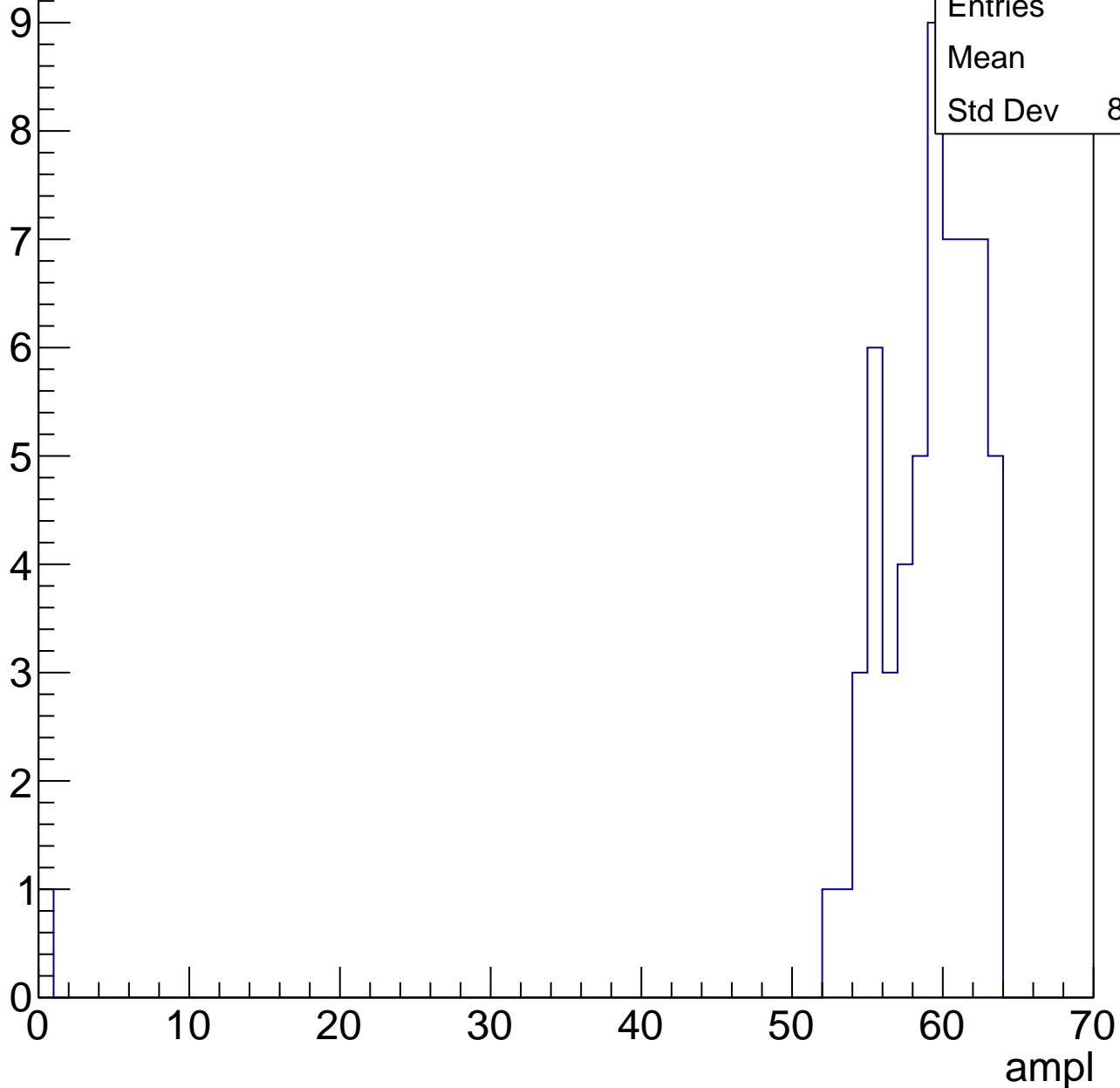
Entry



B0L001S, U11-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

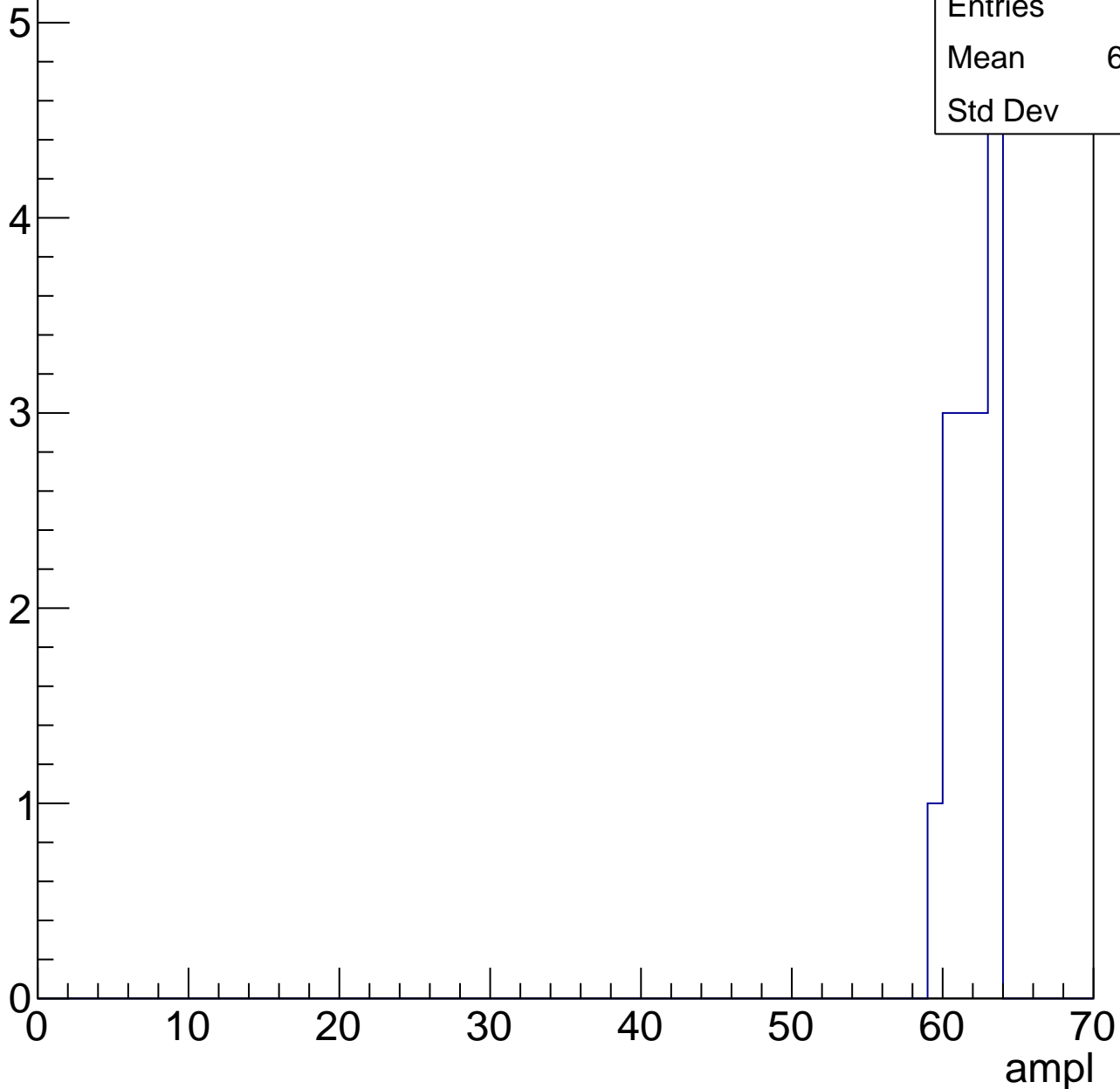


B0L001S, U11-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.53
Std Dev	1.31



B0L001S, U11-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U11-ch23, adc0

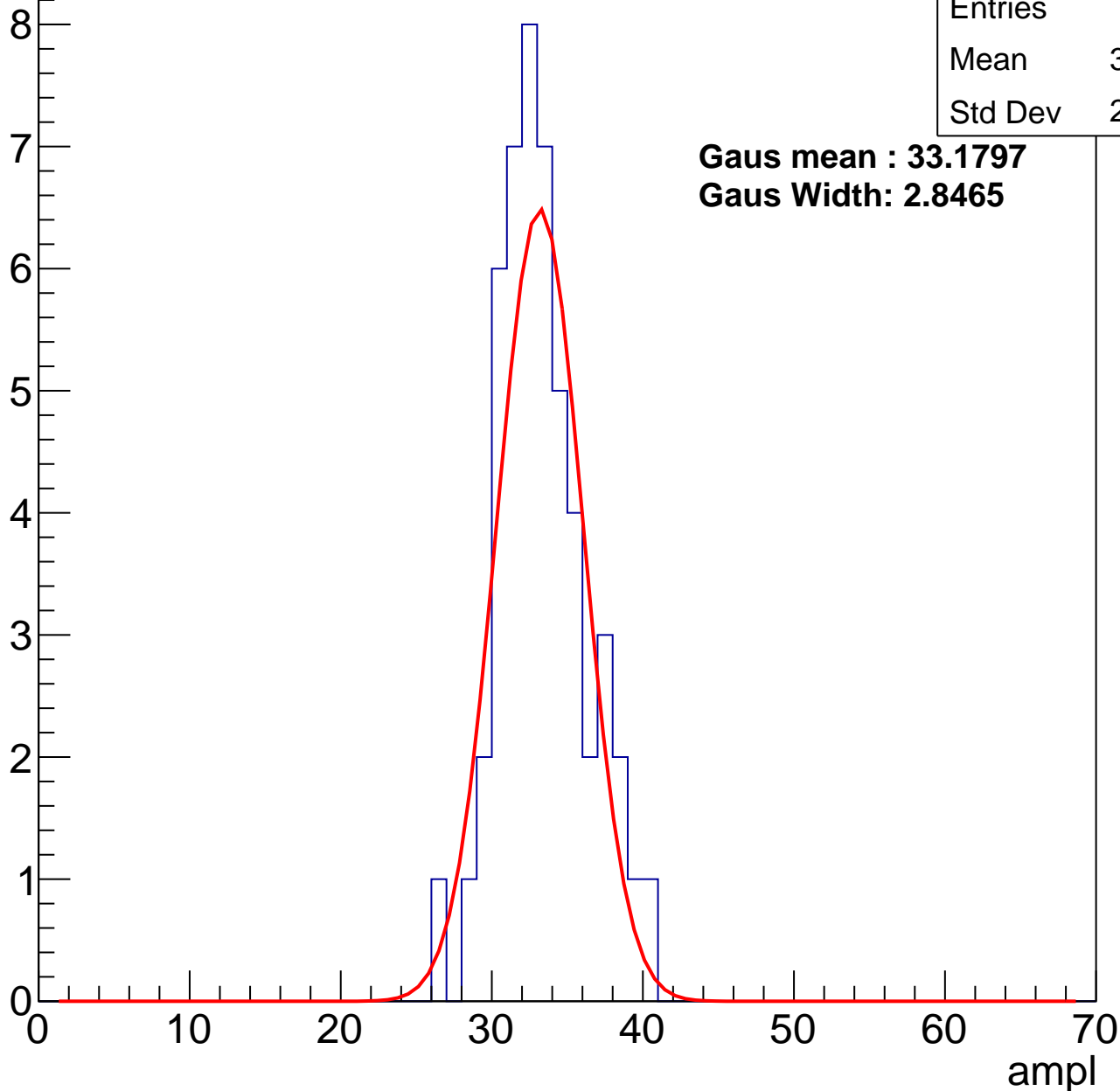
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	32.88
Std Dev	2.889

Gaus mean : 33.1797

Gaus Width: 2.8465



B0L001S, U11-ch23, adc1

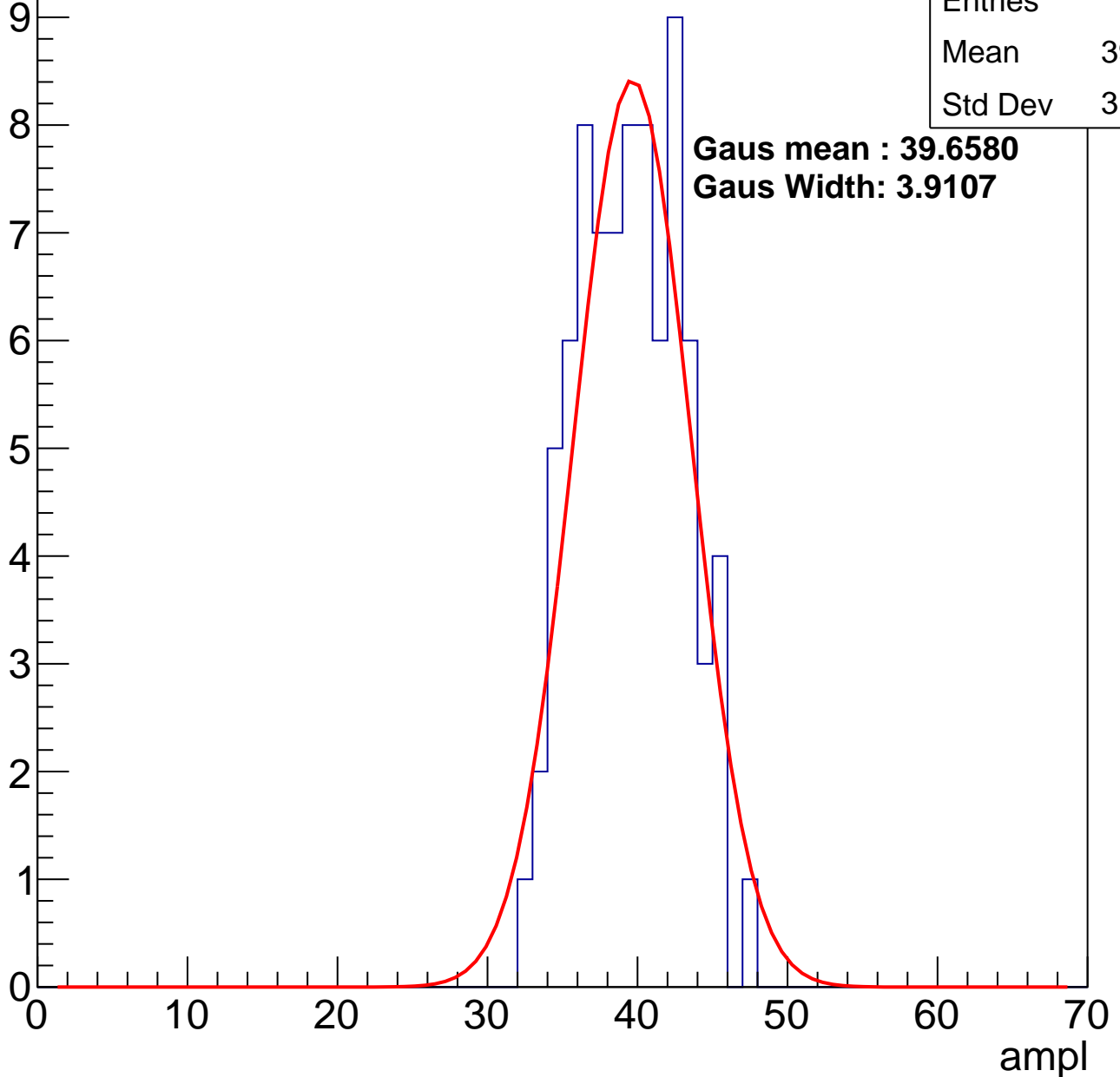
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	39.06
Std Dev	3.408

Gaus mean : 39.6580

Gaus Width: 3.9107



B0L001S, U11-ch23, adc2

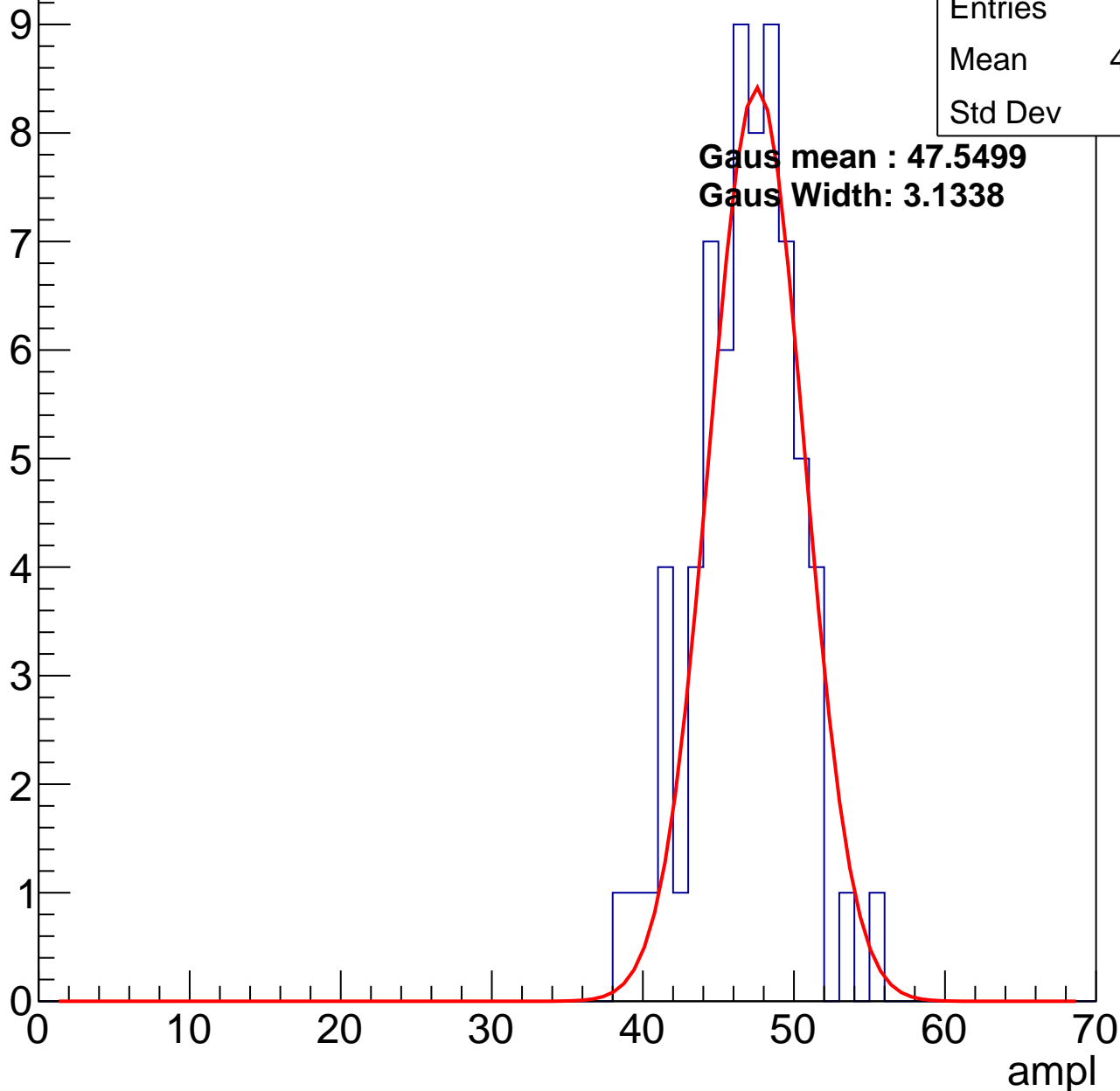
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	46.38
Std Dev	3.28

Gaus mean : 47.5499

Gaus Width: 3.1338

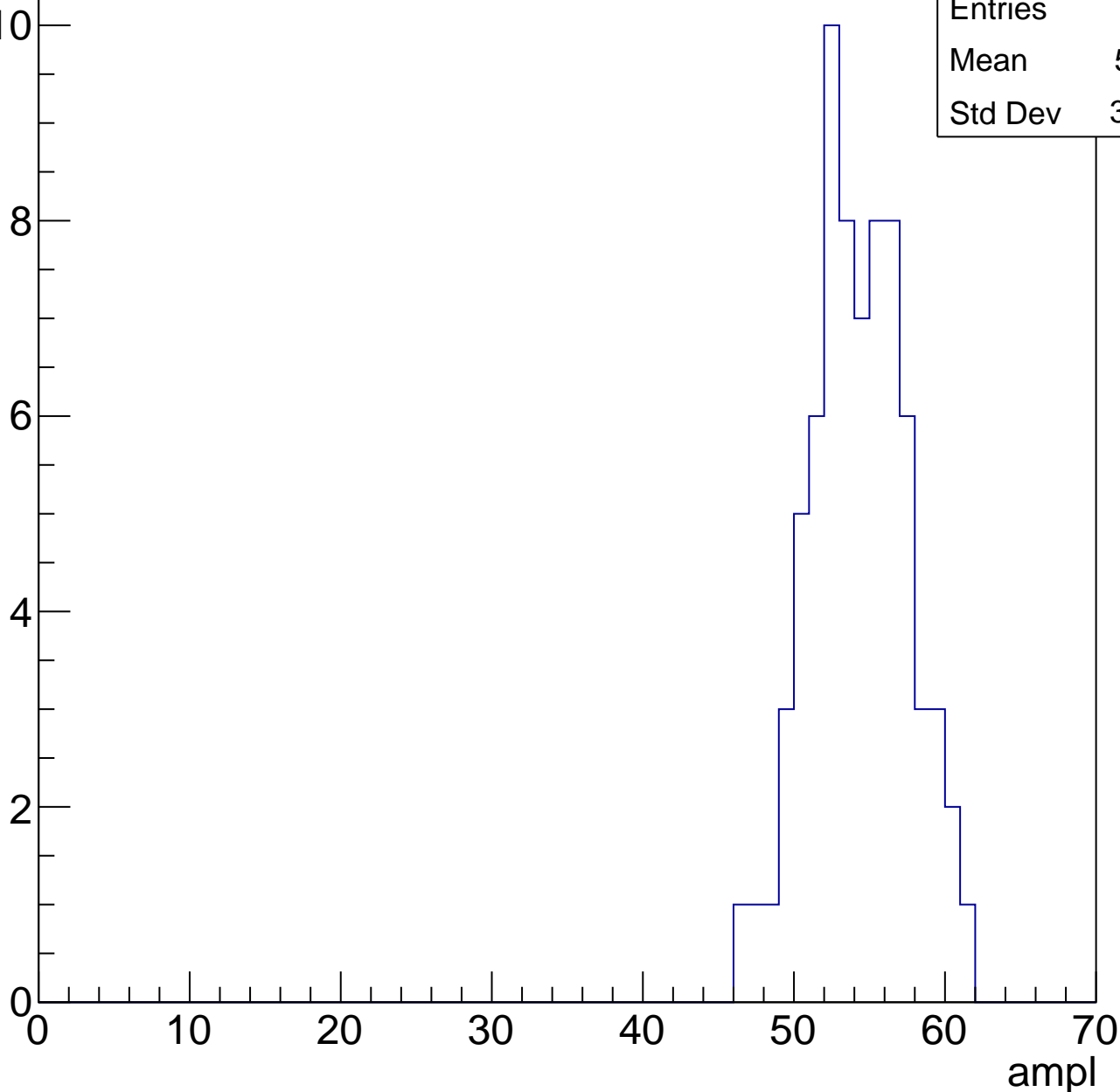


B0L001S, U11-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	53.81
Std Dev	3.187



B0L001S, U11-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	54
Mean	58.33
Std Dev	8.409

Entry

10

8

6

4

2

0

0

10

20

30

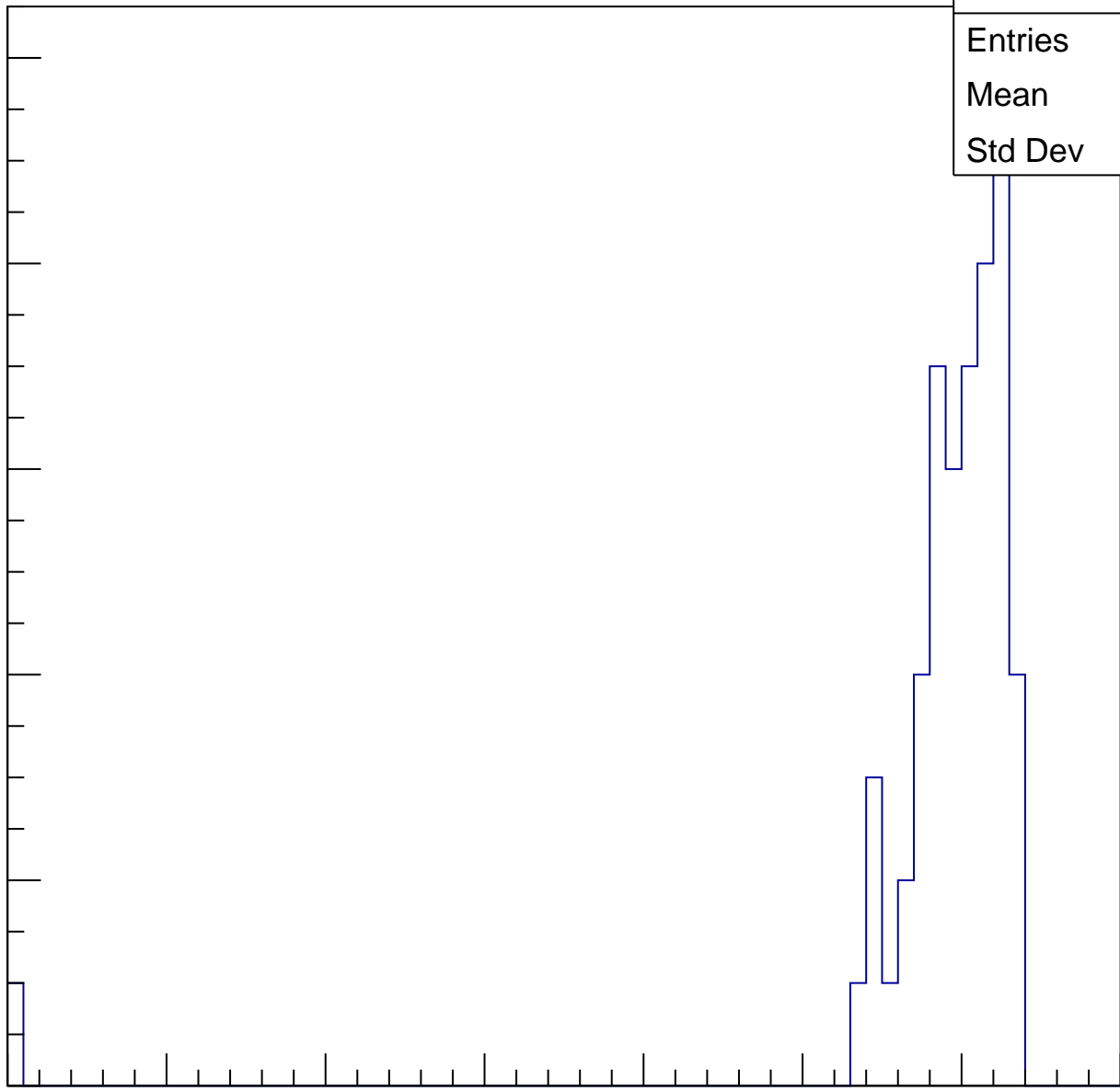
40

50

60

70

ampl

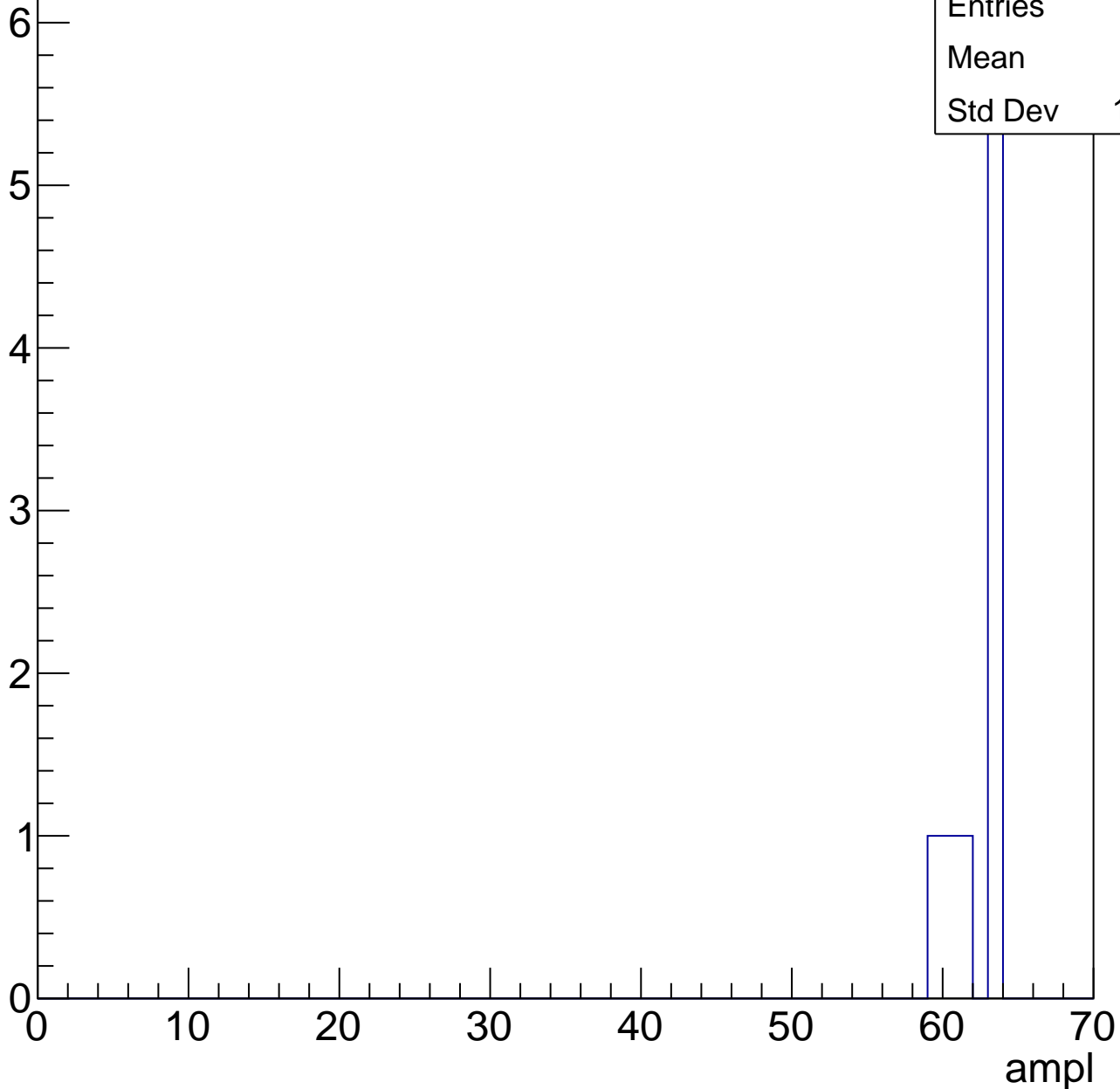


B0L001S, U11-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	62
Std Dev	1.491



B0L001S, U11-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch24, adc0

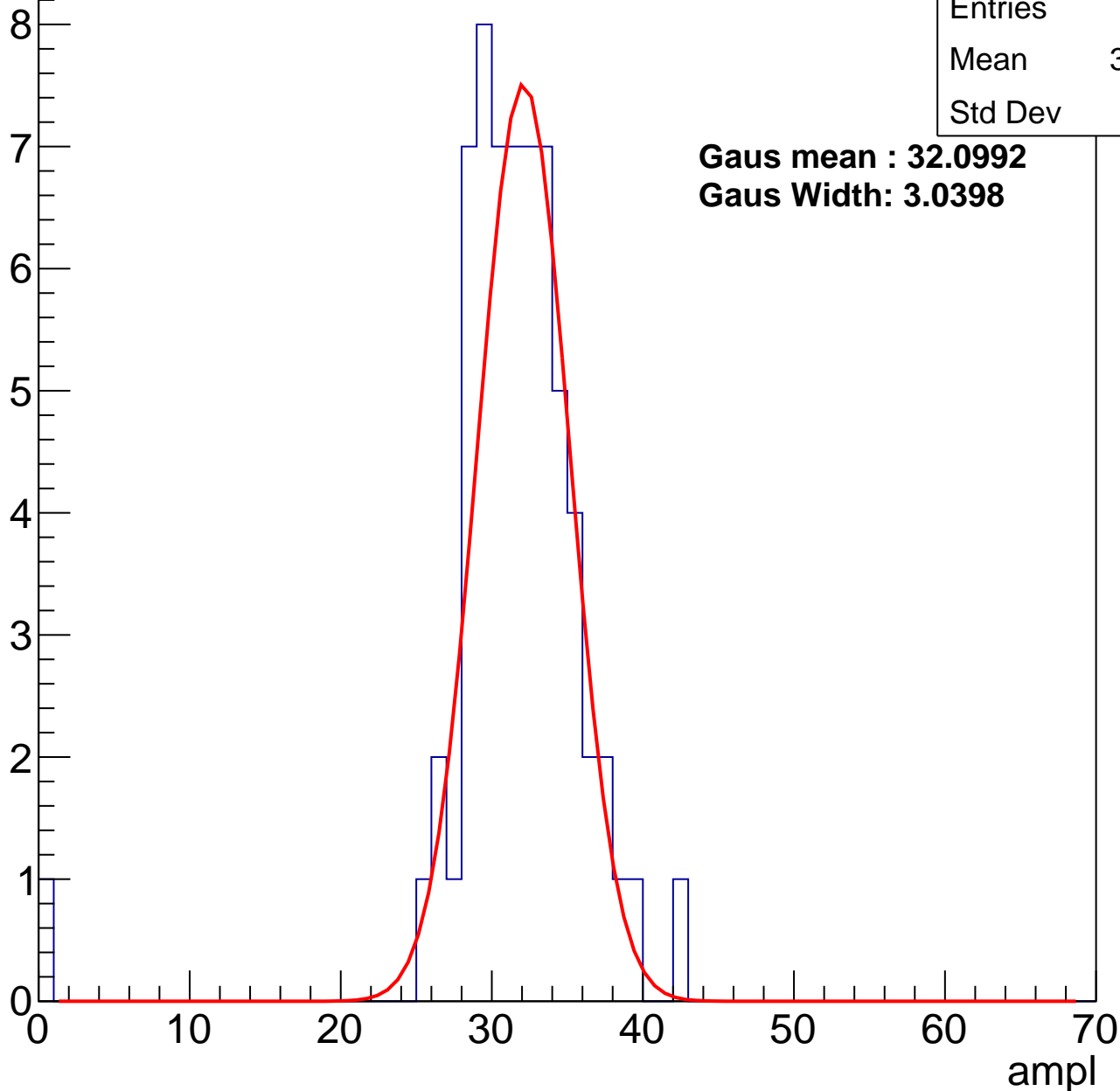
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.08
Std Dev	5.1

Gaus mean : 32.0992

Gaus Width: 3.0398



B0L001S, U11-ch24, adc1

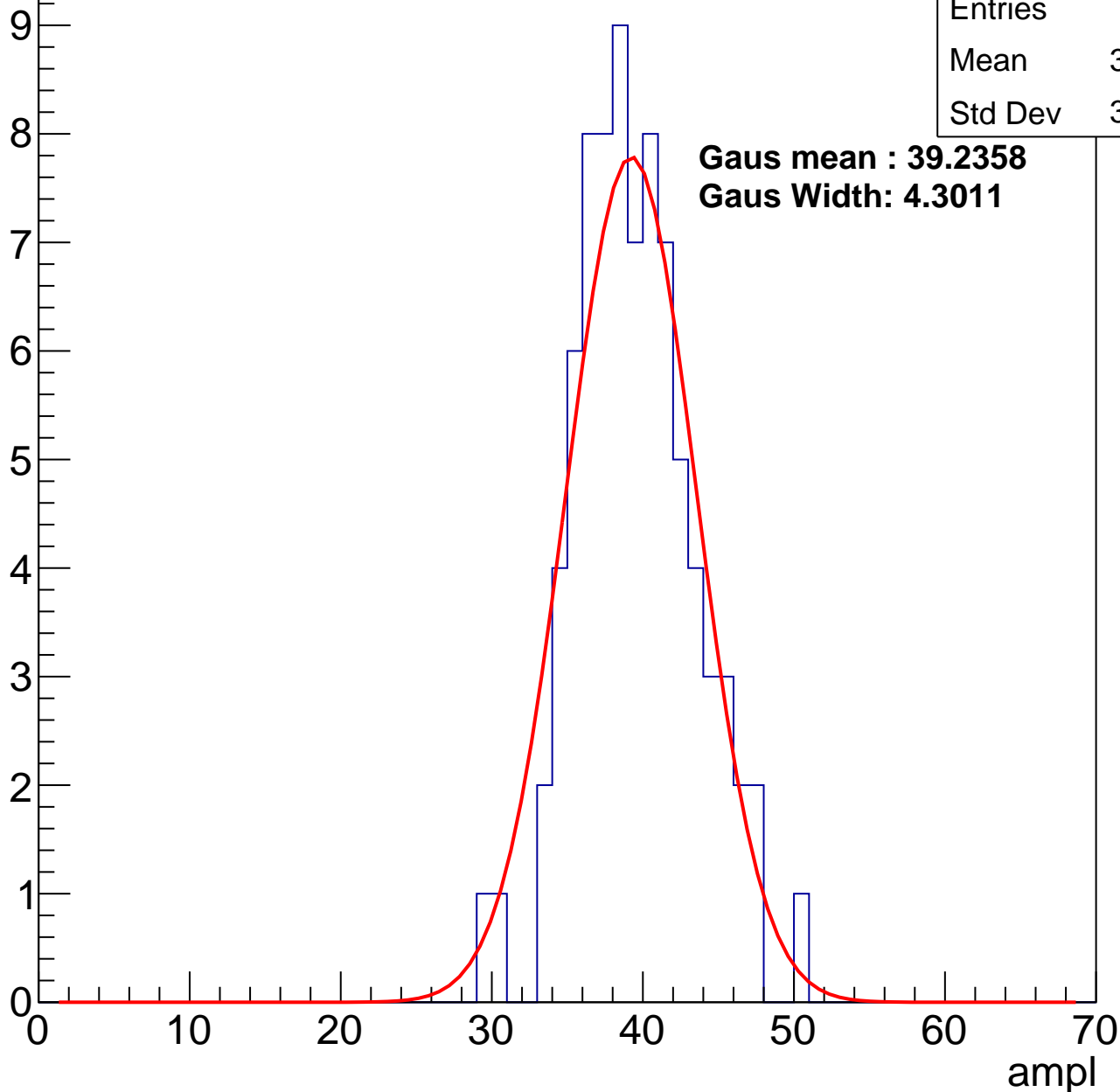
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	39.04
Std Dev	3.908

Gaus mean : 39.2358

Gaus Width: 4.3011



B0L001S, U11-ch24, adc2

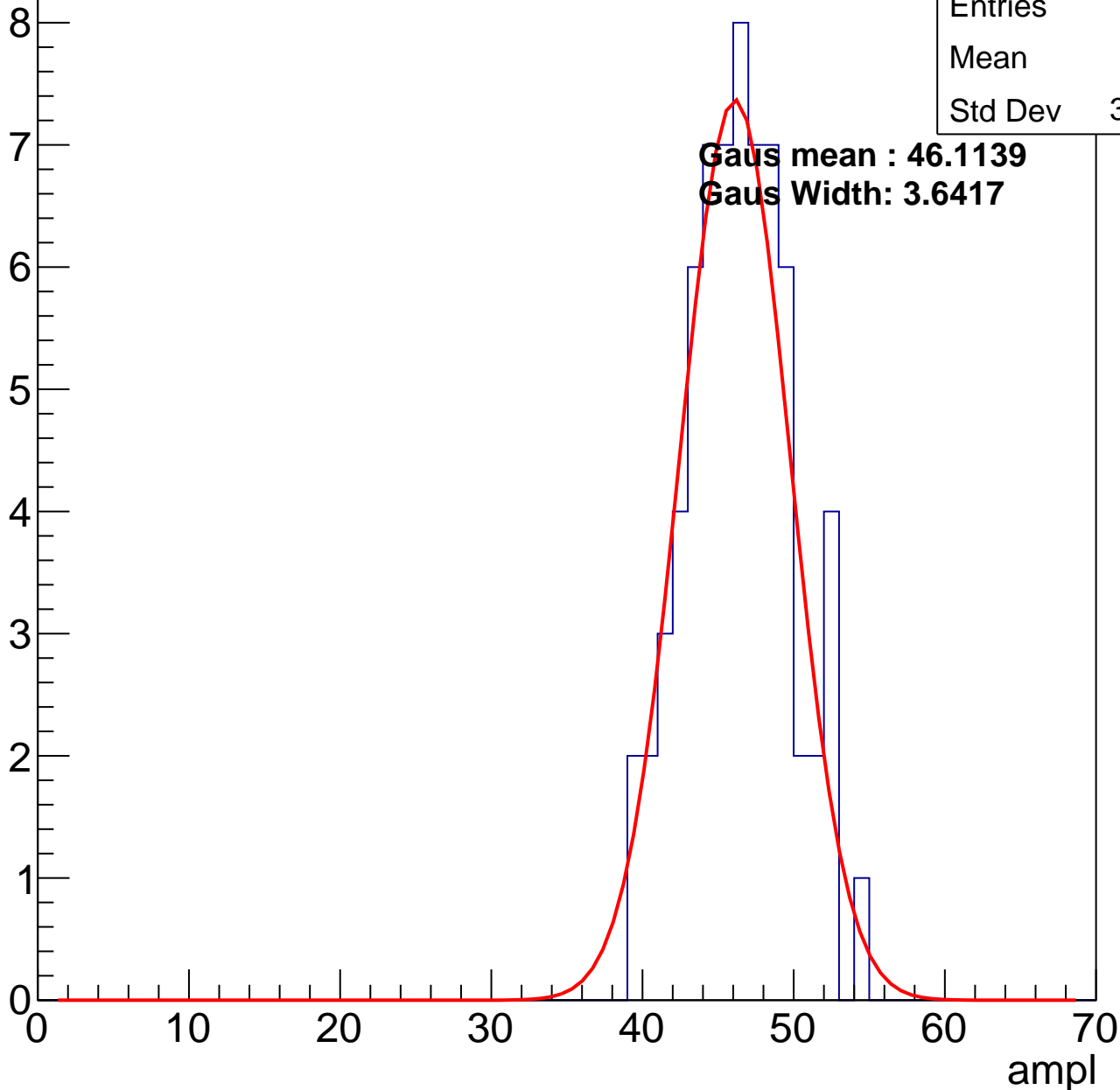
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45.9
Std Dev	3.379

Gaus mean : 46.1139

Gaus Width: 3.6417

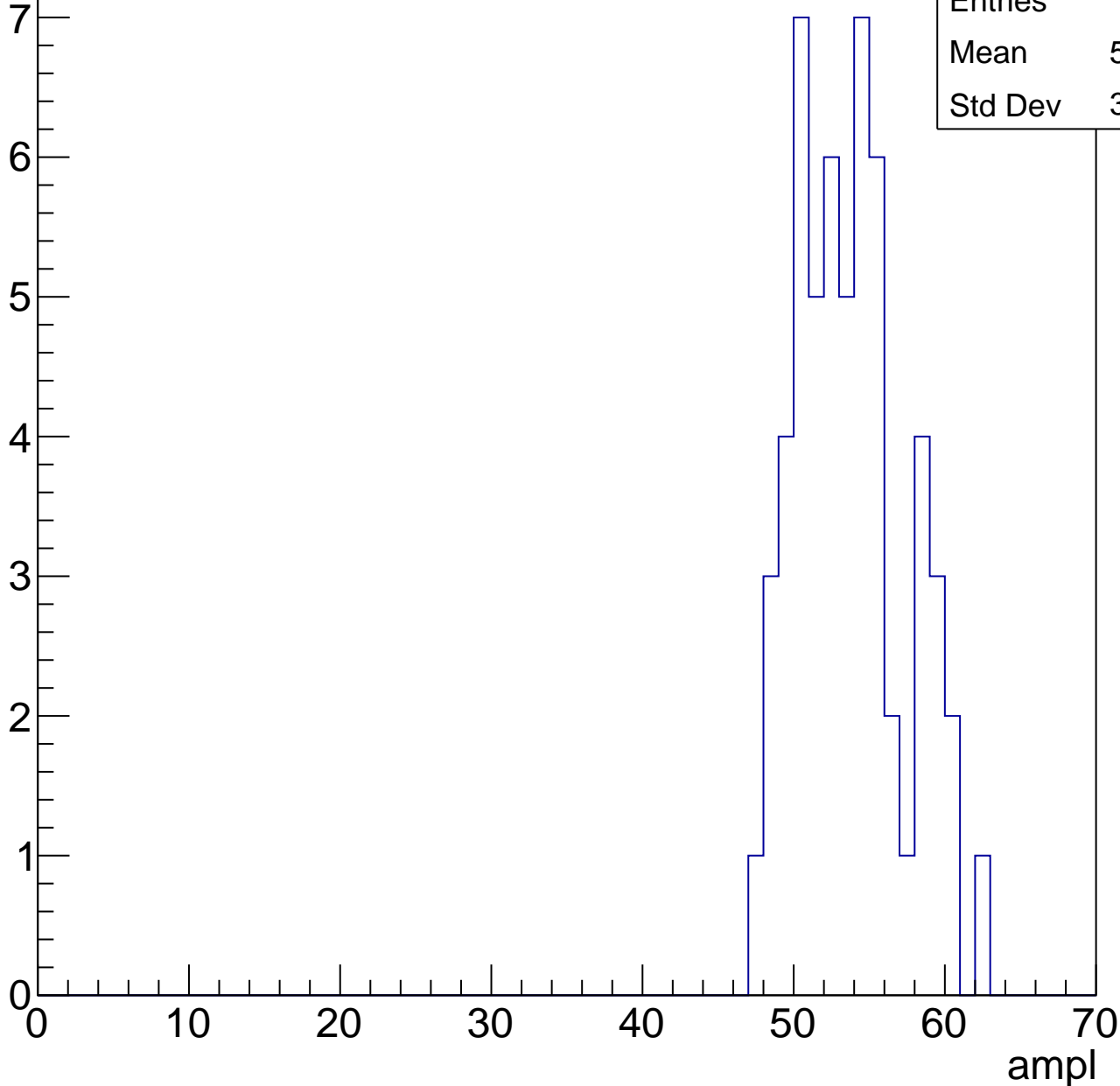


B0L001S, U11-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

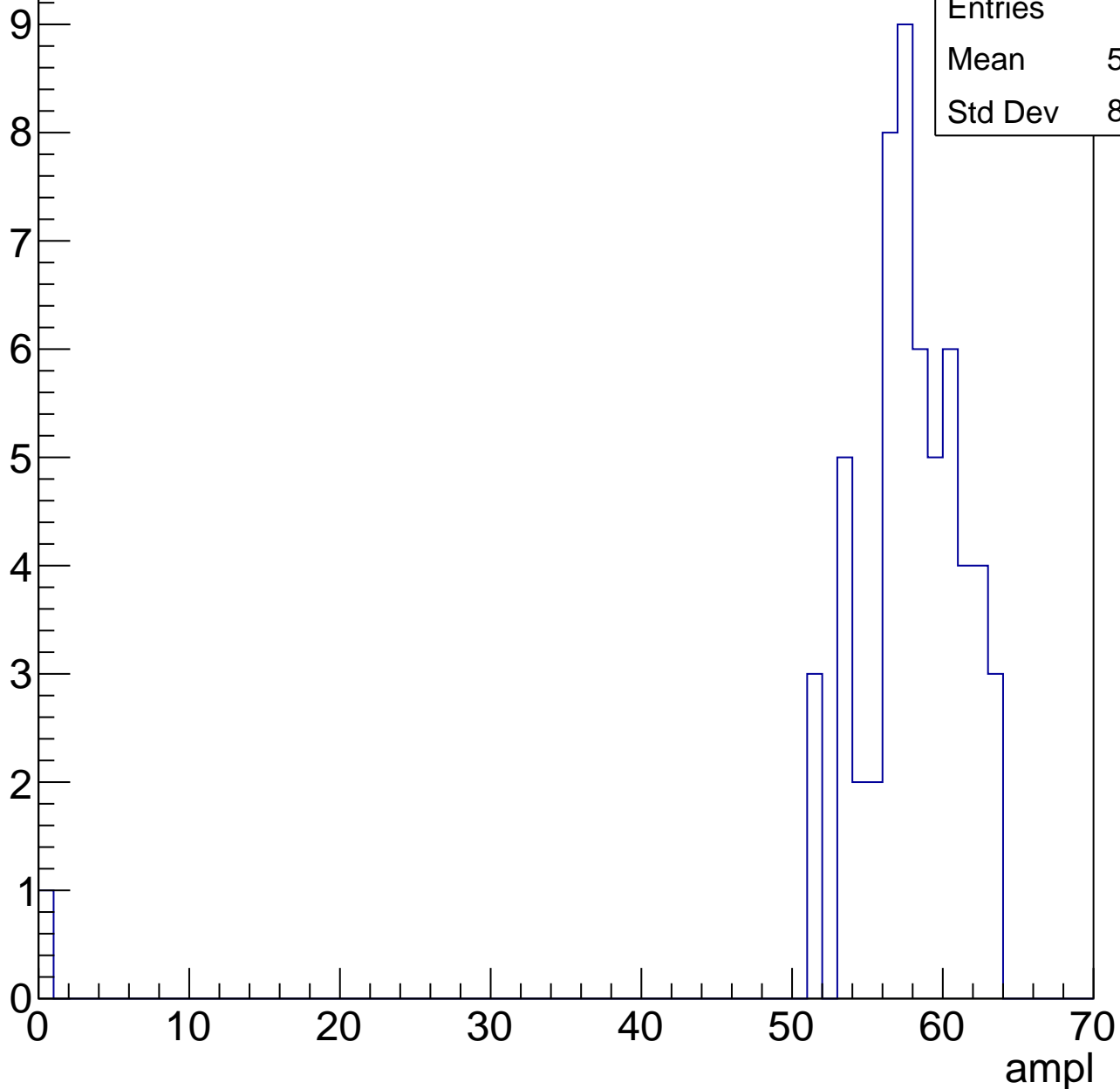
Entries	57
Mean	53.28
Std Dev	3.533



B0L001S, U11-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

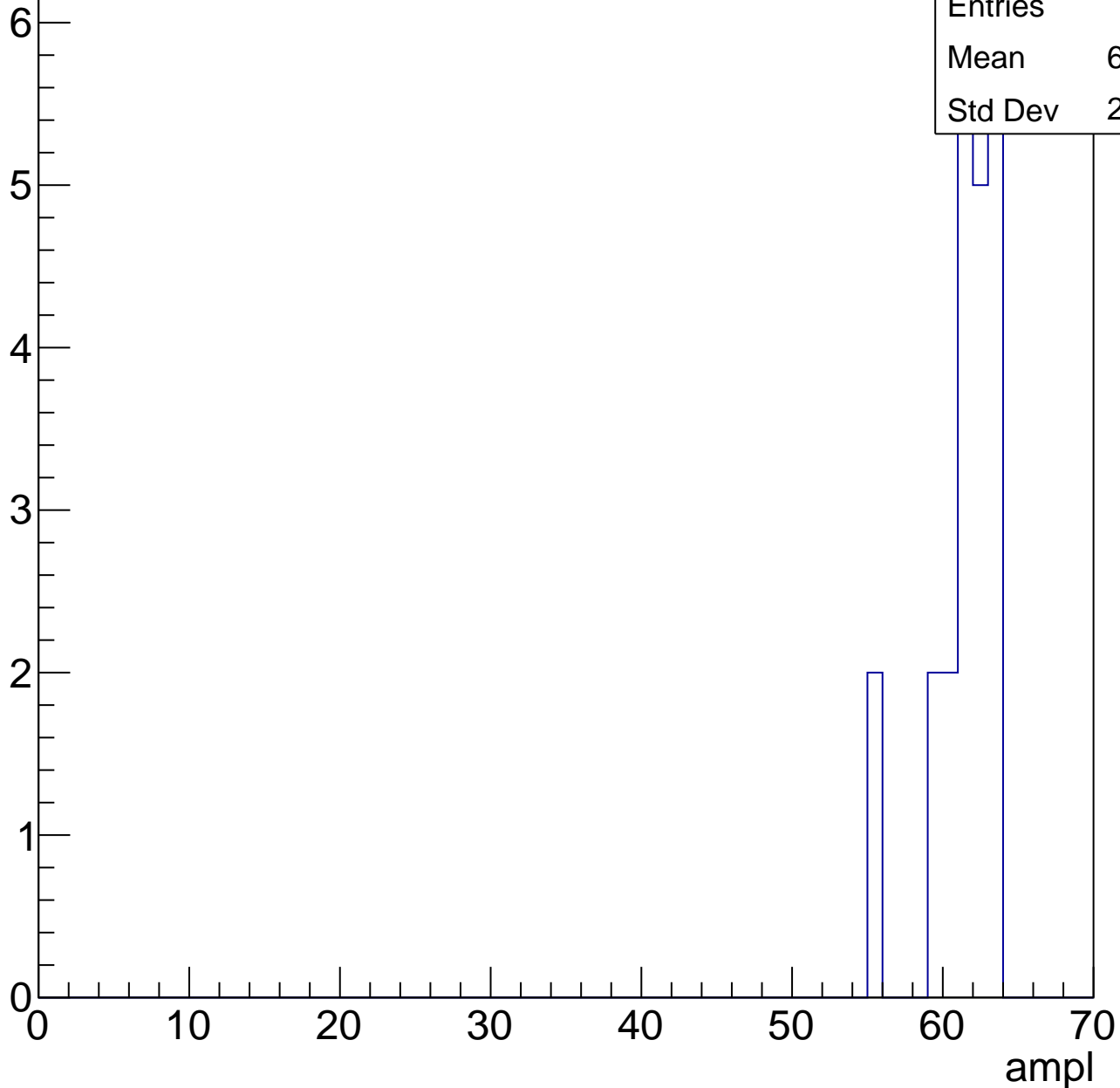
Entry



B0L001S, U11-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	23
Mean	60.96
Std Dev	2.196

B0L001S, U11-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch25, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.82
Std Dev	7.026

Gaus mean : 32.2410

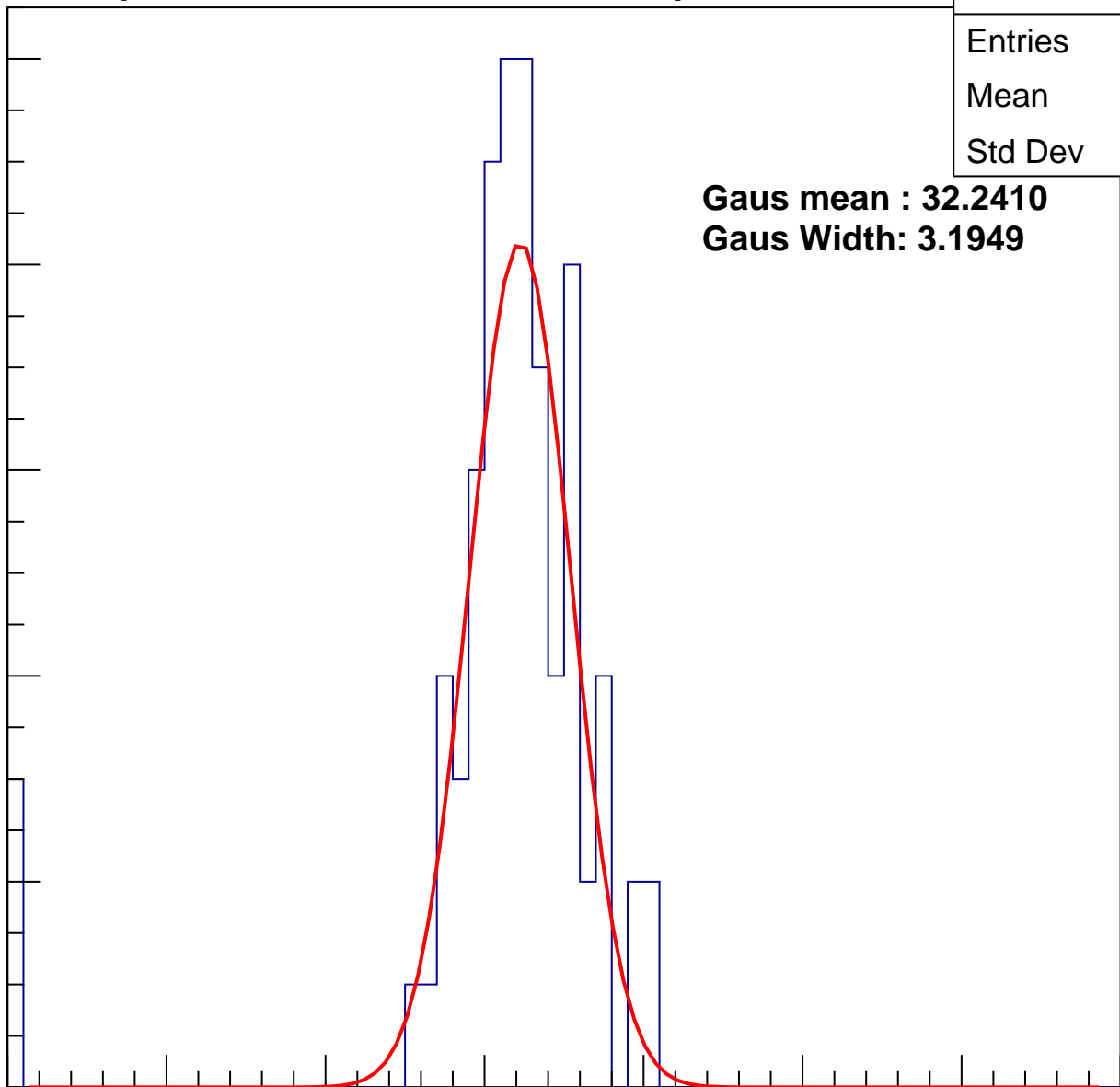
Gaus Width: 3.1949

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch25, adc1

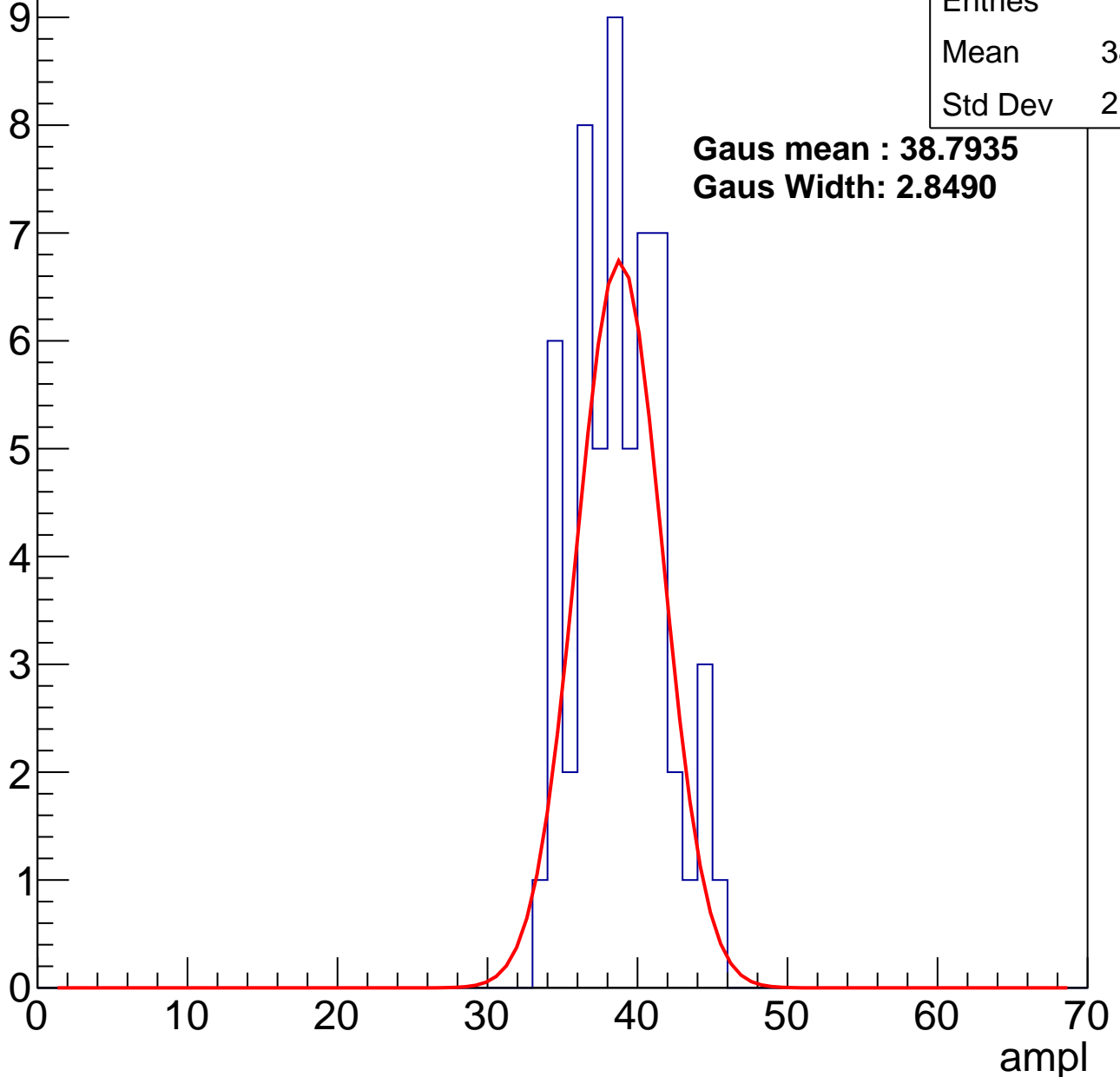
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	38.39
Std Dev	2.894

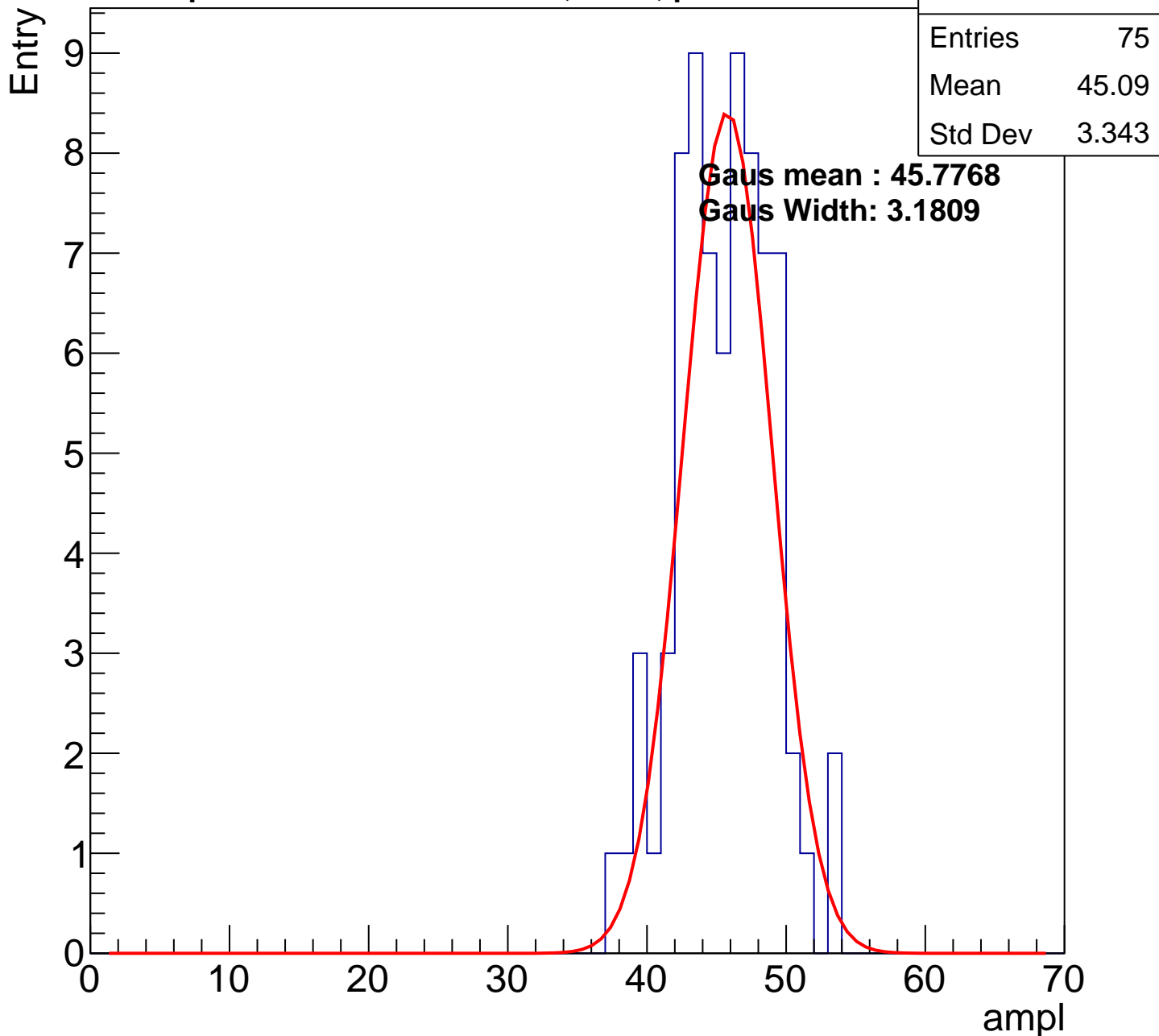
Gaus mean : 38.7935

Gaus Width: 2.8490



B0L001S, U11-ch25, adc2

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U11-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	52.55
Std Dev	3.5

Entry

10

8

6

4

2

0

0

10

20

30

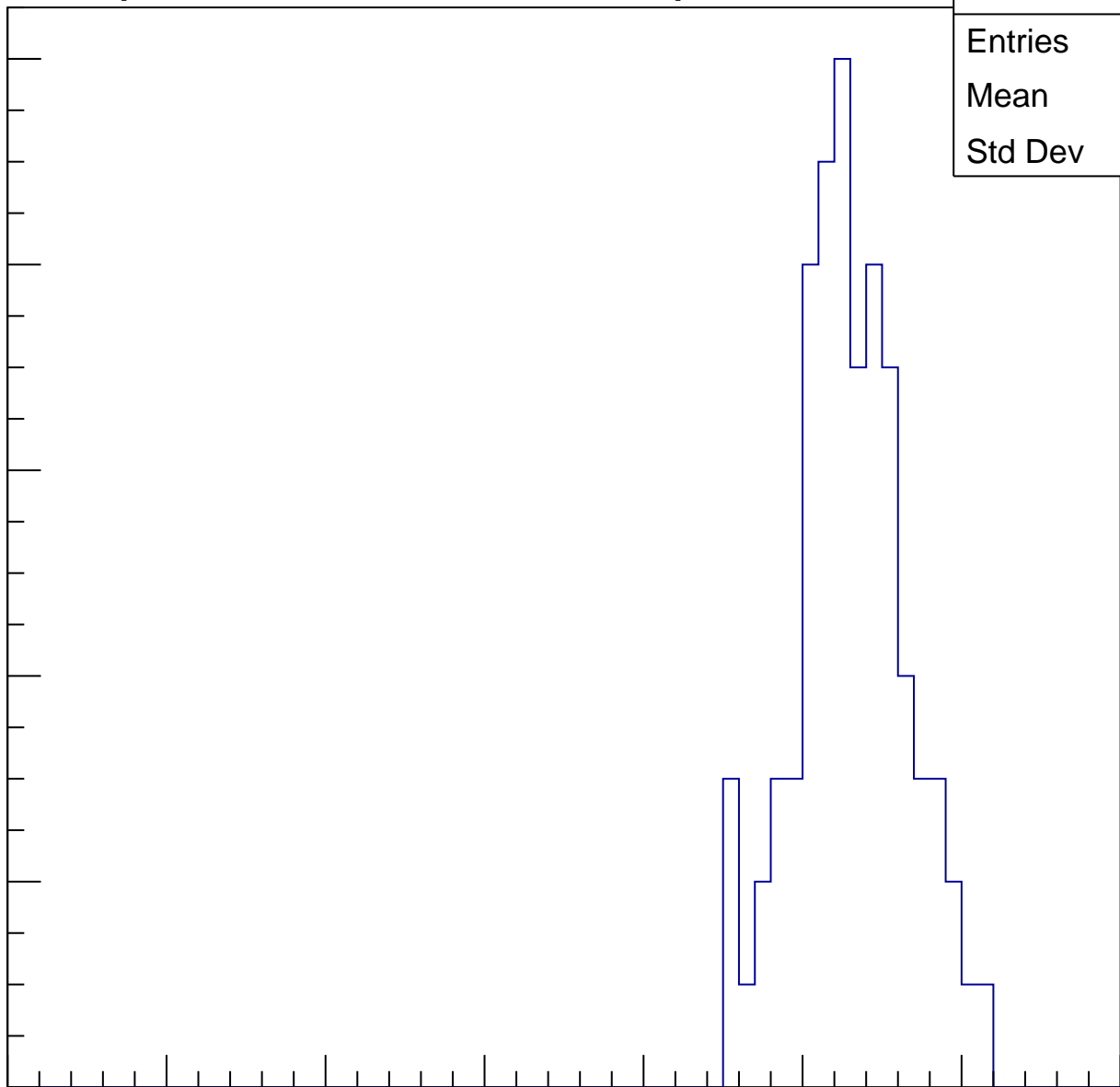
40

50

60

70

ampl

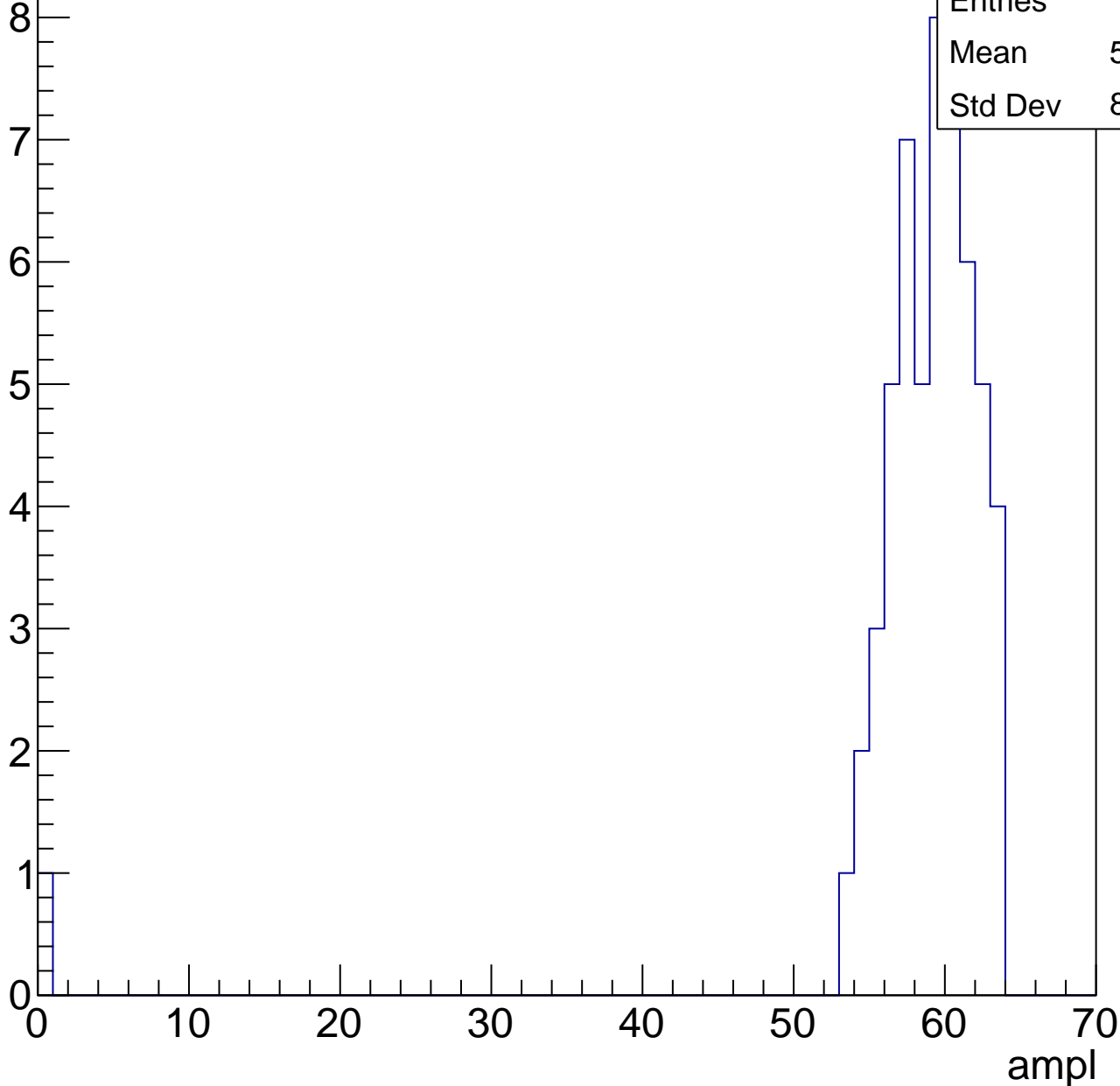


B0L001S, U11-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.73
Std Dev	8.252

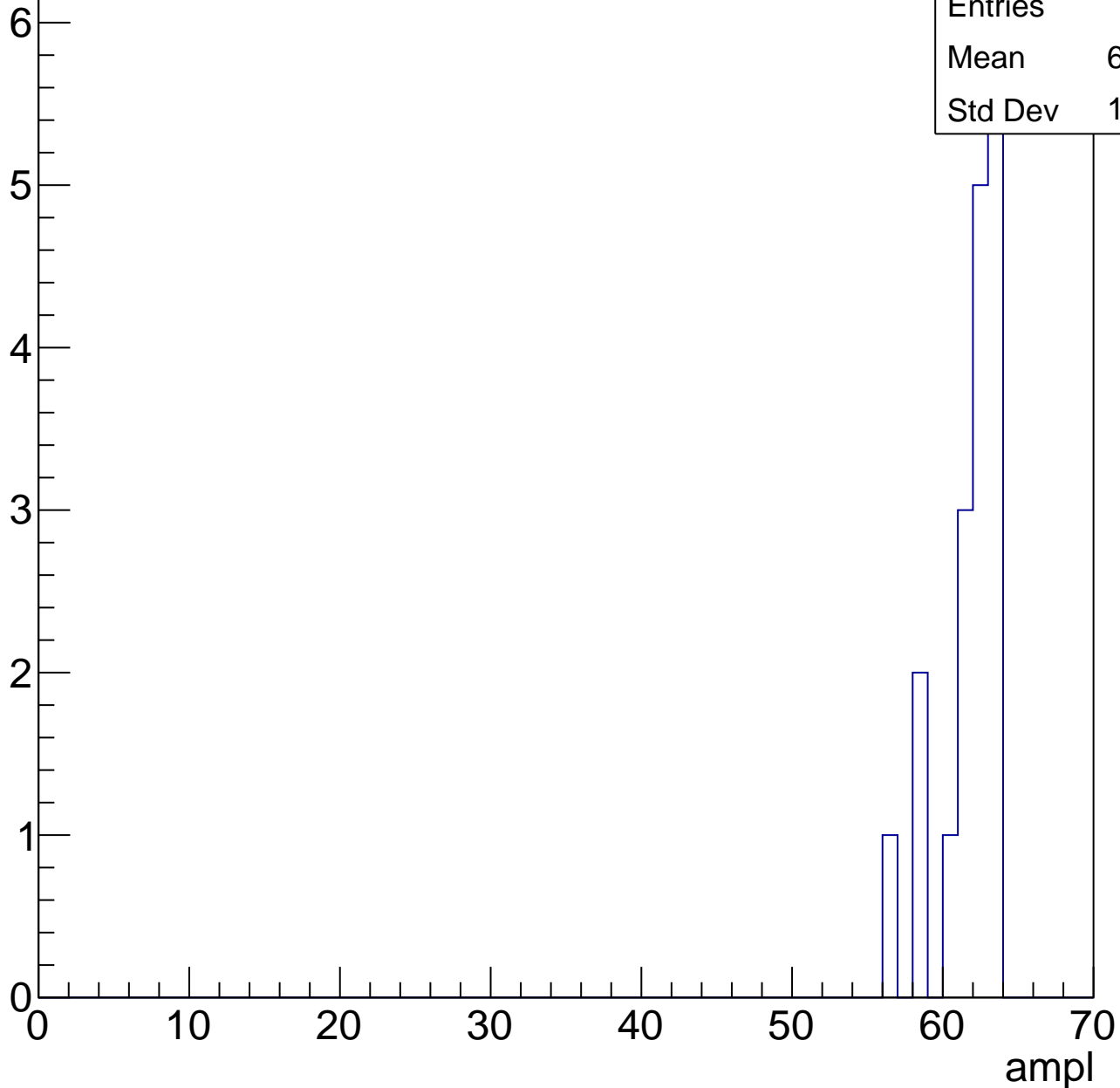


B0L001S, U11-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	61.28
Std Dev	1.995



B0L001S, U11-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch26, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	31.17
Std Dev	5.167

Gaus mean : 31.9972

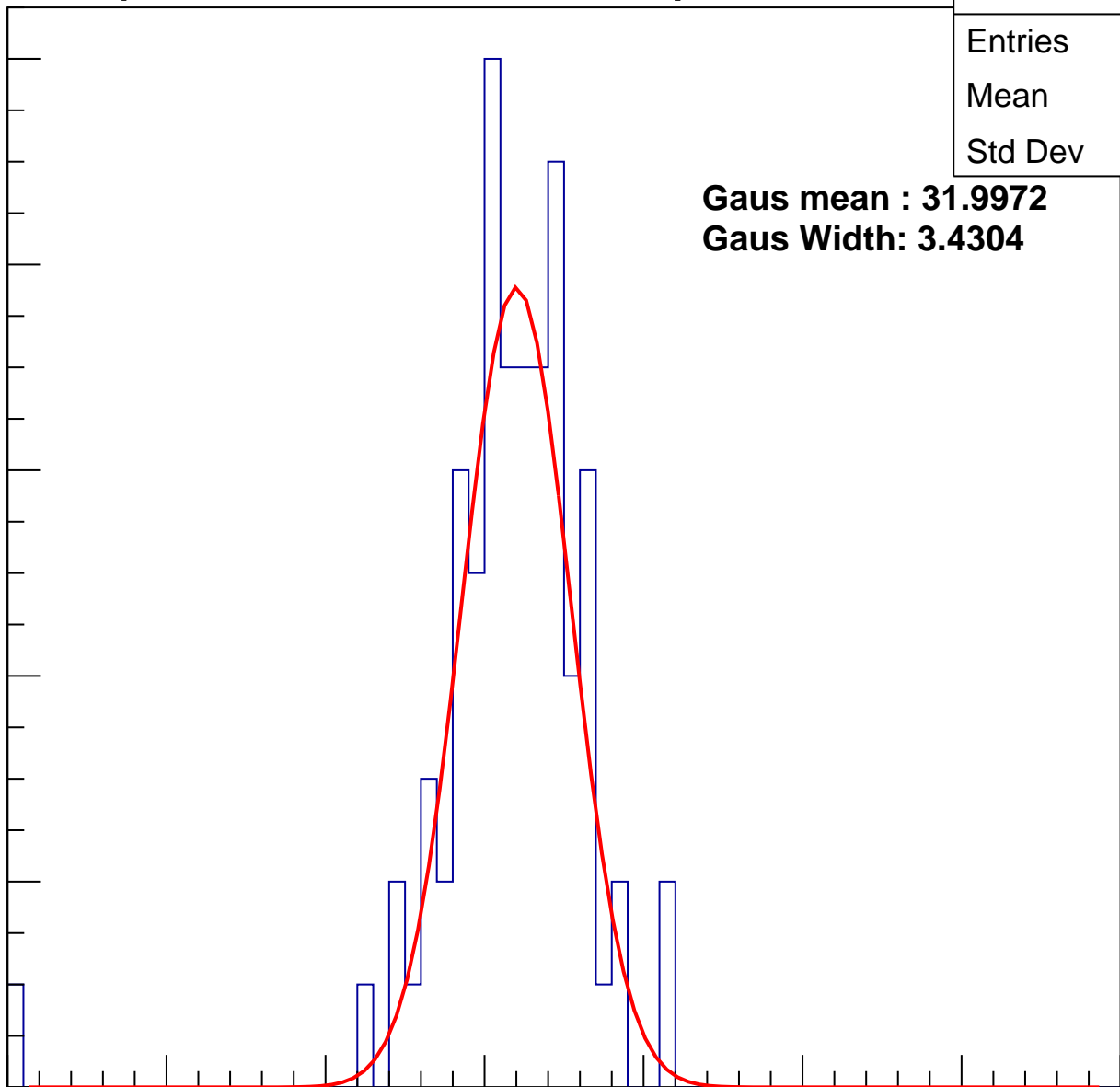
Gaus Width: 3.4304

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch26, adc1

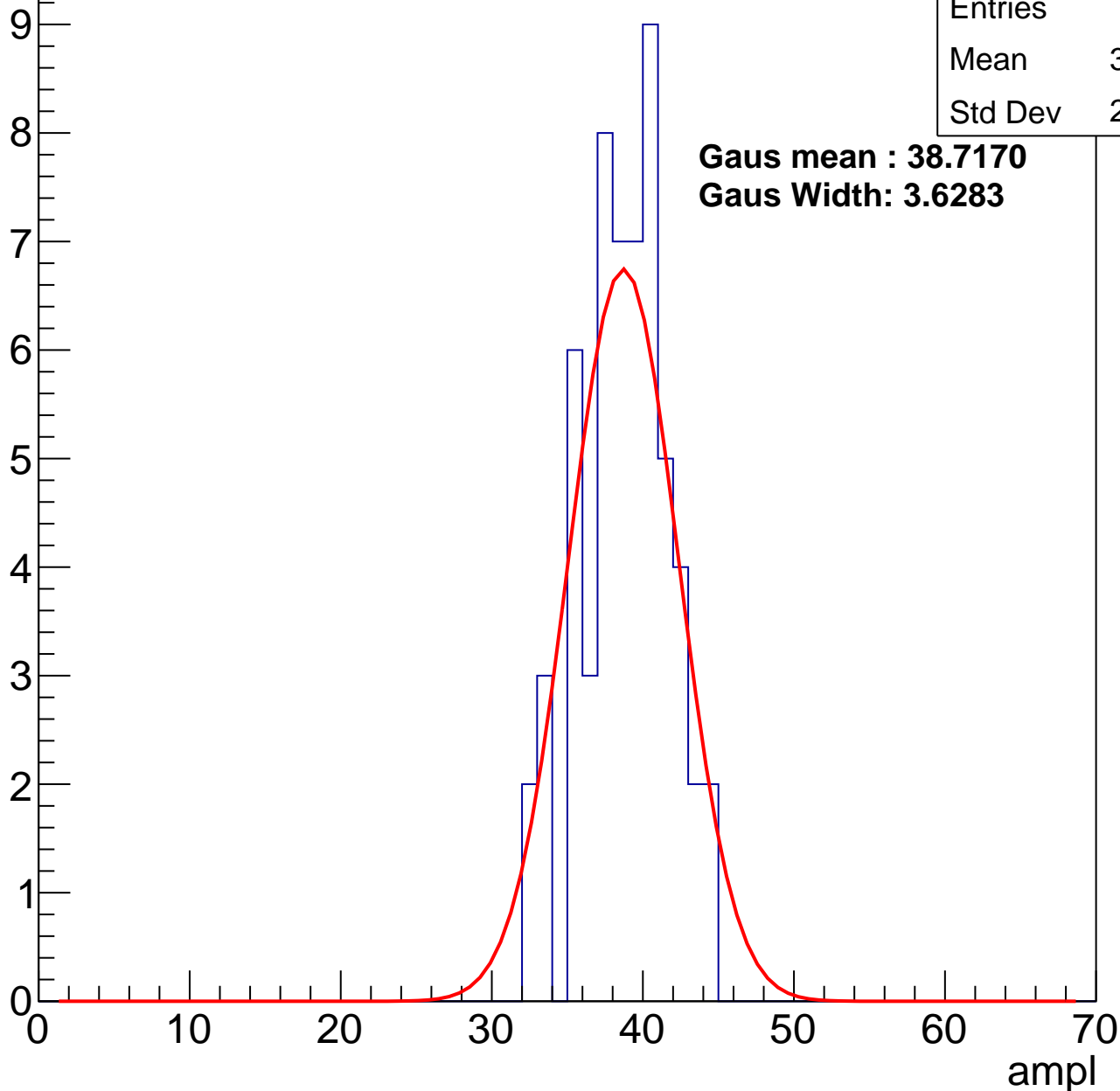
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	38.33
Std Dev	2.903

Gaus mean : 38.7170

Gaus Width: 3.6283



B0L001S, U11-ch26, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	44.72
Std Dev	3.57

Gaus mean : 45.7256

Gaus Width: 3.1518

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

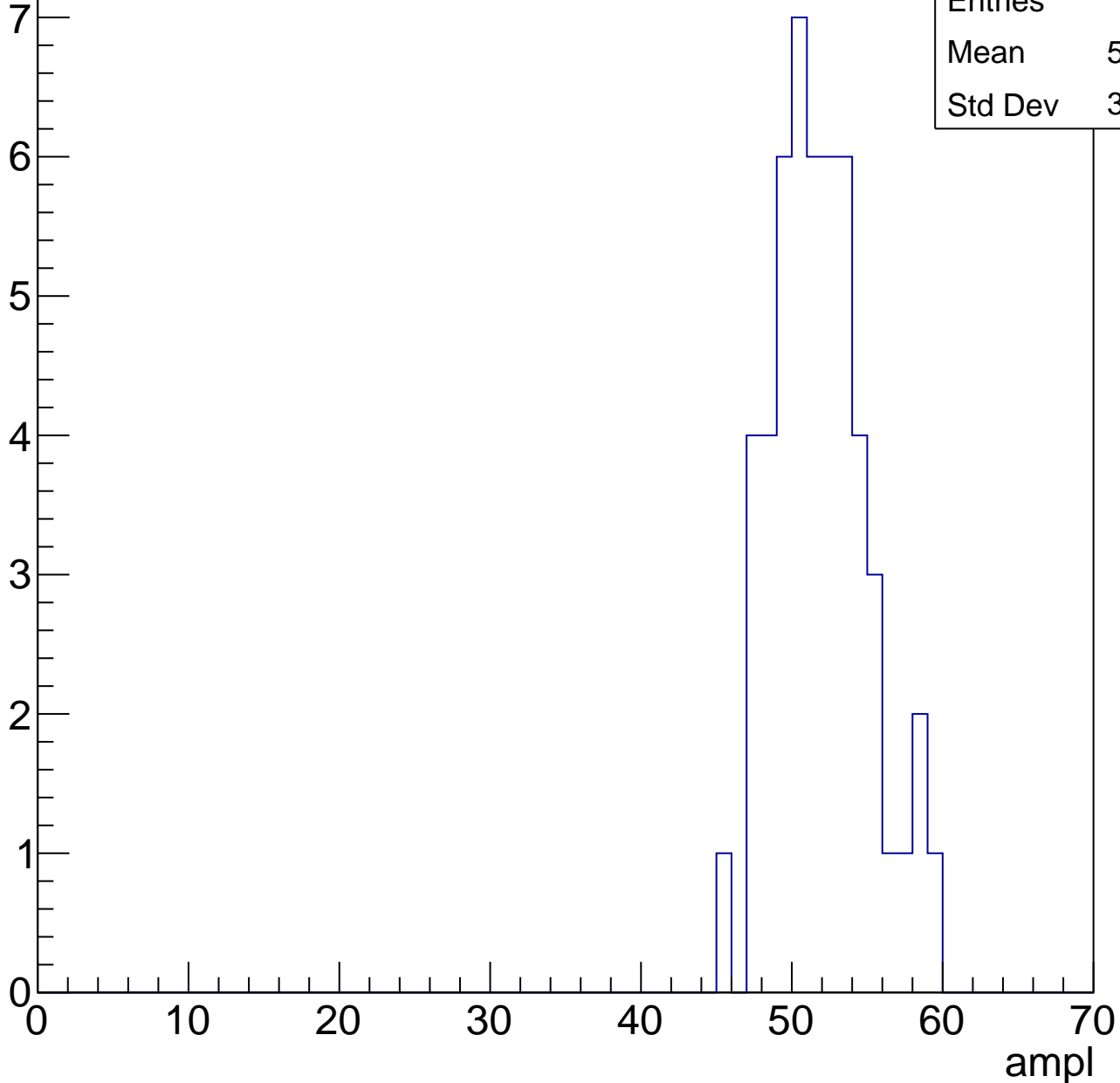


B0L001S, U11-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	51.42
Std Dev	3.084

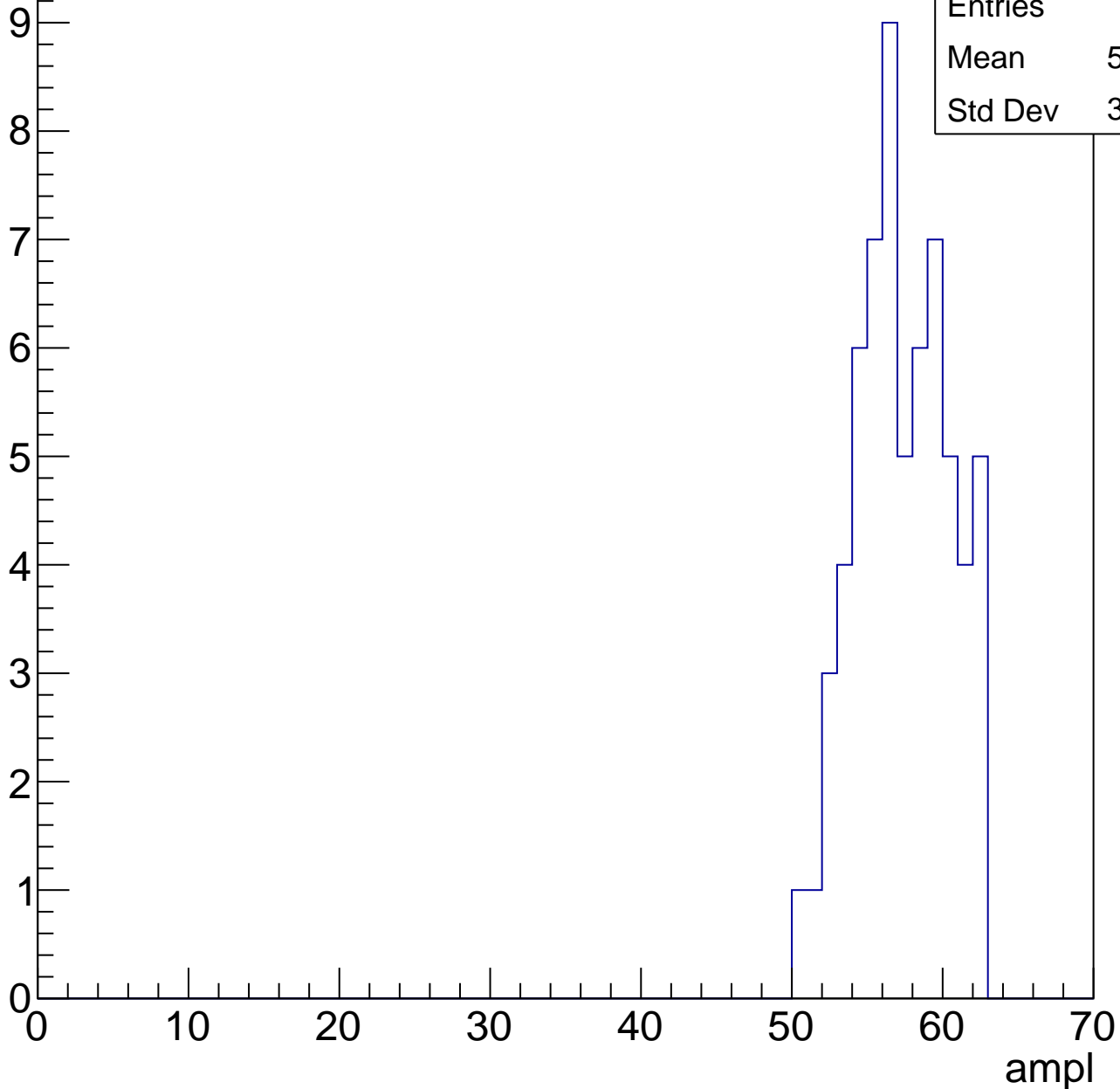


B0L001S, U11-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	56.86
Std Dev	3.039

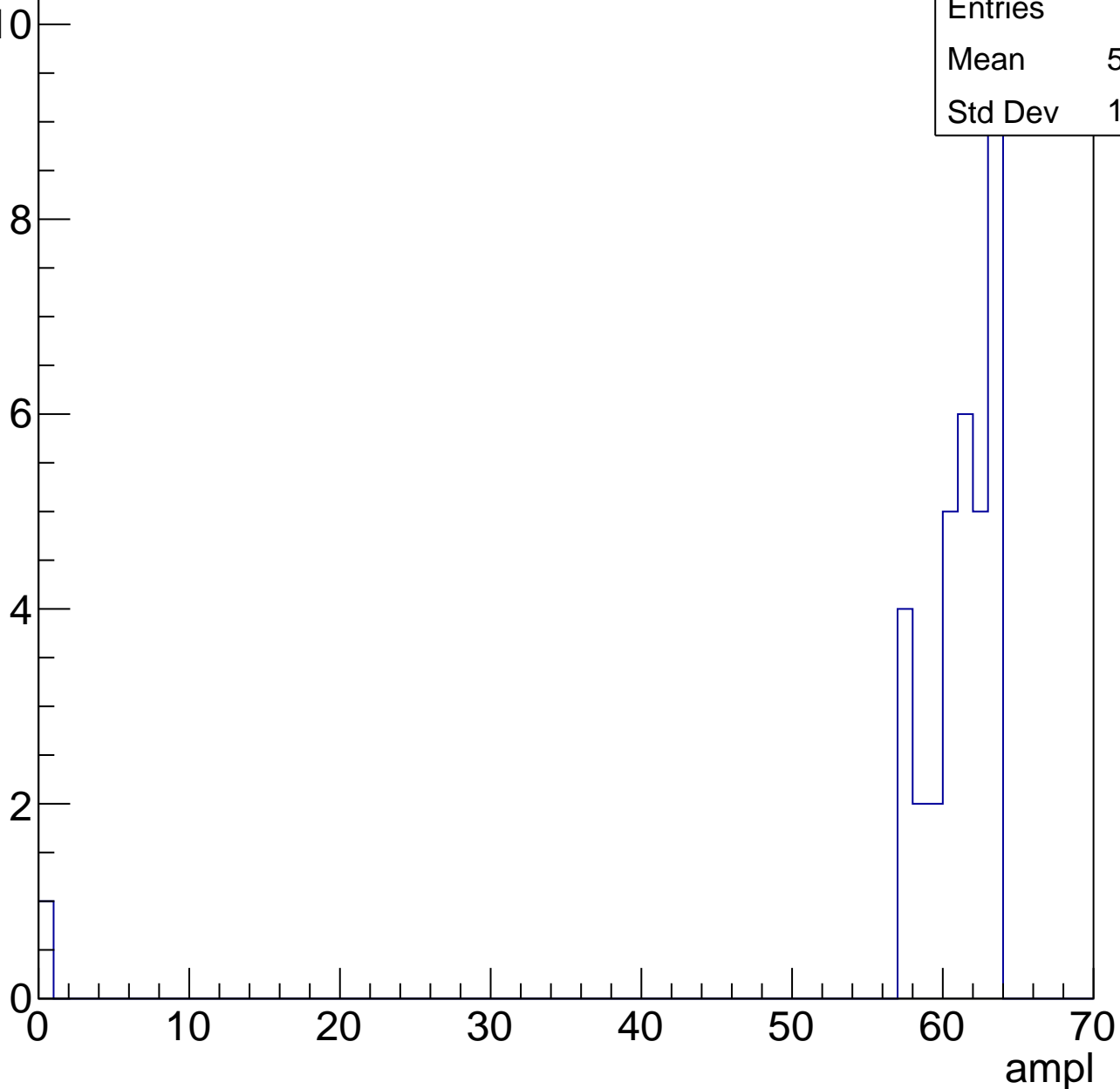


B0L001S, U11-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	59.09
Std Dev	10.33



B0L001S, U11-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch27, adc0

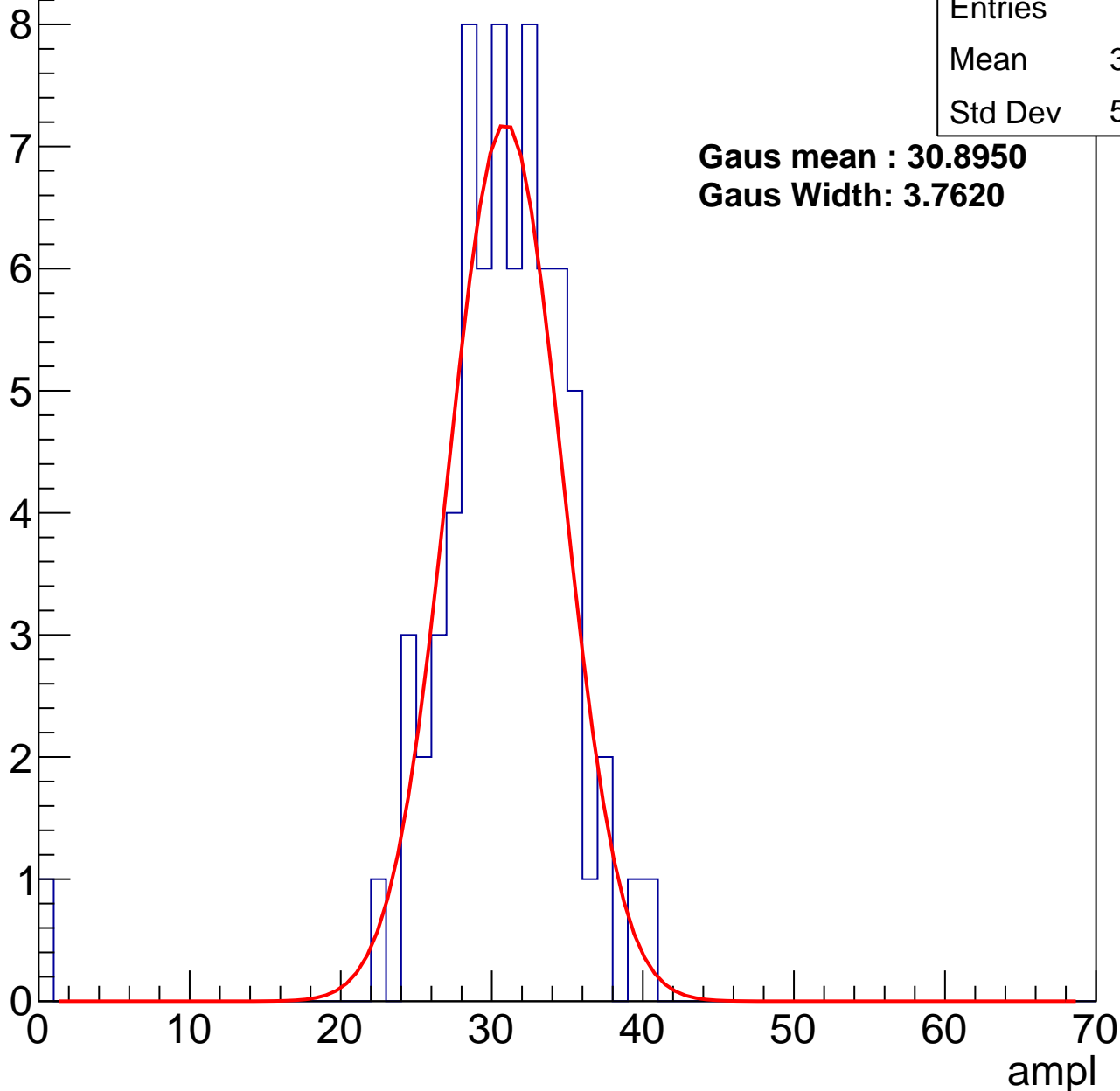
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	30.22
Std Dev	5.094

Gaus mean : 30.8950

Gaus Width: 3.7620



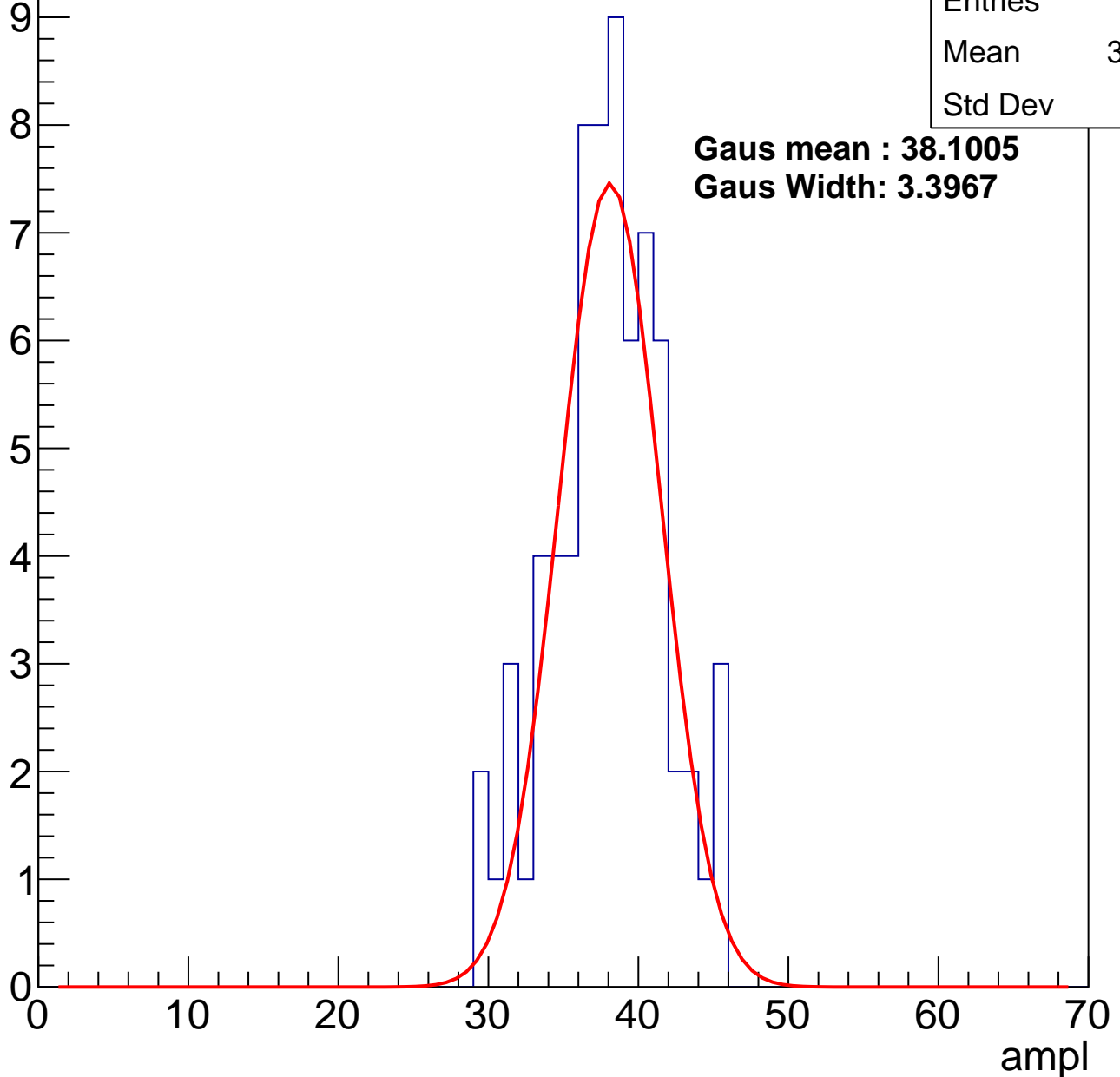
B0L001S, U11-ch27, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.41
Std Dev	3.71

Gaus mean : 38.1005
Gaus Width: 3.3967



B0L001S, U11-ch27, adc2

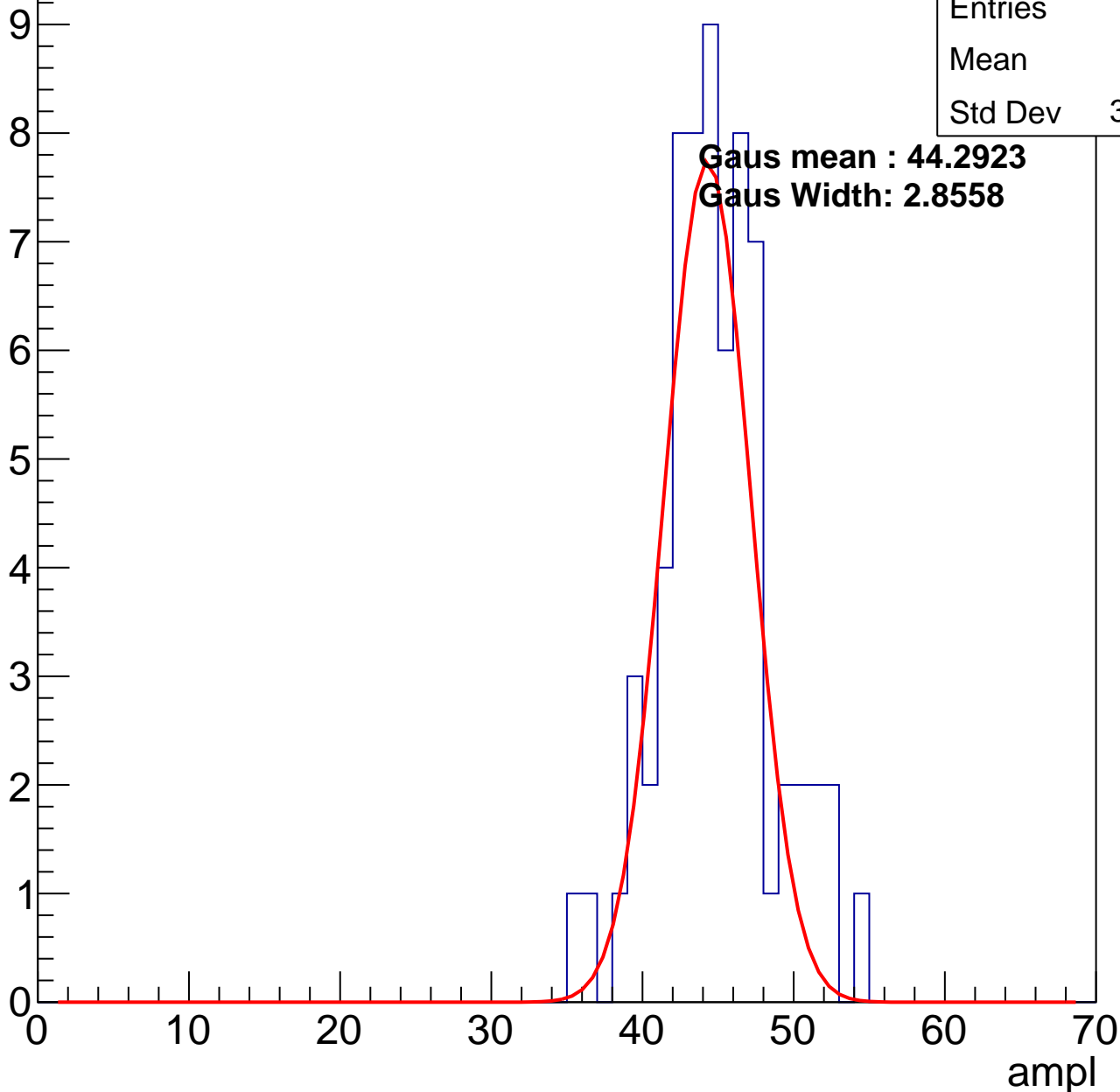
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.4
Std Dev	3.675

Gaus mean : 44.2923

Gaus Width: 2.8558

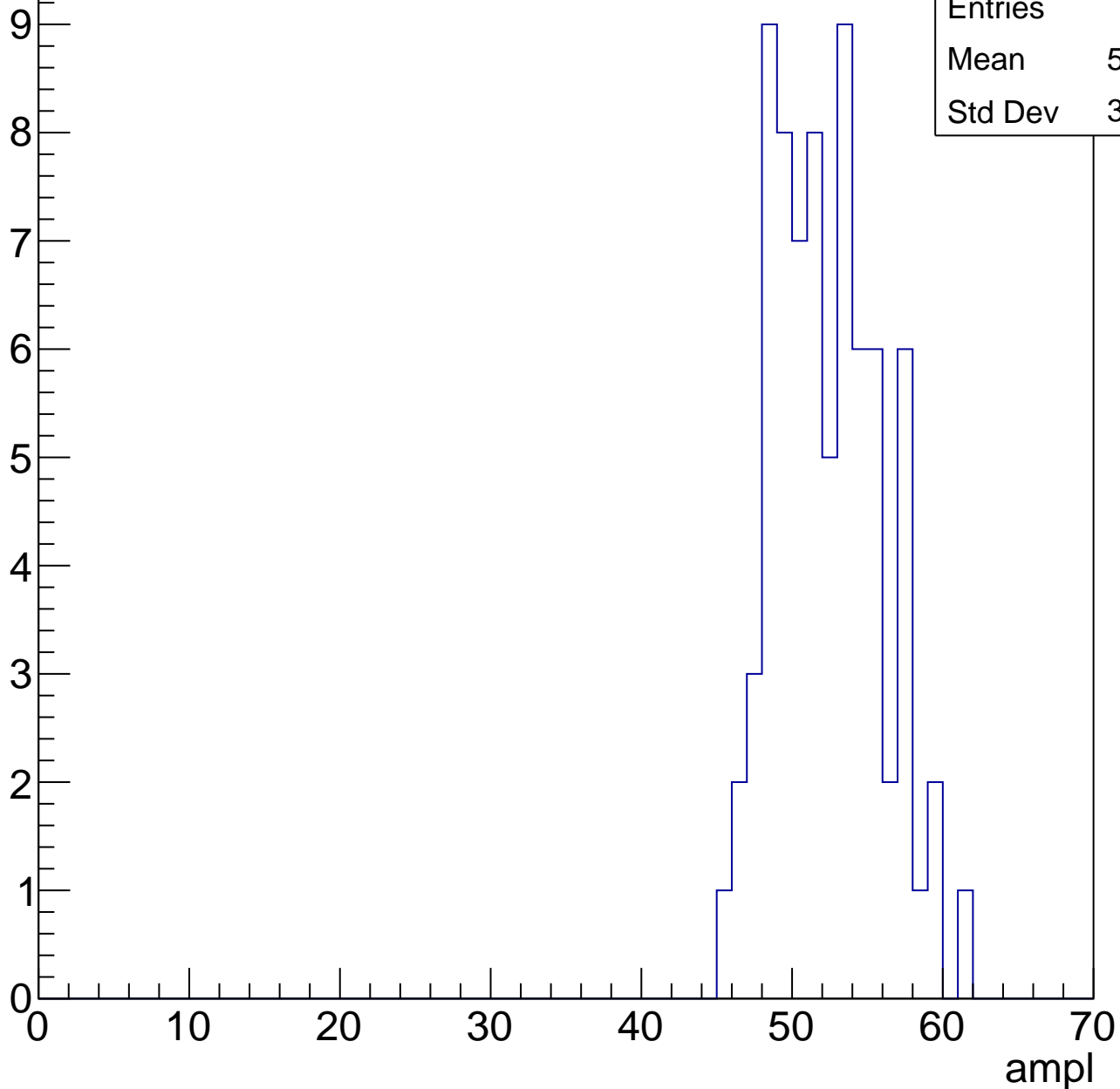


B0L001S, U11-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.87
Std Dev	3.499

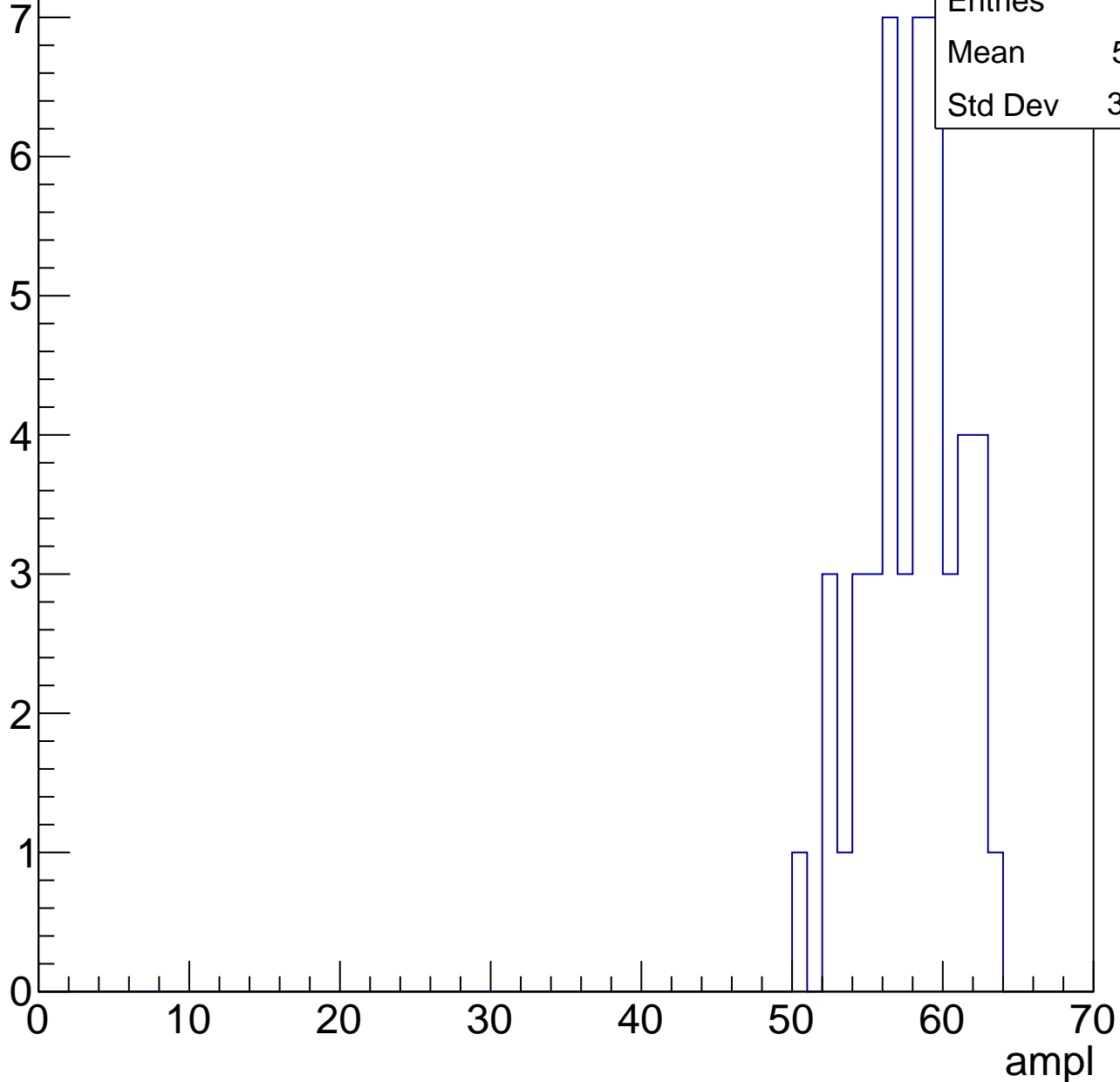


B0L001S, U11-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

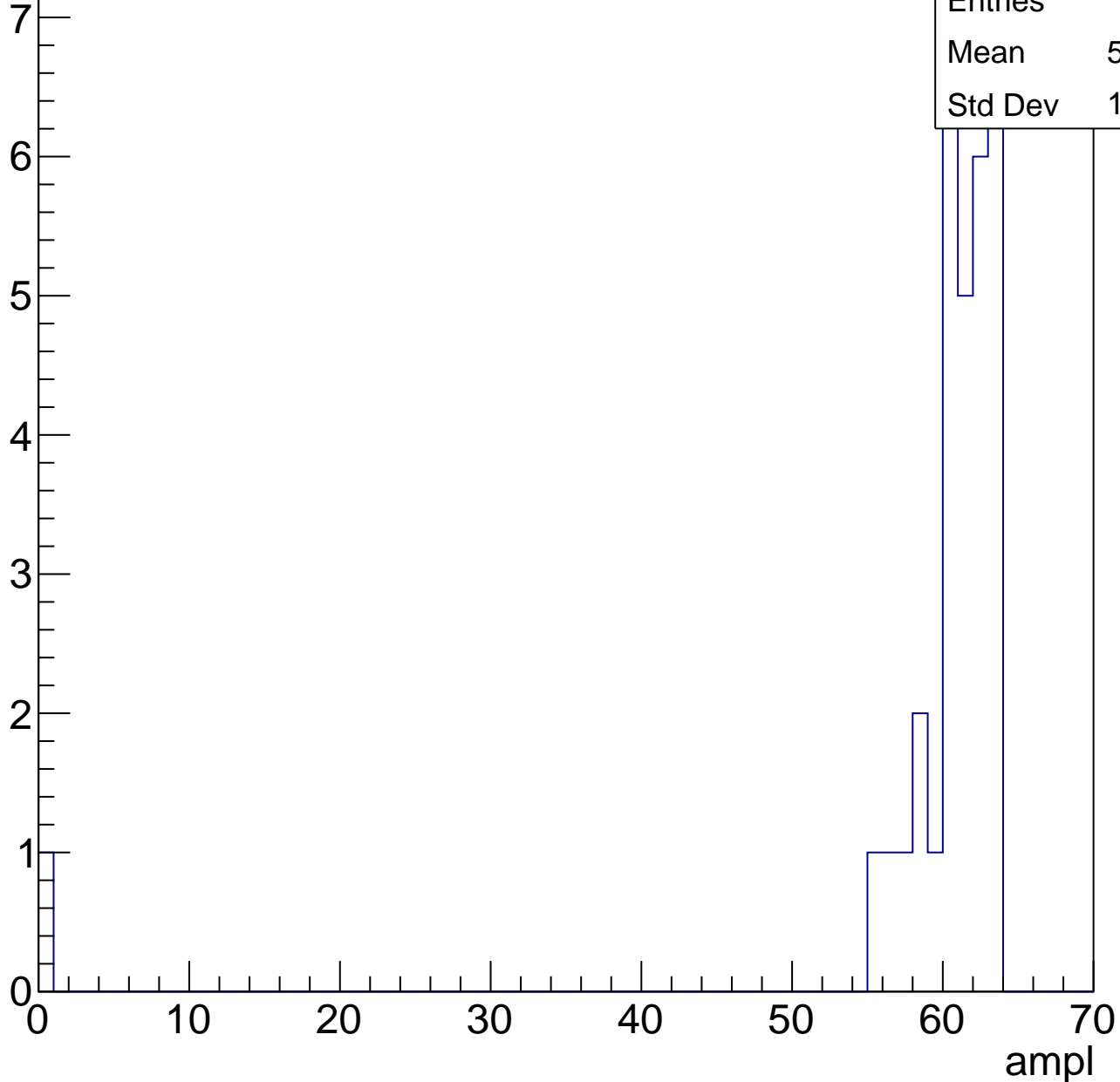
Entries	47
Mean	57.51
Std Dev	3.045



B0L001S, U11-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch28, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	29.97
Std Dev	7.109

Gaus mean : 31.6601

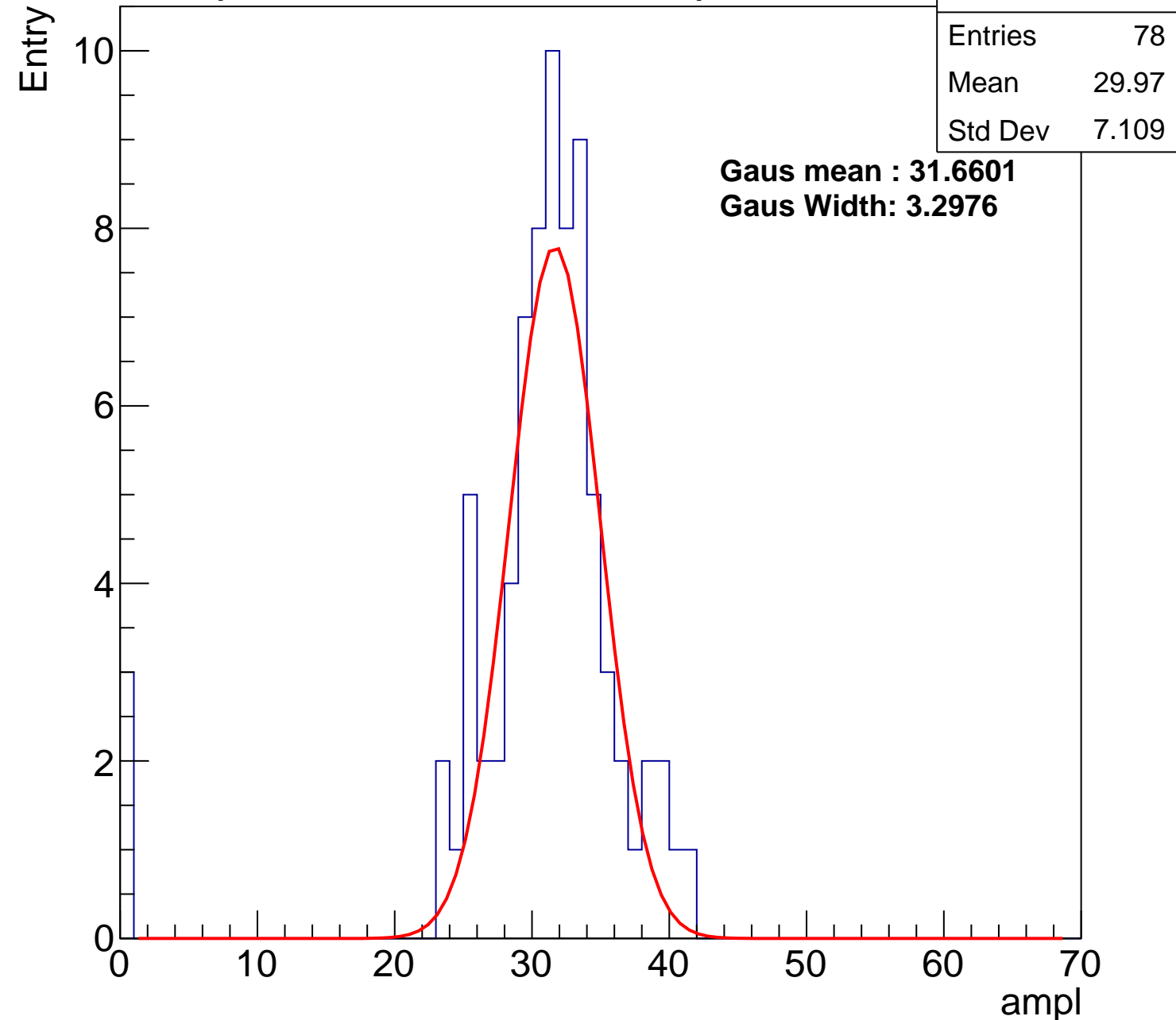
Gaus Width: 3.2976

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



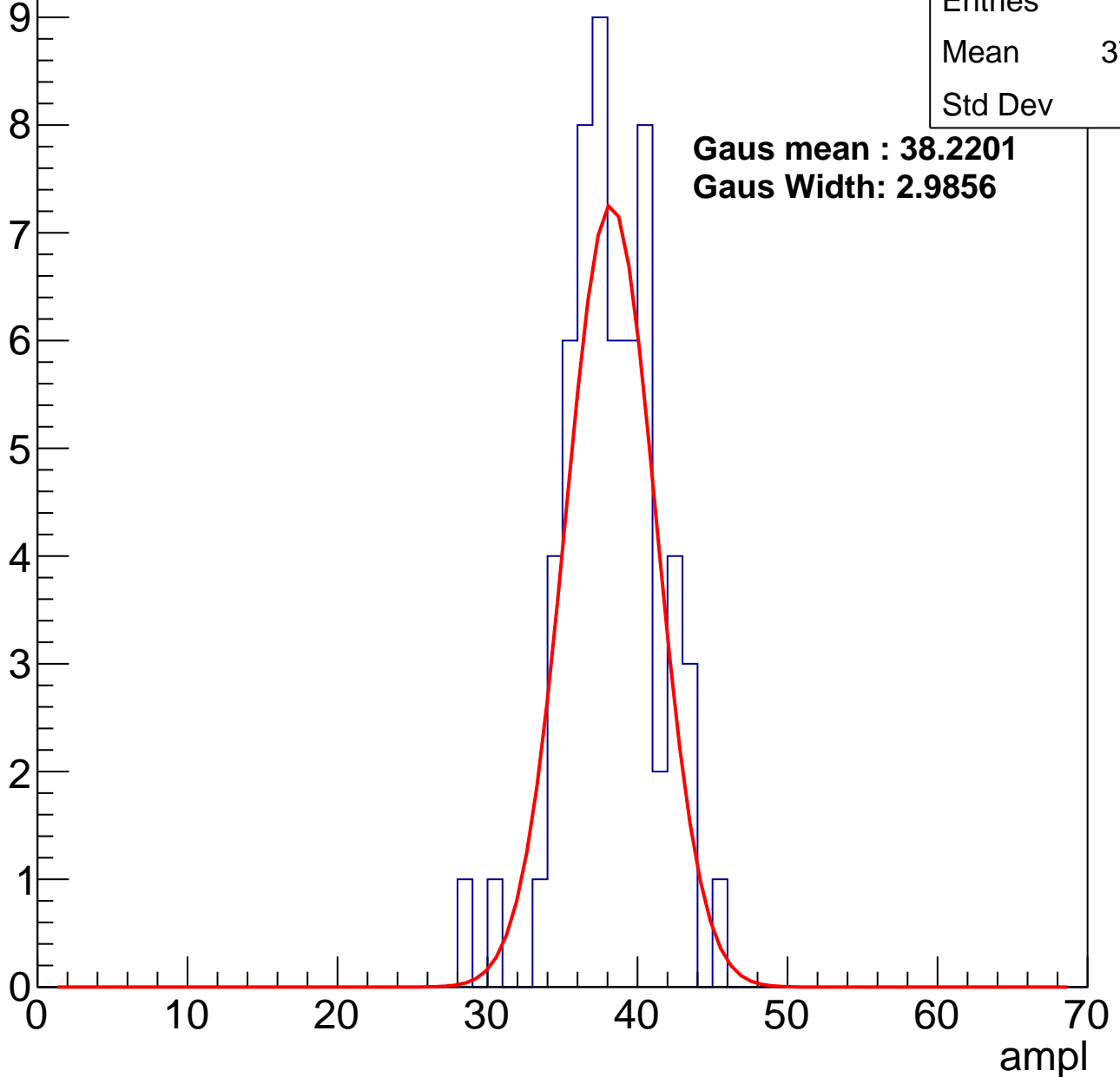
B0L001S, U11-ch28, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.73
Std Dev	3.13

Gaus mean : 38.2201
Gaus Width: 2.9856



B0L001S, U11-ch28, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	45.12
Std Dev	3.337

Gaus mean : 45.2509

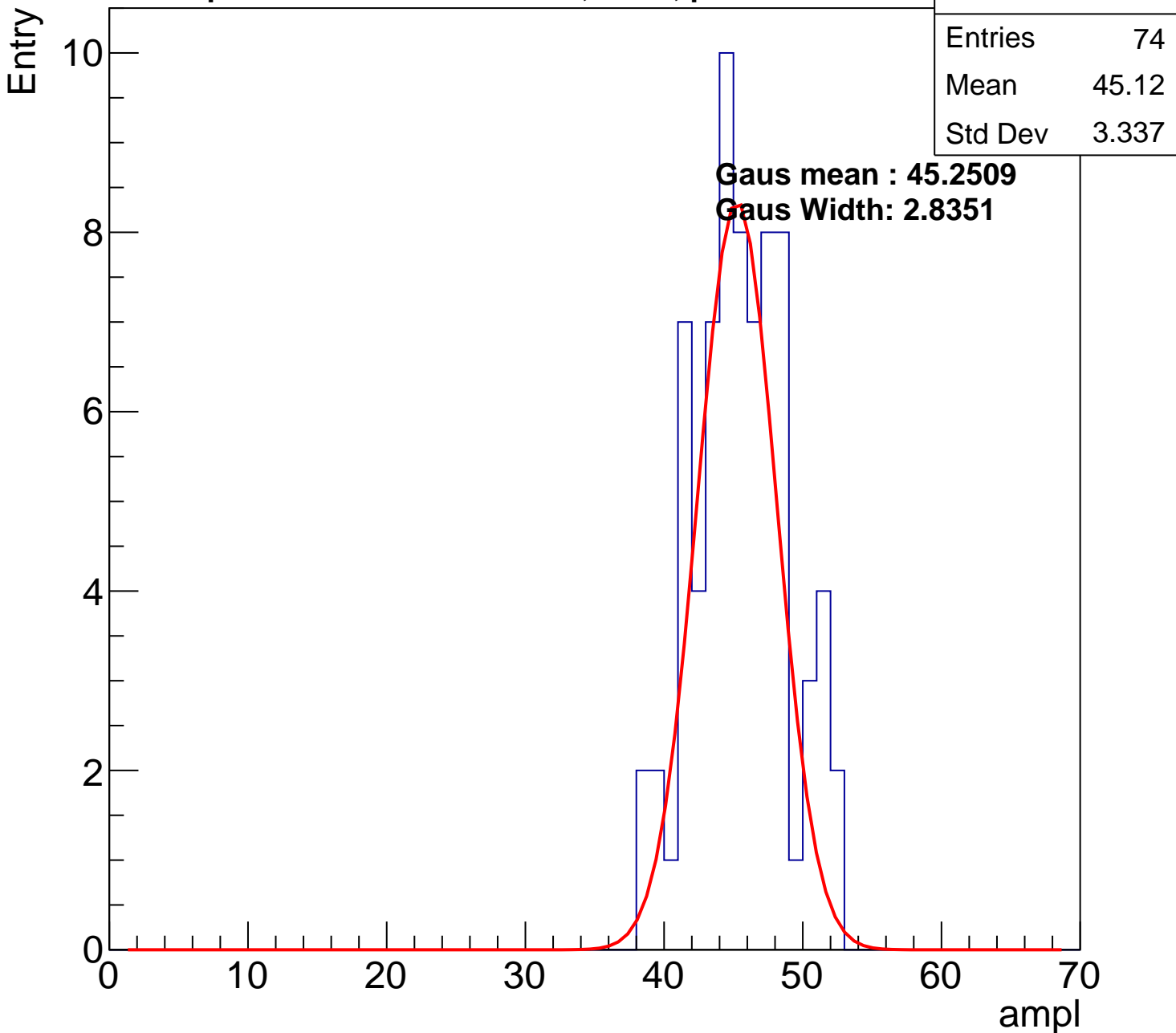
Gaus Width: 2.8351

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

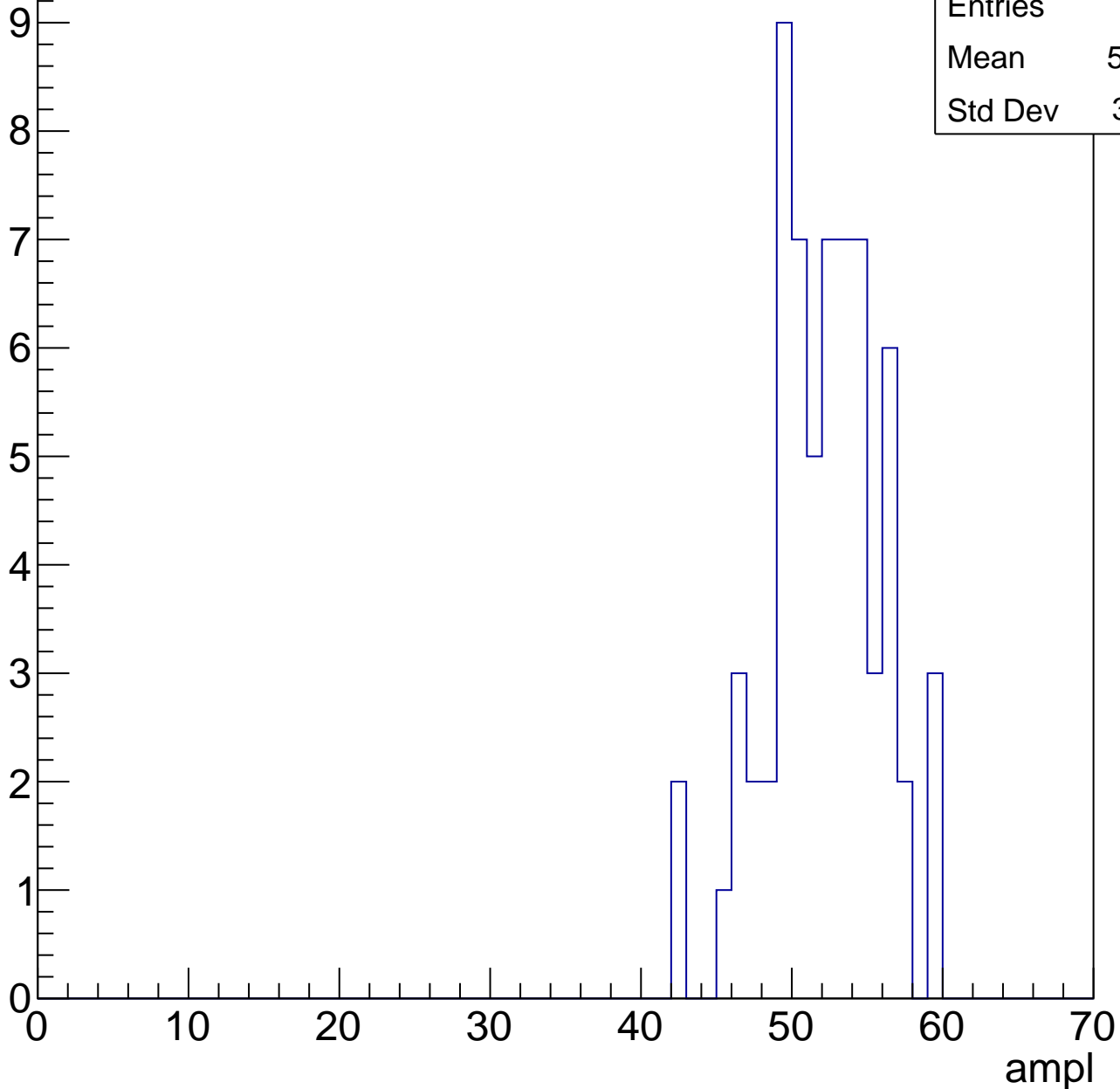


B0L001S, U11-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.64
Std Dev	3.691

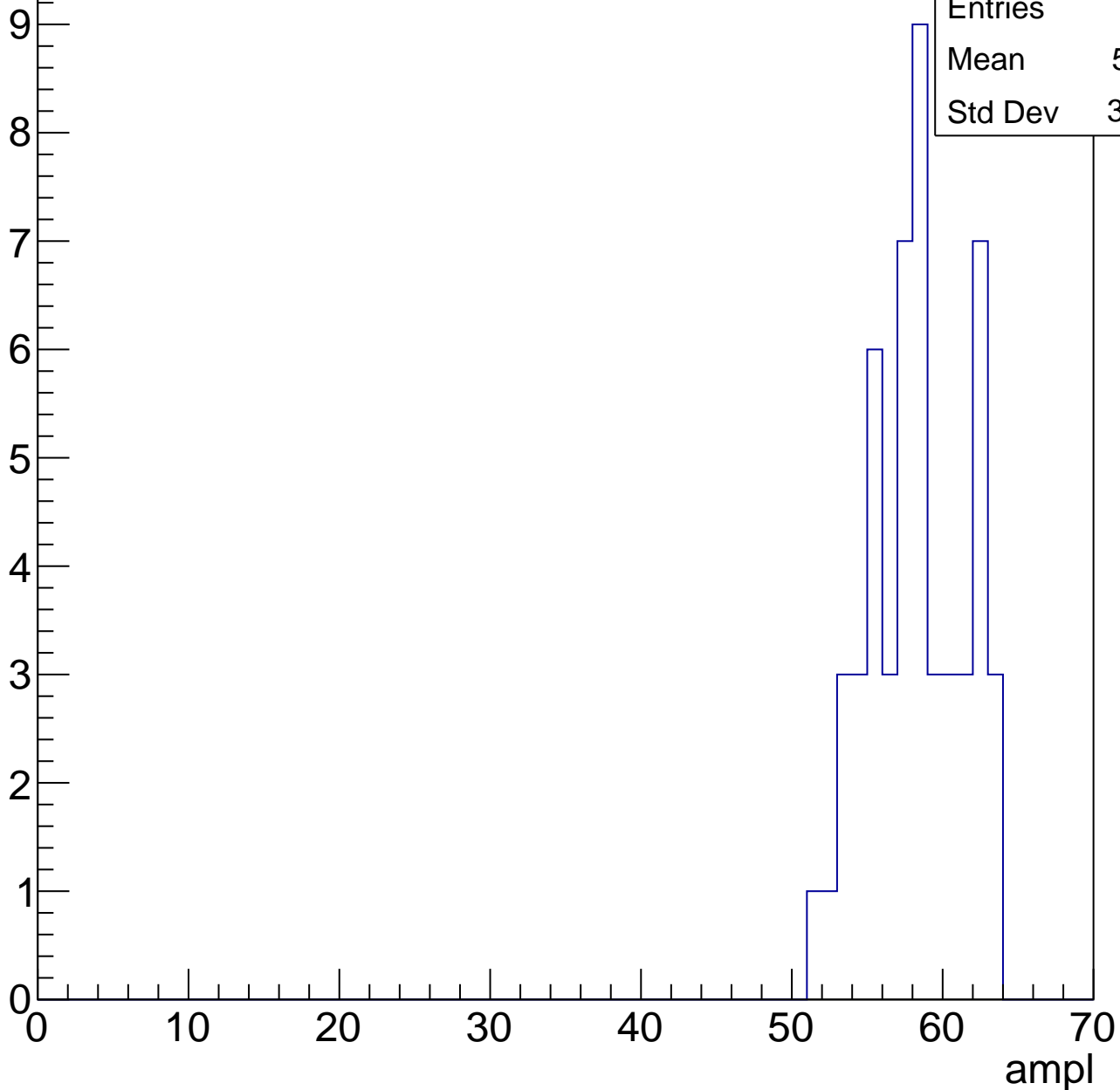


B0L001S, U11-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

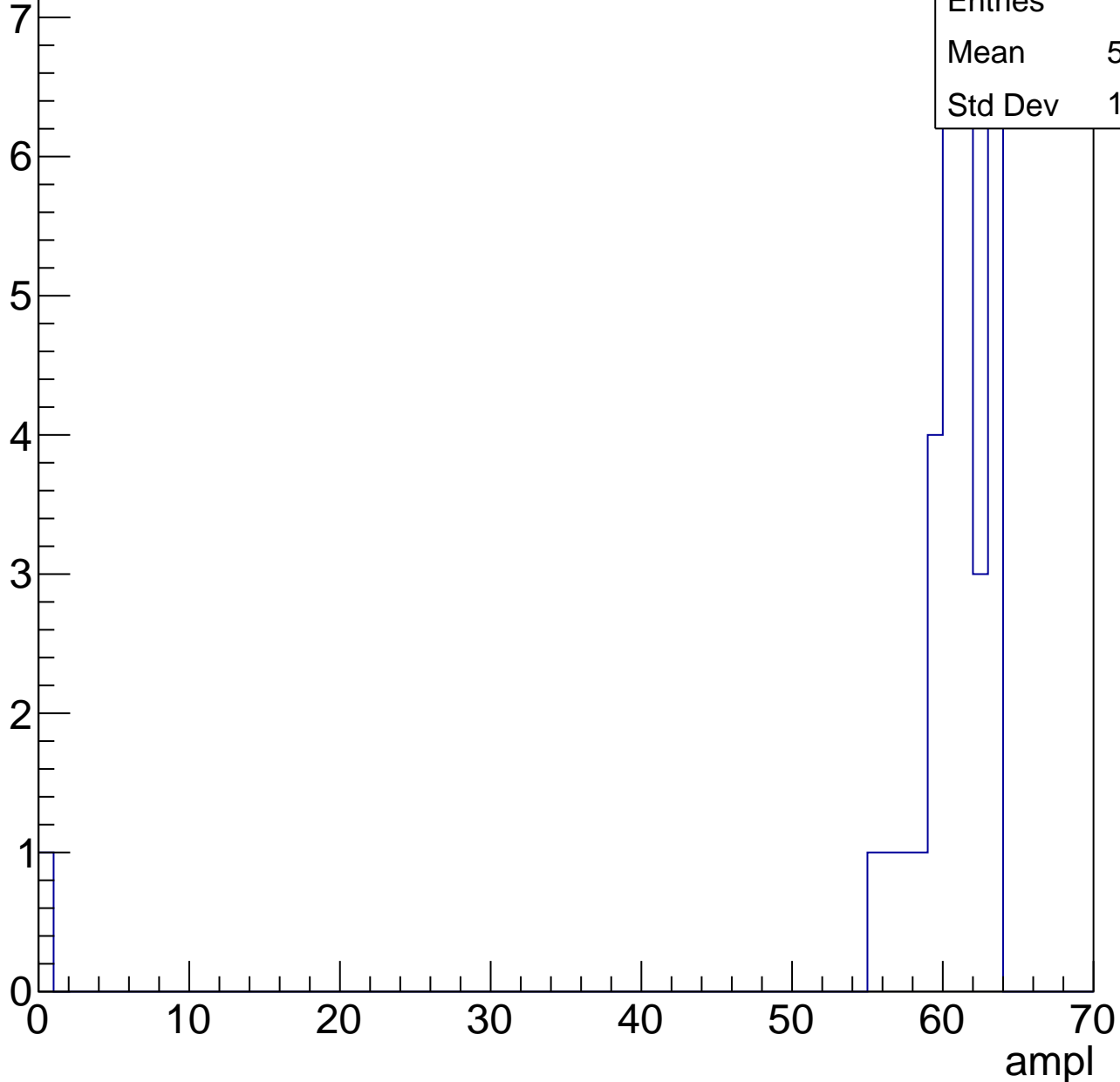
Entries	52
Mean	57.81
Std Dev	3.126



B0L001S, U11-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch29, adc0

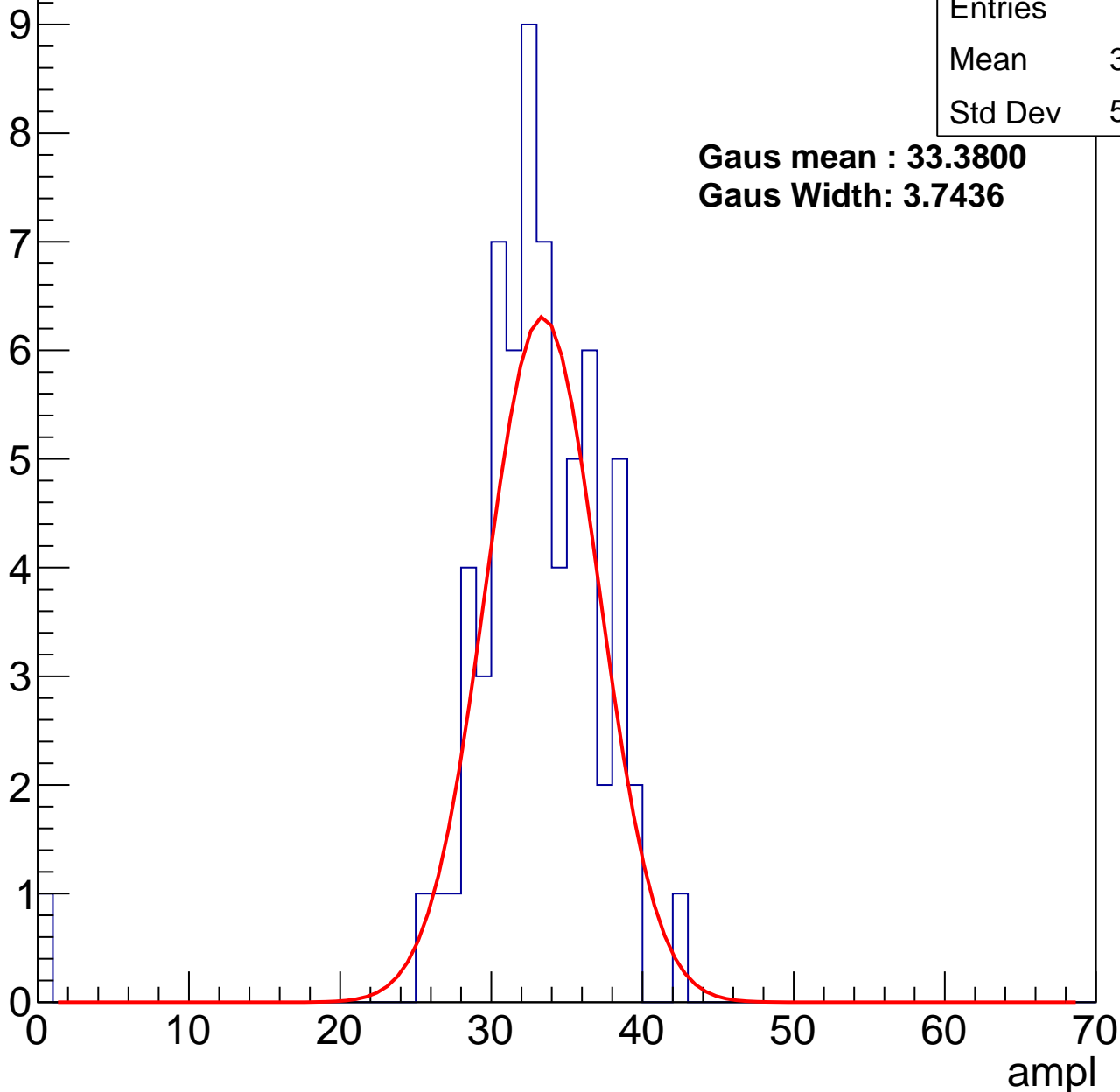
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.35
Std Dev	5.325

Gaus mean : 33.3800

Gaus Width: 3.7436



B0L001S, U11-ch29, adc1

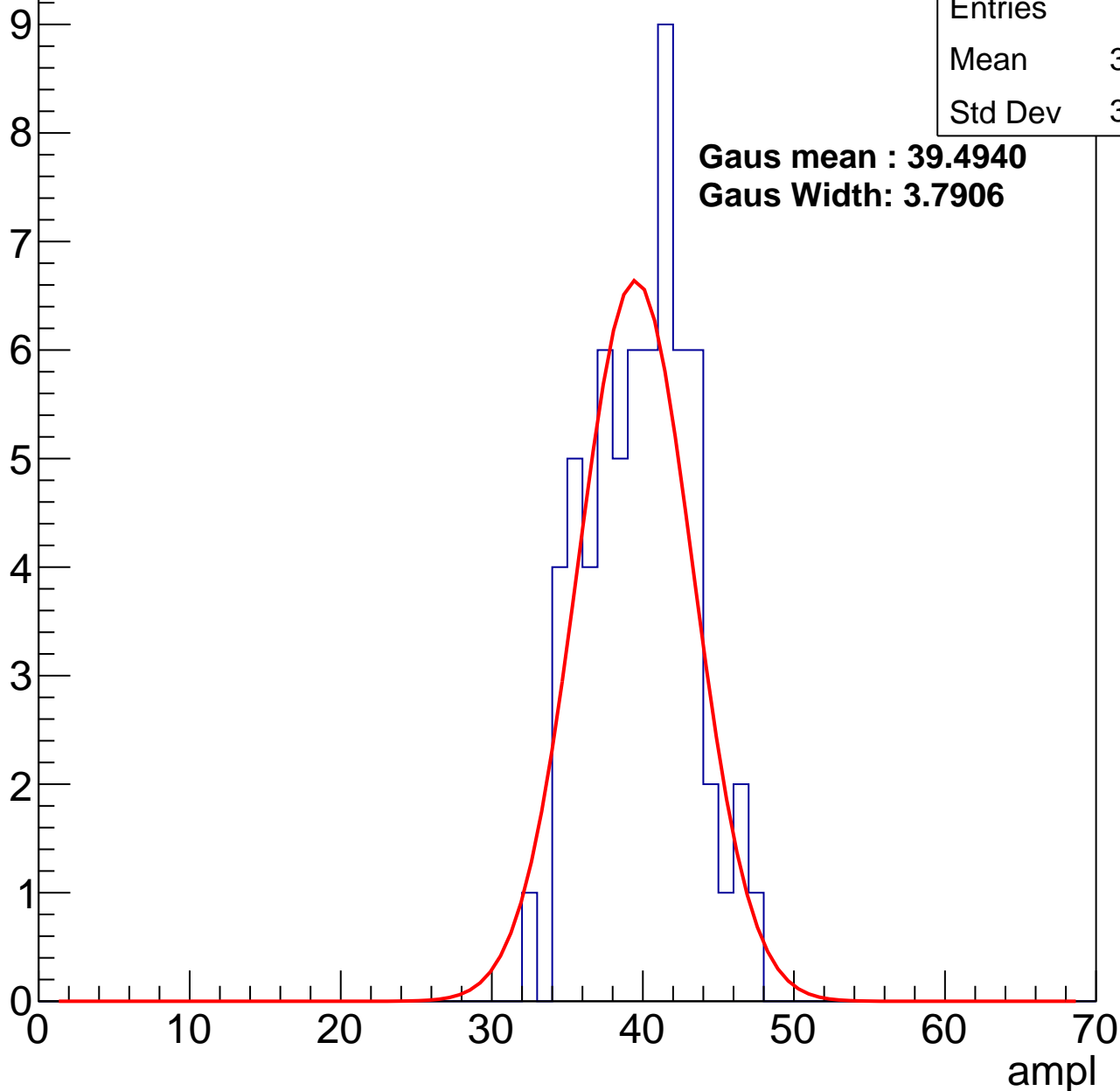
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	39.44
Std Dev	3.358

Gaus mean : 39.4940

Gaus Width: 3.7906



B0L001S, U11-ch29, adc2

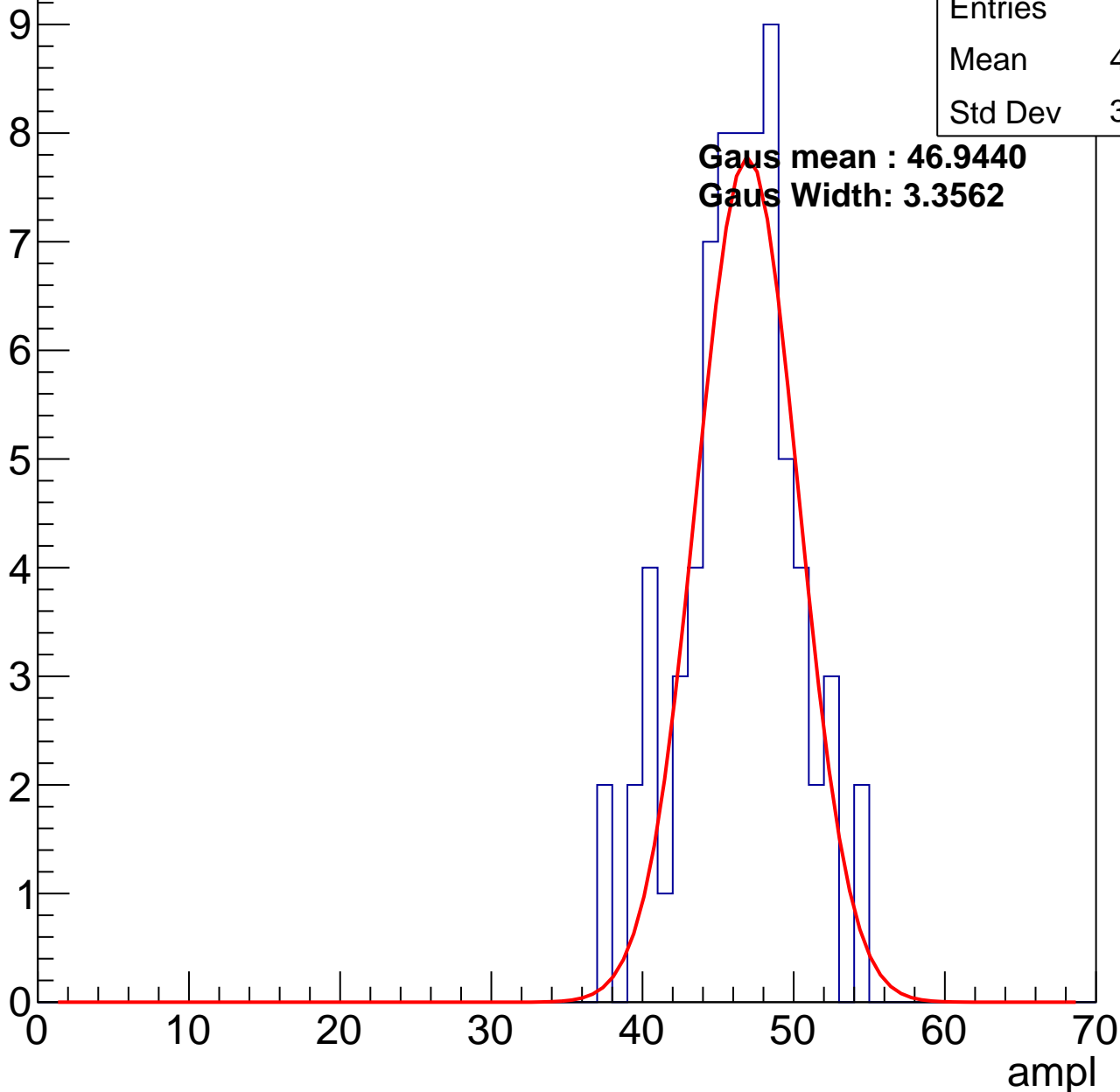
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	45.92
Std Dev	3.703

Gaus mean : 46.9440

Gaus Width: 3.3562

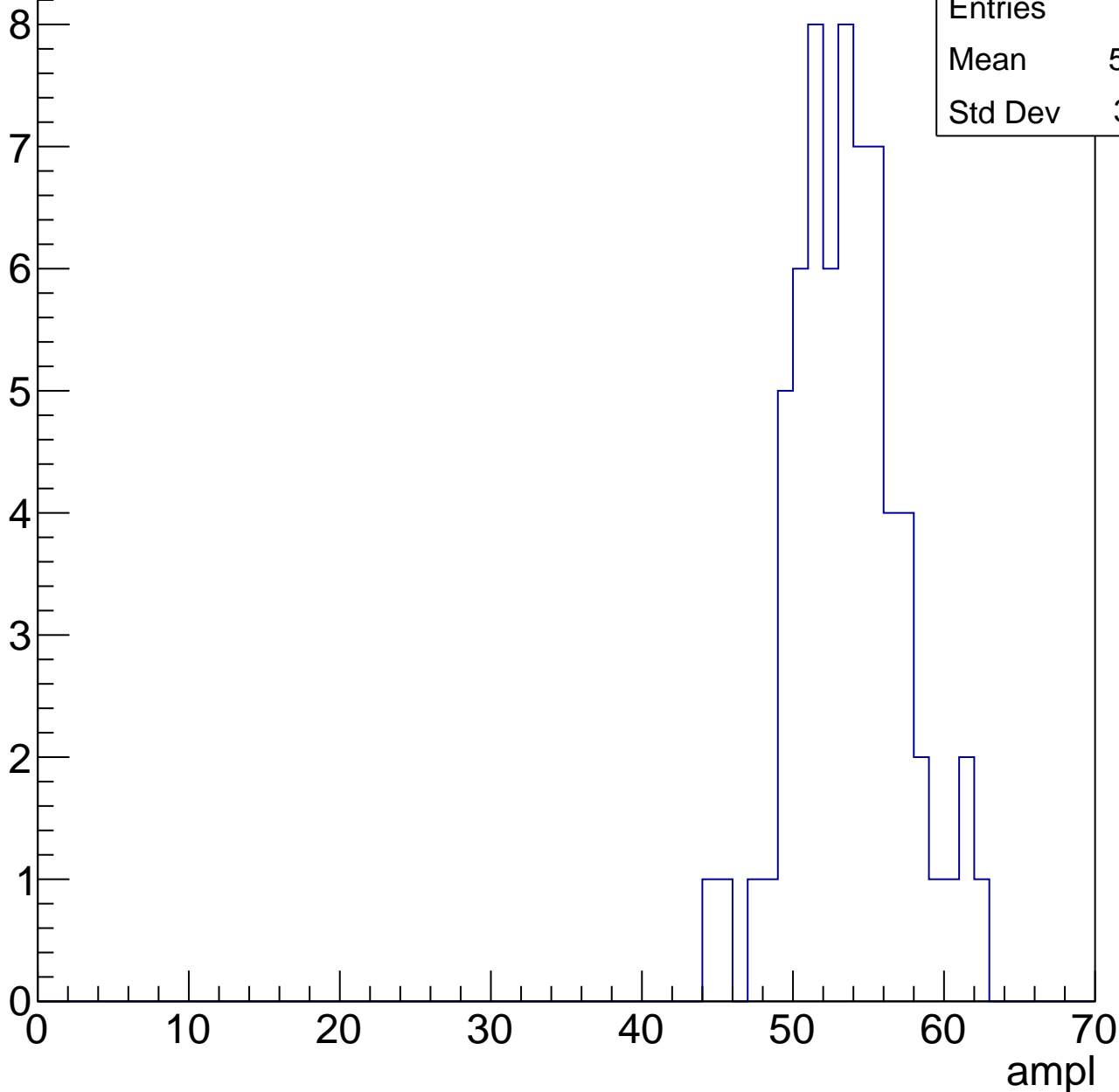


B0L001S, U11-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	53.14
Std Dev	3.601

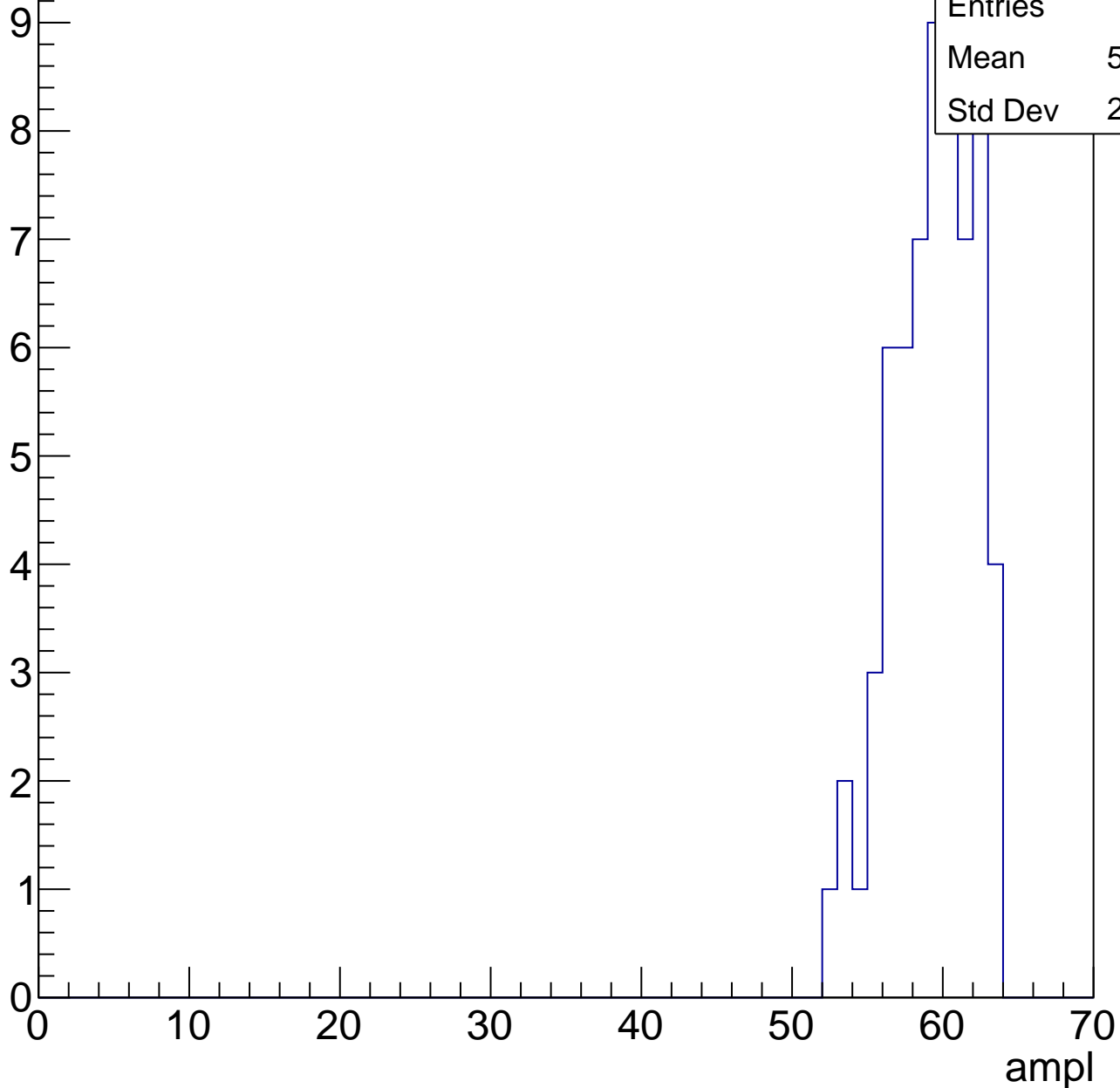


B0L001S, U11-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	58.84
Std Dev	2.674

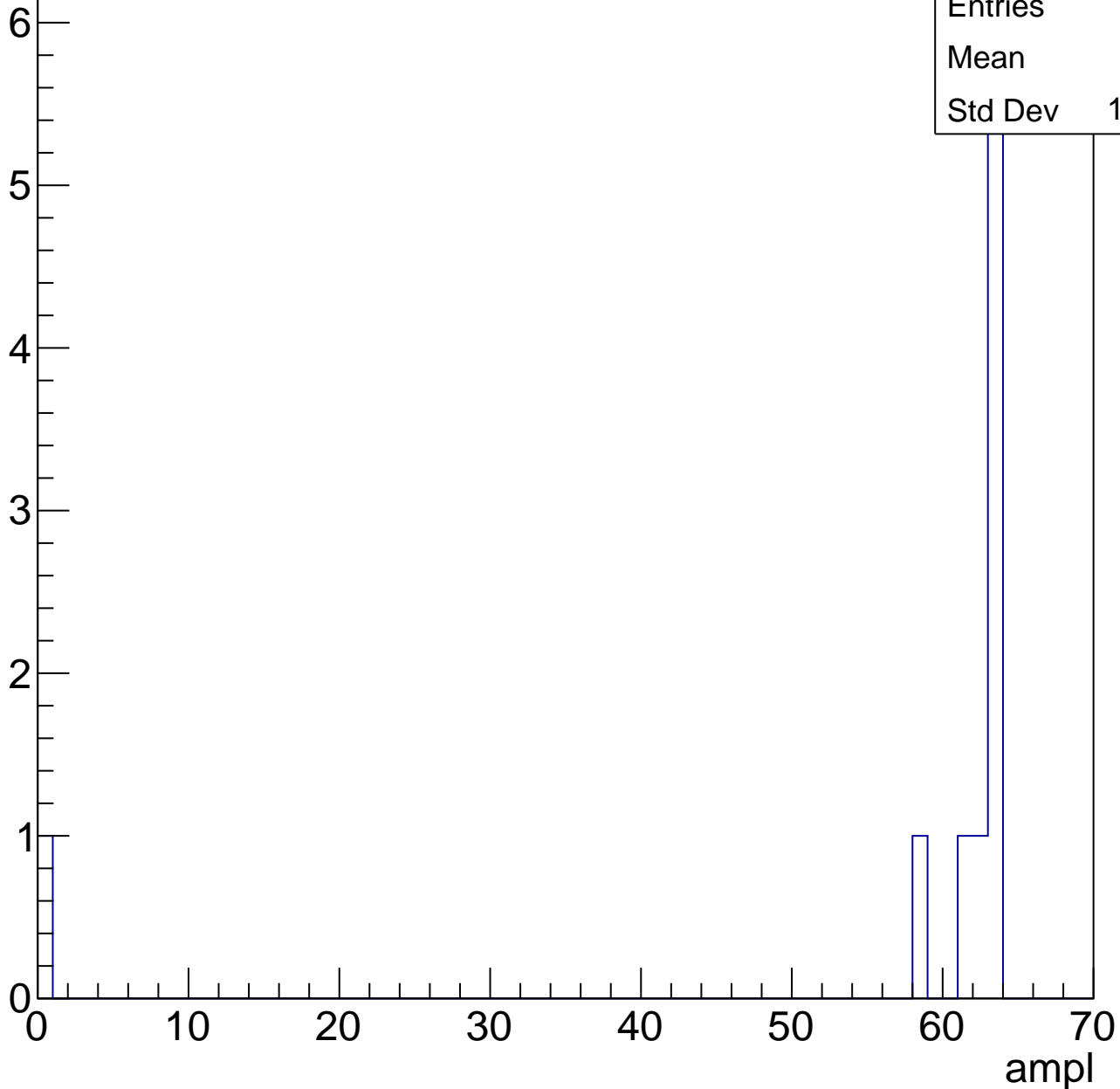


B0L001S, U11-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	10
Mean	55.9
Std Dev	18.69



B0L001S, U11-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U11-ch30, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	30.45
Std Dev	3.847

Gaus mean : 30.4830

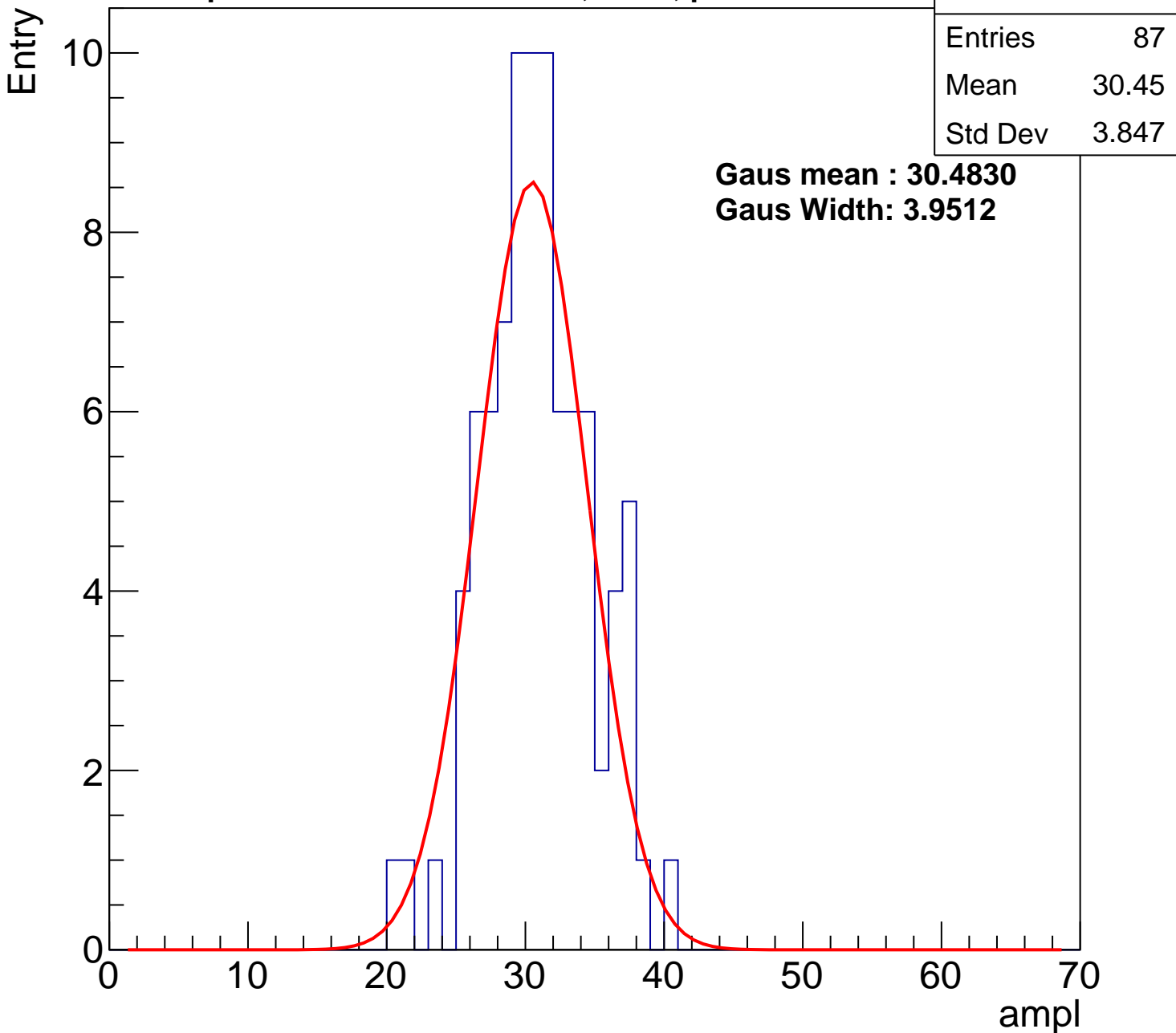
Gaus Width: 3.9512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch30, adc1

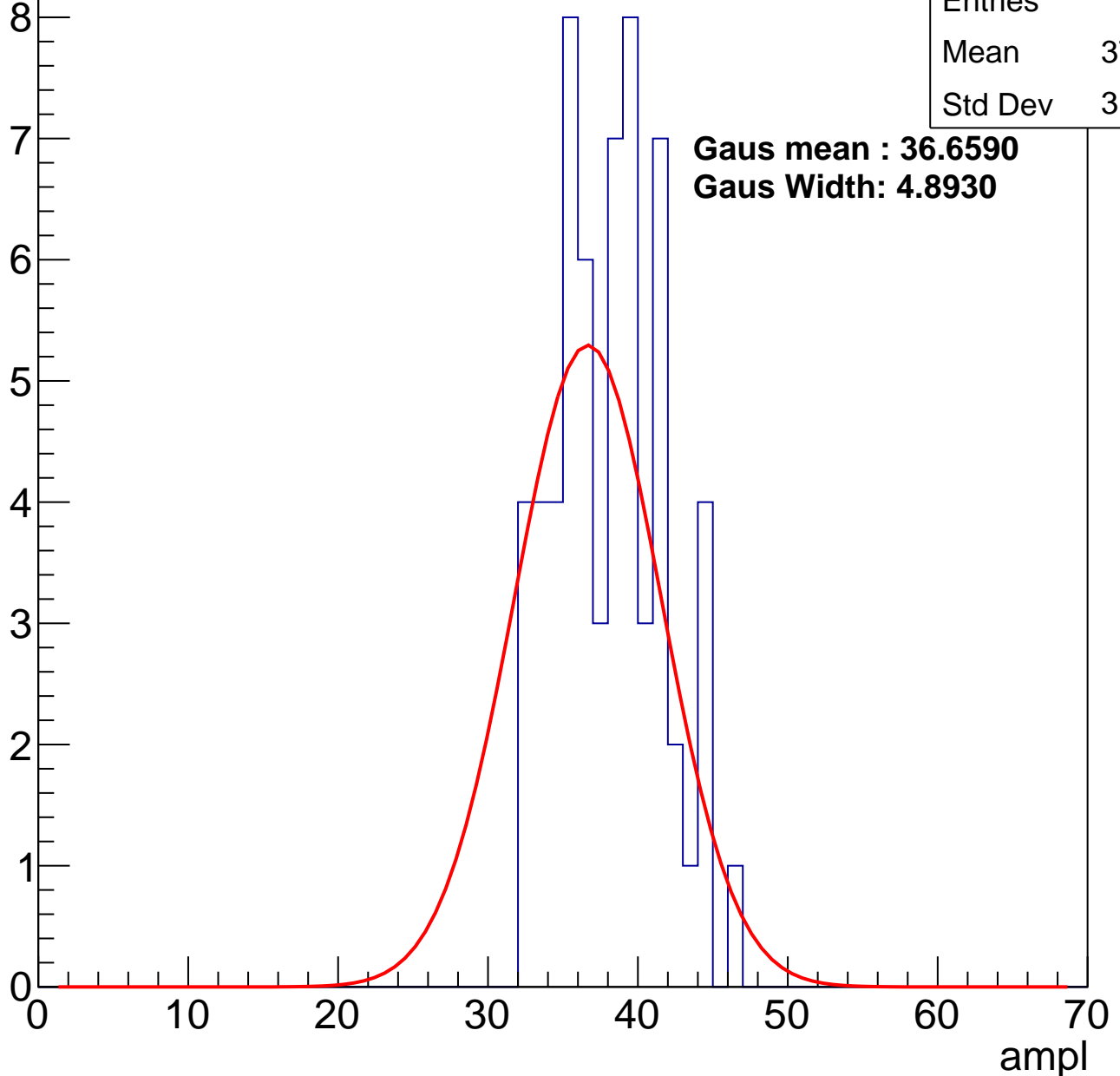
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.69
Std Dev	3.476

Gaus mean : 36.6590

Gaus Width: 4.8930



B0L001S, U11-ch30, adc2

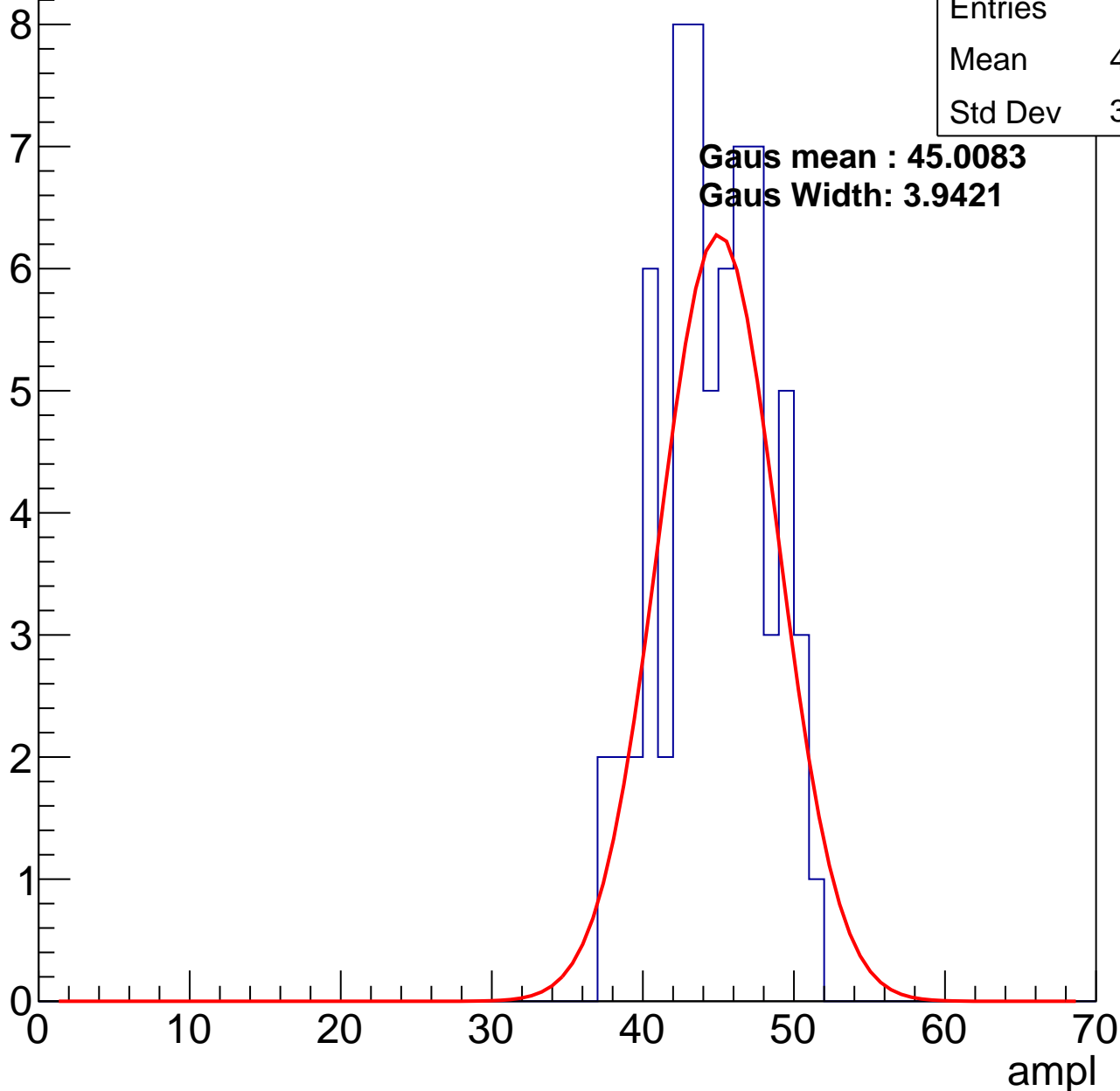
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.19
Std Dev	3.452

Gaus mean : 45.0083

Gaus Width: 3.9421

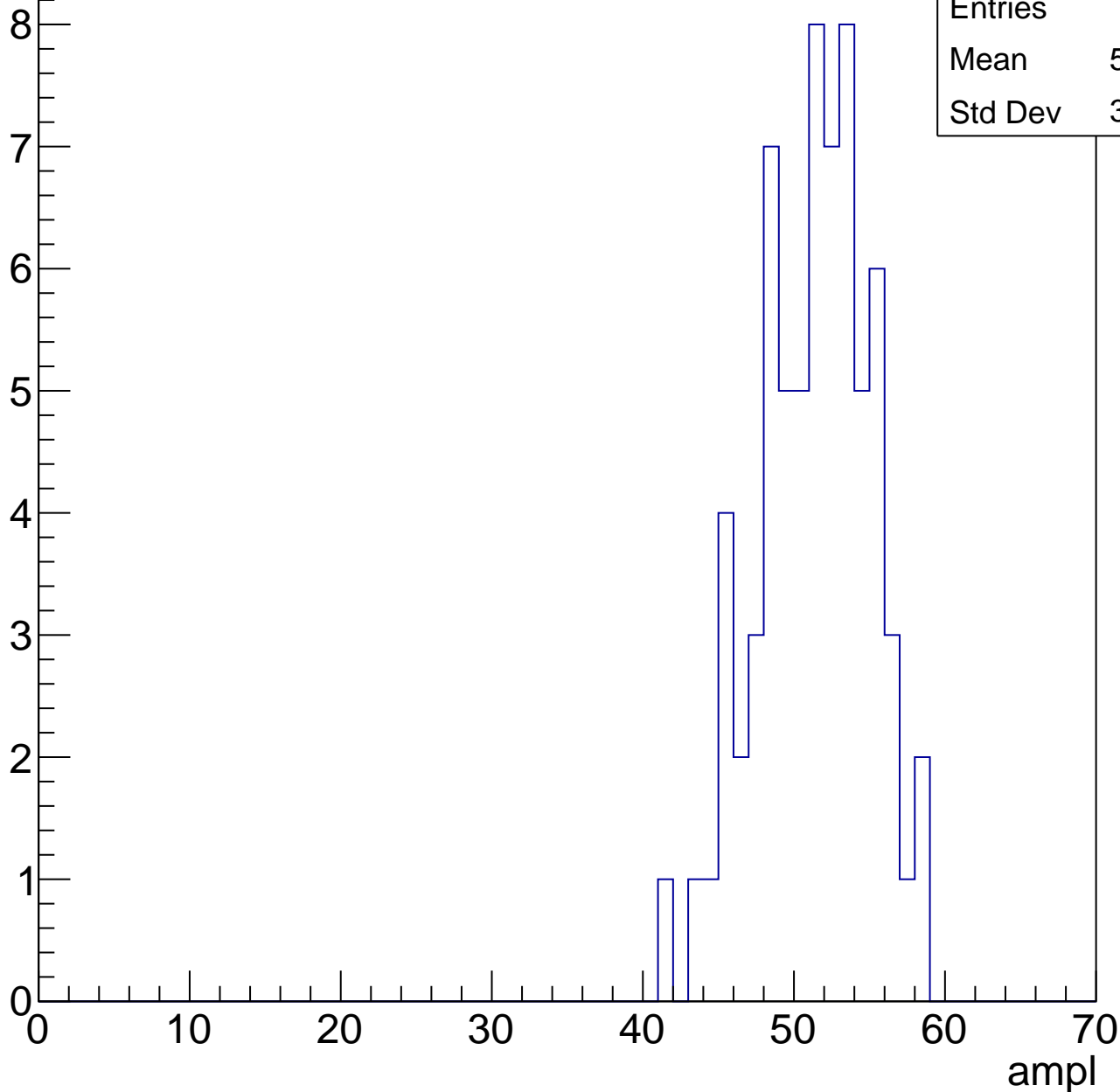


B0L001S, U11-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.86
Std Dev	3.672

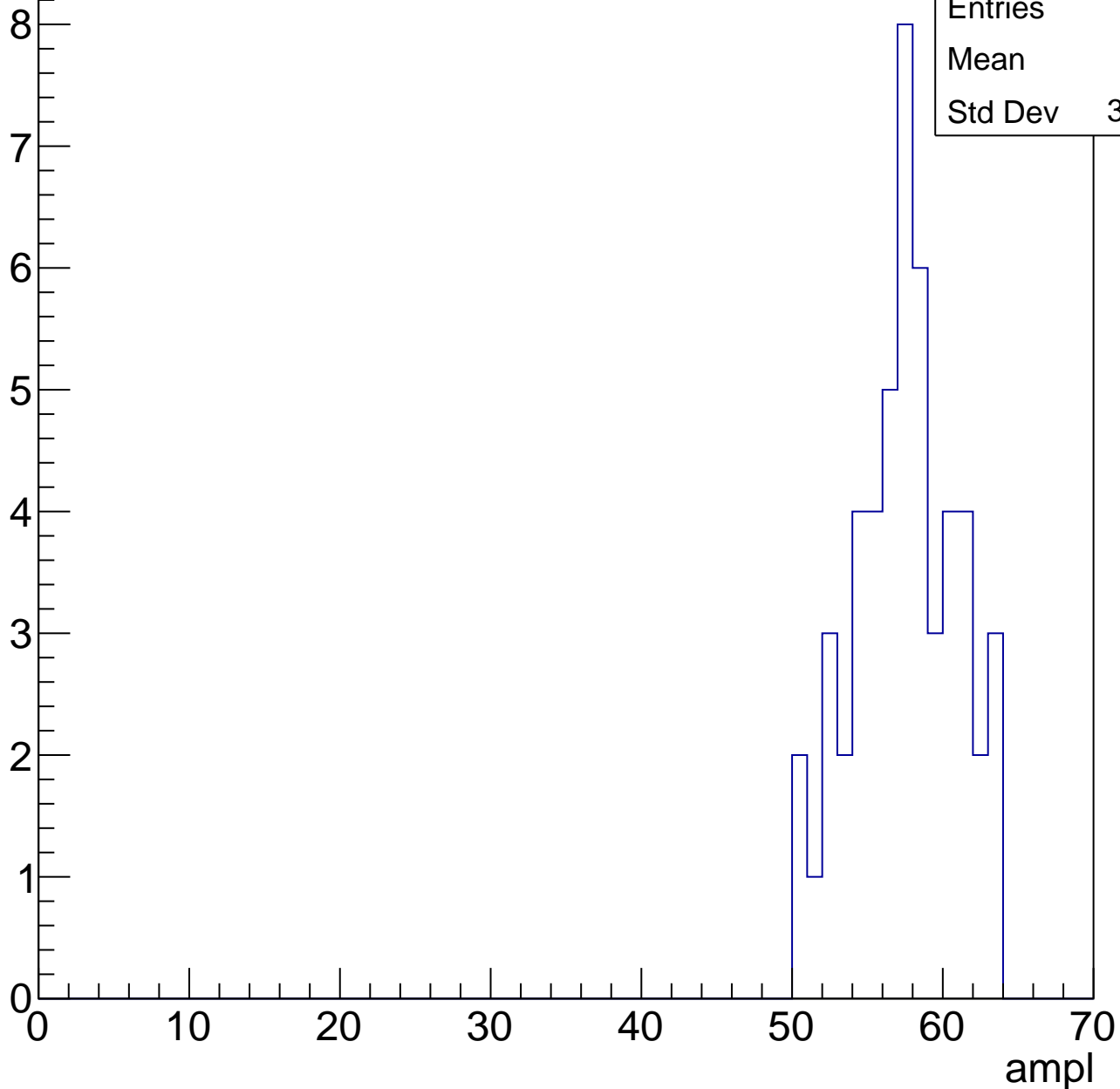


B0L001S, U11-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57
Std Dev	3.355



B0L001S, U11-ch30, adc5

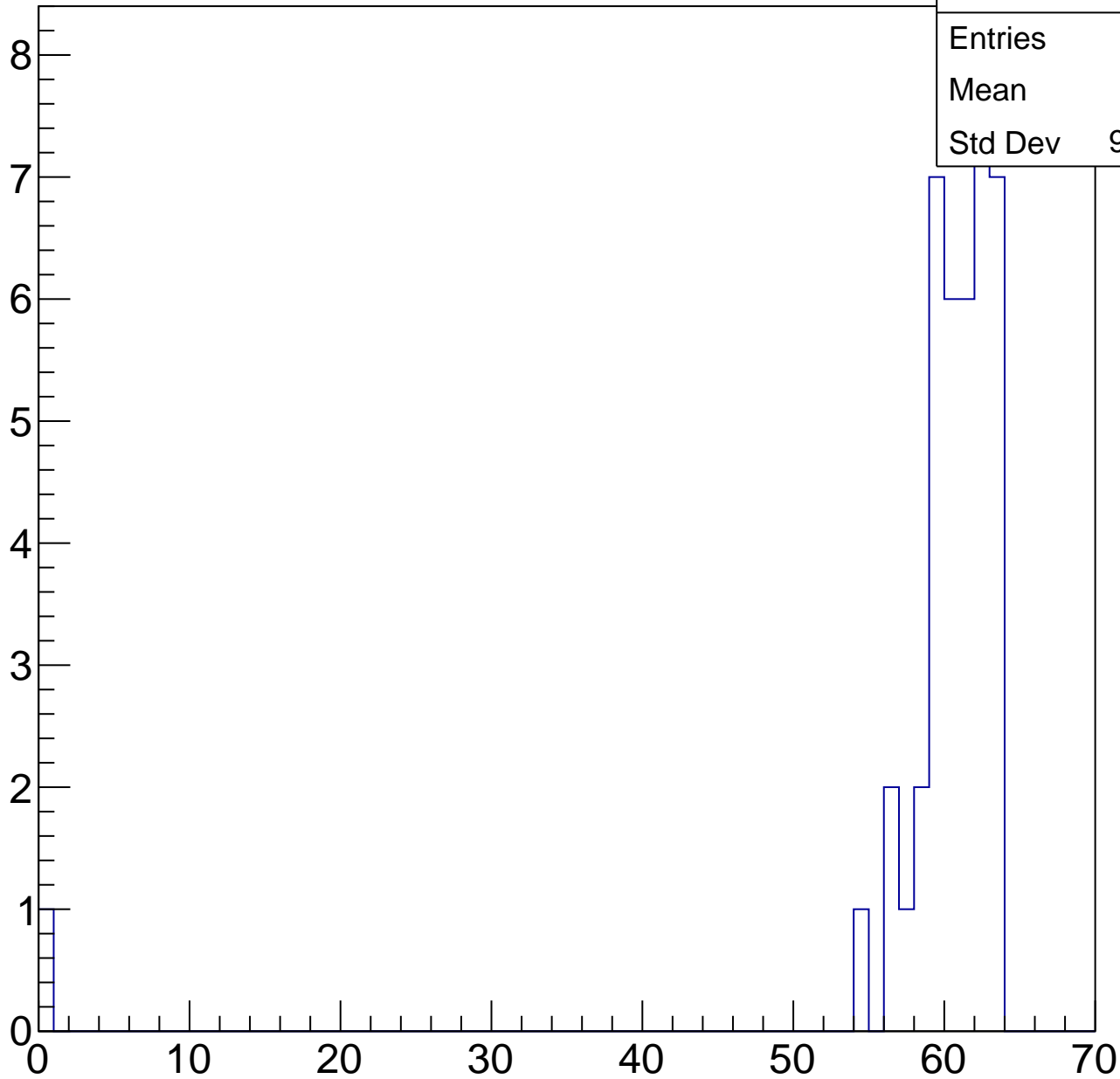
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.9
Std Dev	9.555

ampl



B0L001S, U11-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U11-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch31, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	32.85
Std Dev	3.324

Gaus mean : 33.1205

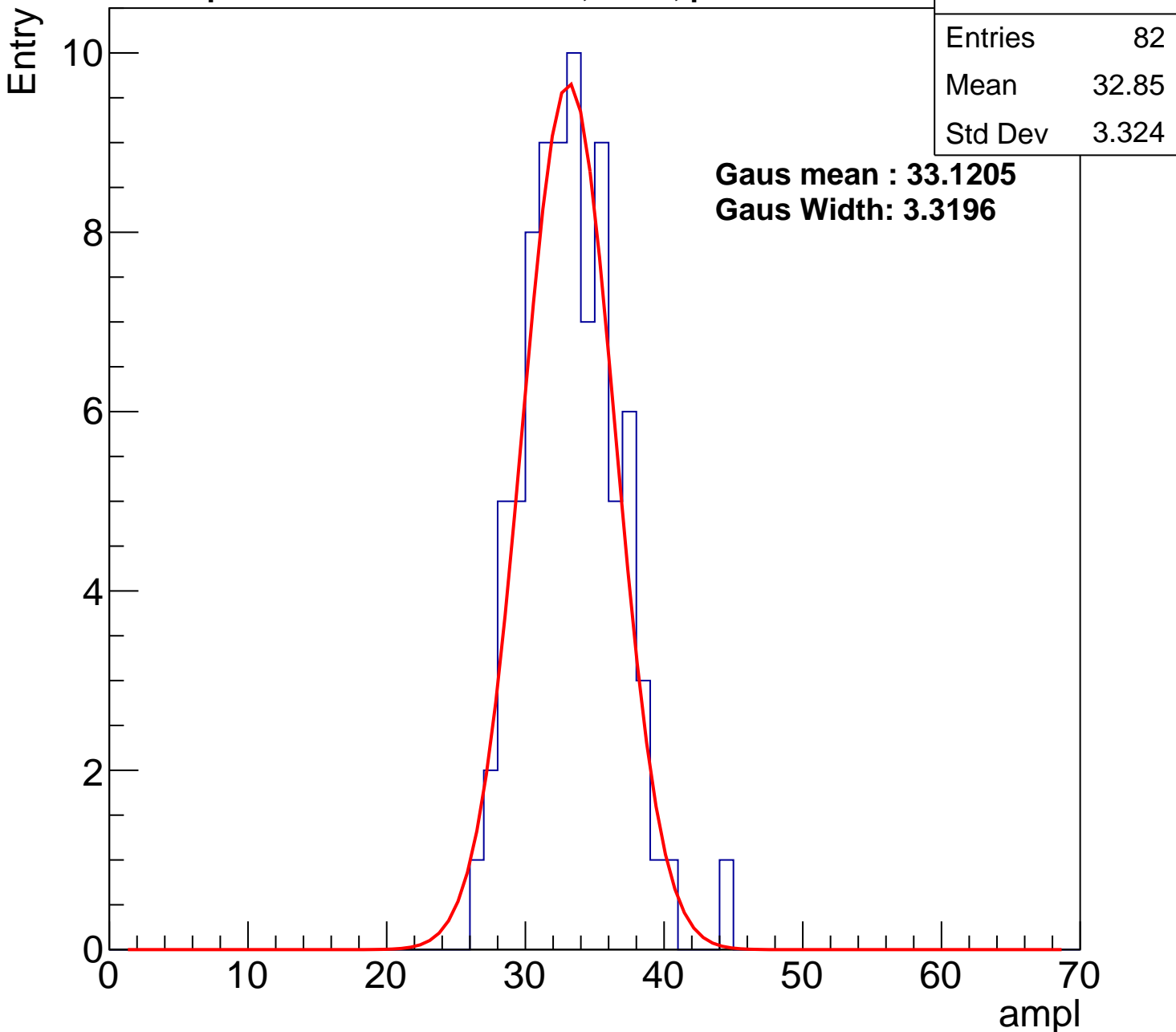
Gaus Width: 3.3196

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

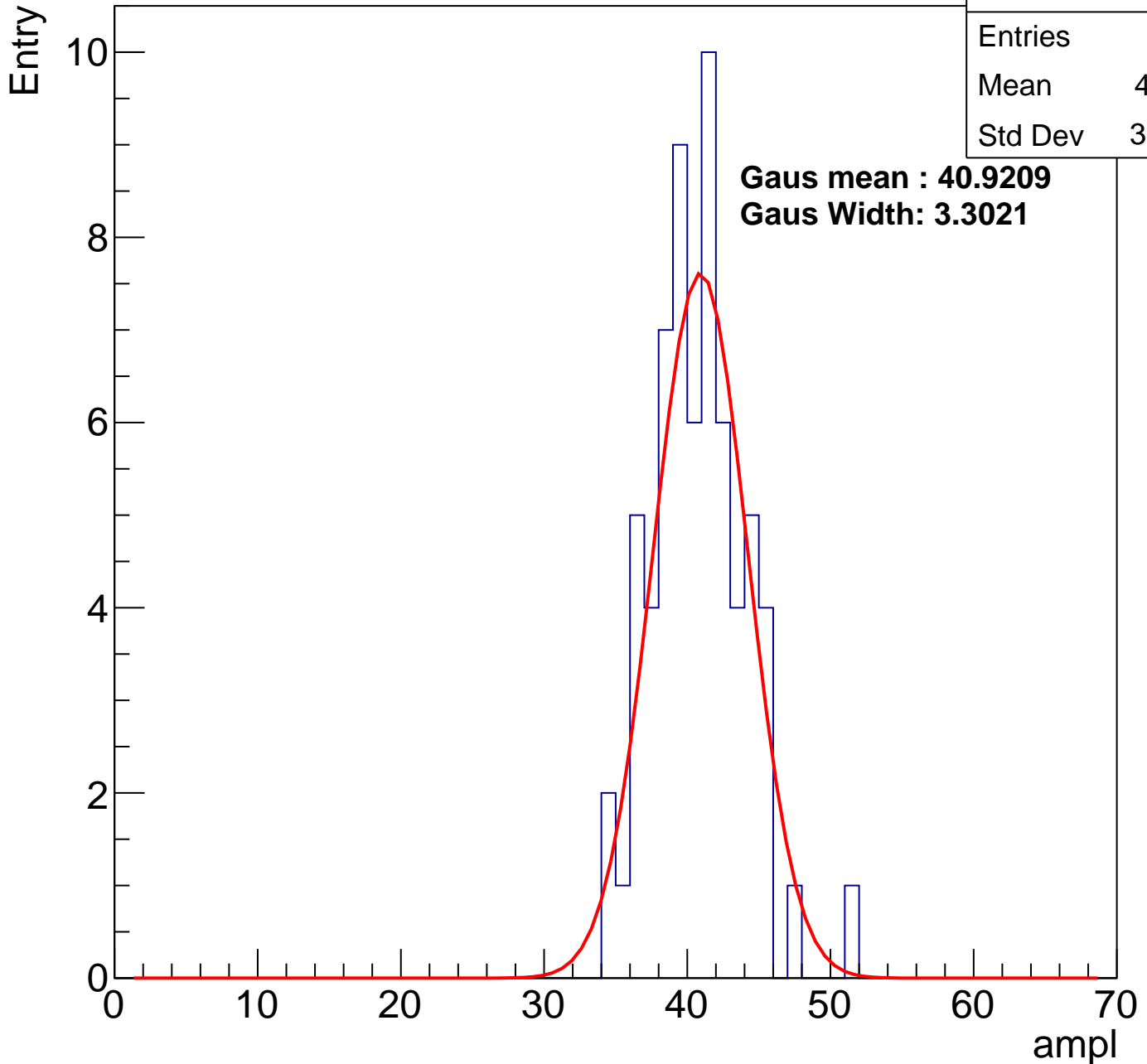


B0L001S, U11-ch31, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	40.31
Std Dev	3.196

Gaus mean : 40.9209
Gaus Width: 3.3021



B0L001S, U11-ch31, adc2

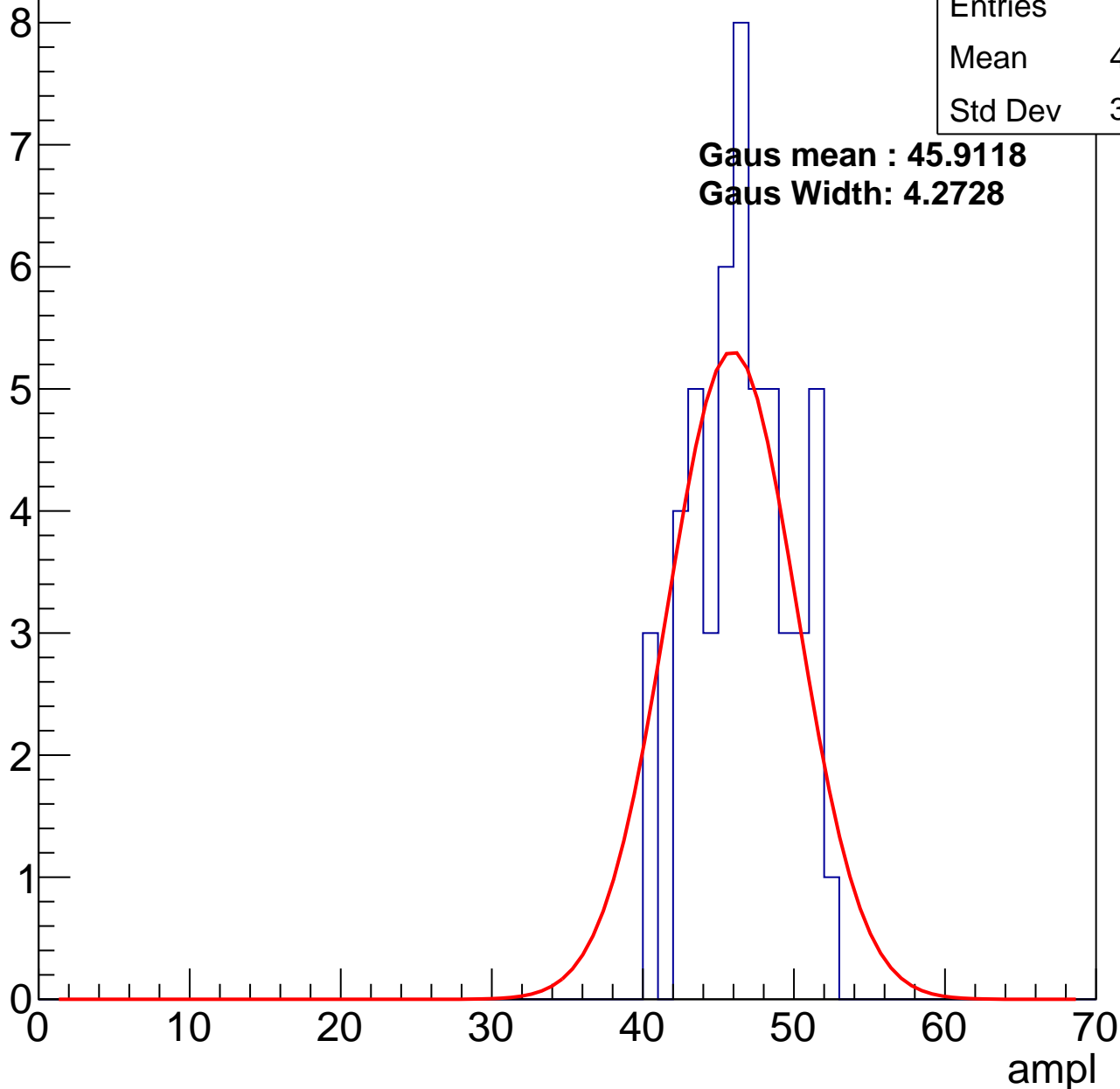
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	46.12
Std Dev	3.116

Gaus mean : 45.9118

Gaus Width: 4.2728

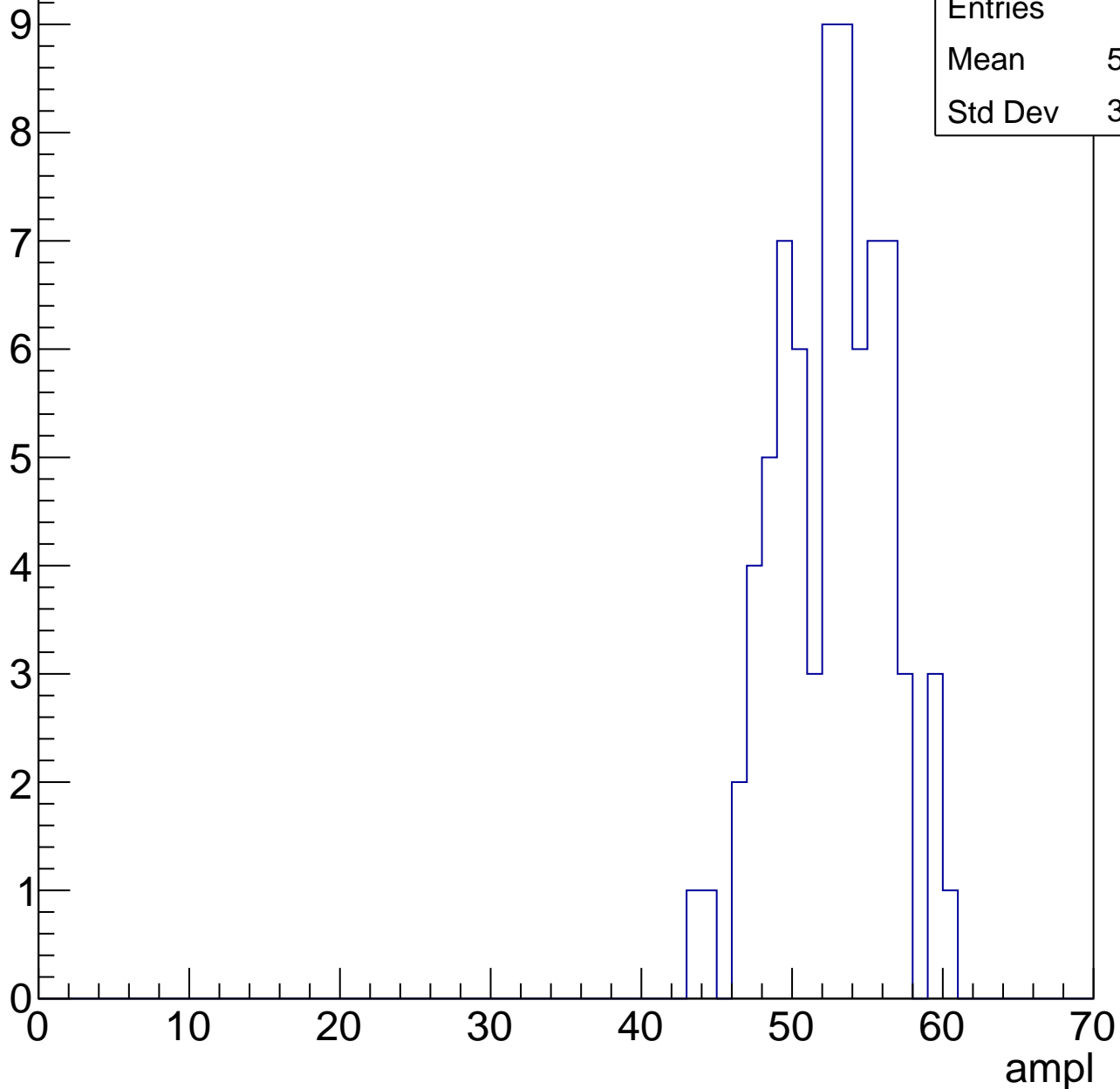


B0L001S, U11-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	52.12
Std Dev	3.639

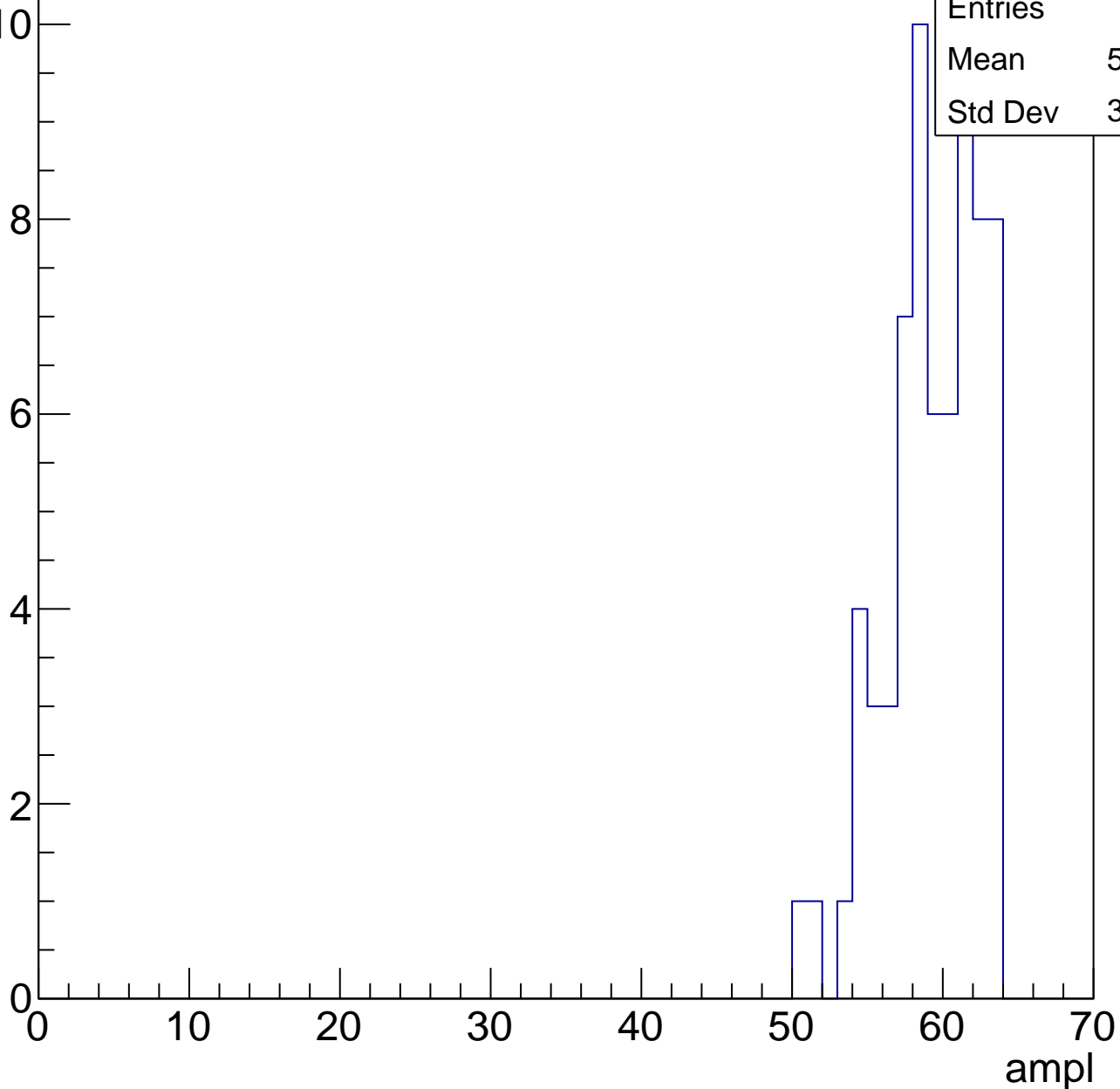


B0L001S, U11-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

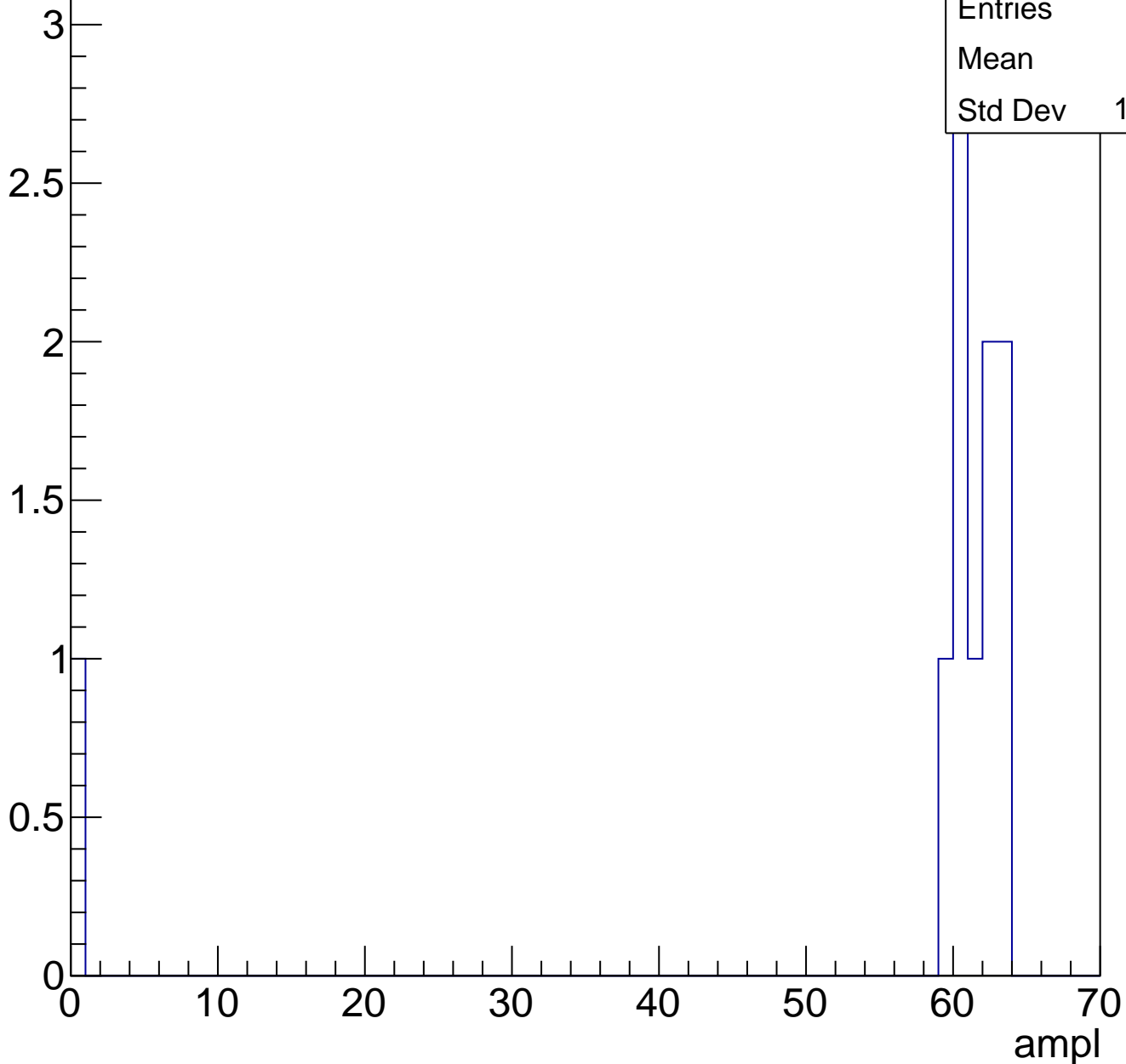
Entries	67
Mean	58.88
Std Dev	3.079



B0L001S, U11-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

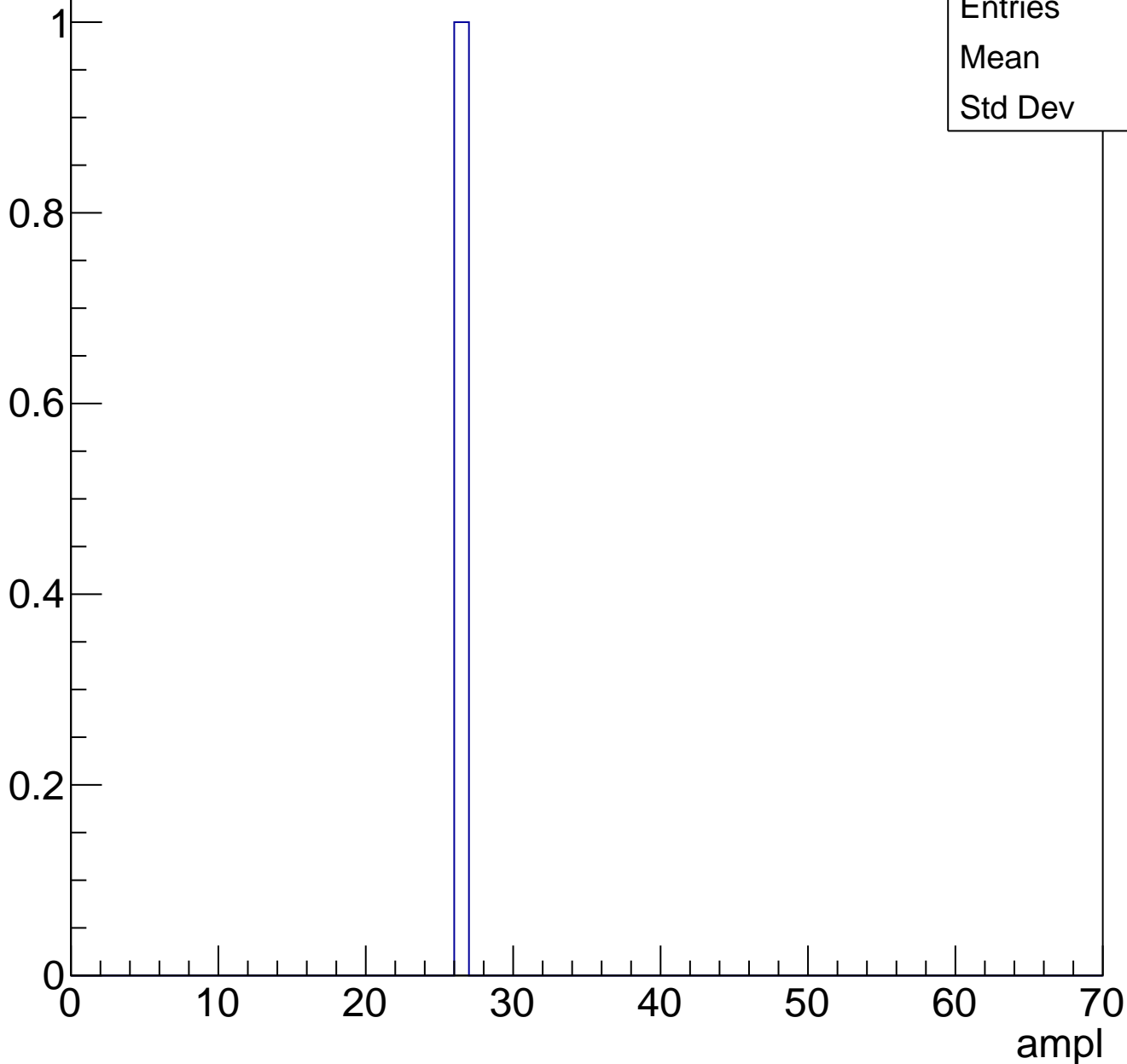


Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch32, adc0

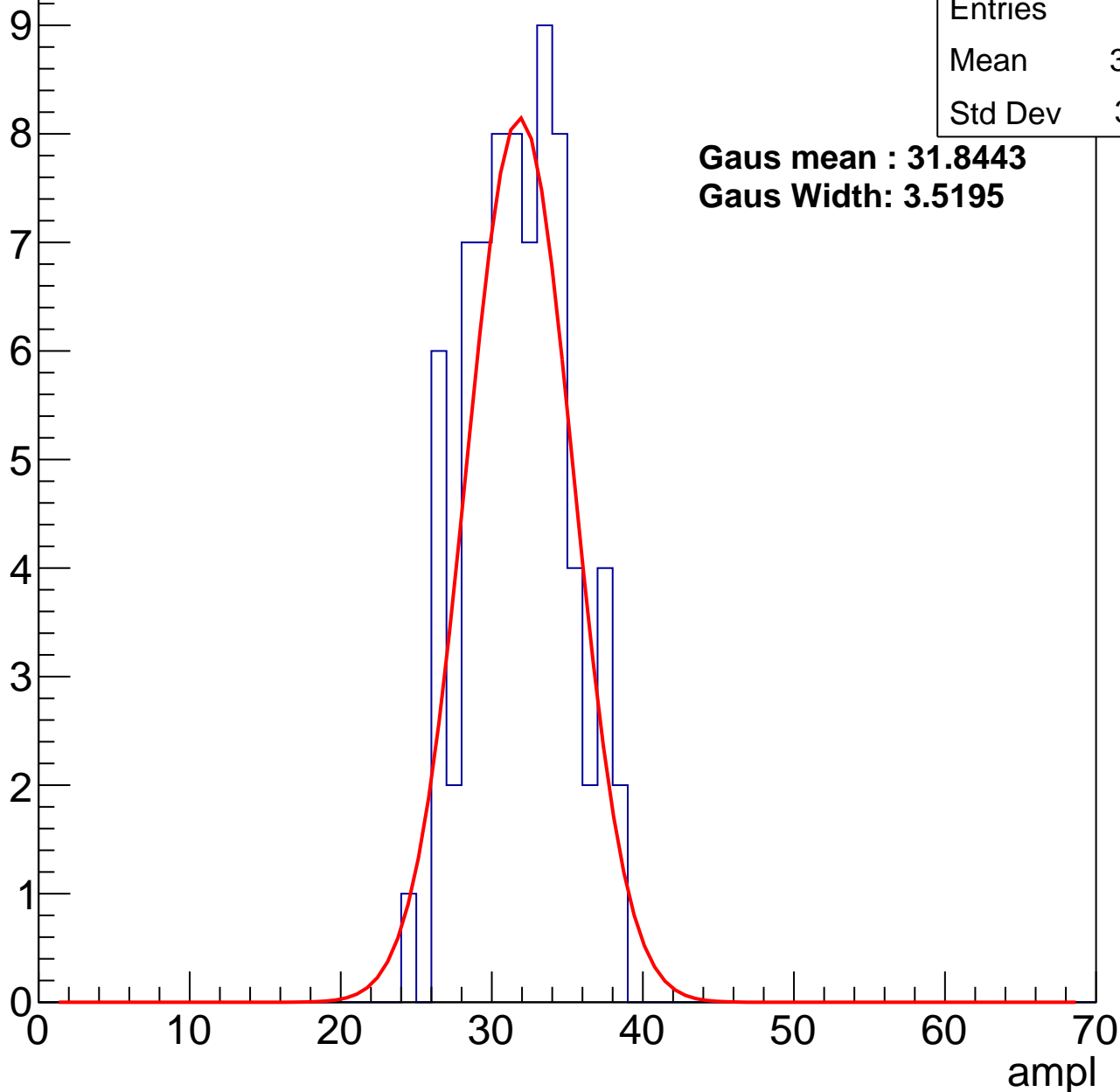
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	31.33
Std Dev	3.251

Gaus mean : 31.8443

Gaus Width: 3.5195



B0L001S, U11-ch32, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	38.49
Std Dev	3.337

Gaus mean : 39.3038

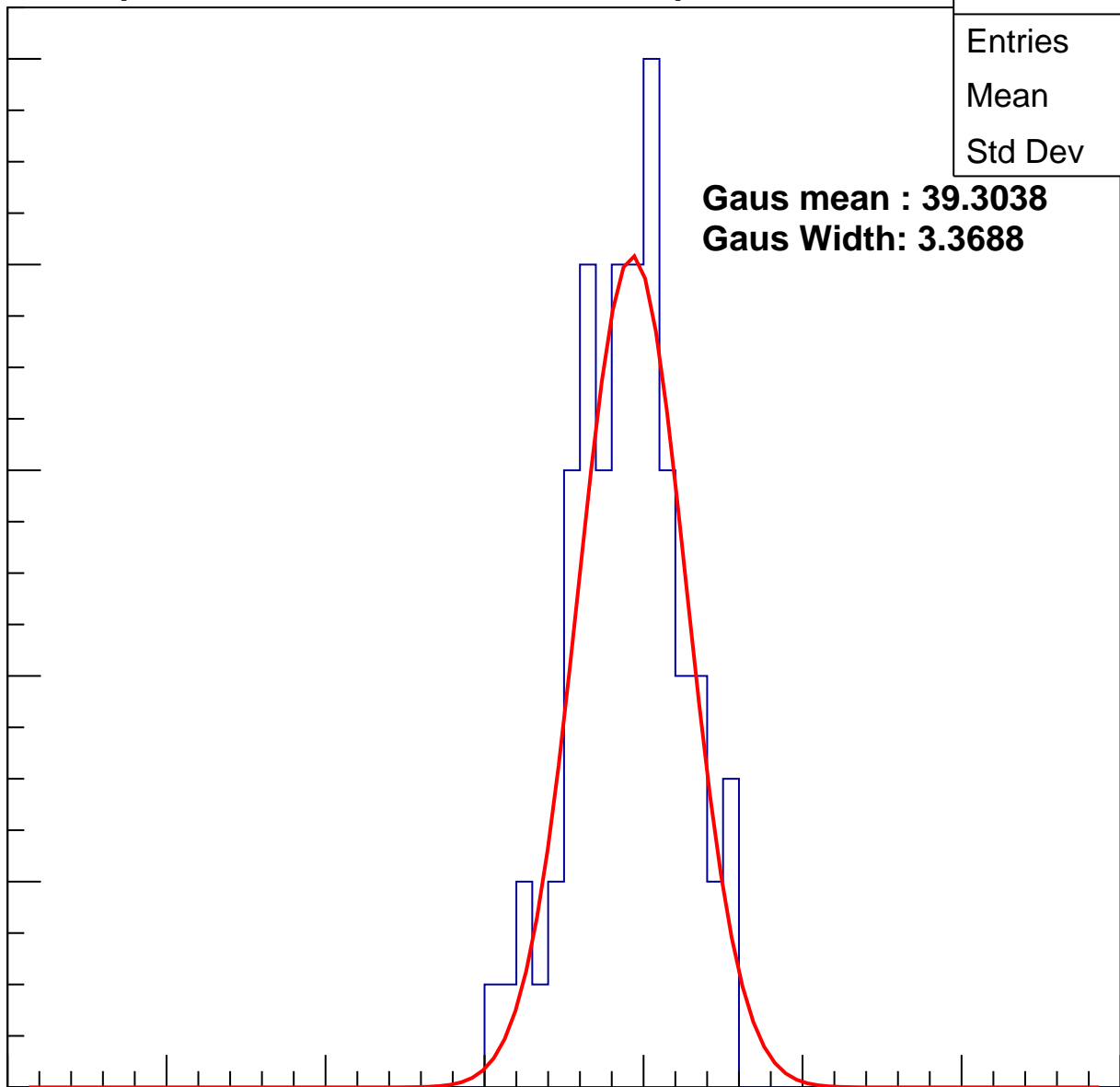
Gaus Width: 3.3688

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch32, adc2

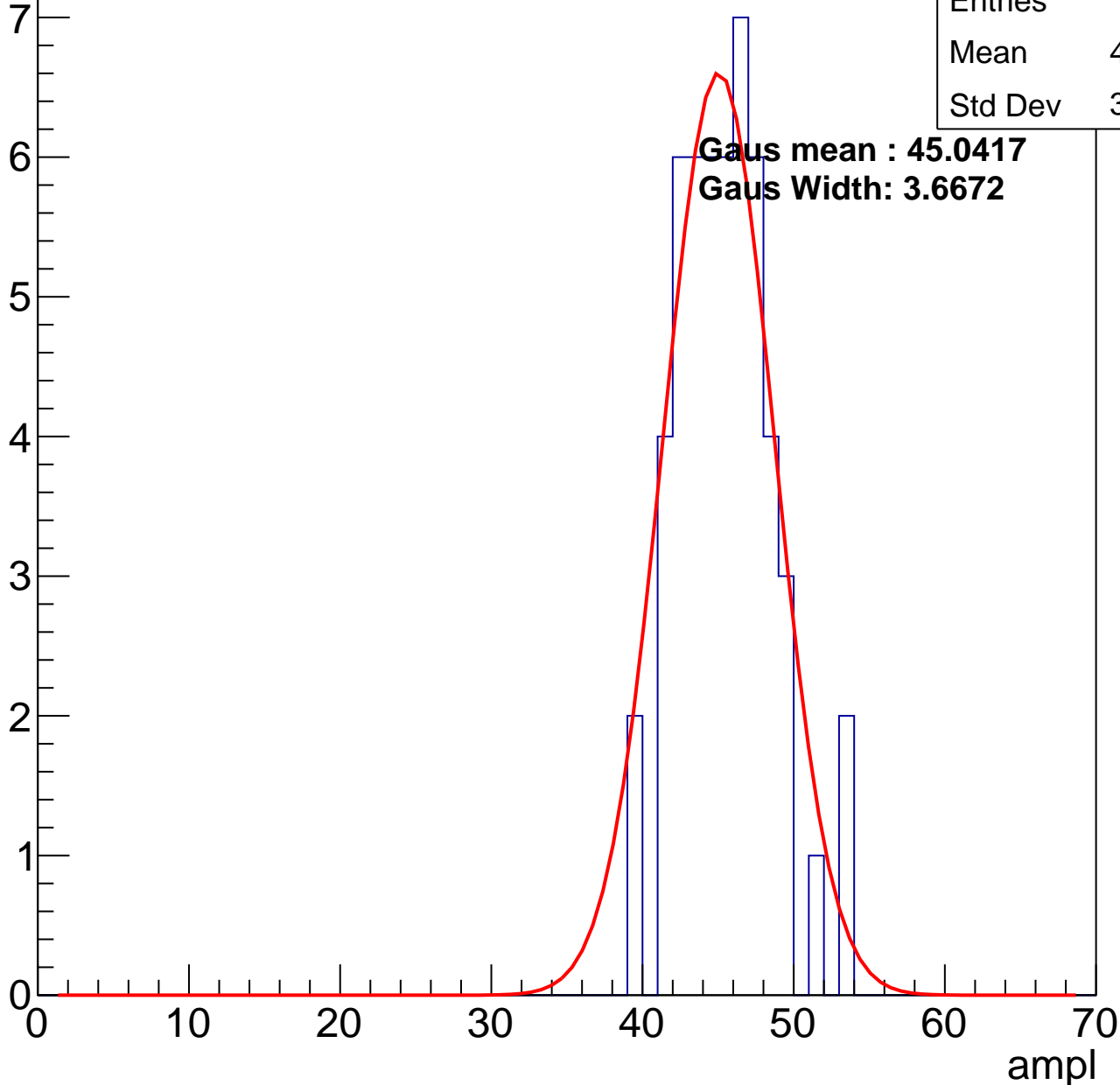
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	45.02
Std Dev	3.068

Gaus mean : 45.0417

Gaus Width: 3.6672



B0L001S, U11-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

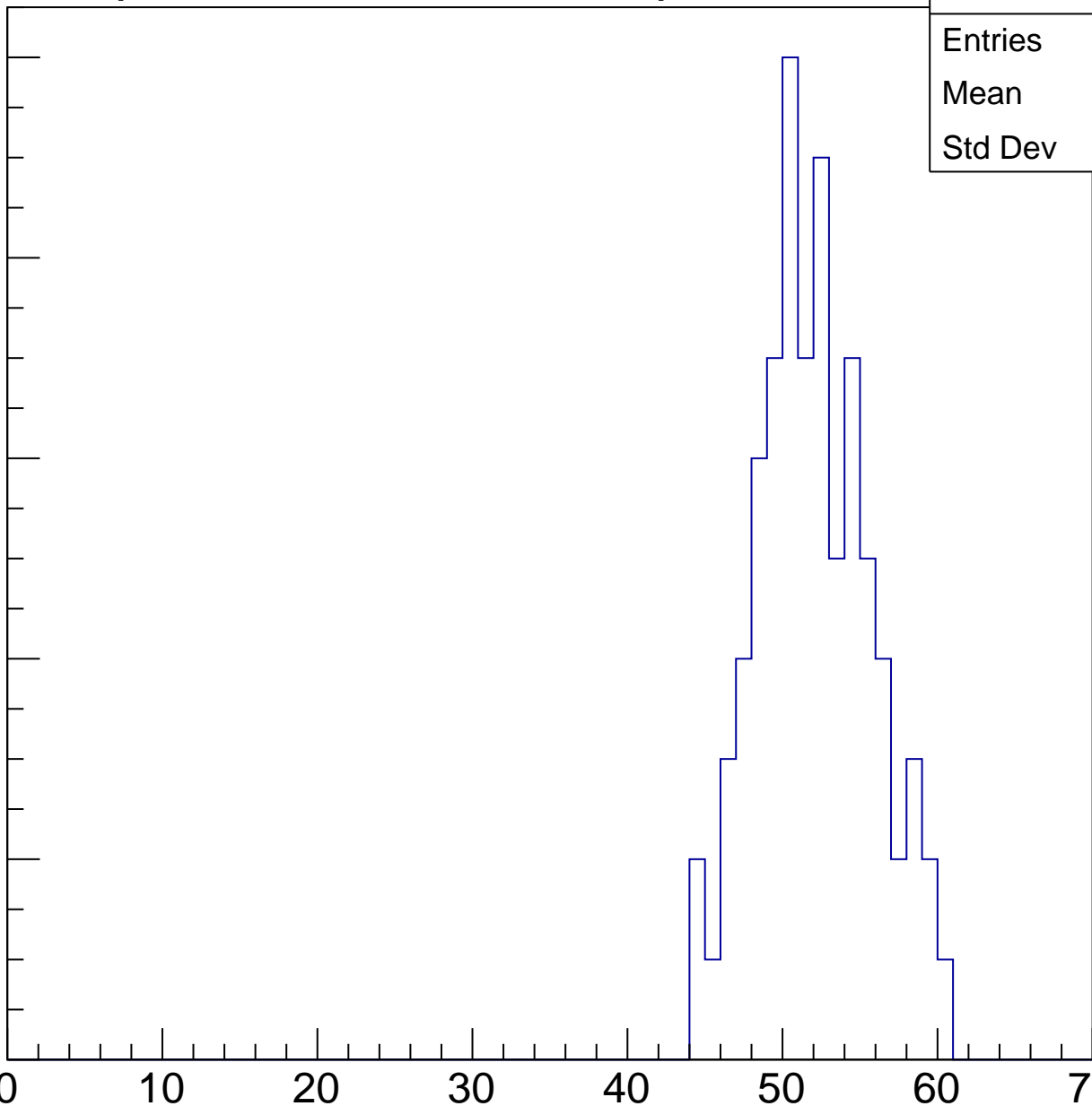
Entries	78
Mean	51.58
Std Dev	3.65

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

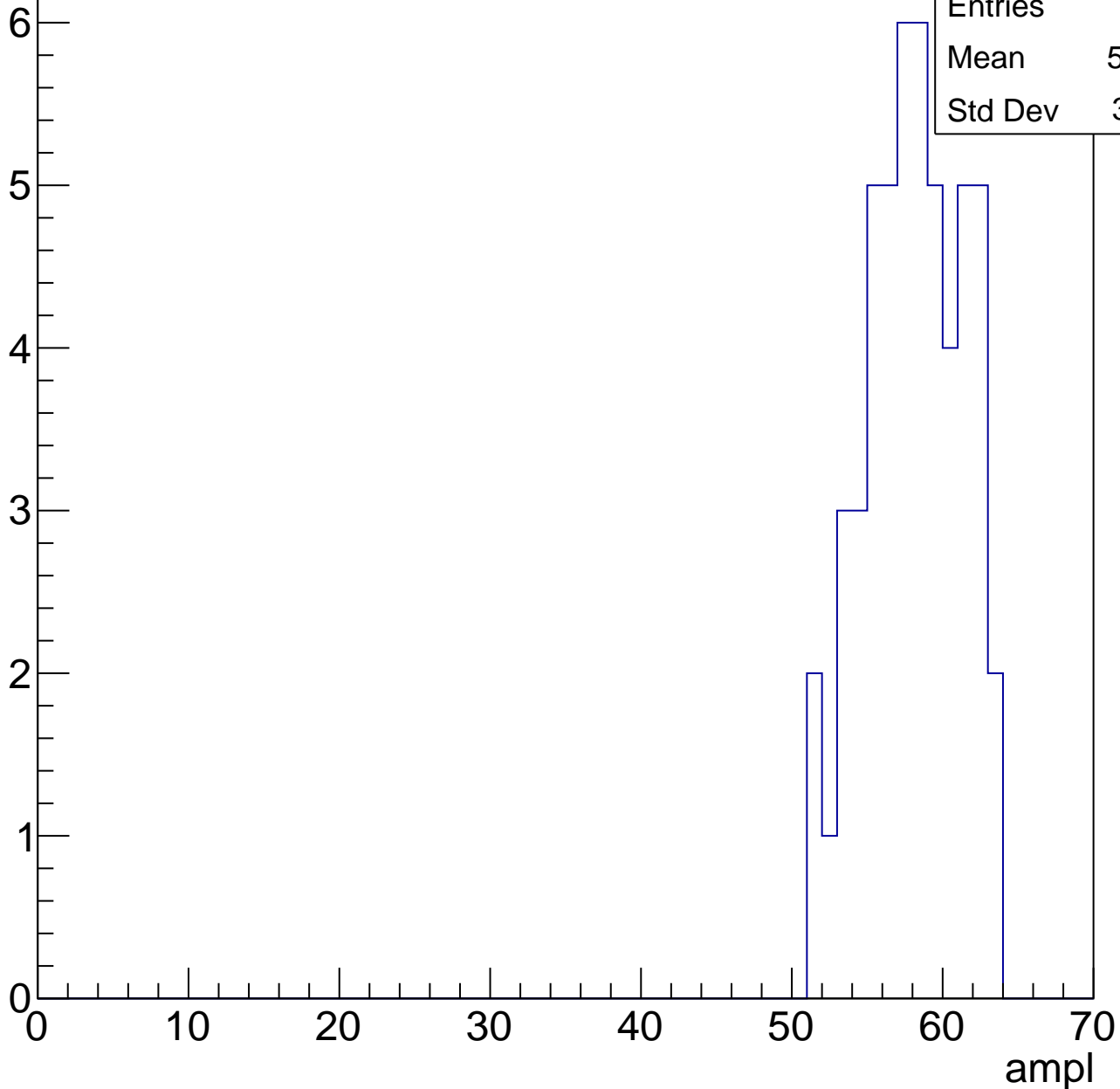


B0L001S, U11-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	57.62
Std Dev	3.151

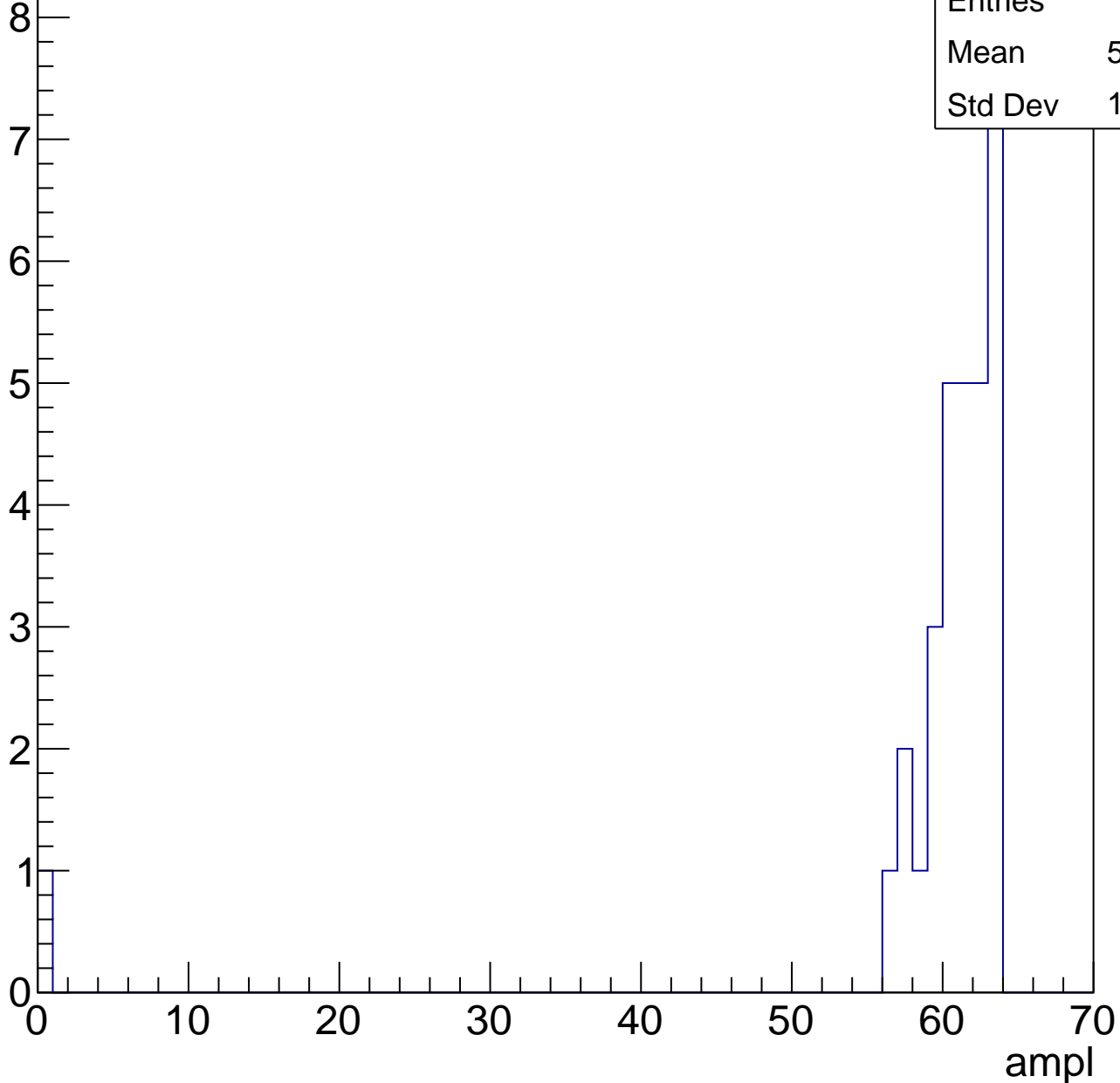


B0L001S, U11-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.84
Std Dev	10.92



B0L001S, U11-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	22
Std Dev	0

B0L001S, U11-ch33, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	31.03
Std Dev	4.981

Gaus mean : 31.8445

Gaus Width: 3.7352

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L001S, U11-ch33, adc1

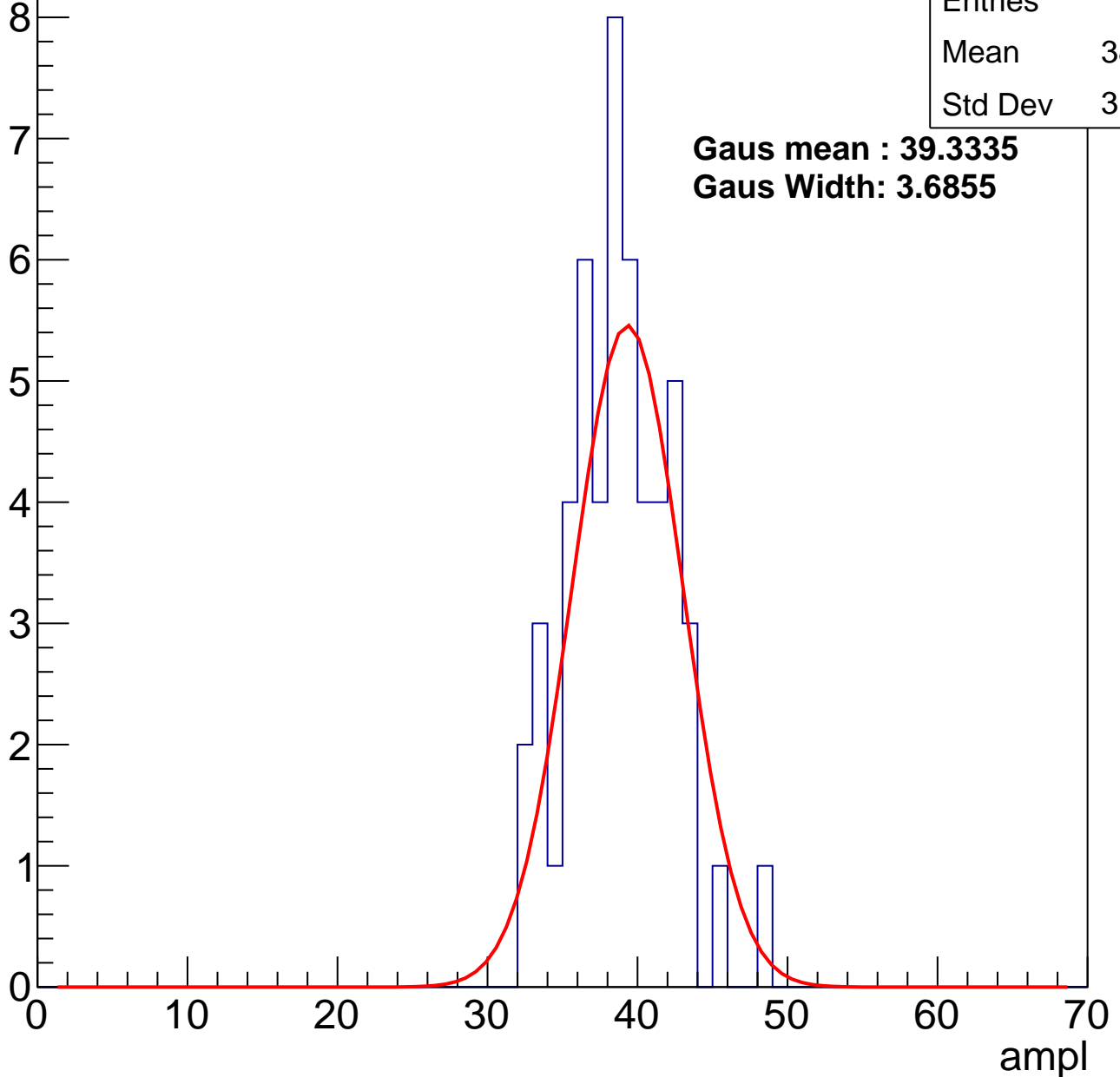
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	38.37
Std Dev	3.346

Gaus mean : 39.3335

Gaus Width: 3.6855



B0L001S, U11-ch33, adc2

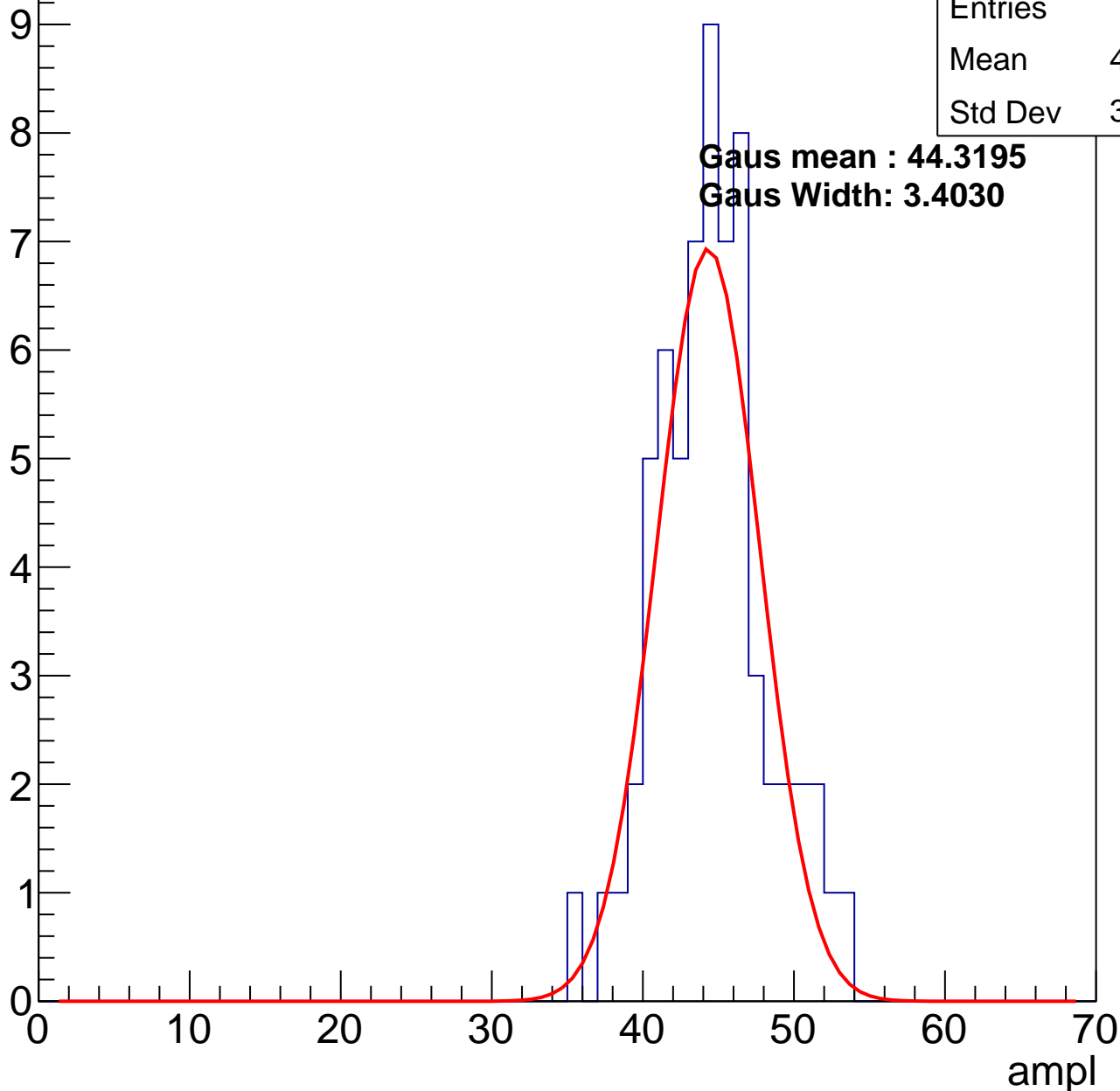
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.09
Std Dev	3.594

Gaus mean : 44.3195

Gaus Width: 3.4030

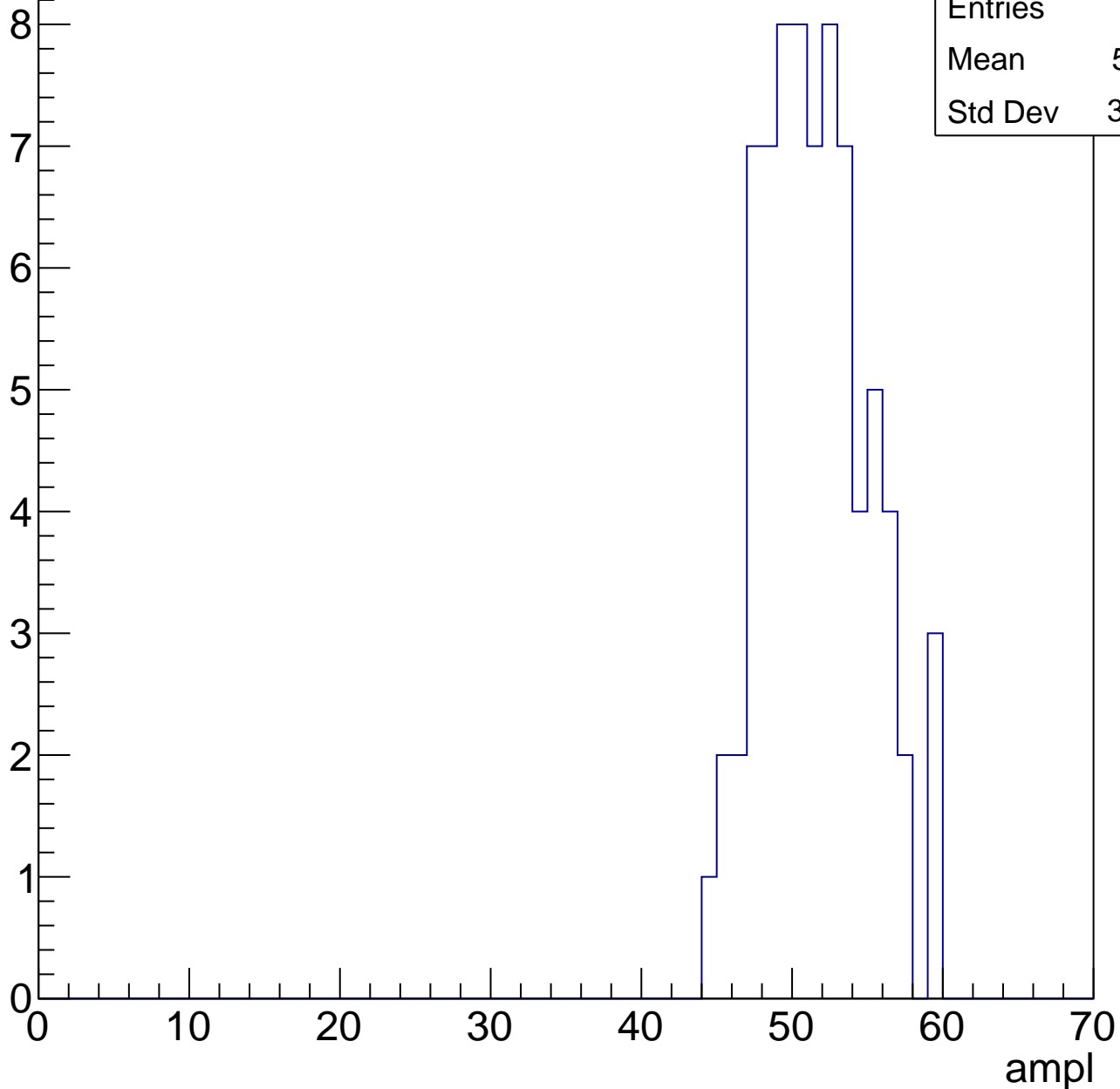


B0L001S, U11-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	51.11
Std Dev	3.466



B0L001S, U11-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	54
Mean	57.06
Std Dev	2.52

Entry

10

8

6

4

2

0

0

10

20

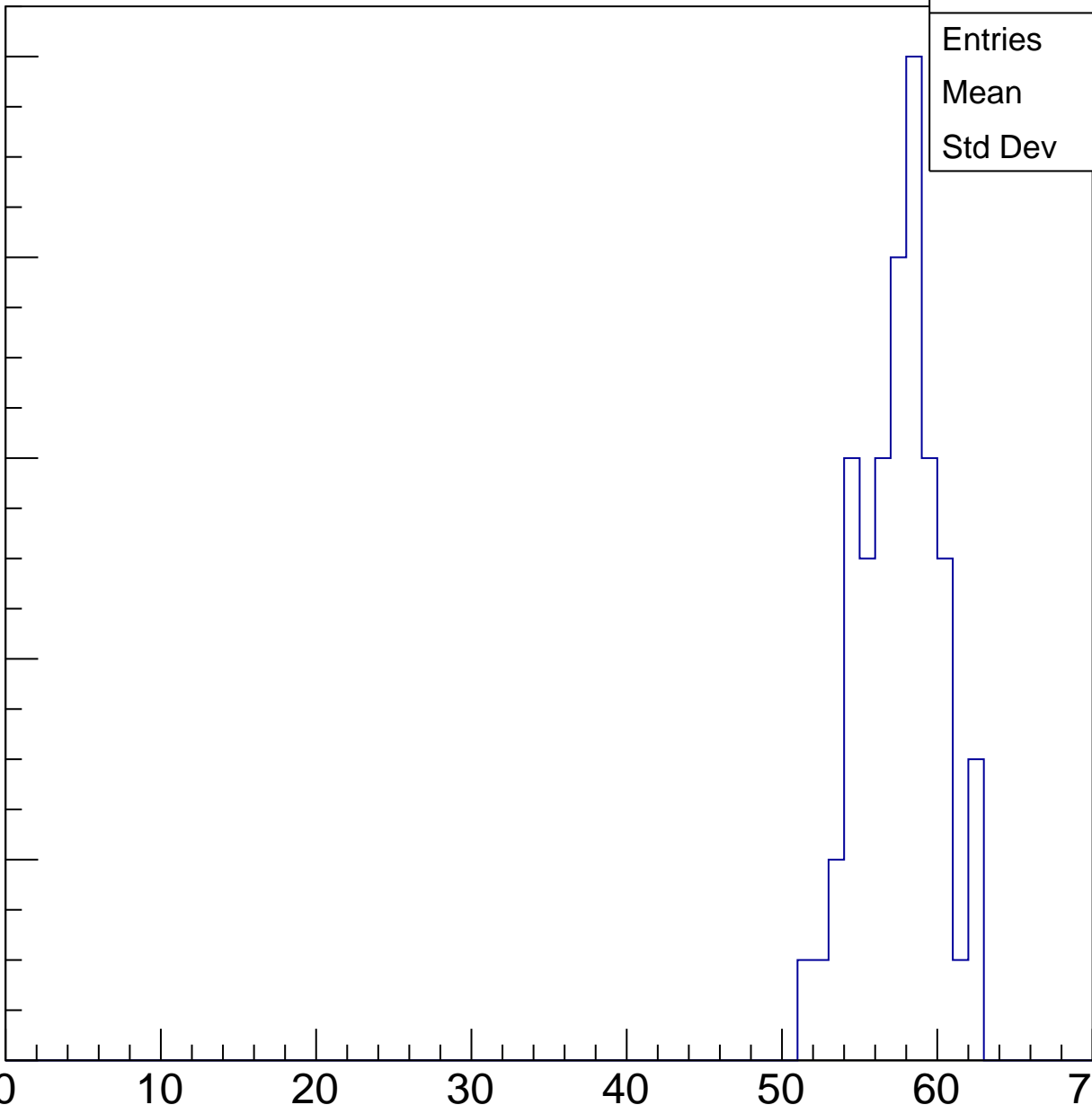
30

40

50

60

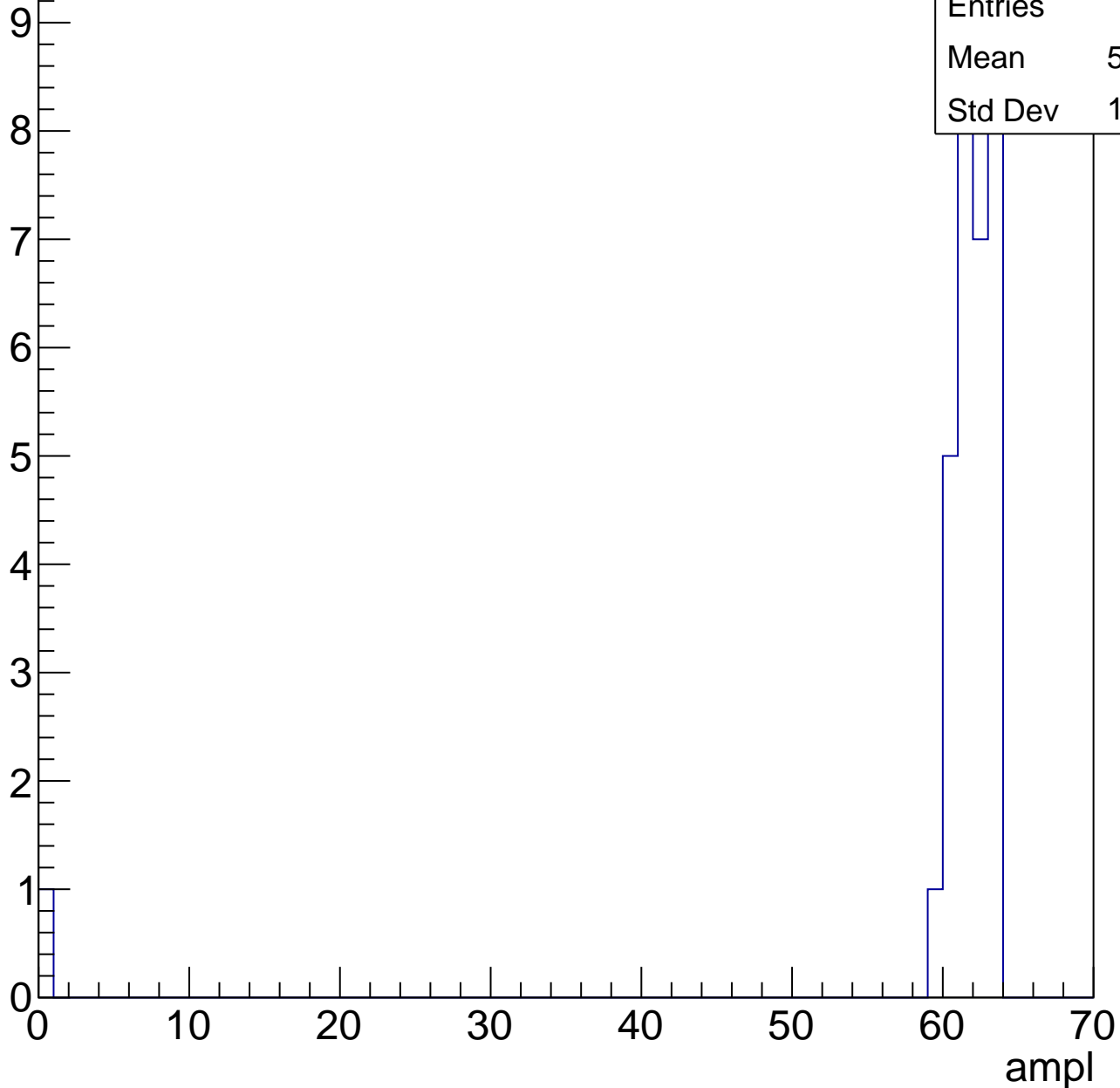
ampl



B0L001S, U11-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch34, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	31.12
Std Dev	3.3

Gaus mean : 31.1867

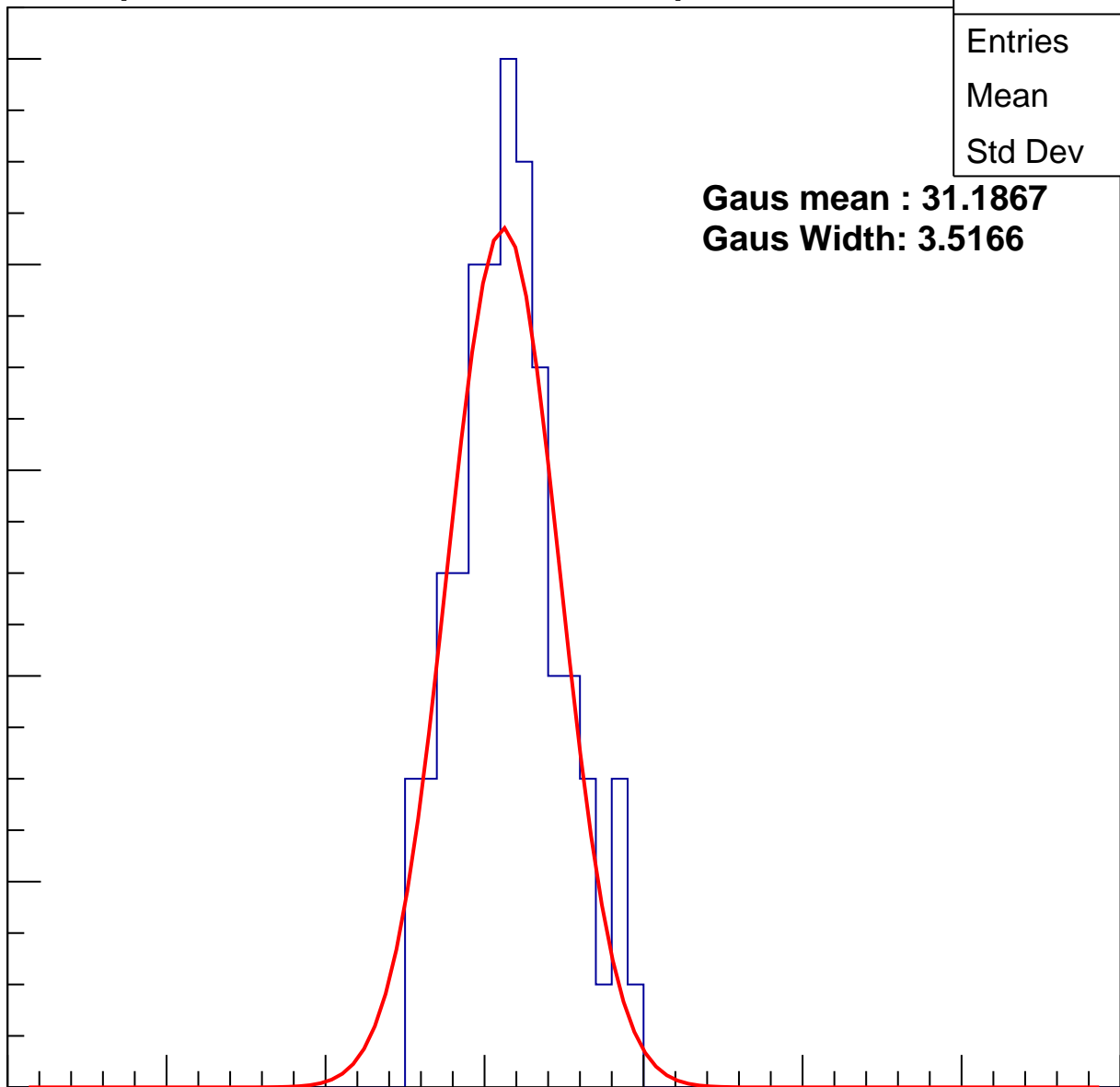
Gaus Width: 3.5166

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch34, adc1

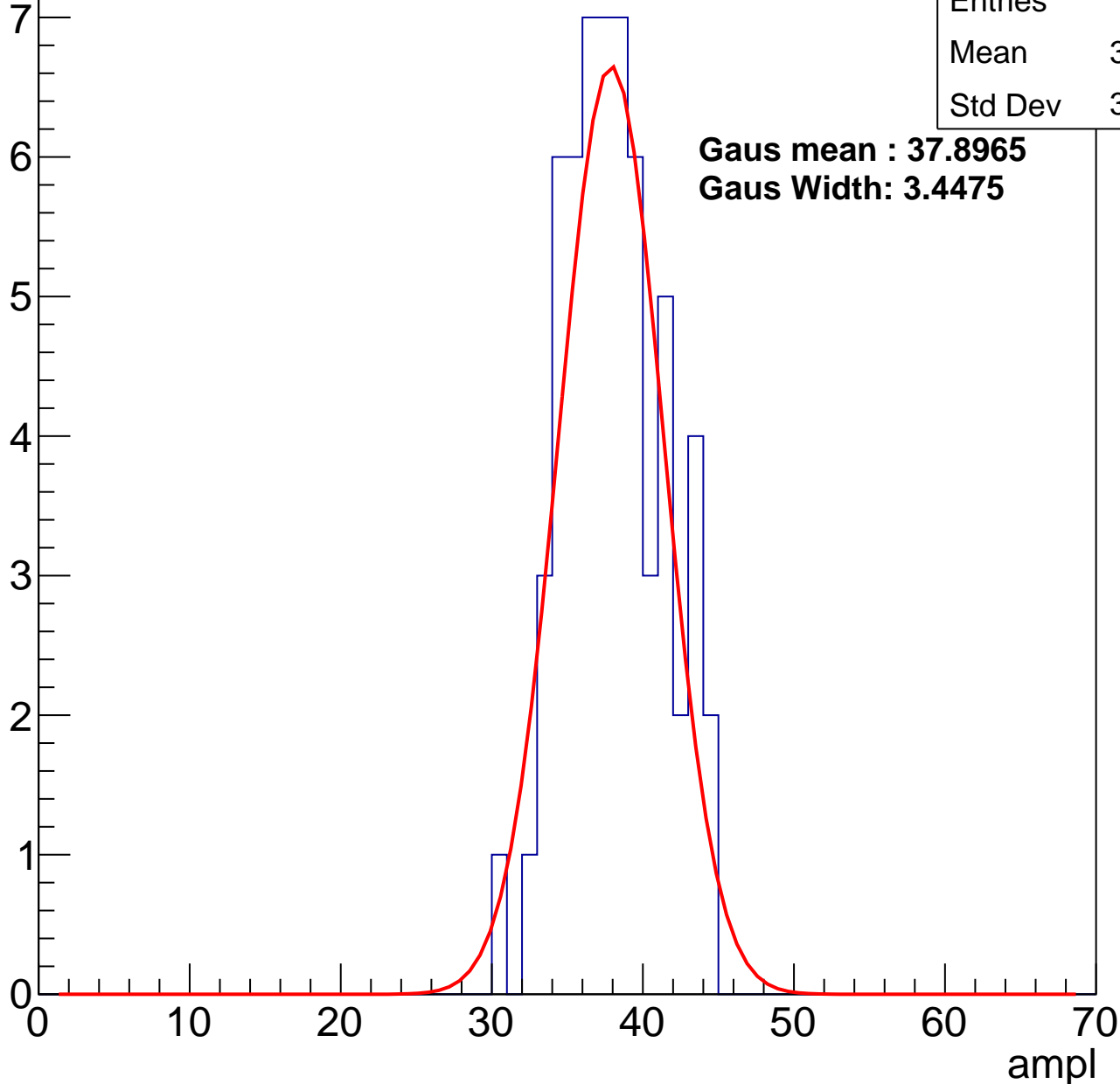
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.58
Std Dev	3.206

Gaus mean : 37.8965

Gaus Width: 3.4475



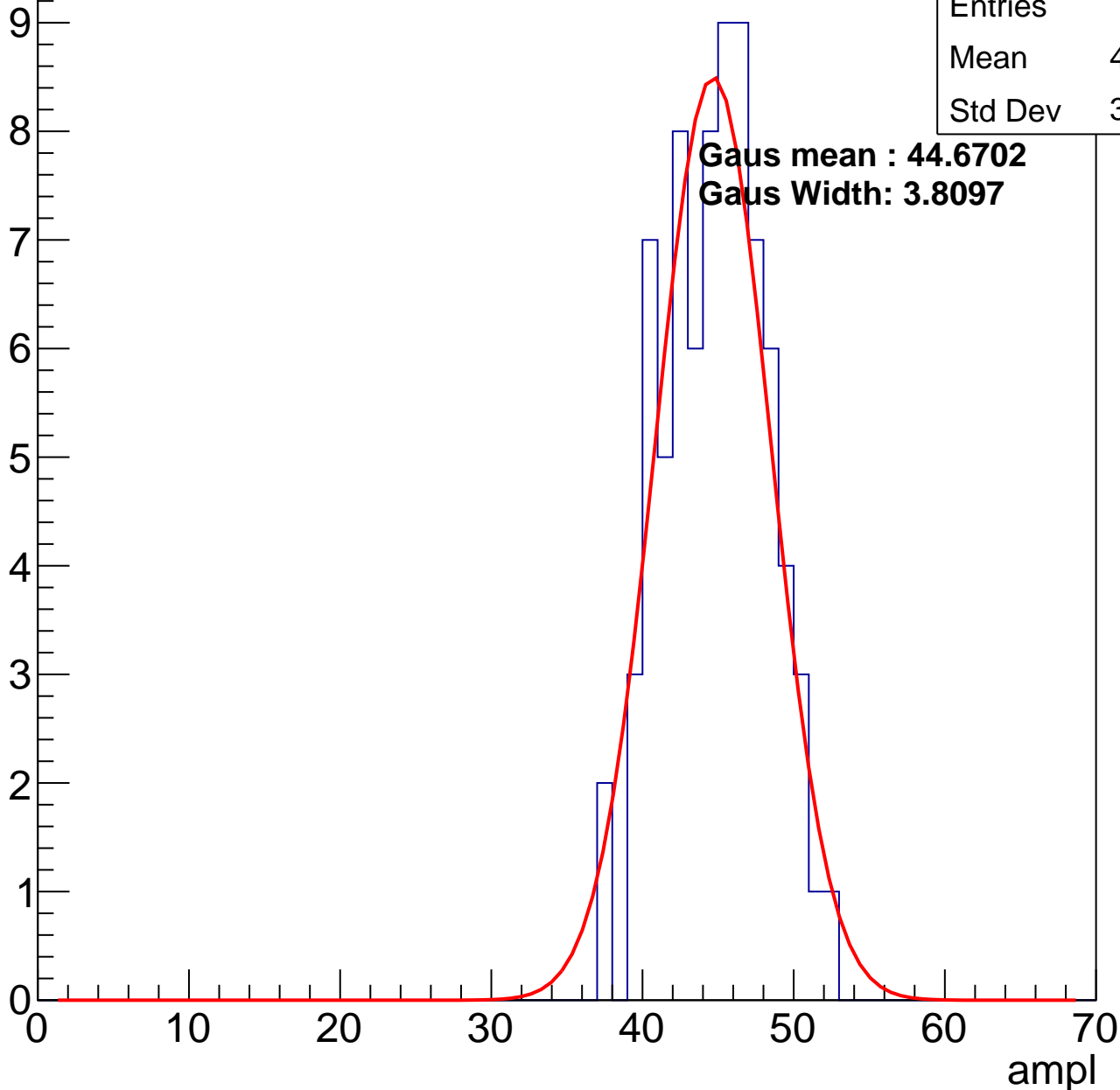
B0L001S, U11-ch34, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	44.39
Std Dev	3.339

Gaus mean : 44.6702
Gaus Width: 3.8097

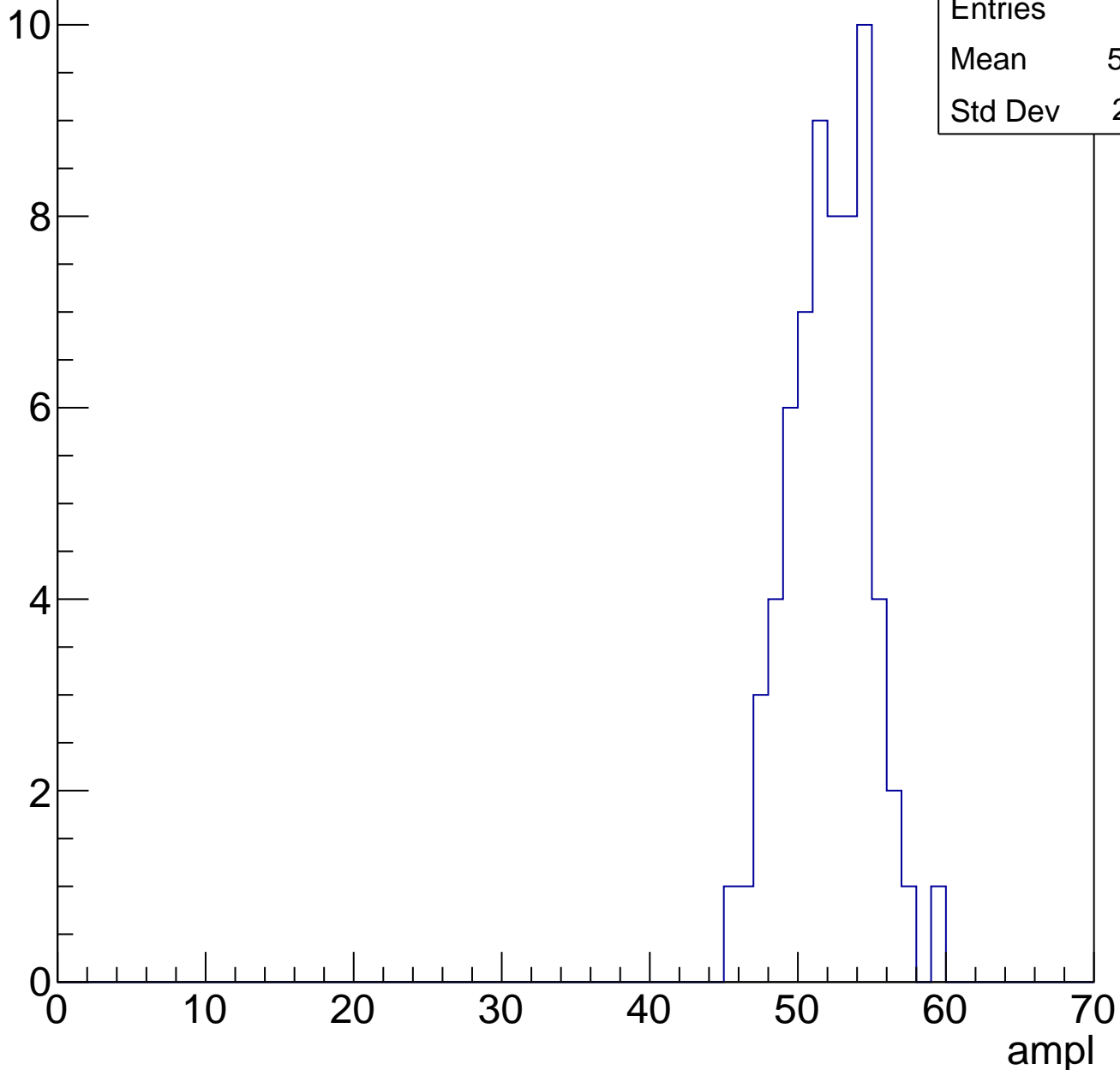


B0L001S, U11-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.62
Std Dev	2.761

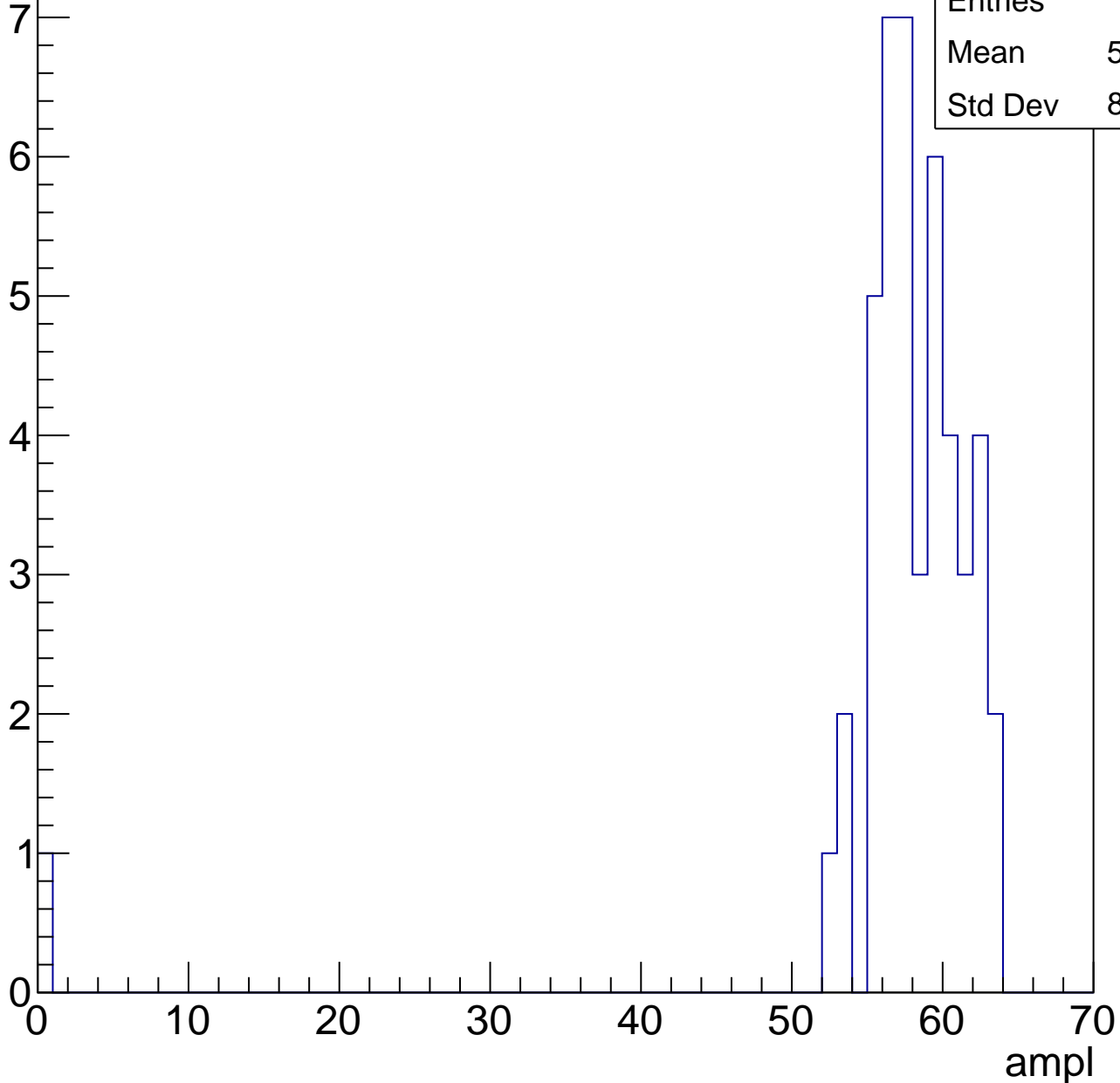


B0L001S, U11-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

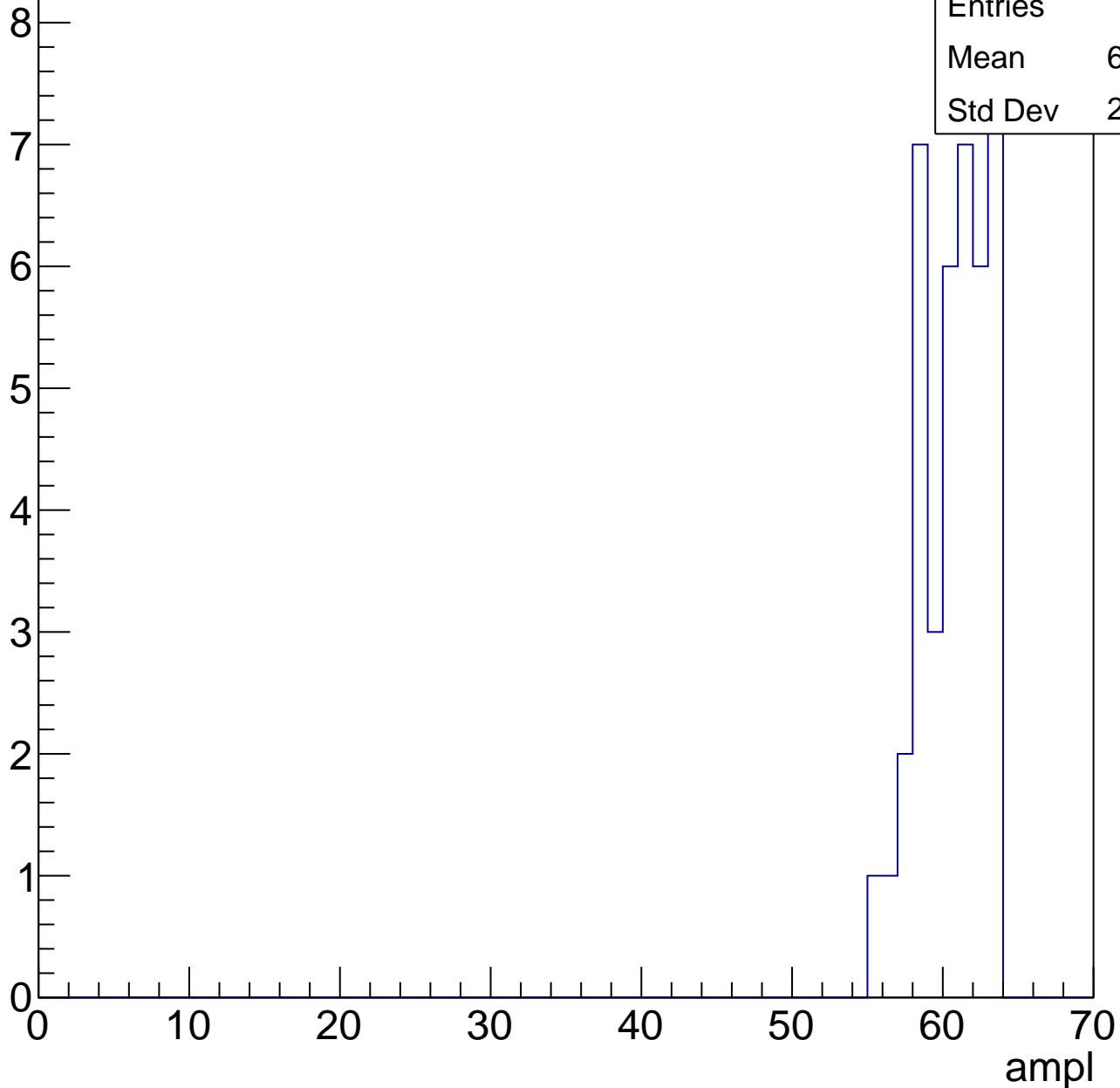
Entries	45
Mean	56.64
Std Dev	8.957



B0L001S, U11-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	41
Mean	60.27
Std Dev	2.153

B0L001S, U11-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch35, adc0

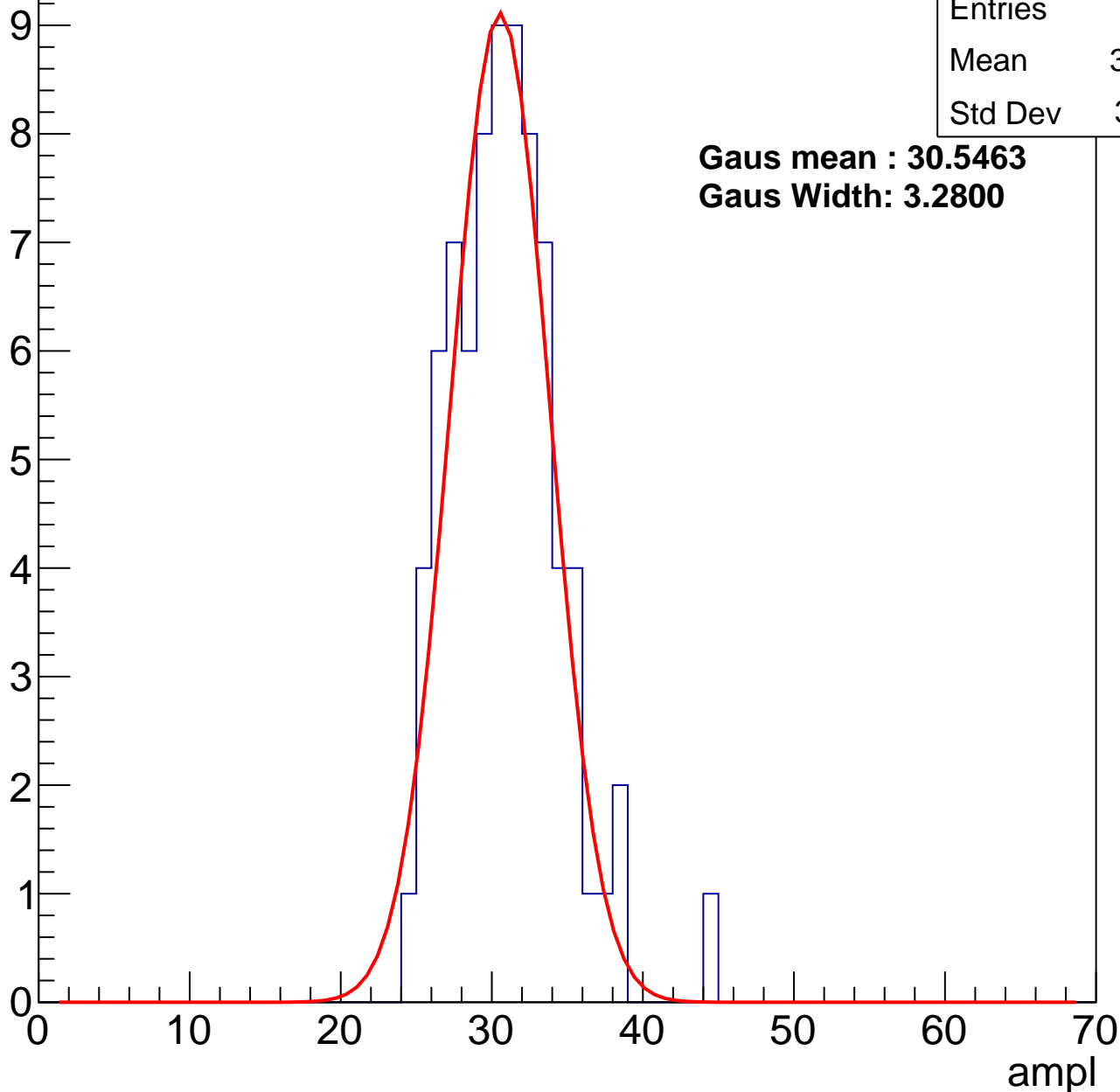
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.44
Std Dev	3.561

Gaus mean : 30.5463

Gaus Width: 3.2800



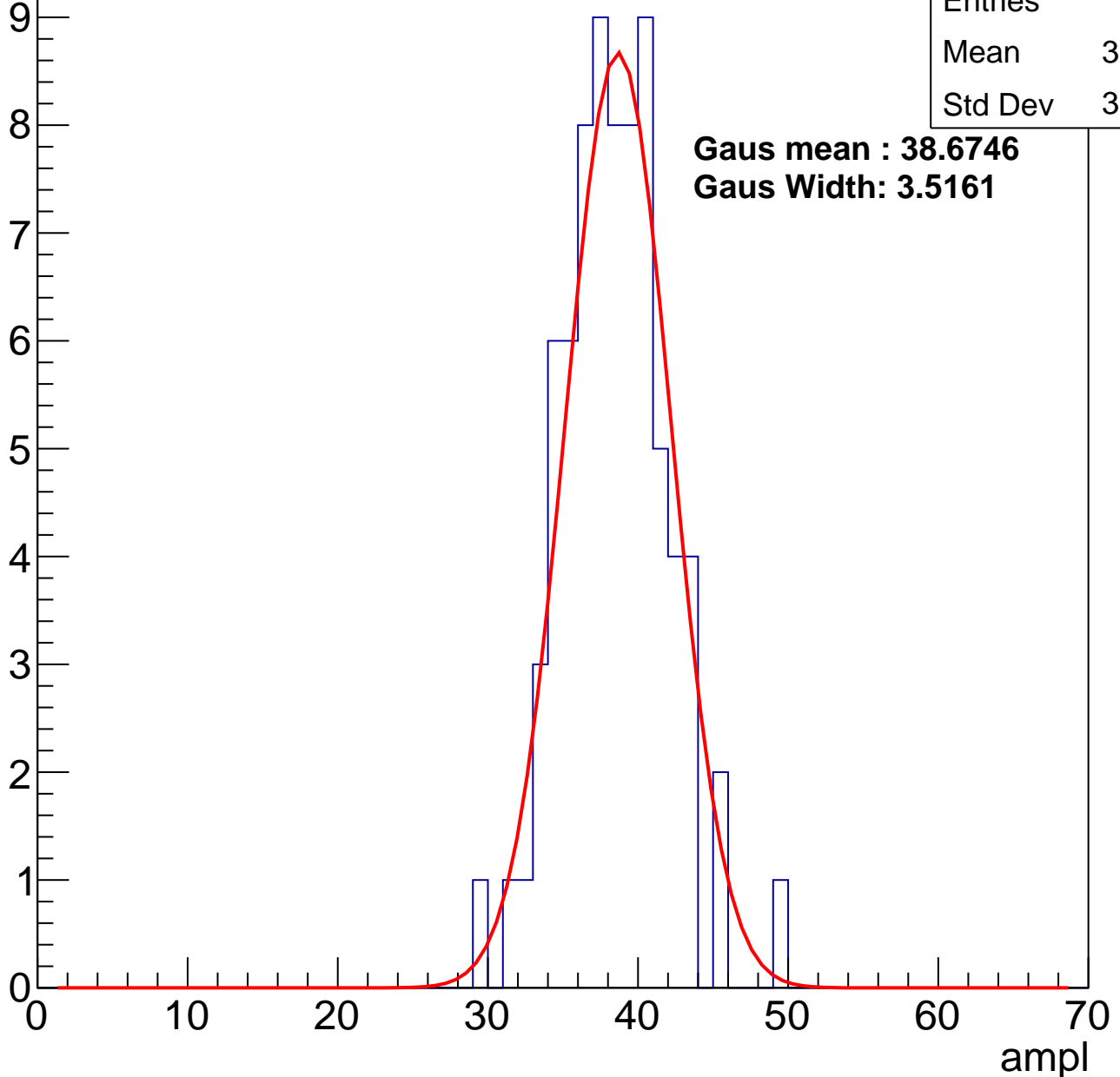
B0L001S, U11-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	37.97
Std Dev	3.449

Gaus mean : 38.6746
Gaus Width: 3.5161



B0L001S, U11-ch35, adc2

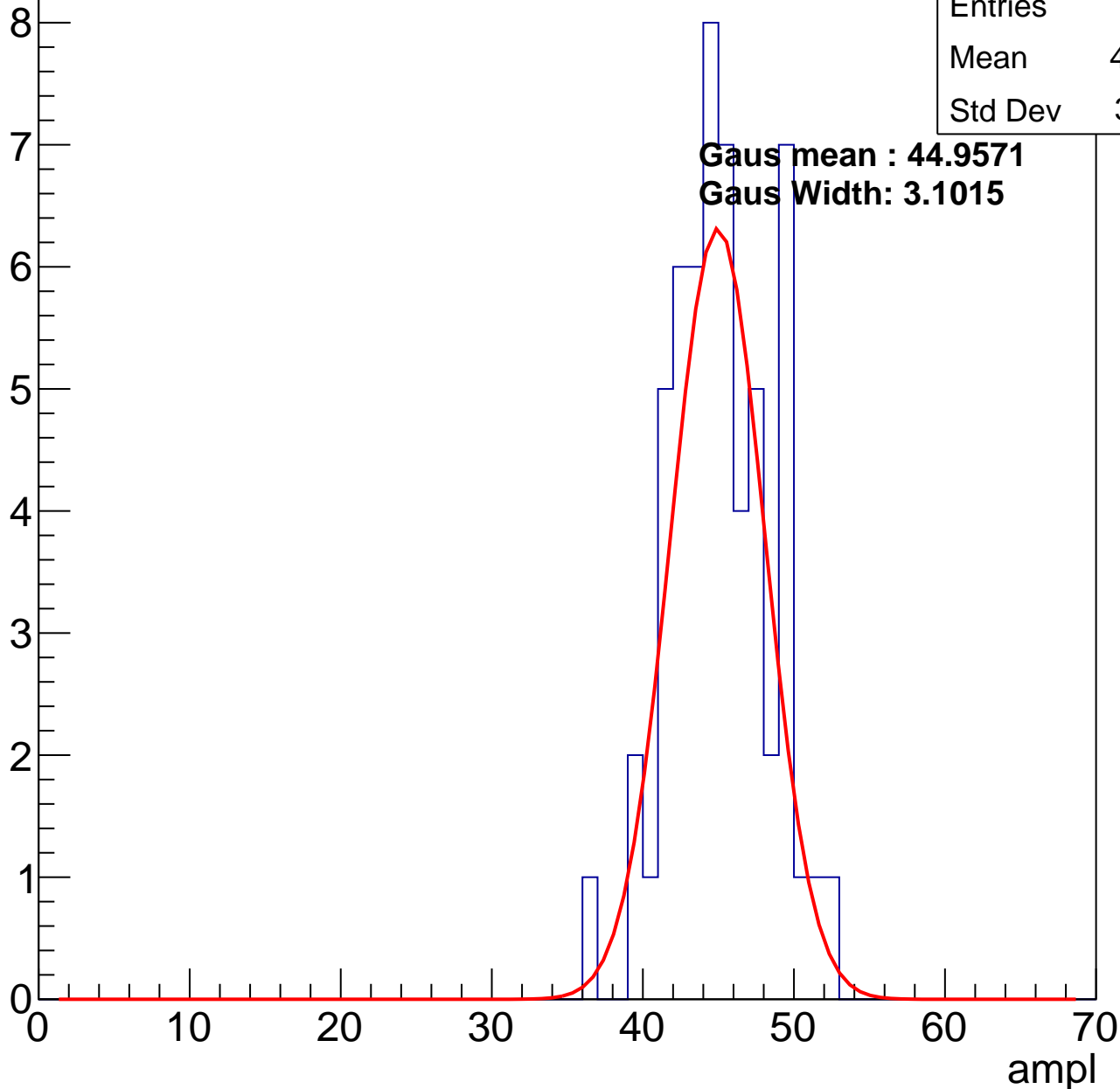
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.68
Std Dev	3.251

Gaus mean : 44.9571

Gaus Width: 3.1015

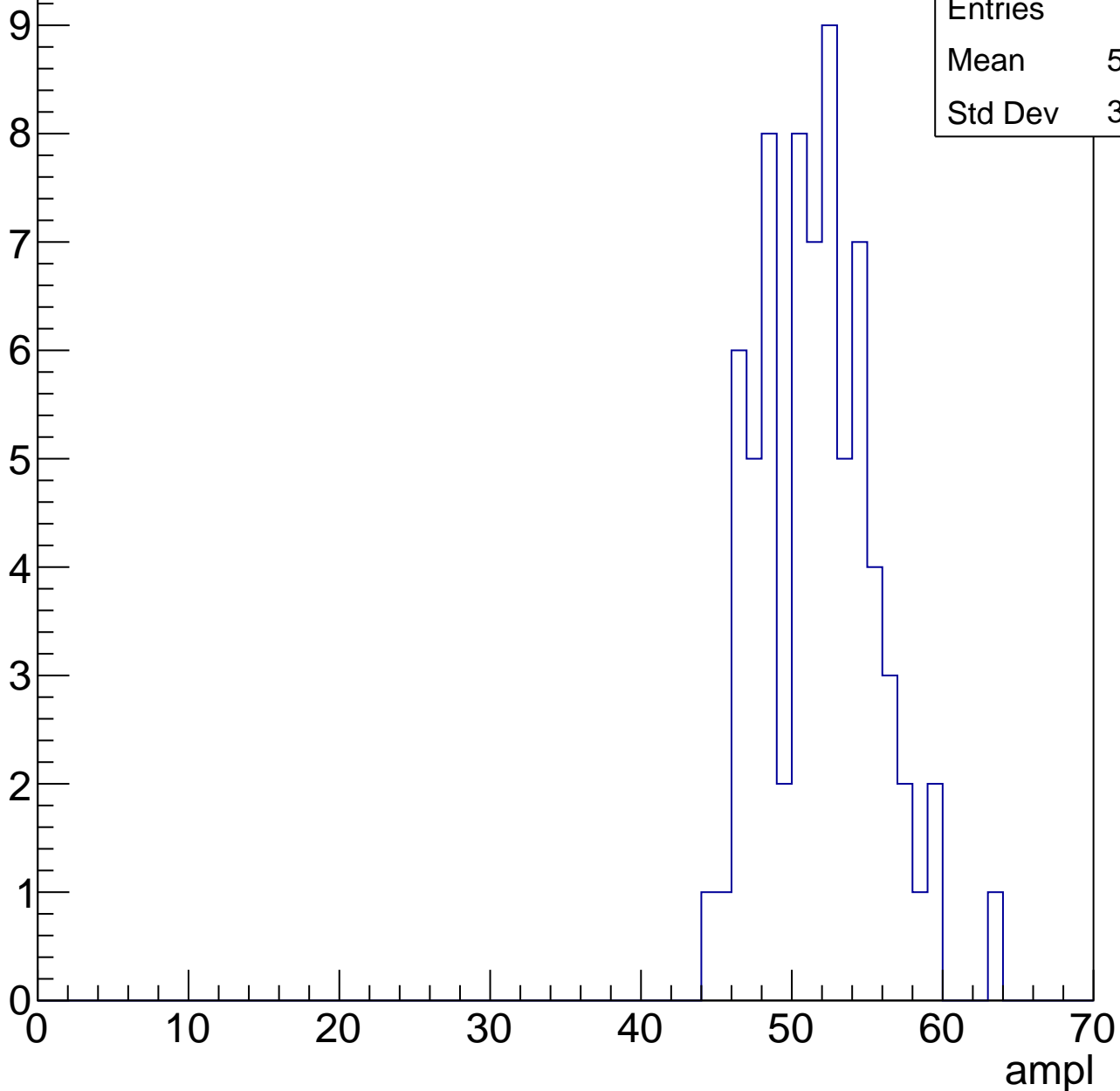


B0L001S, U11-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.26
Std Dev	3.768

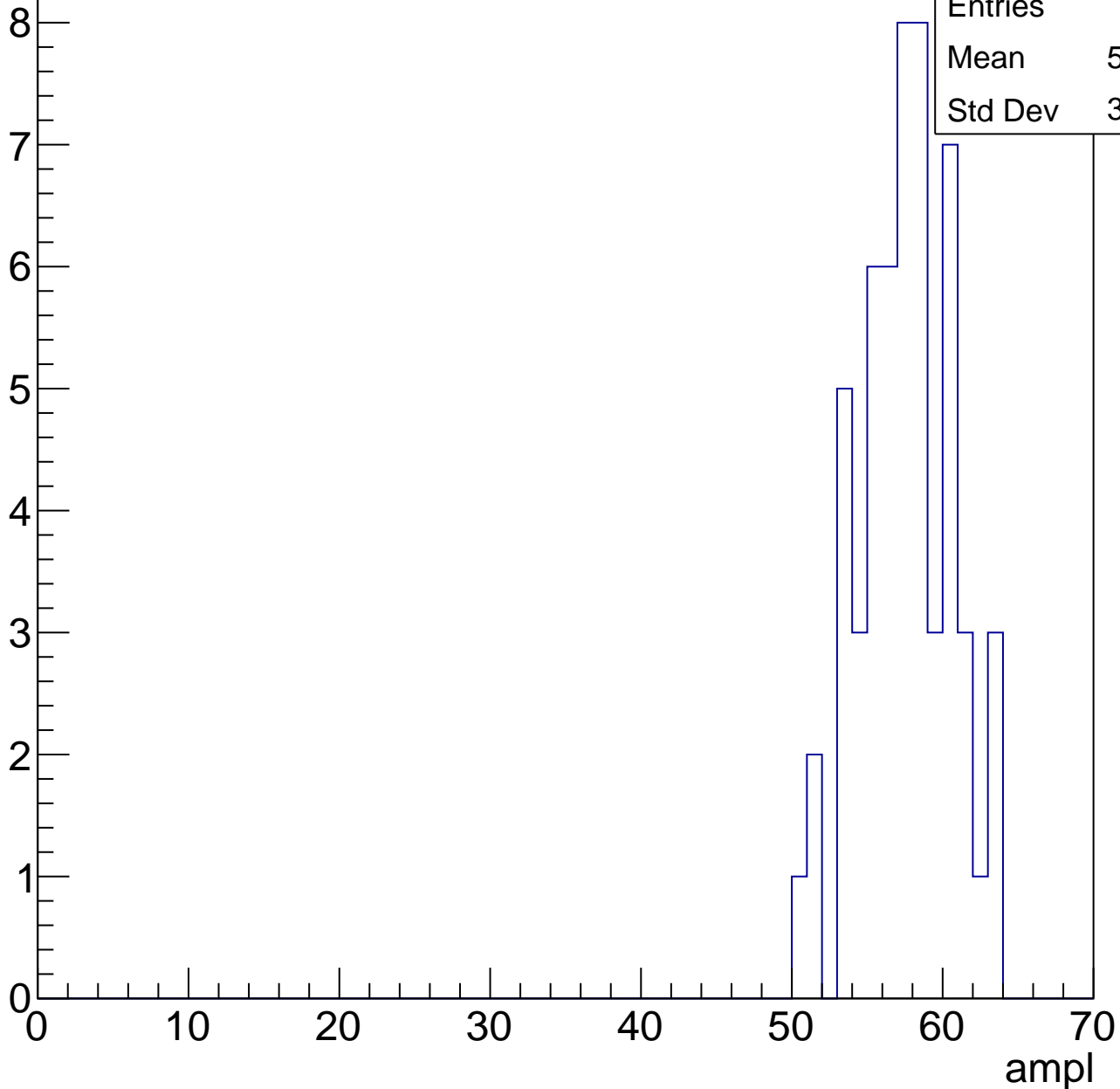


B0L001S, U11-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.07
Std Dev	3.052

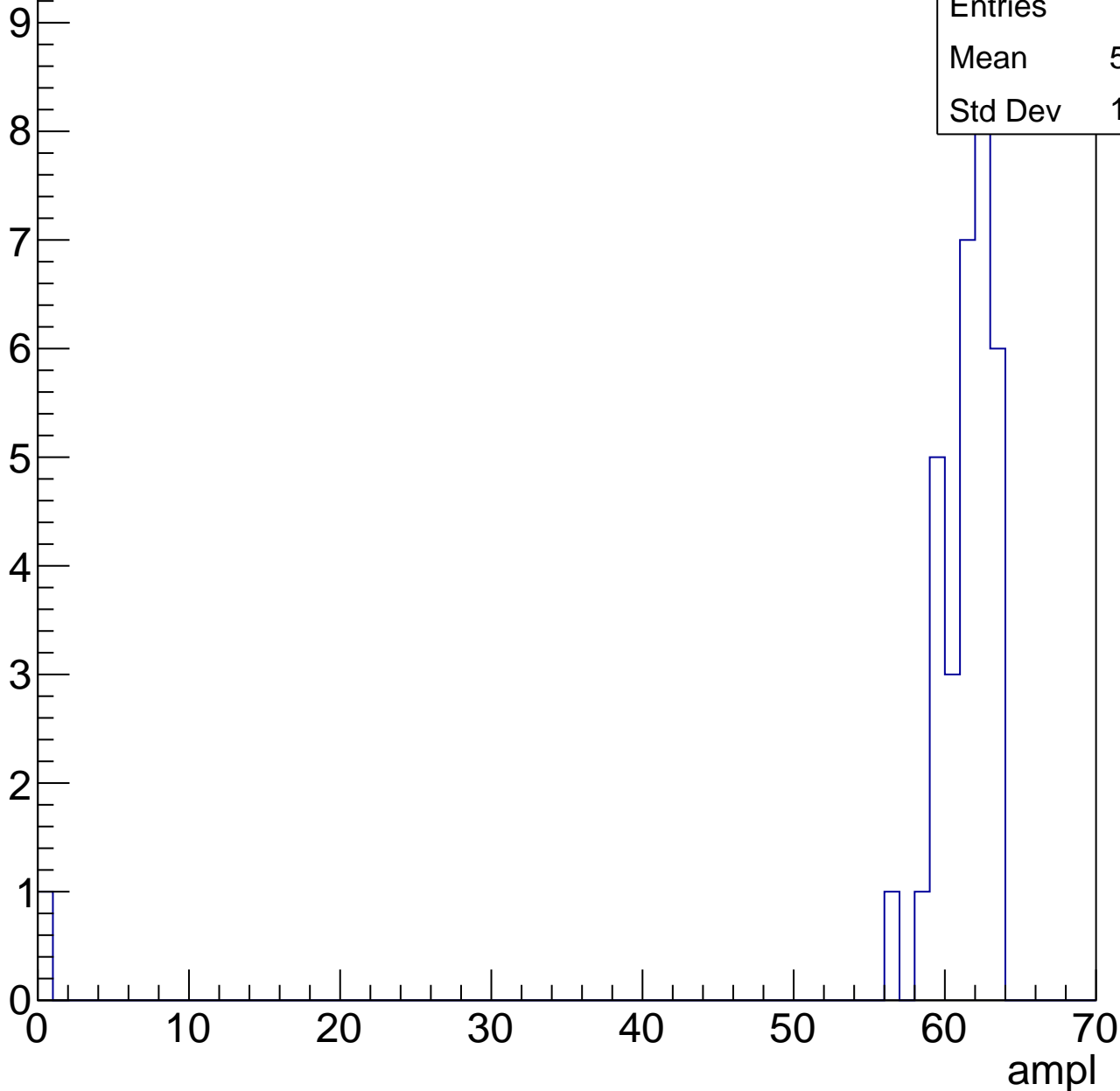


B0L001S, U11-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.15
Std Dev	10.59



B0L001S, U11-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch36, adc0

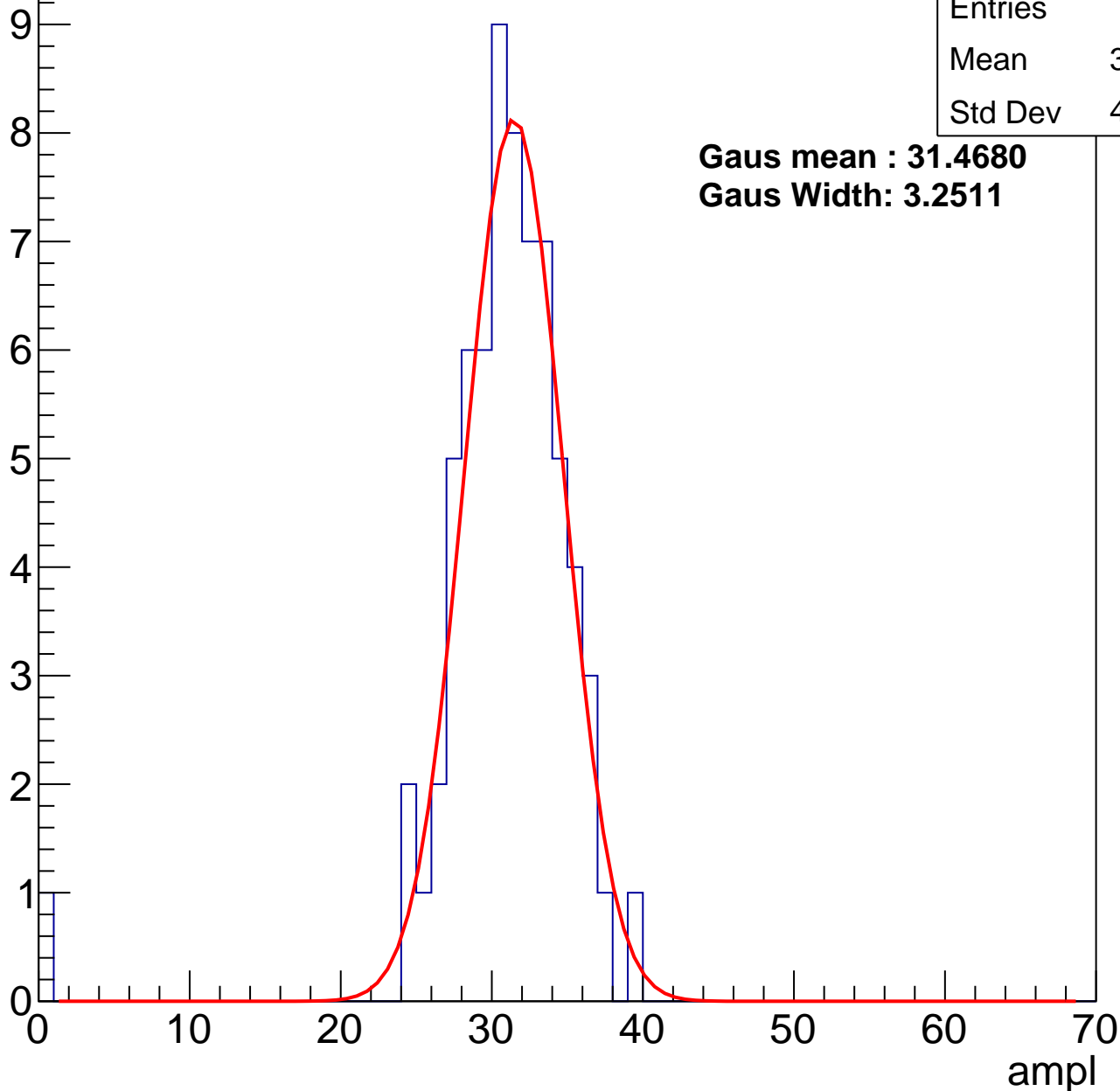
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.43
Std Dev	4.864

Gaus mean : 31.4680

Gaus Width: 3.2511



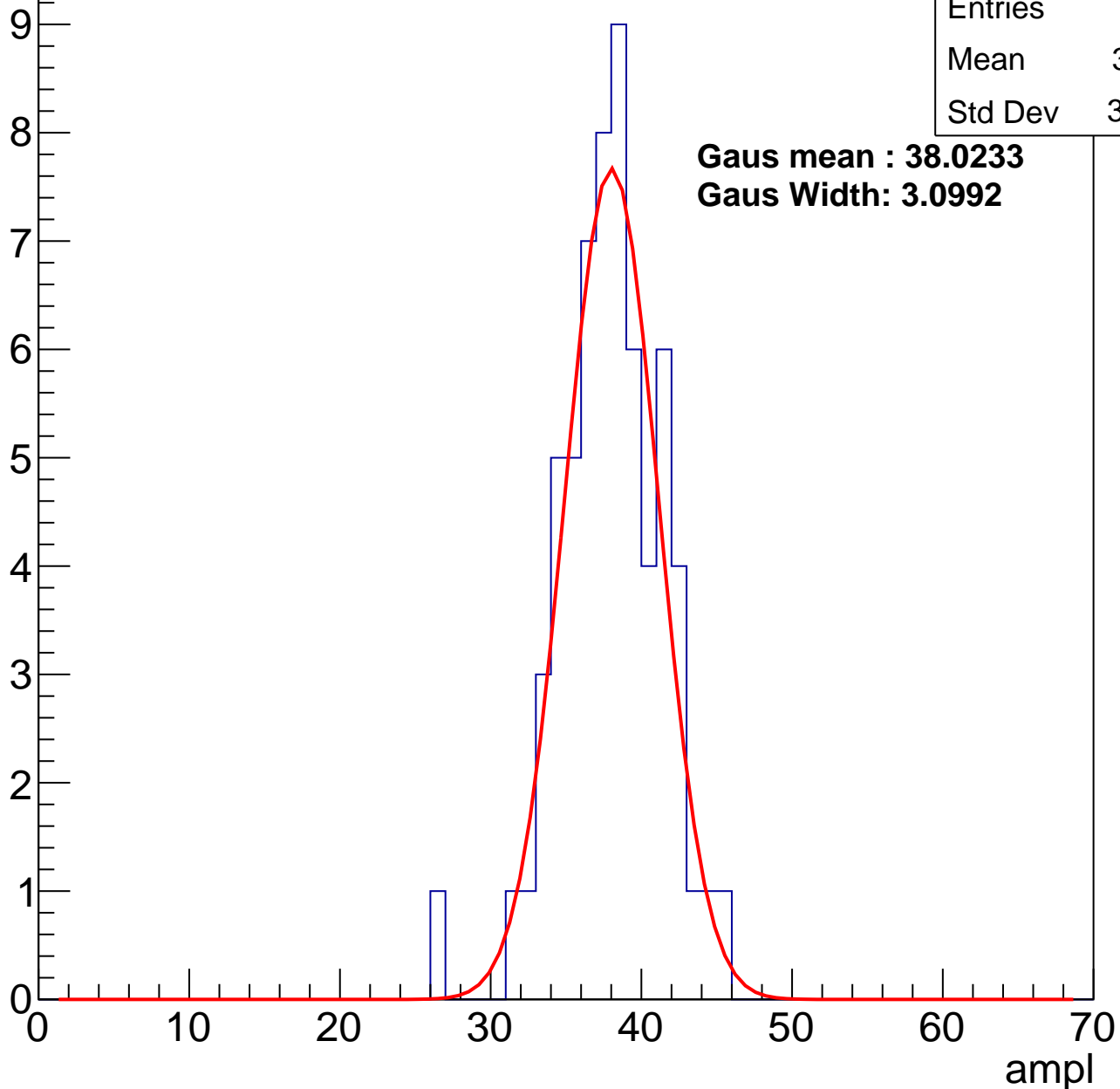
B0L001S, U11-ch36, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.51
Std Dev	3.333

Gaus mean : 38.0233
Gaus Width: 3.0992



B0L001S, U11-ch36, adc2

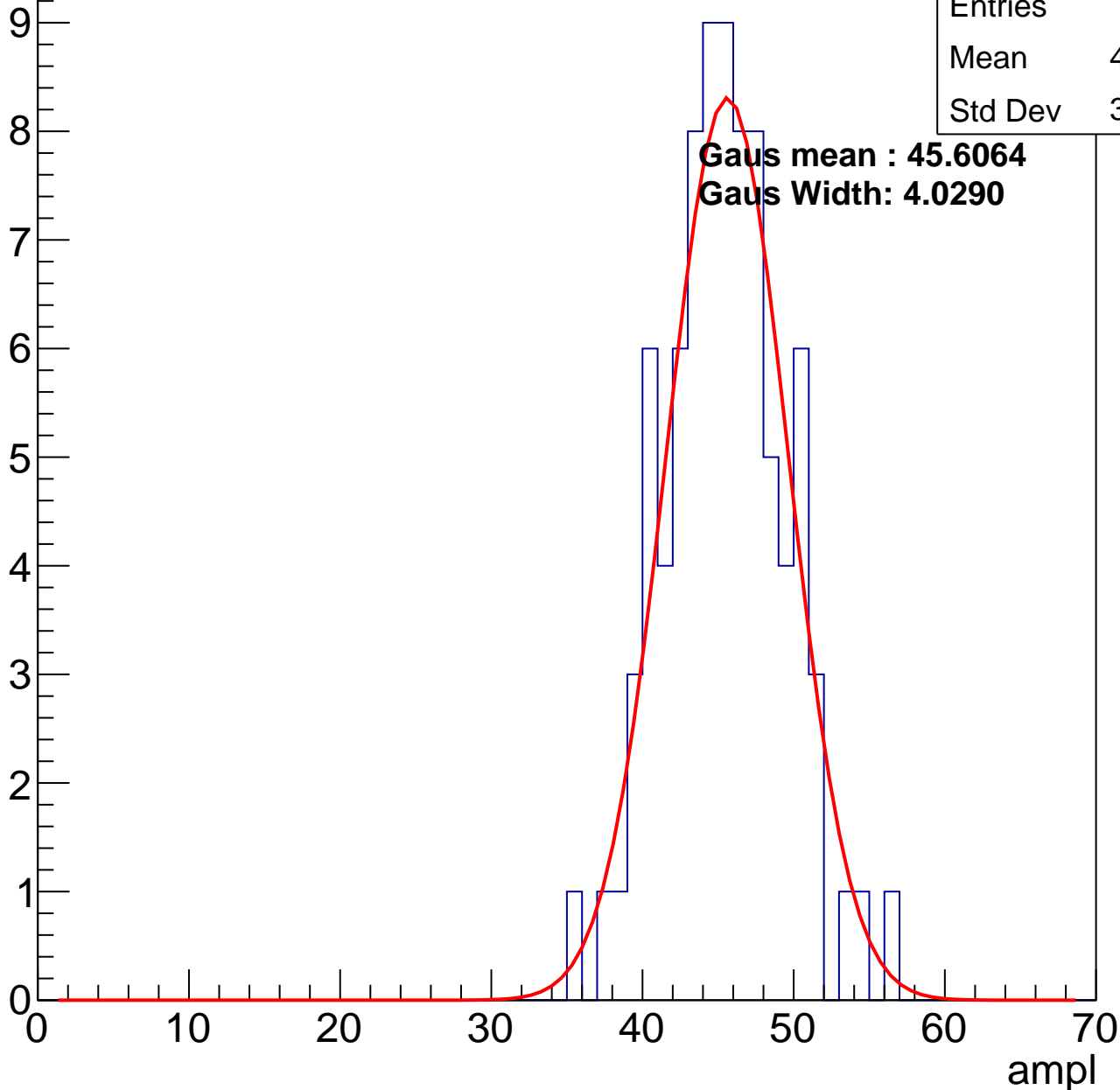
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	44.99
Std Dev	3.915

Gaus mean : 45.6064

Gaus Width: 4.0290

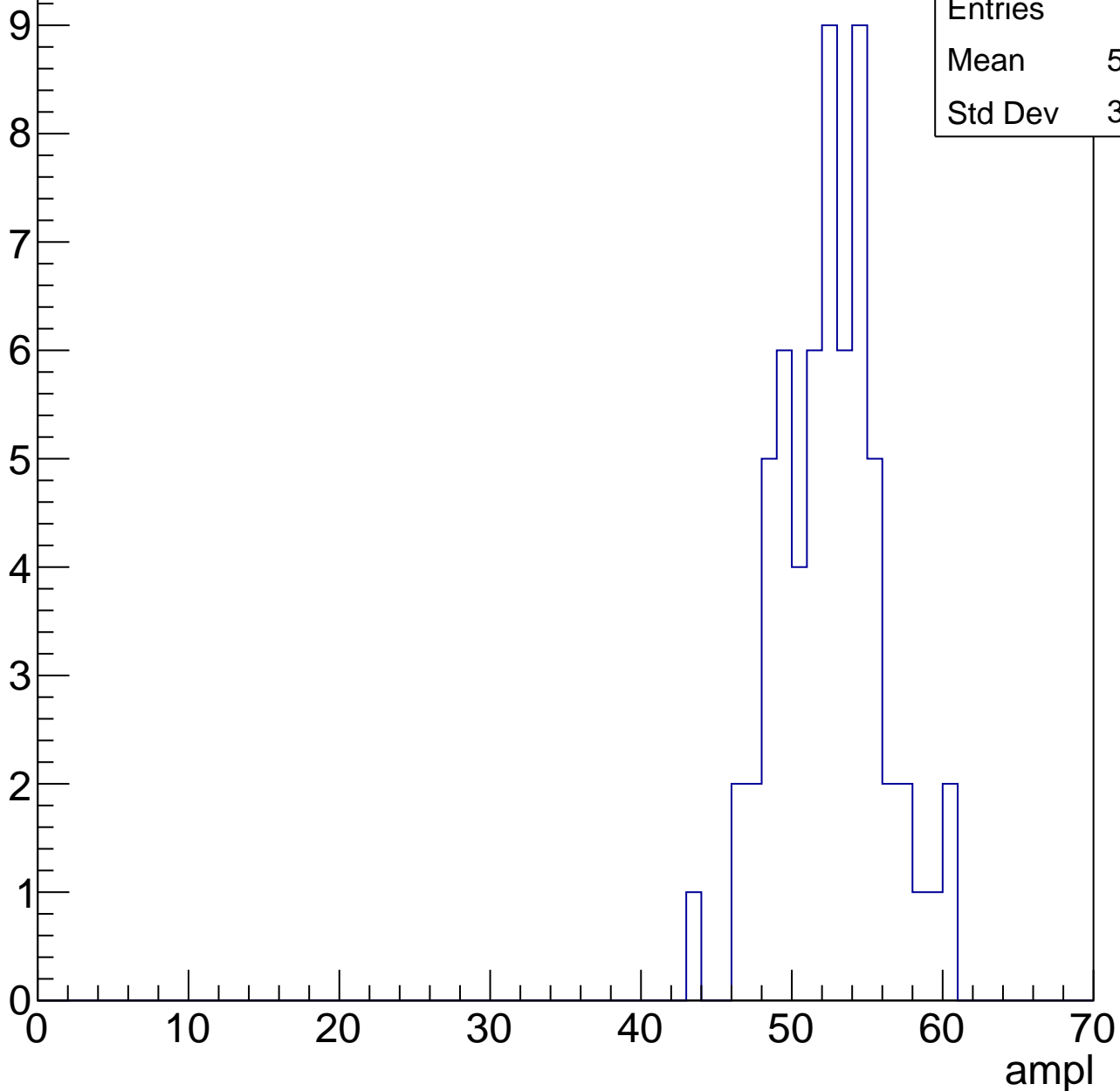


B0L001S, U11-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.05
Std Dev	3.429

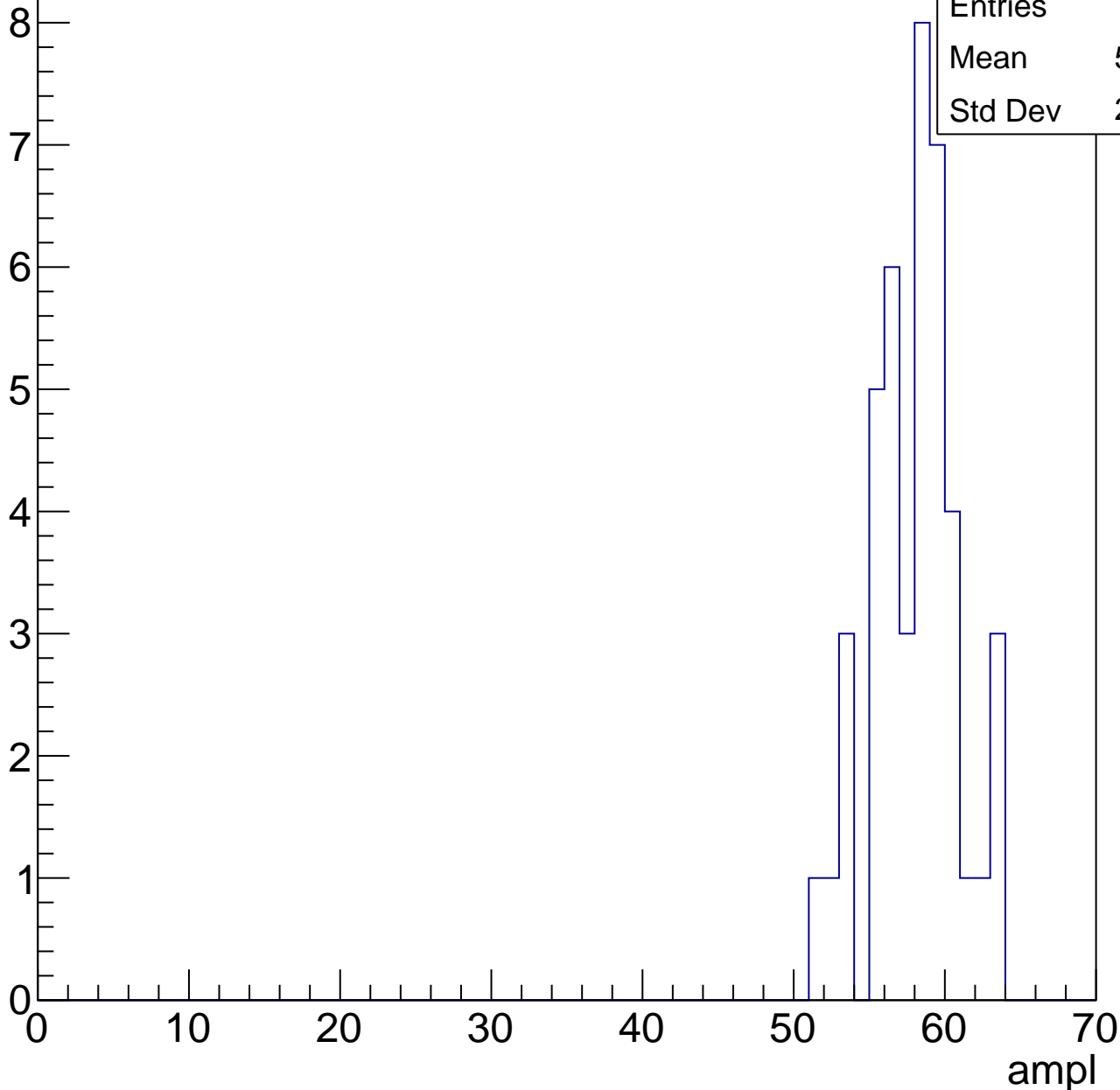


B0L001S, U11-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

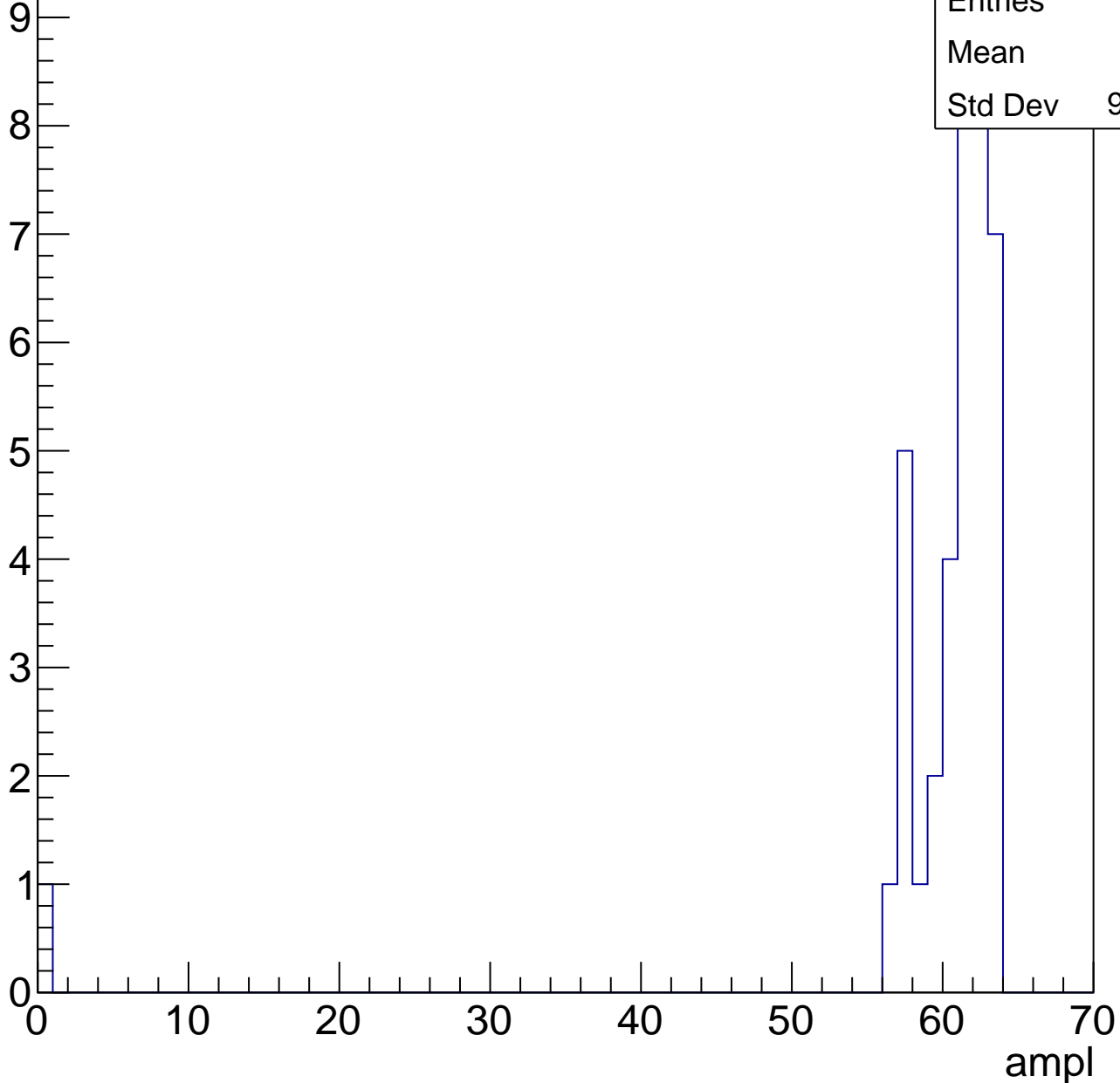
Entries	43
Mean	57.51
Std Dev	2.831



B0L001S, U11-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch37, adc0

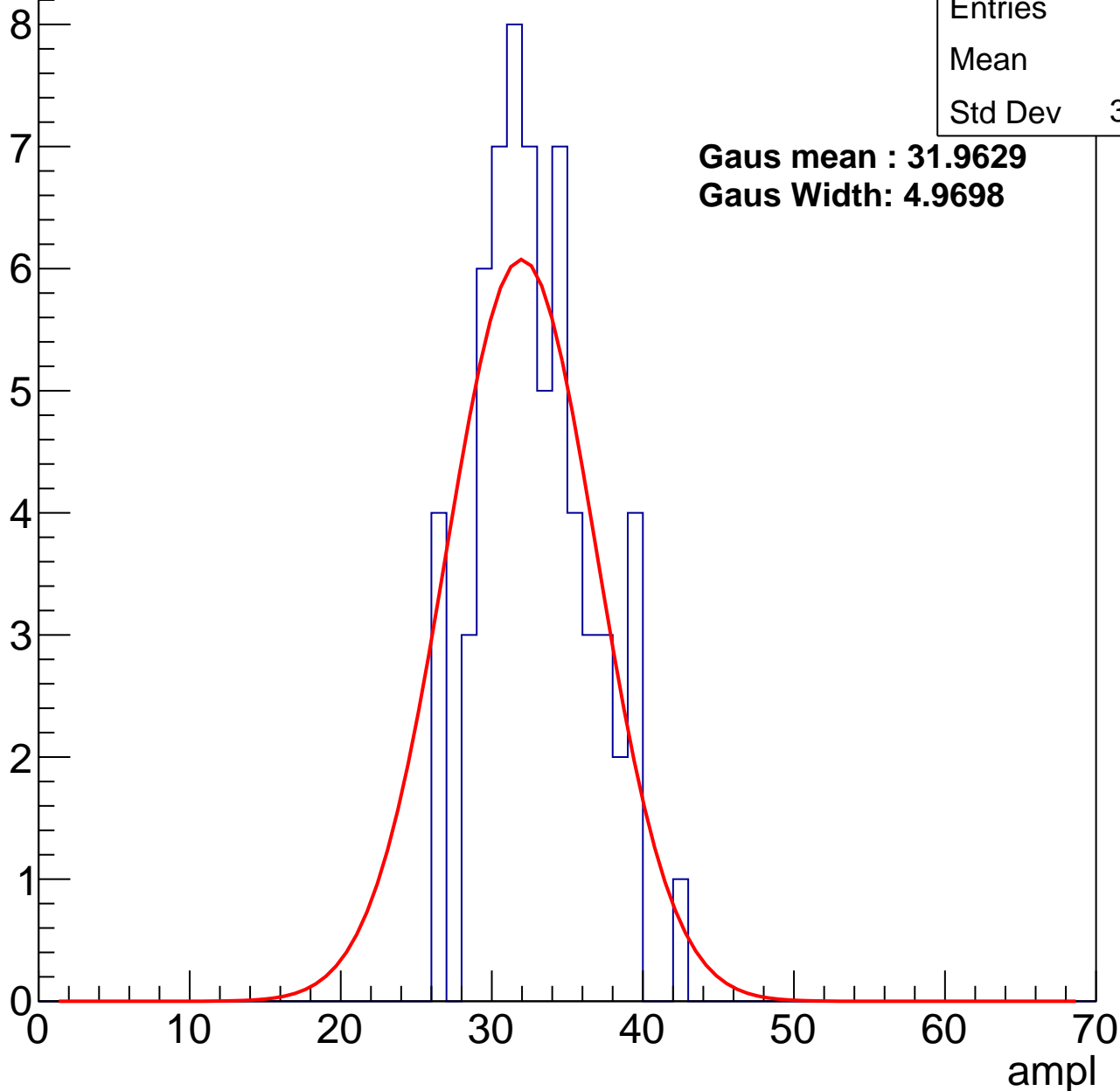
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	32.5
Std Dev	3.593

Gaus mean : 31.9629

Gaus Width: 4.9698



B0L001S, U11-ch37, adc1

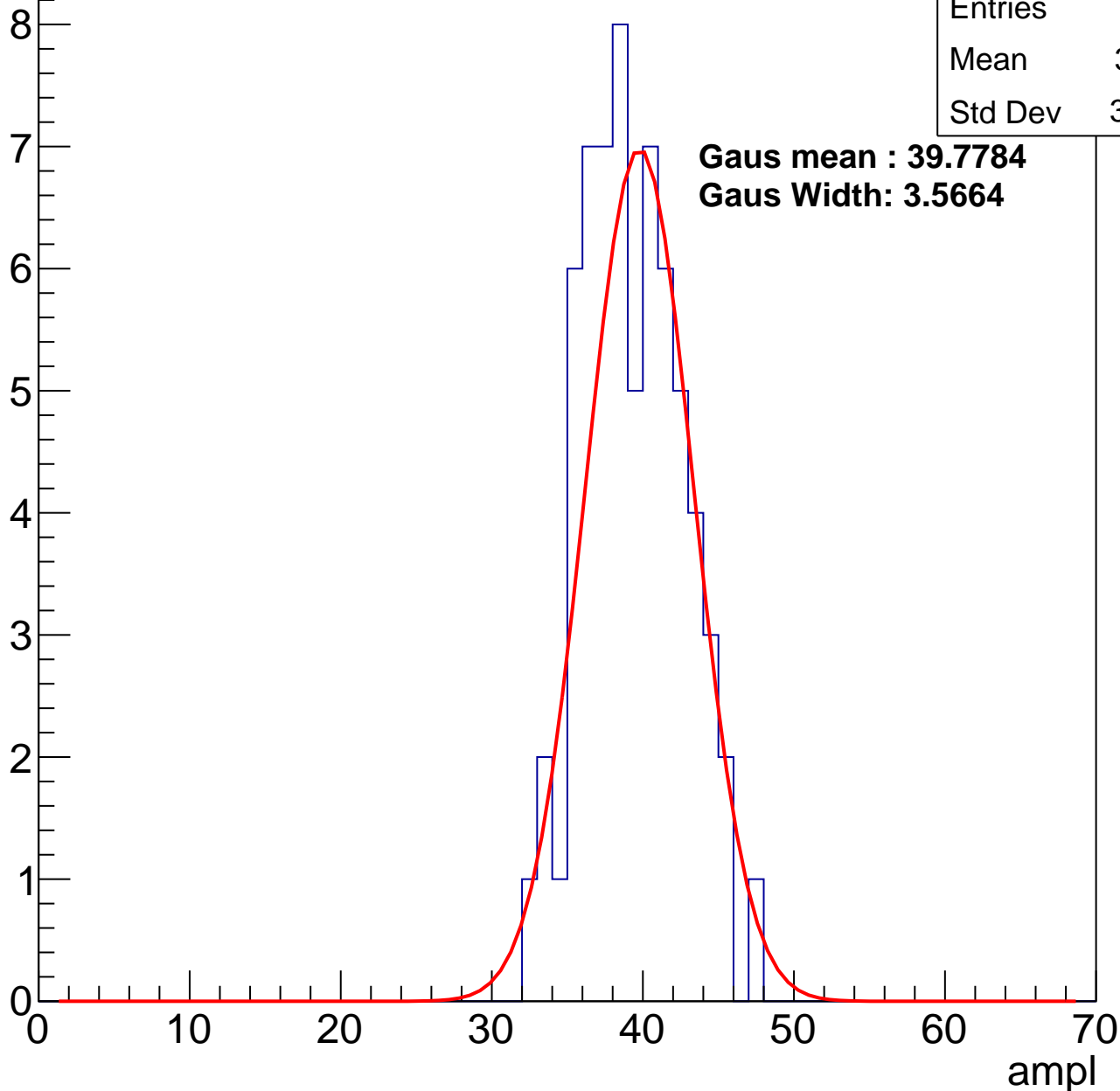
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.91
Std Dev	3.262

Gaus mean : 39.7784

Gaus Width: 3.5664



B0L001S, U11-ch37, adc2

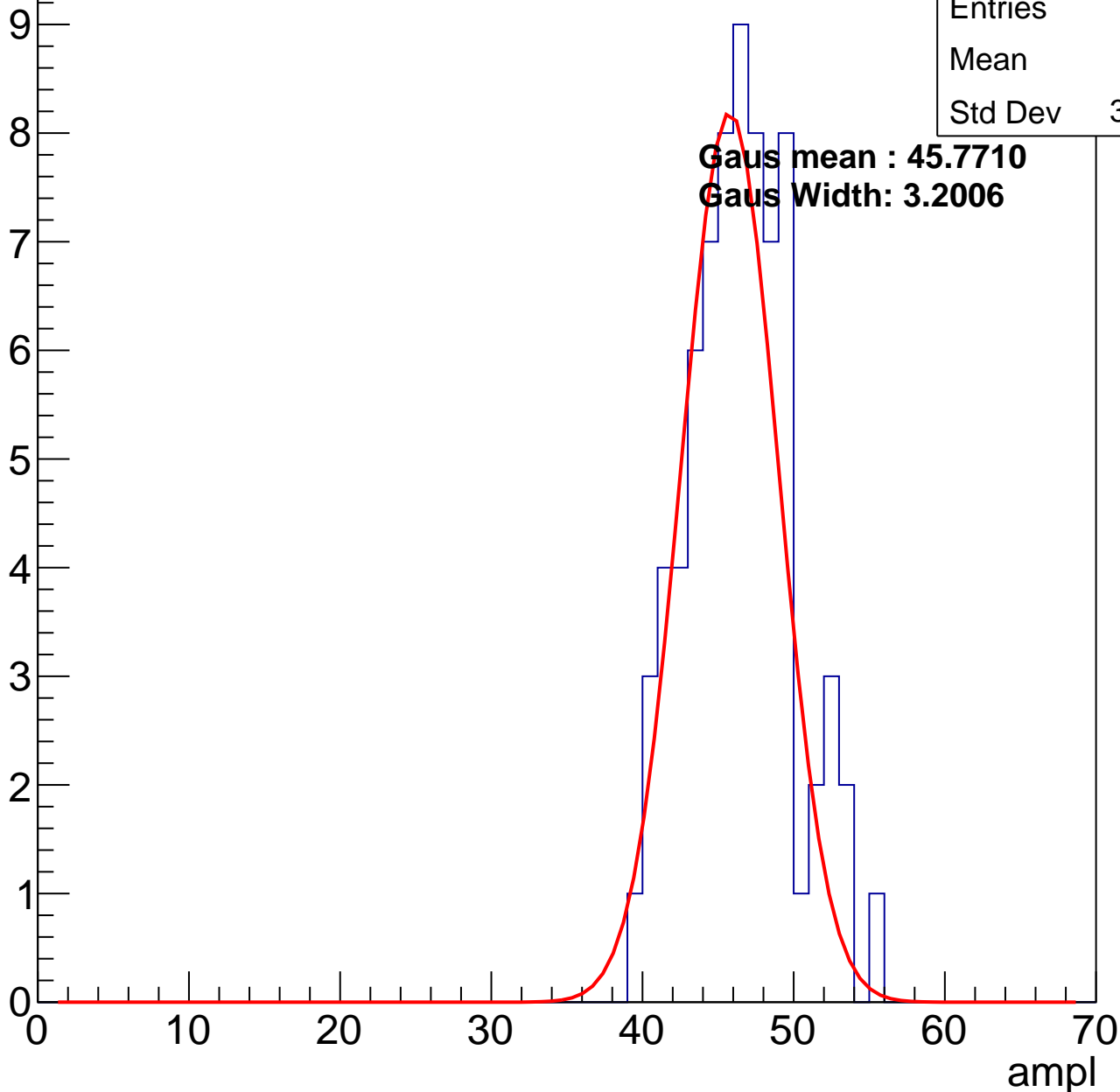
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	46
Std Dev	3.433

Gaus mean : 45.7710

Gaus Width: 3.2006

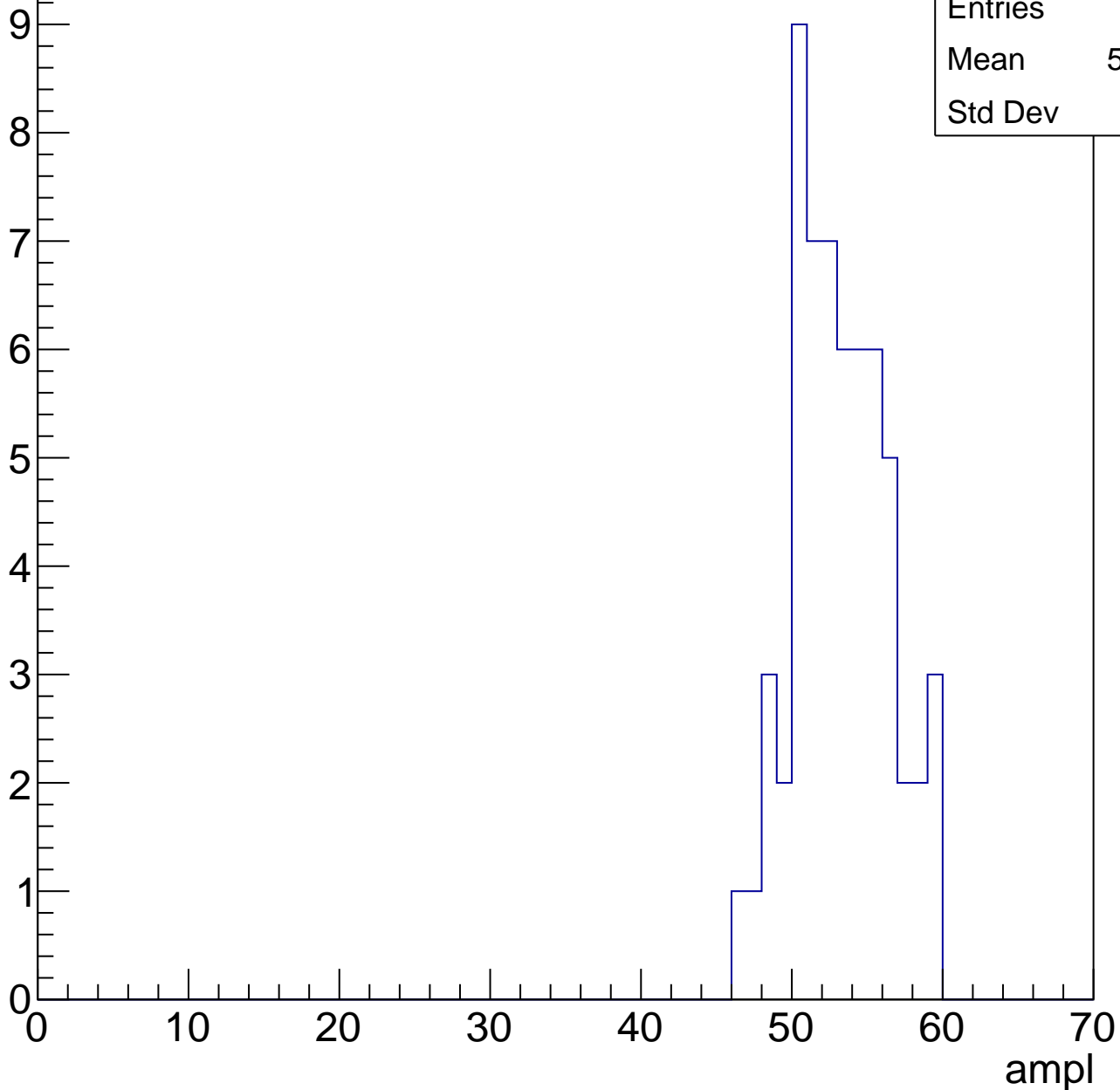


B0L001S, U11-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	52.75
Std Dev	3.08

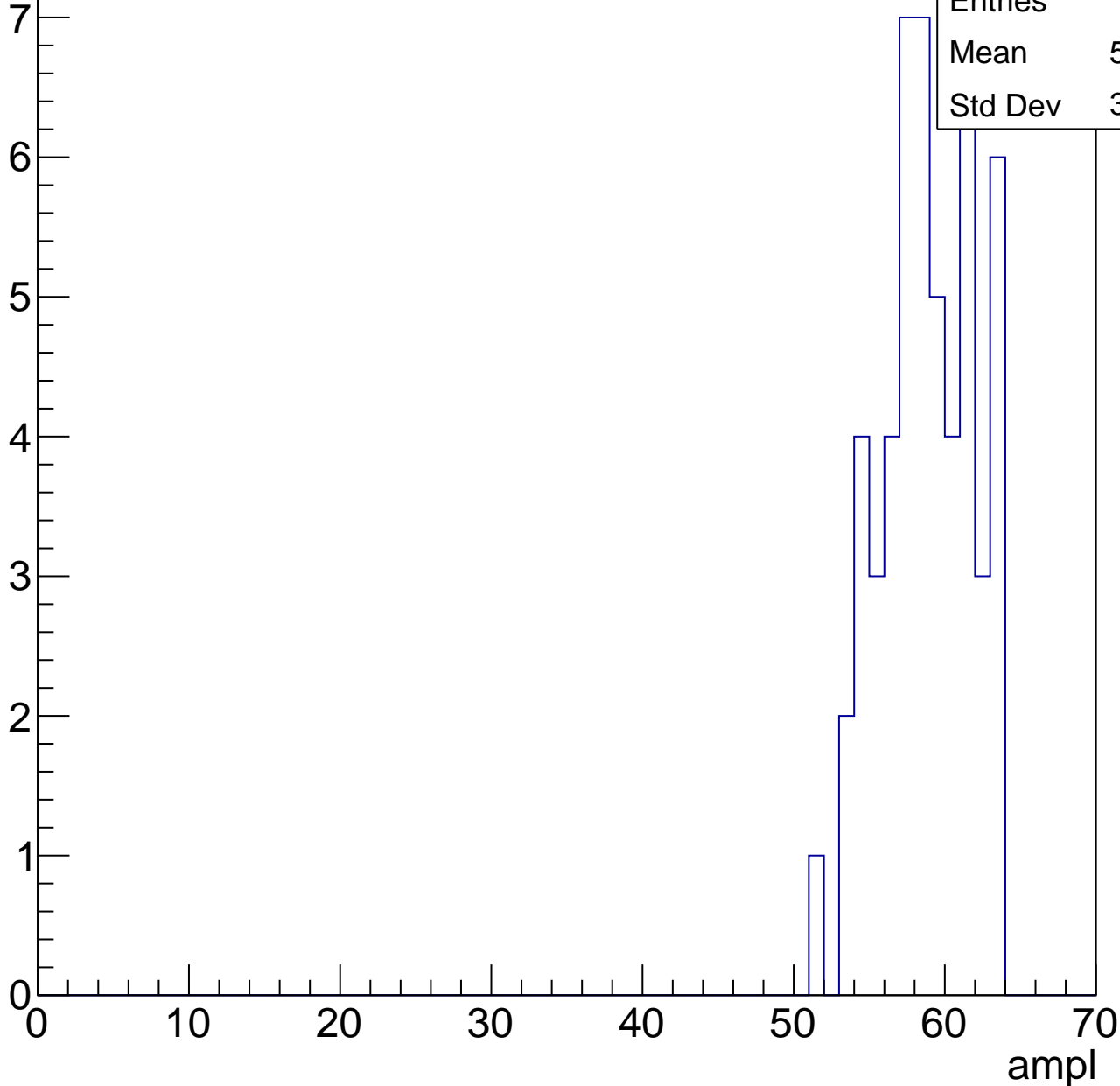


B0L001S, U11-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

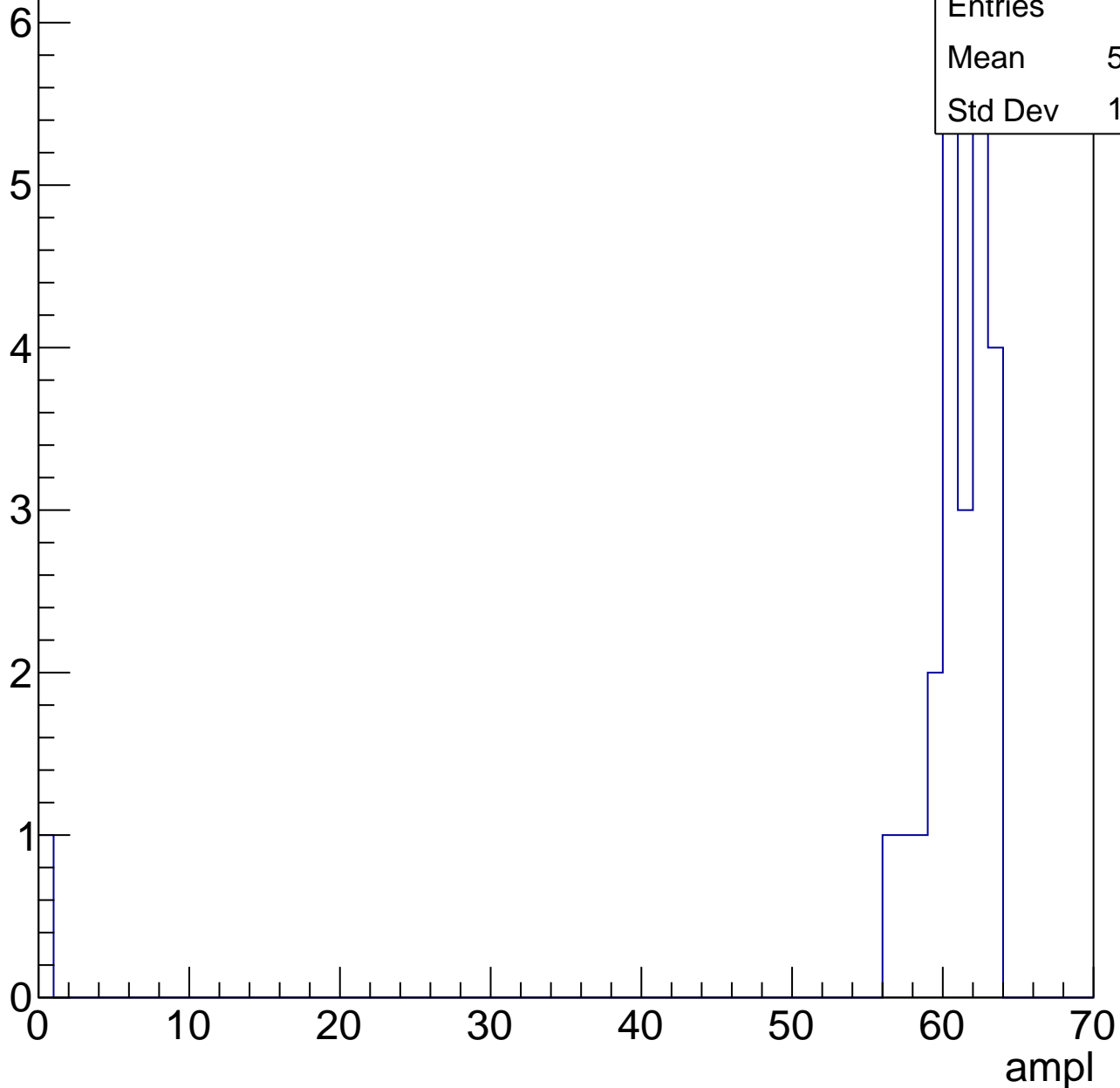
Entries	53
Mean	58.36
Std Dev	3.035



B0L001S, U11-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch38, adc0

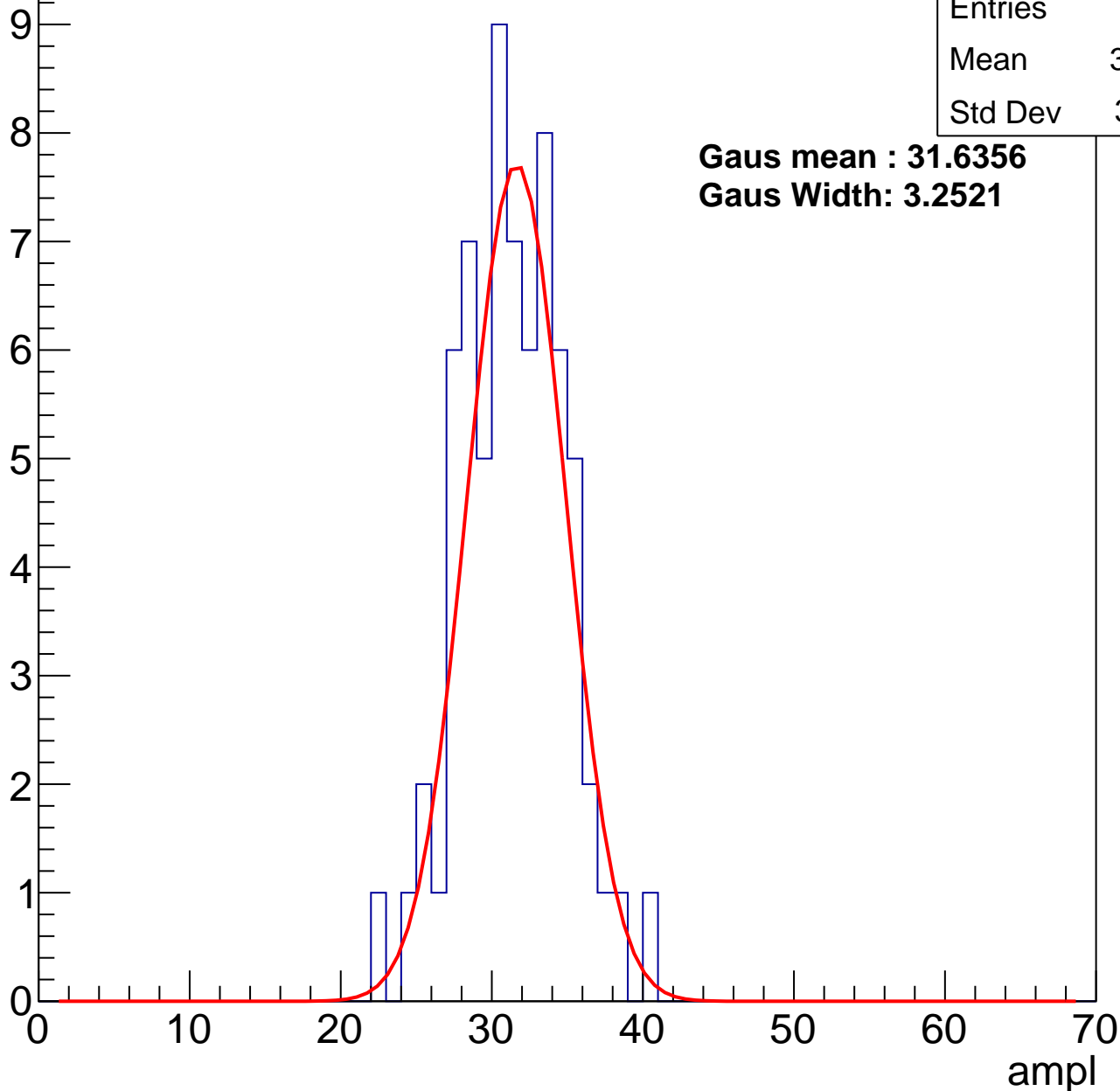
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.93
Std Dev	3.411

Gaus mean : 31.6356

Gaus Width: 3.2521



B0L001S, U11-ch38, adc1

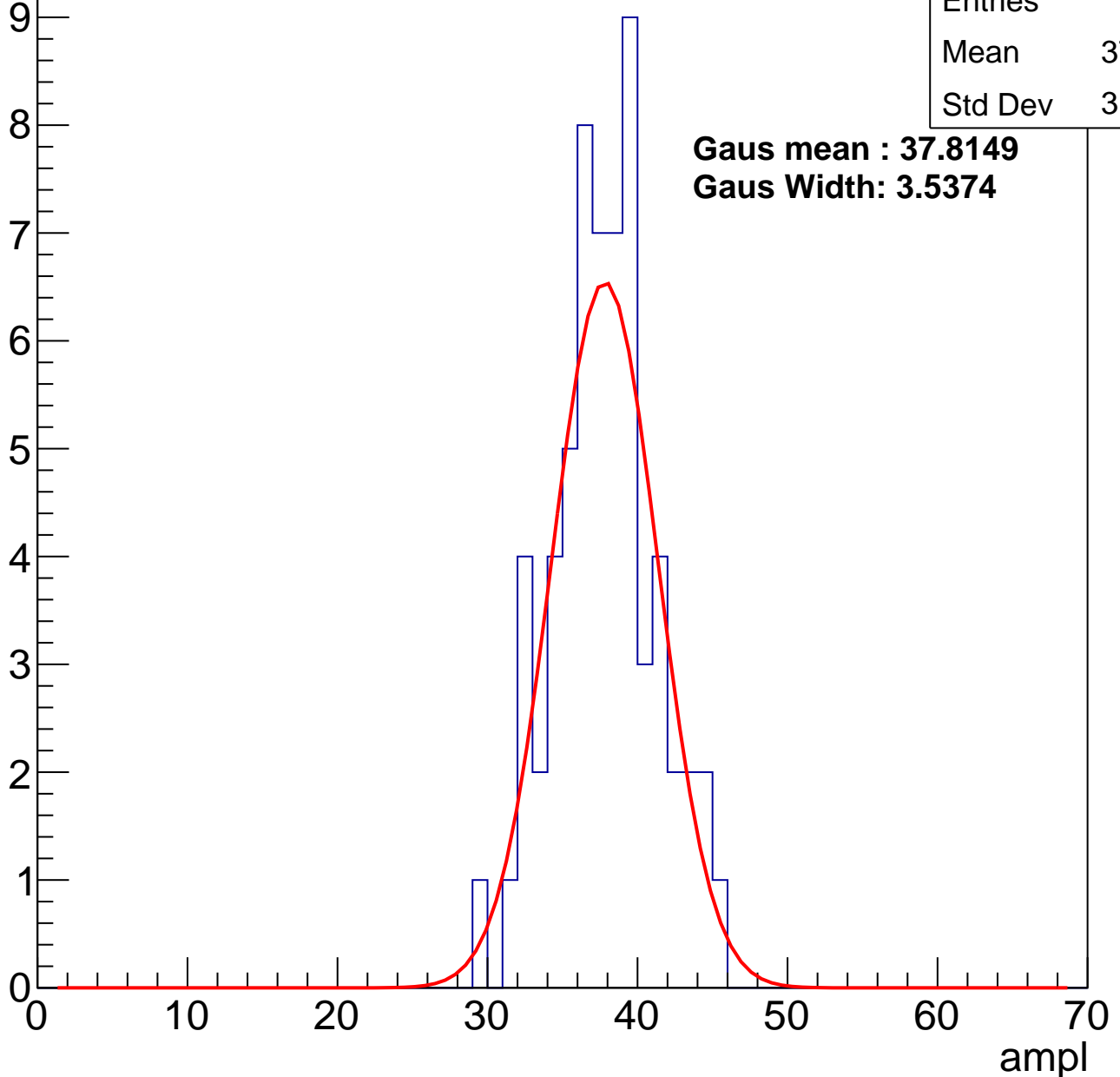
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.35
Std Dev	3.375

Gaus mean : 37.8149

Gaus Width: 3.5374



B0L001S, U11-ch38, adc2

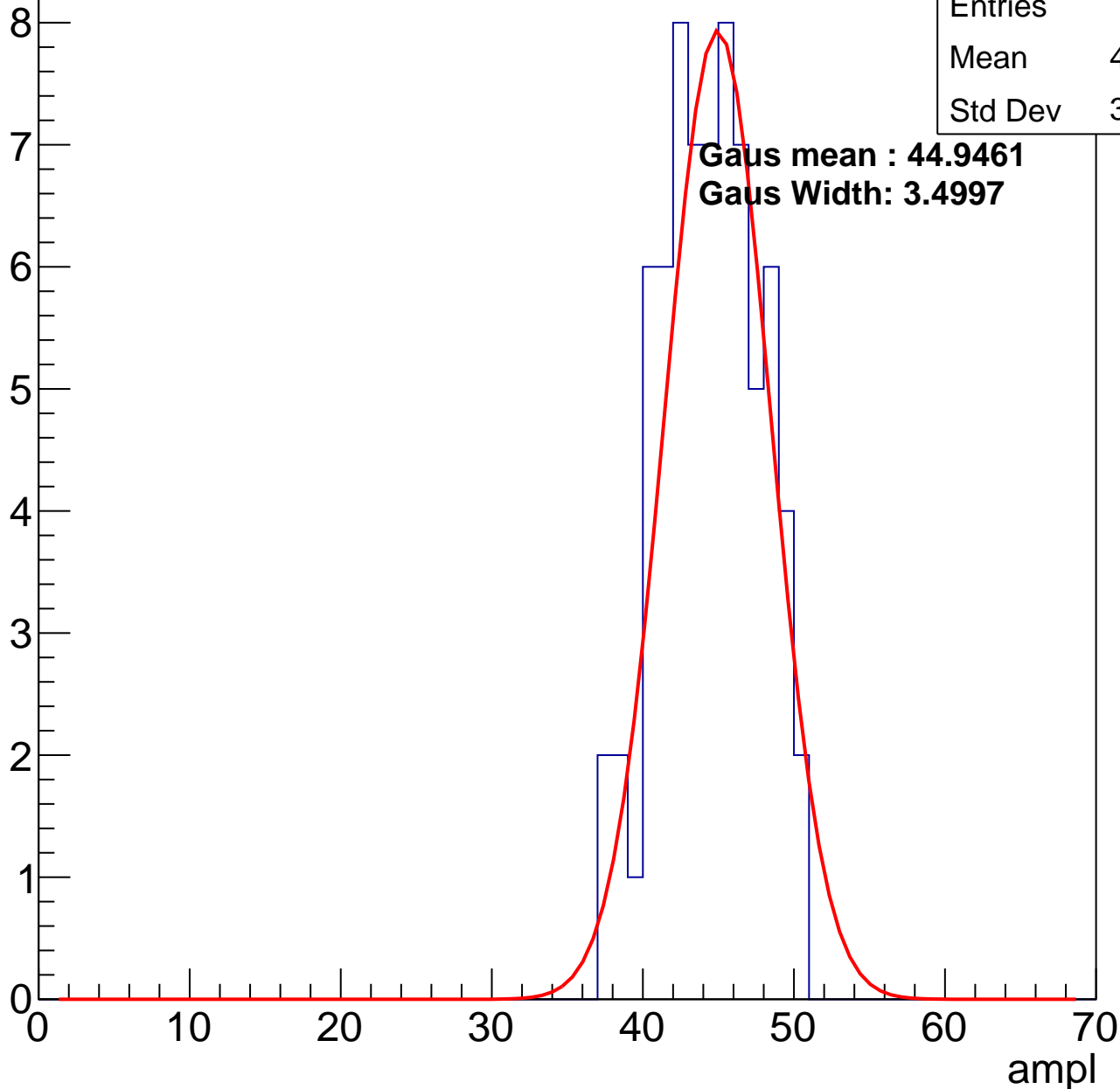
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.96
Std Dev	3.213

Gaus mean : 44.9461

Gaus Width: 3.4997

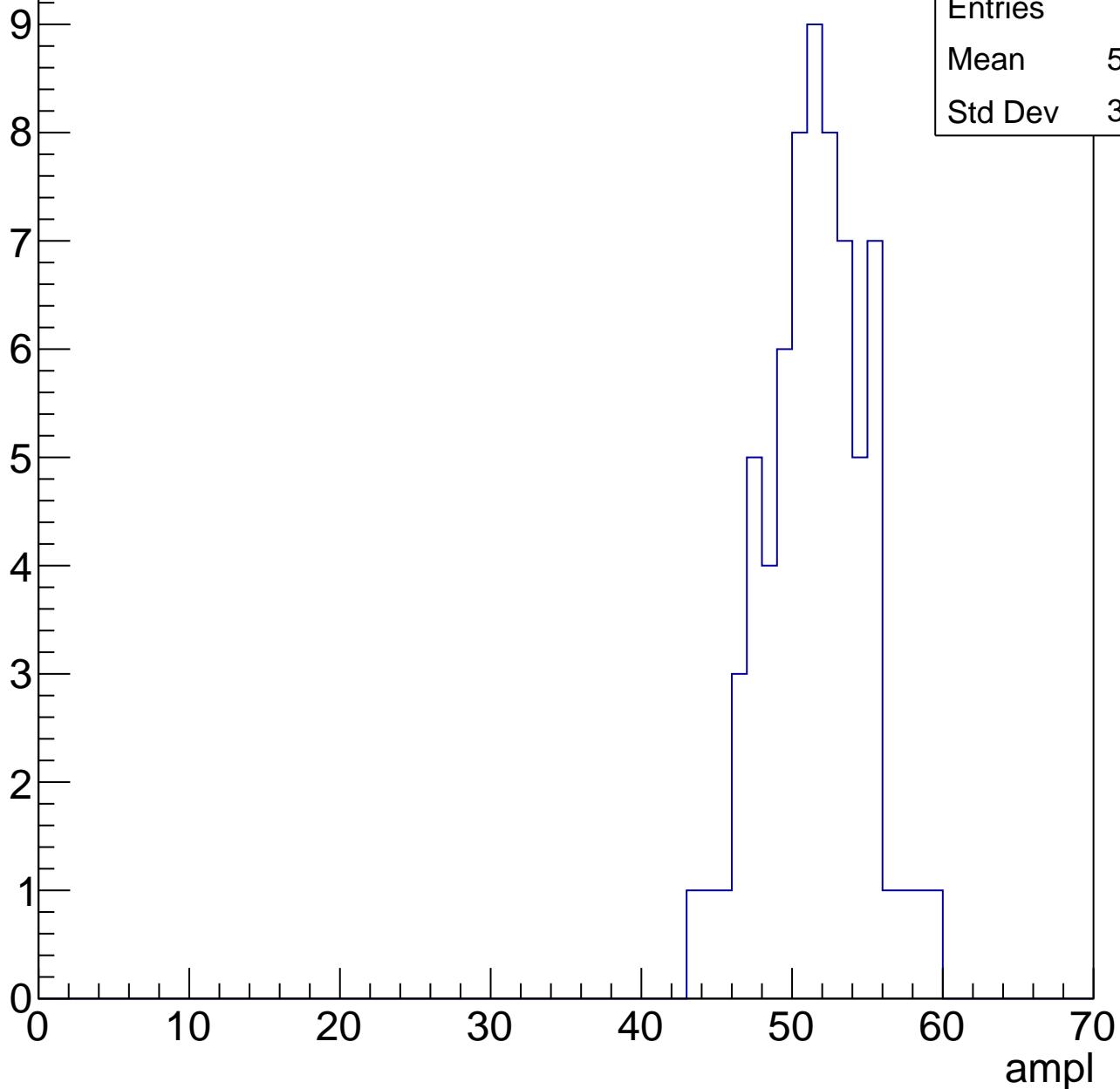


B0L001S, U11-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.04
Std Dev	3.272

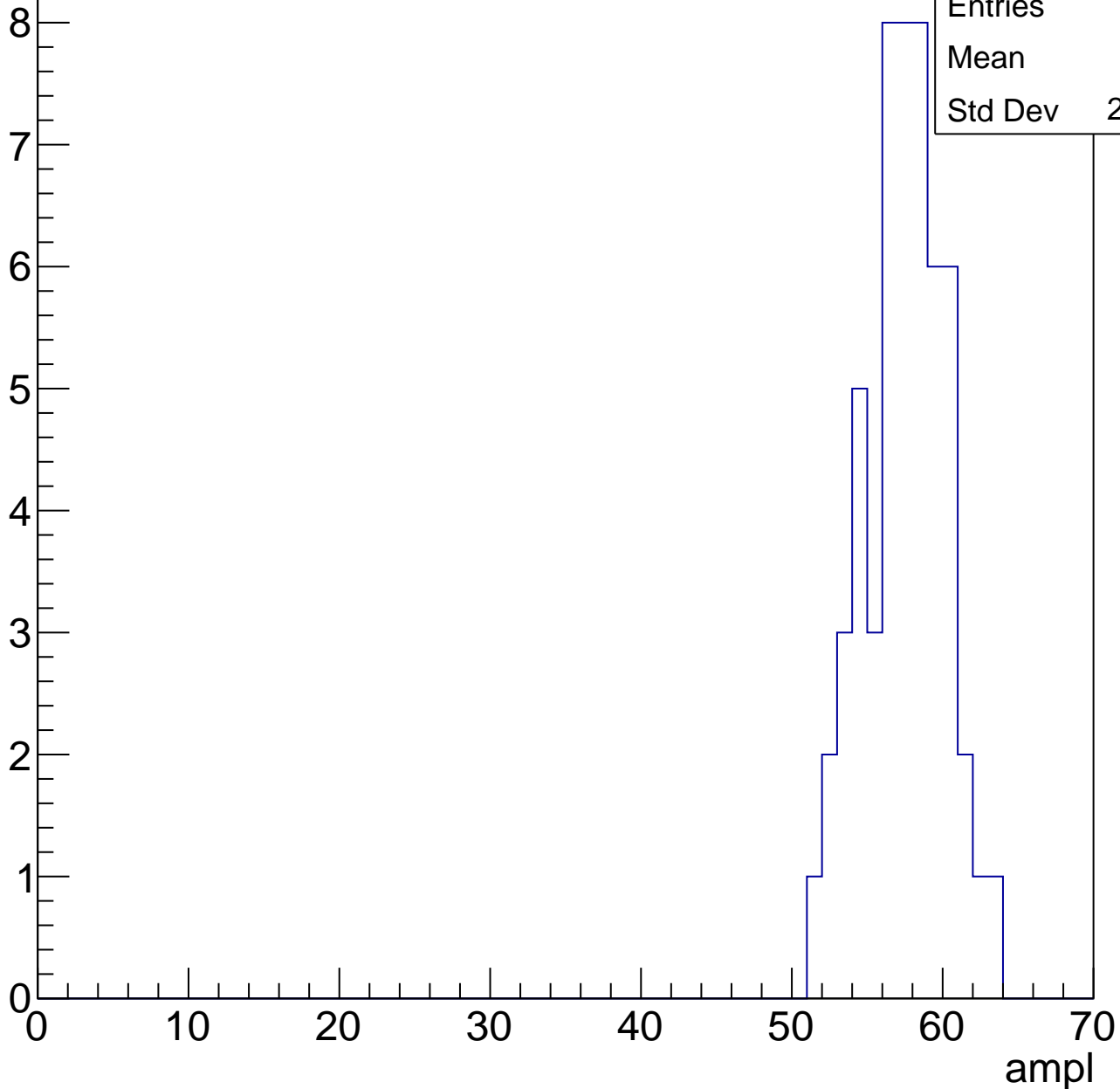


B0L001S, U11-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

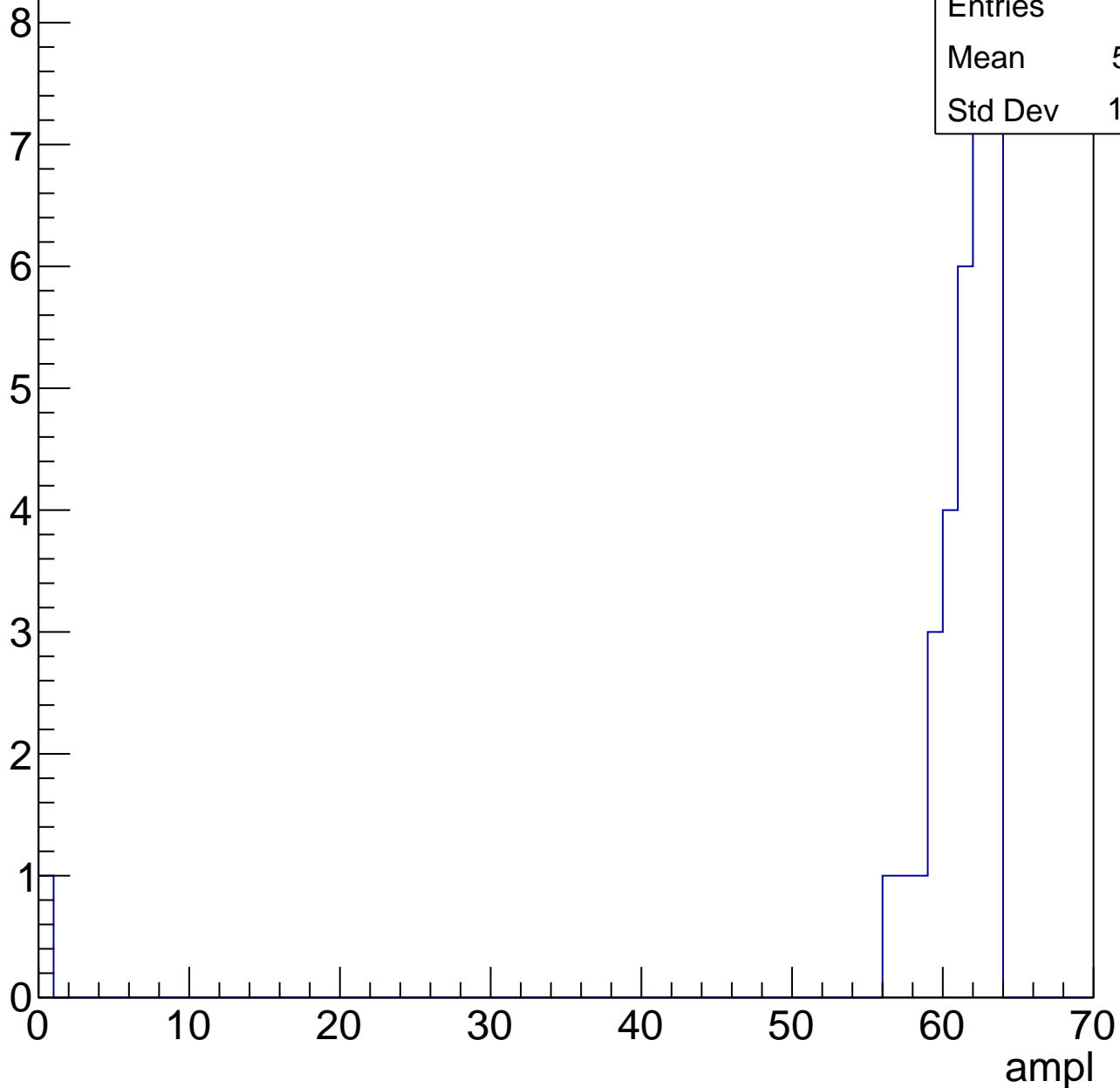
Entries	54
Mean	57
Std Dev	2.646



B0L001S, U11-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L001S, U11-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch39, adc0

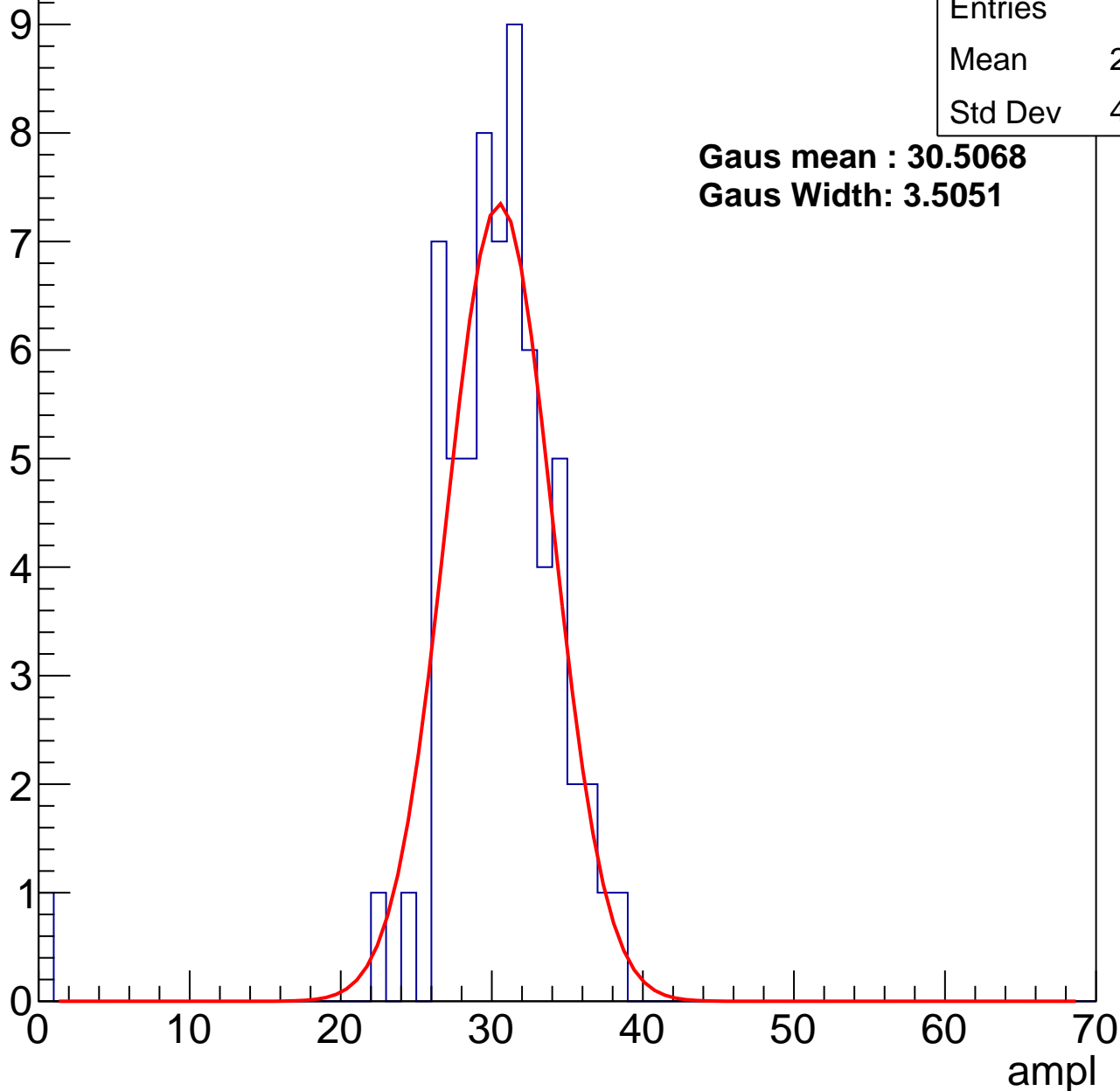
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.77
Std Dev	4.908

Gaus mean : 30.5068

Gaus Width: 3.5051



B0L001S, U11-ch39, adc1

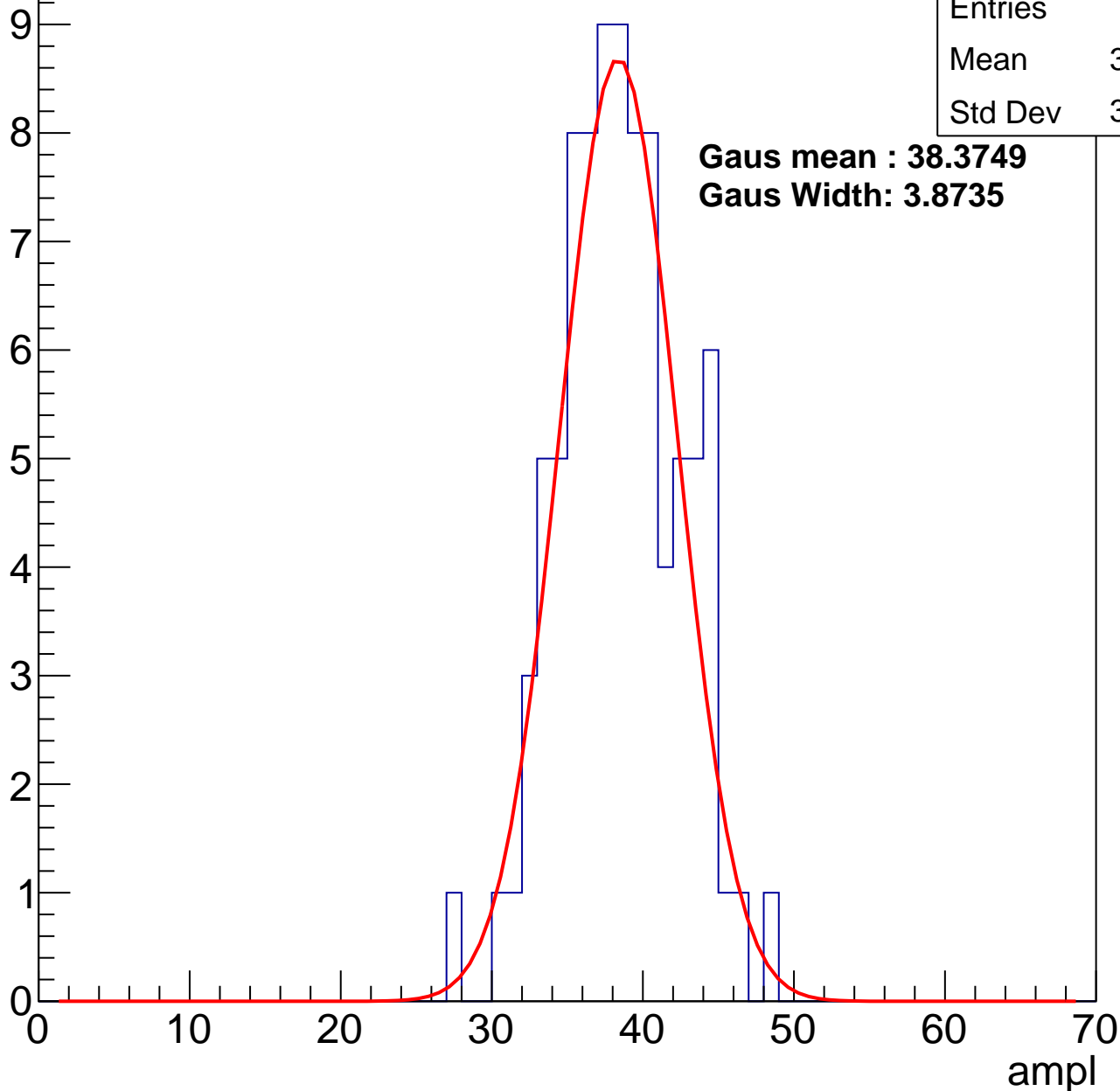
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	89
Mean	38.04
Std Dev	3.923

Gaus mean : 38.3749

Gaus Width: 3.8735



B0L001S, U11-ch39, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	45.65
Std Dev	3.345

Gaus mean : 45.9196

Gaus Width: 3.6367

Entry

10

8

6

4

2

0

0

10

20

30

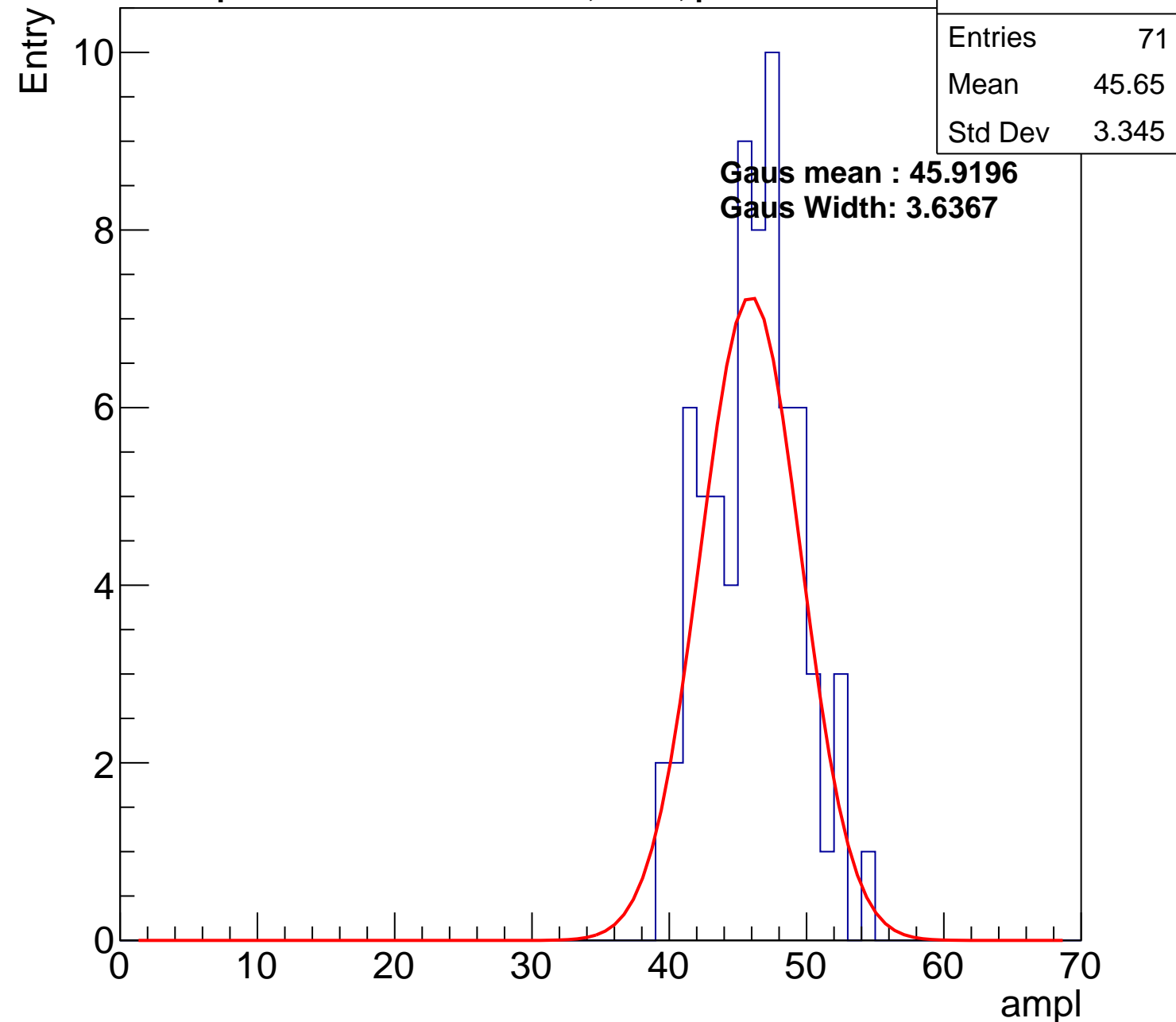
40

50

60

70

ampl

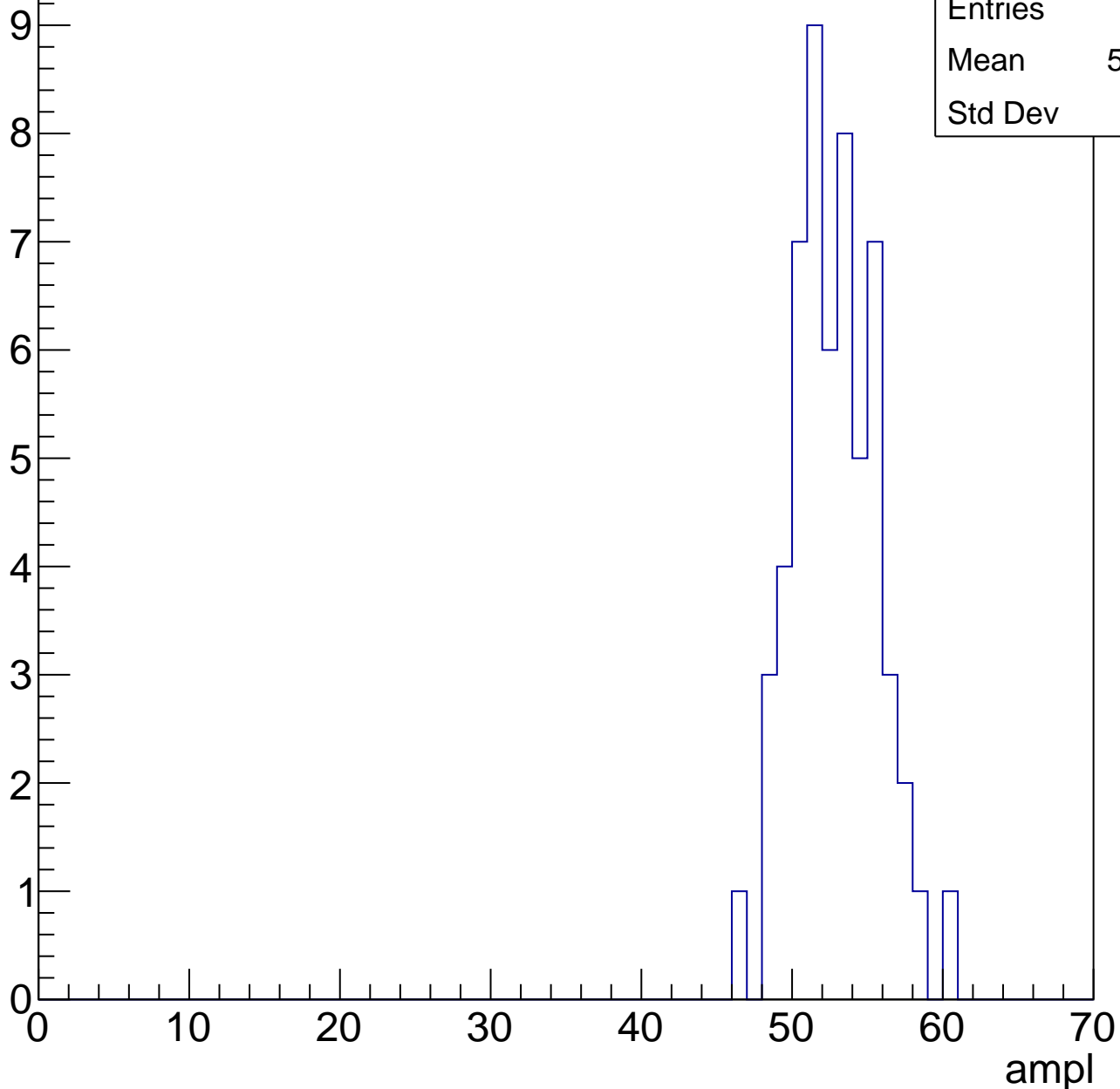


B0L001S, U11-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	52.39
Std Dev	2.77

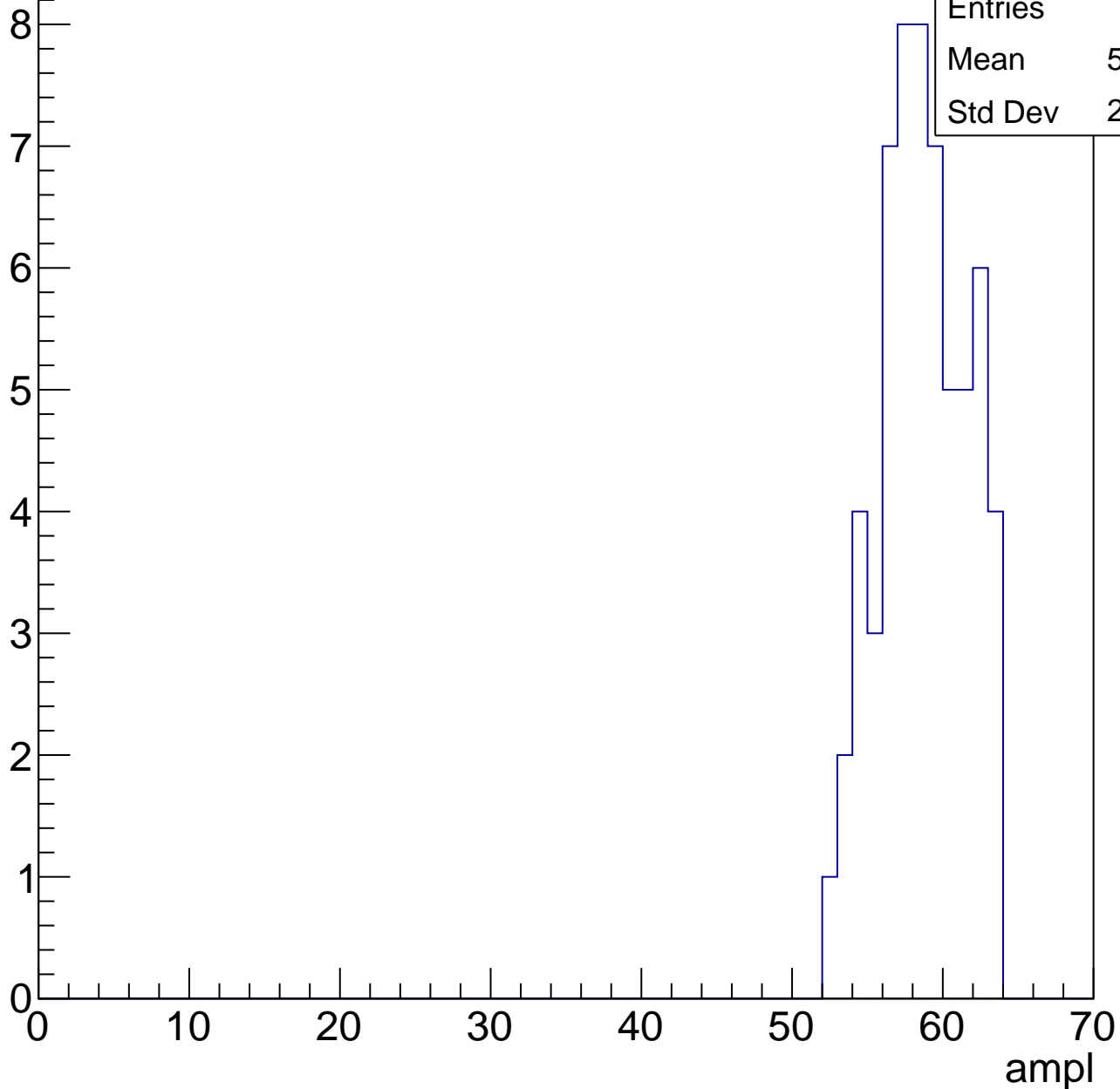


B0L001S, U11-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	58.22
Std Dev	2.823

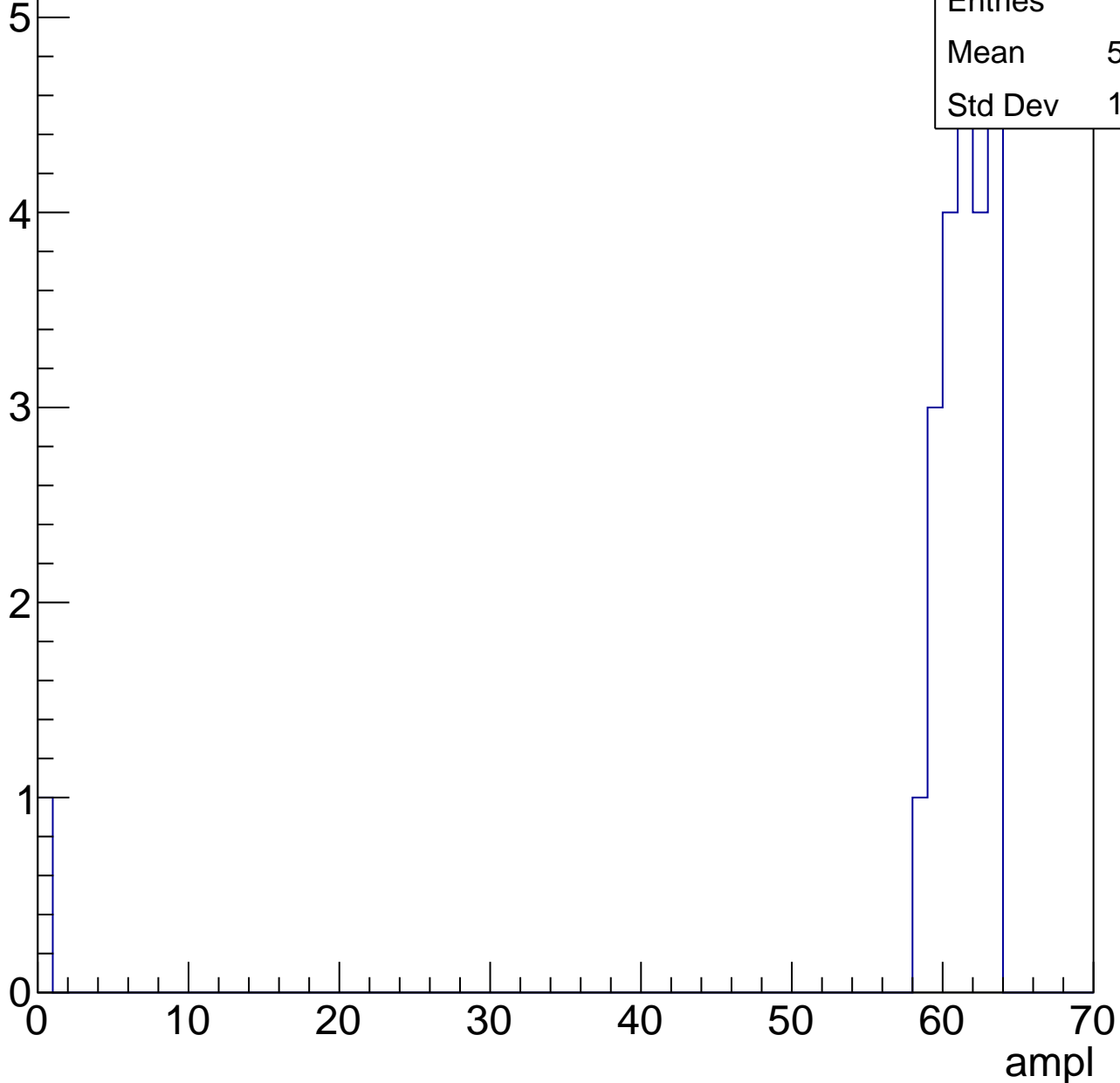


B0L001S, U11-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.39
Std Dev	12.53



B0L001S, U11-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch40, adc0

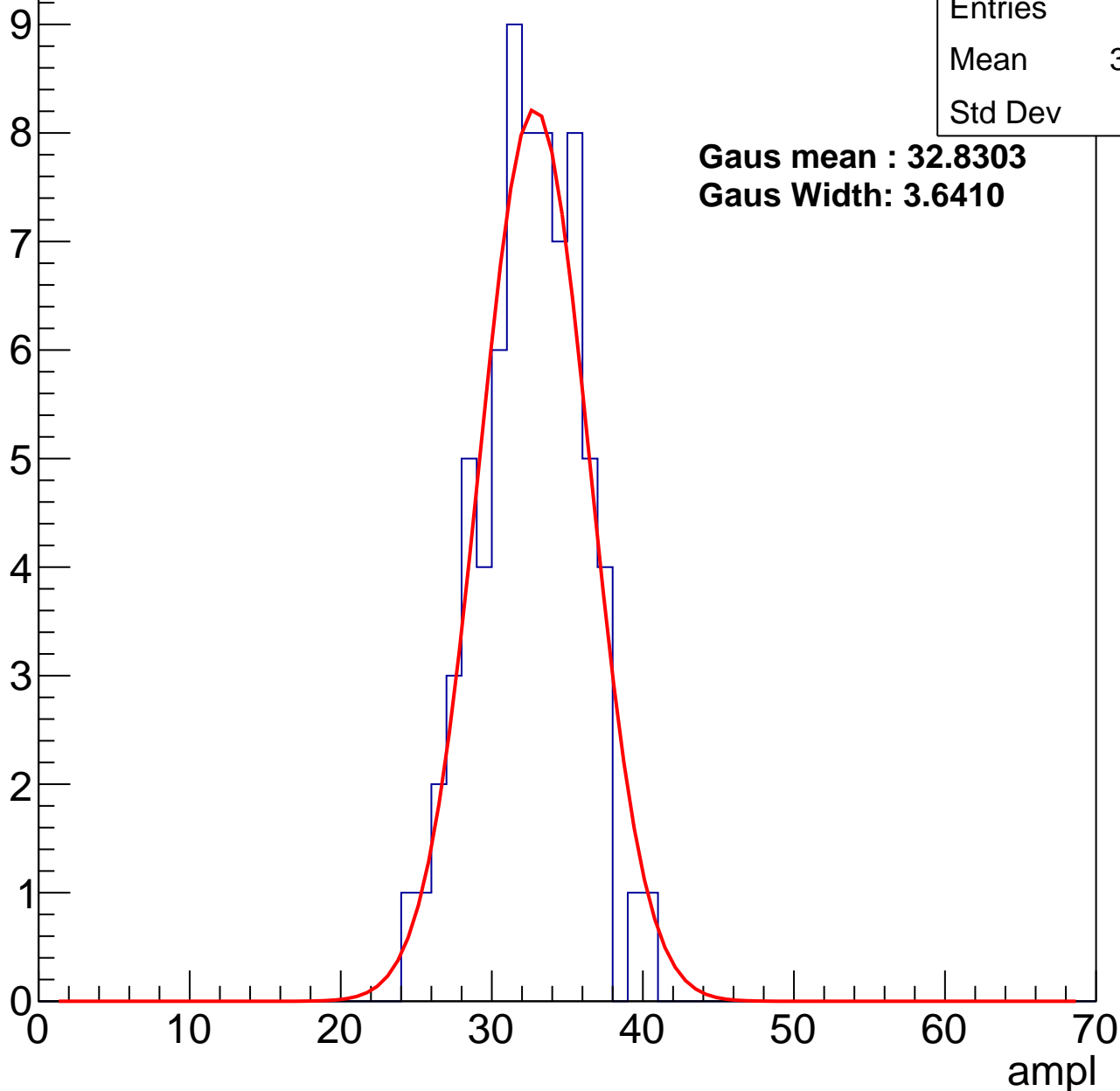
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	32.08
Std Dev	3.33

Gaus mean : 32.8303

Gaus Width: 3.6410



B0L001S, U11-ch40, adc1

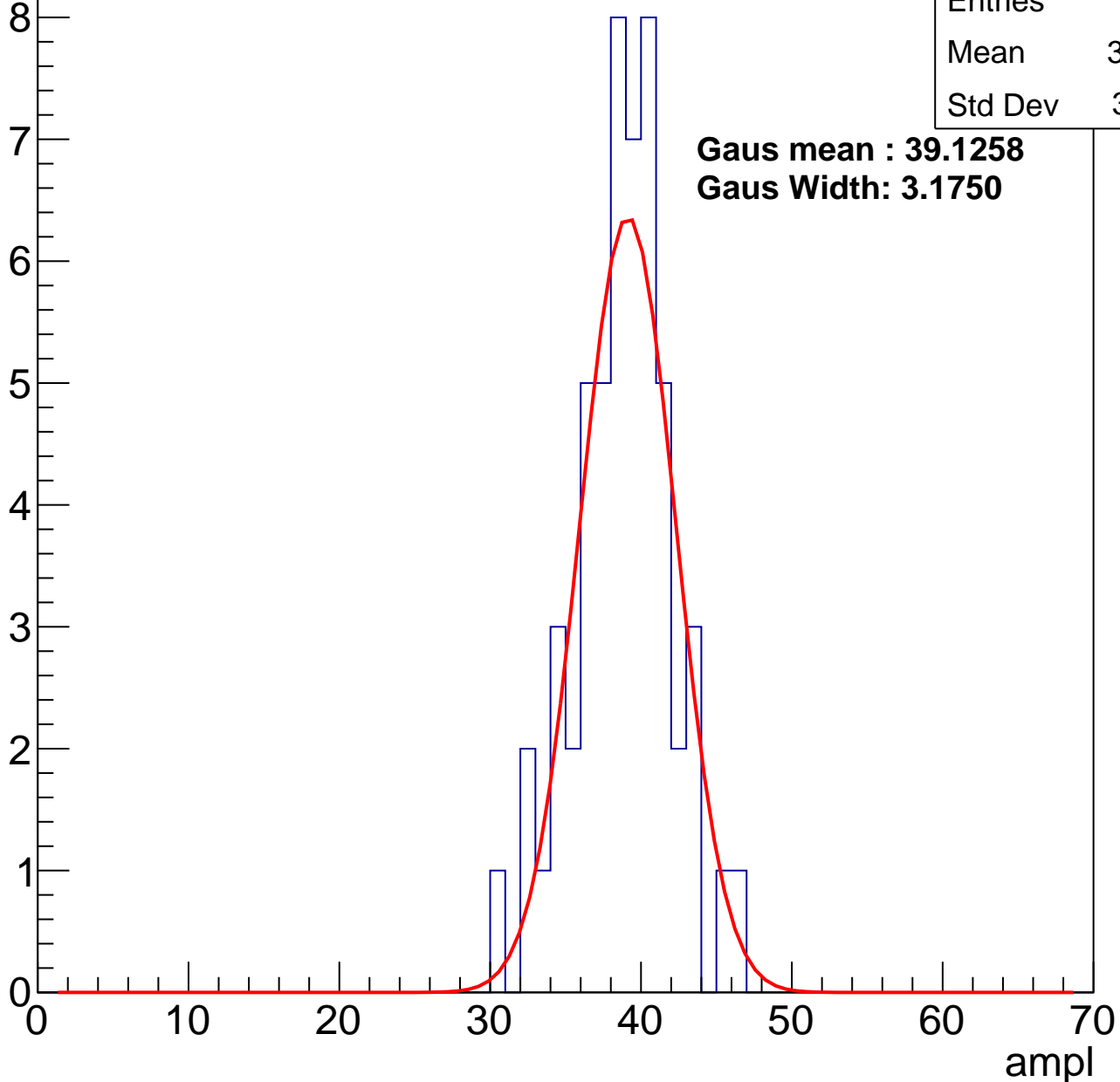
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	38.33
Std Dev	3.191

Gaus mean : 39.1258

Gaus Width: 3.1750



B0L001S, U11-ch40, adc2

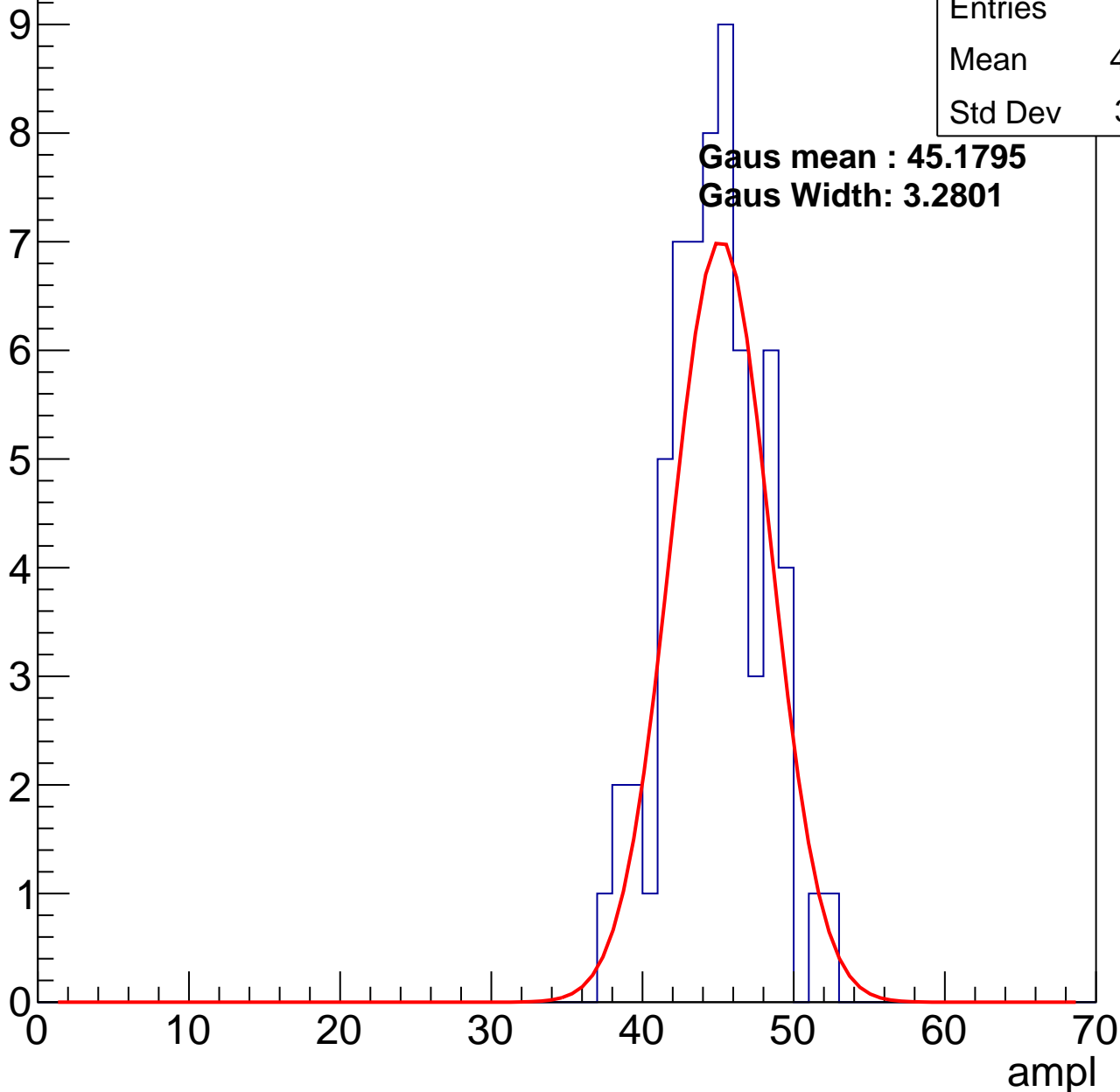
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.32
Std Dev	3.161

Gaus mean : 45.1795

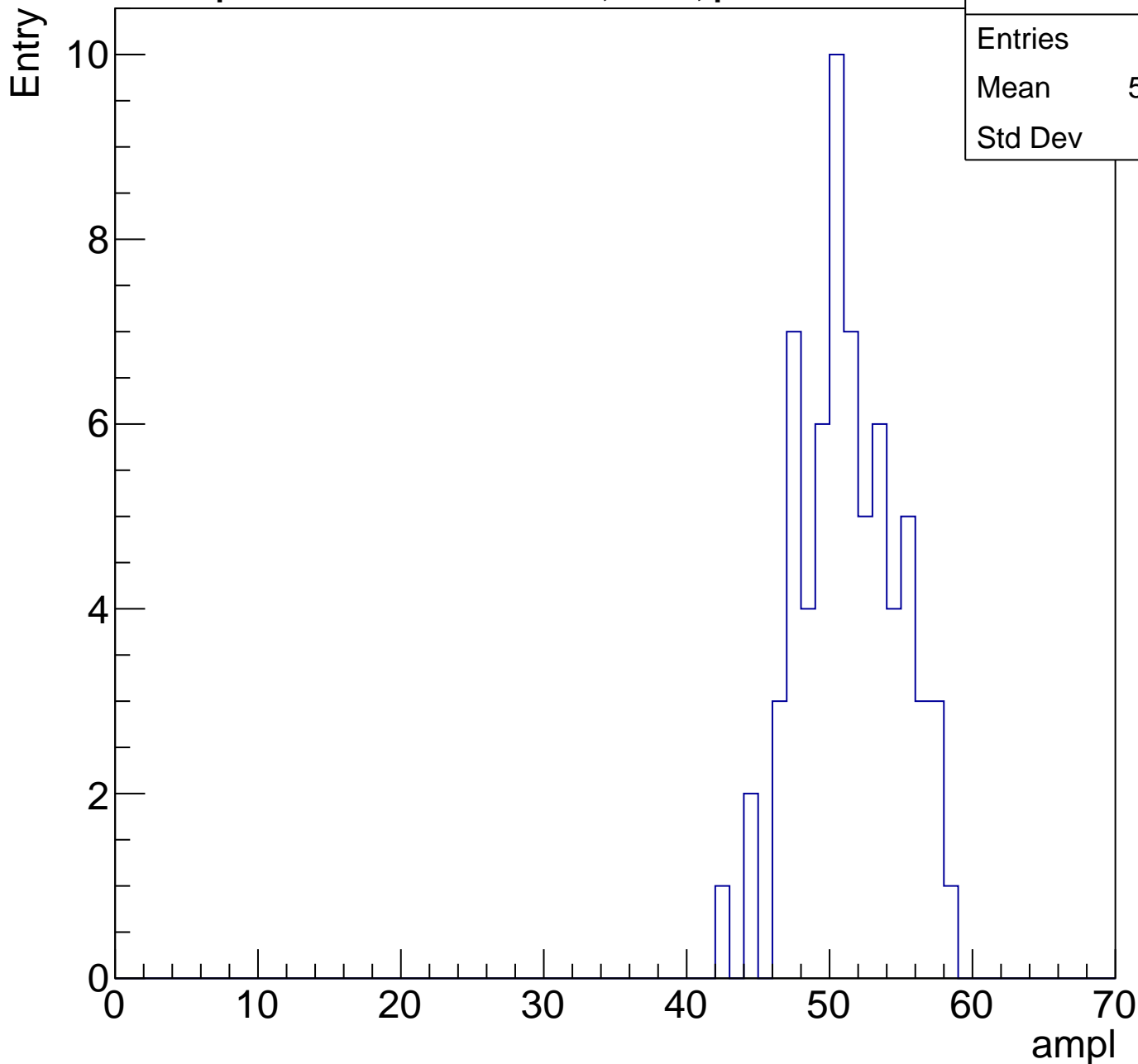
Gaus Width: 3.2801



B0L001S, U11-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	50.84
Std Dev	3.48

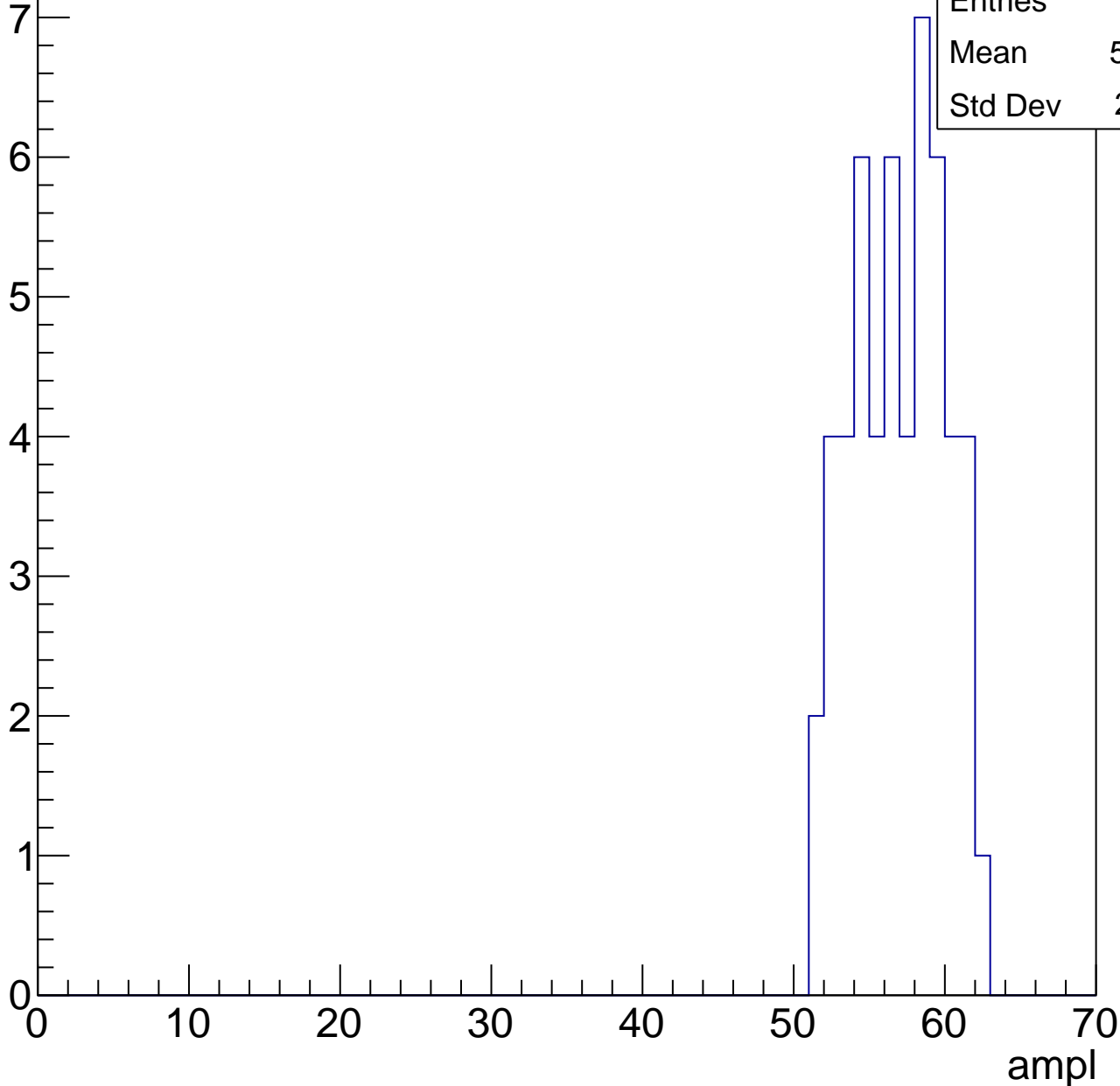


B0L001S, U11-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.46
Std Dev	2.951

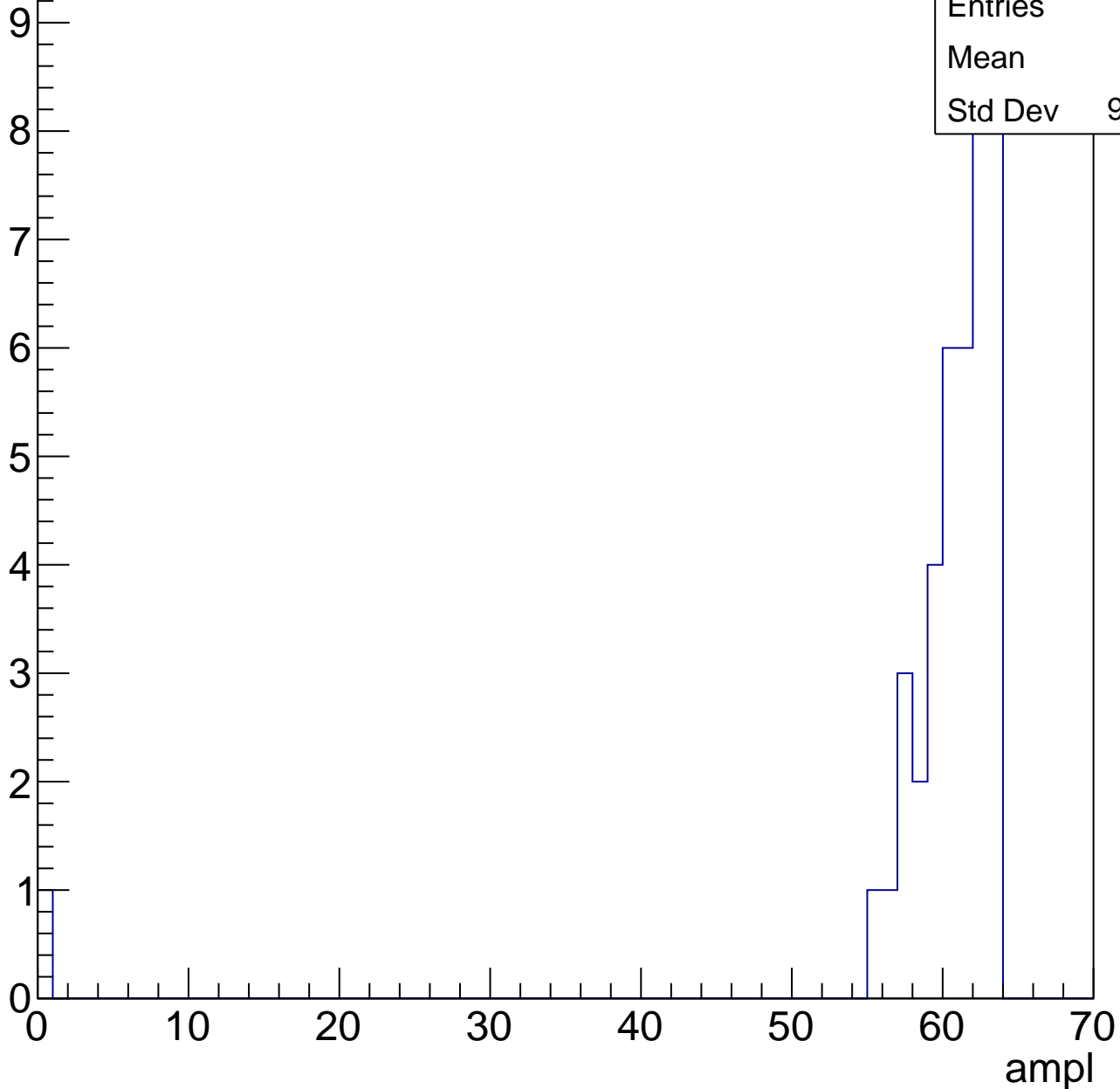


B0L001S, U11-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	59.1
Std Dev	9.584



B0L001S, U11-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch41, adc0

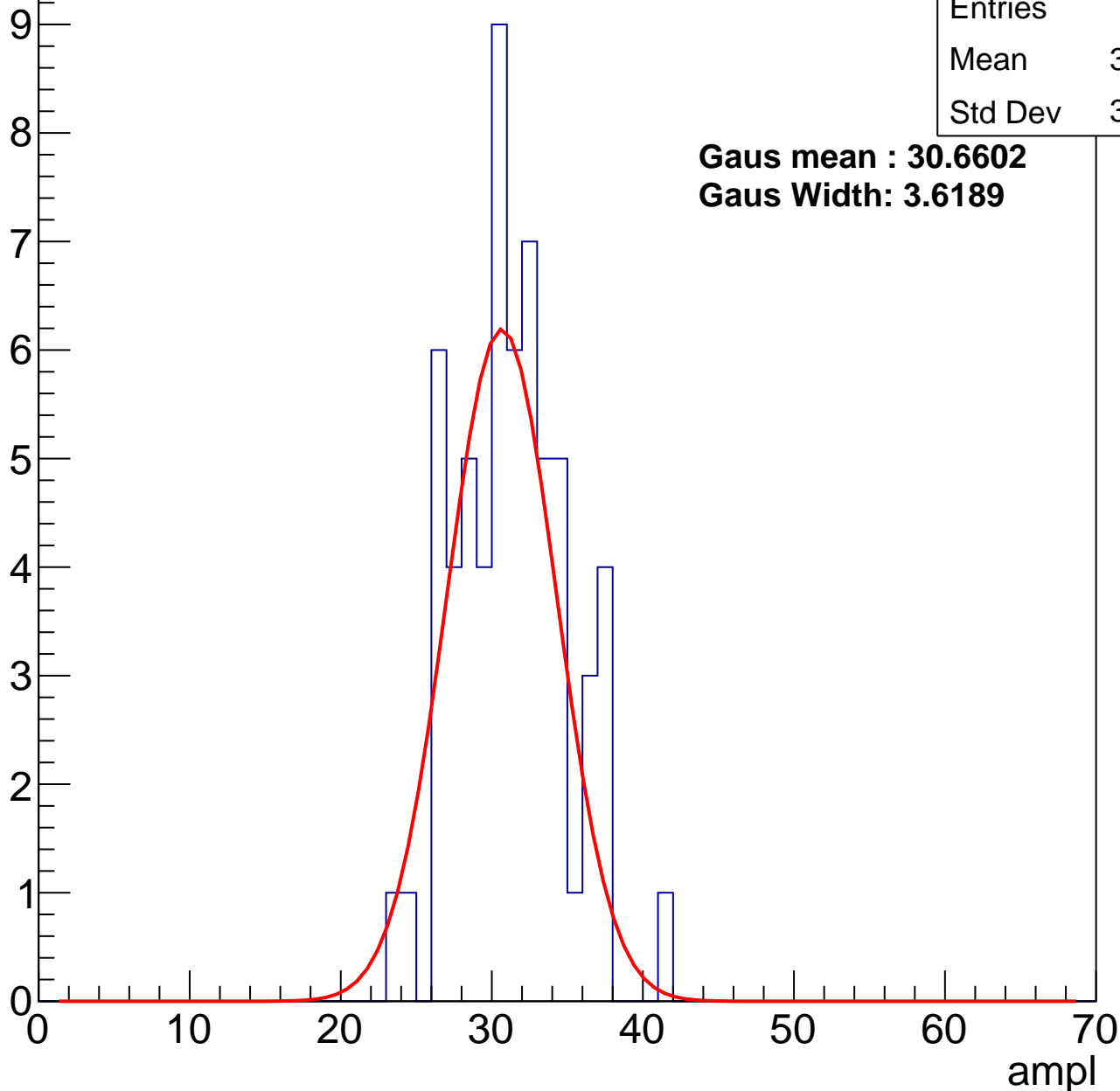
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.87
Std Dev	3.599

Gaus mean : 30.6602

Gaus Width: 3.6189



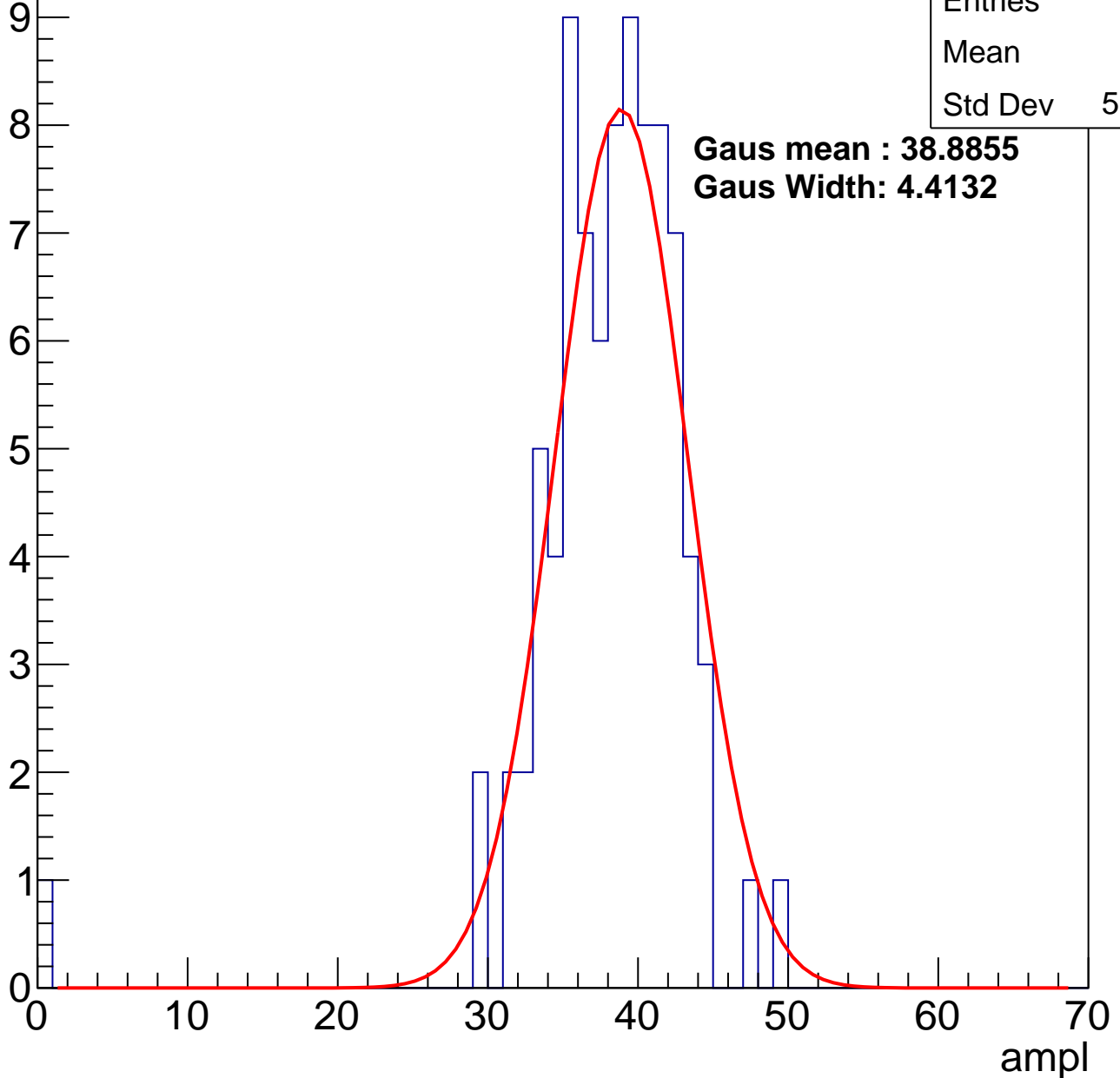
B0L001S, U11-ch41, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	37.6
Std Dev	5.572

Gaus mean : 38.8855
Gaus Width: 4.4132



B0L001S, U11-ch41, adc2

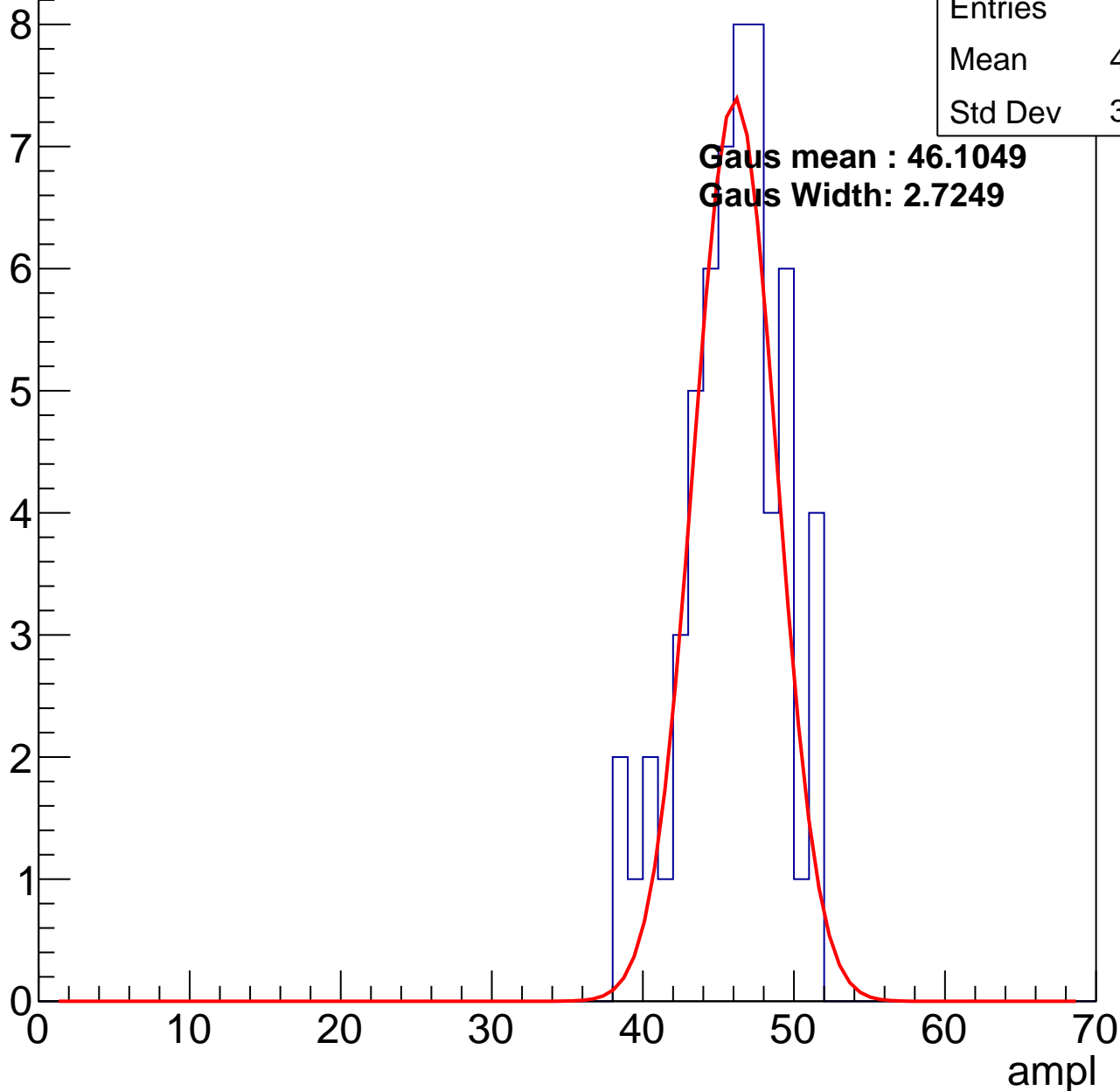
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.52
Std Dev	3.158

Gaus mean : 46.1049

Gaus Width: 2.7249

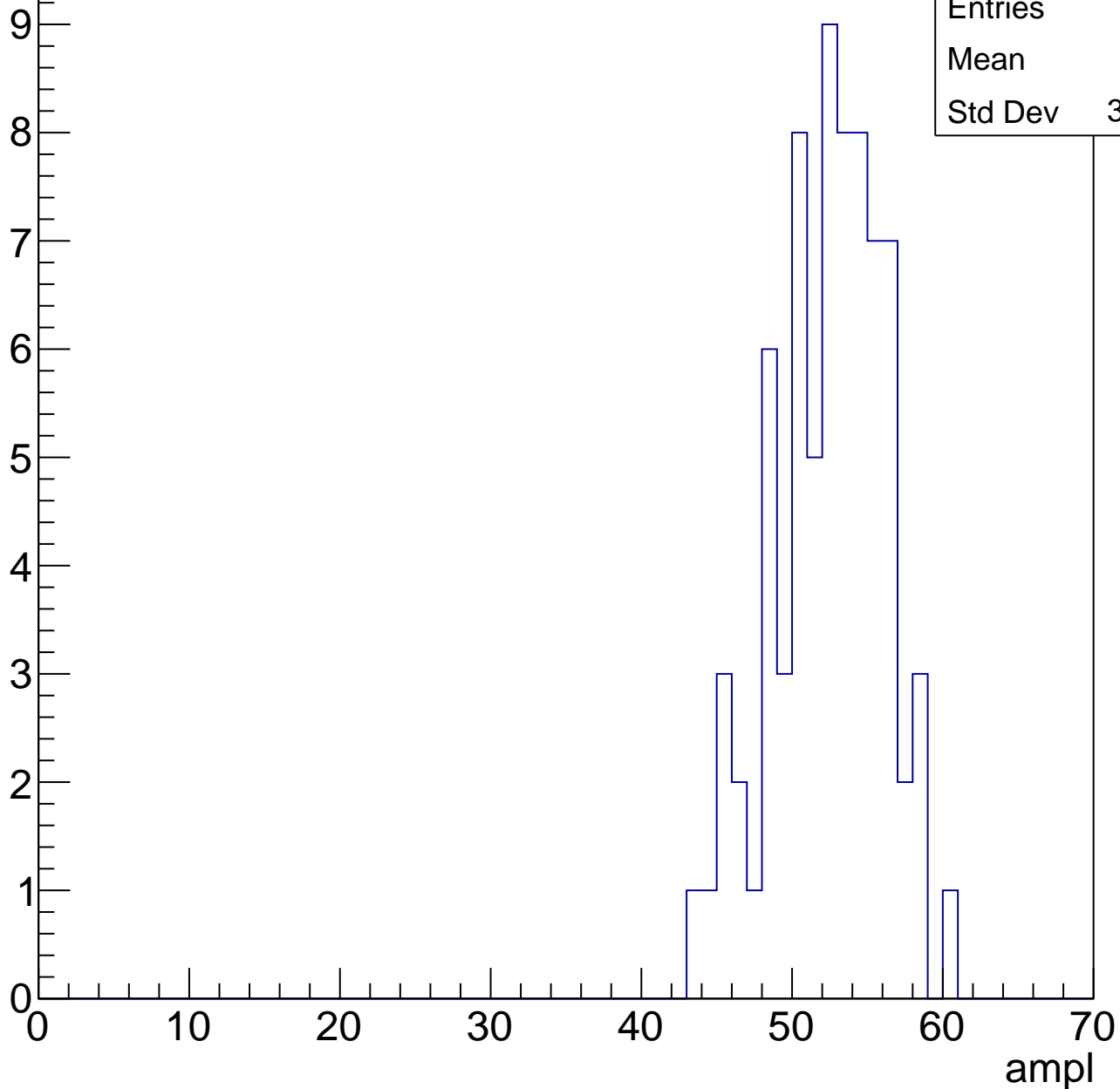


B0L001S, U11-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	52
Std Dev	3.626

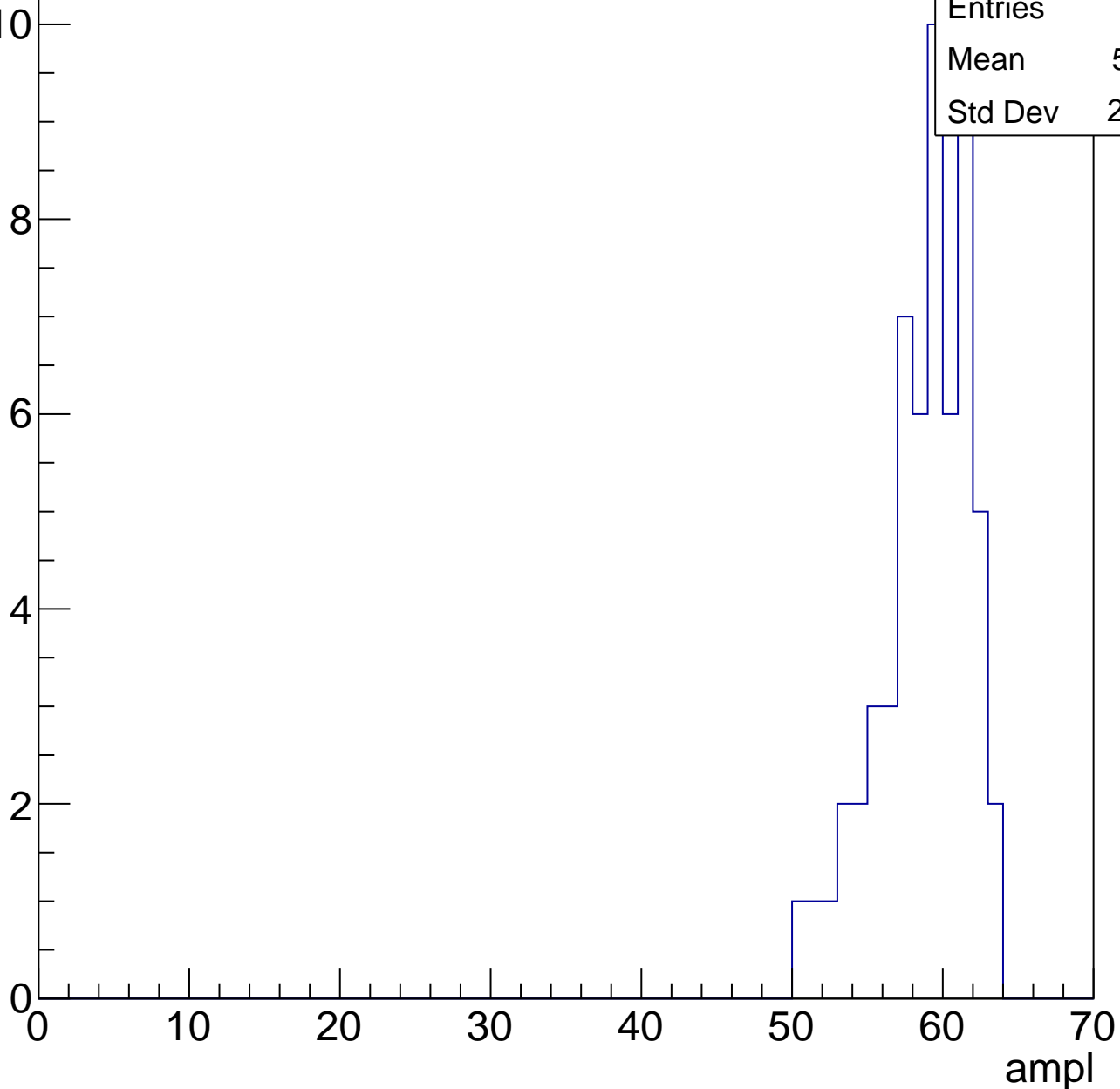


B0L001S, U11-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	58.31
Std Dev	2.984

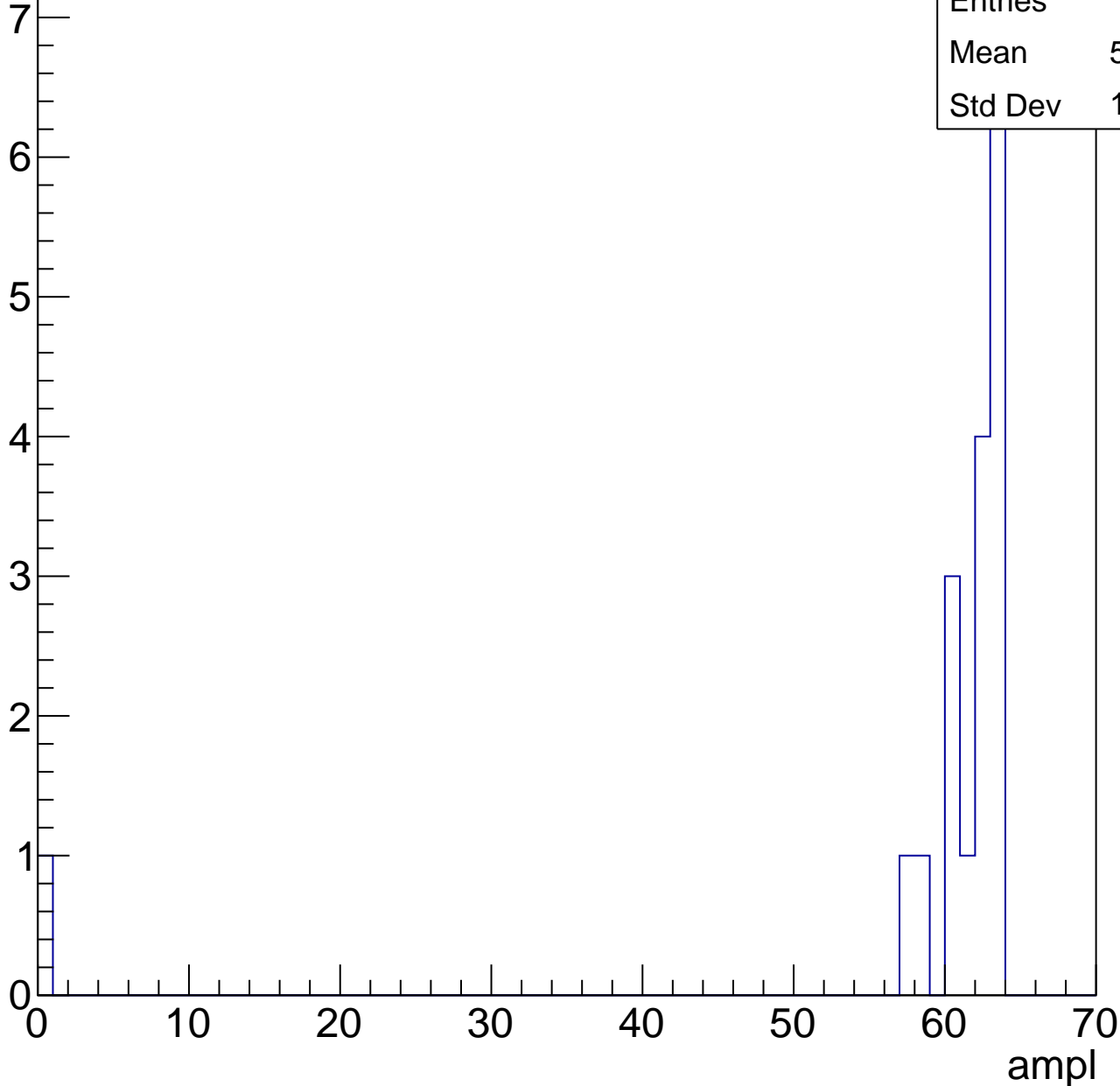


B0L001S, U11-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	58.06
Std Dev	14.19



B0L001S, U11-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch42, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	91
Mean	30.01
Std Dev	3.469

Gaus mean : 30.2865

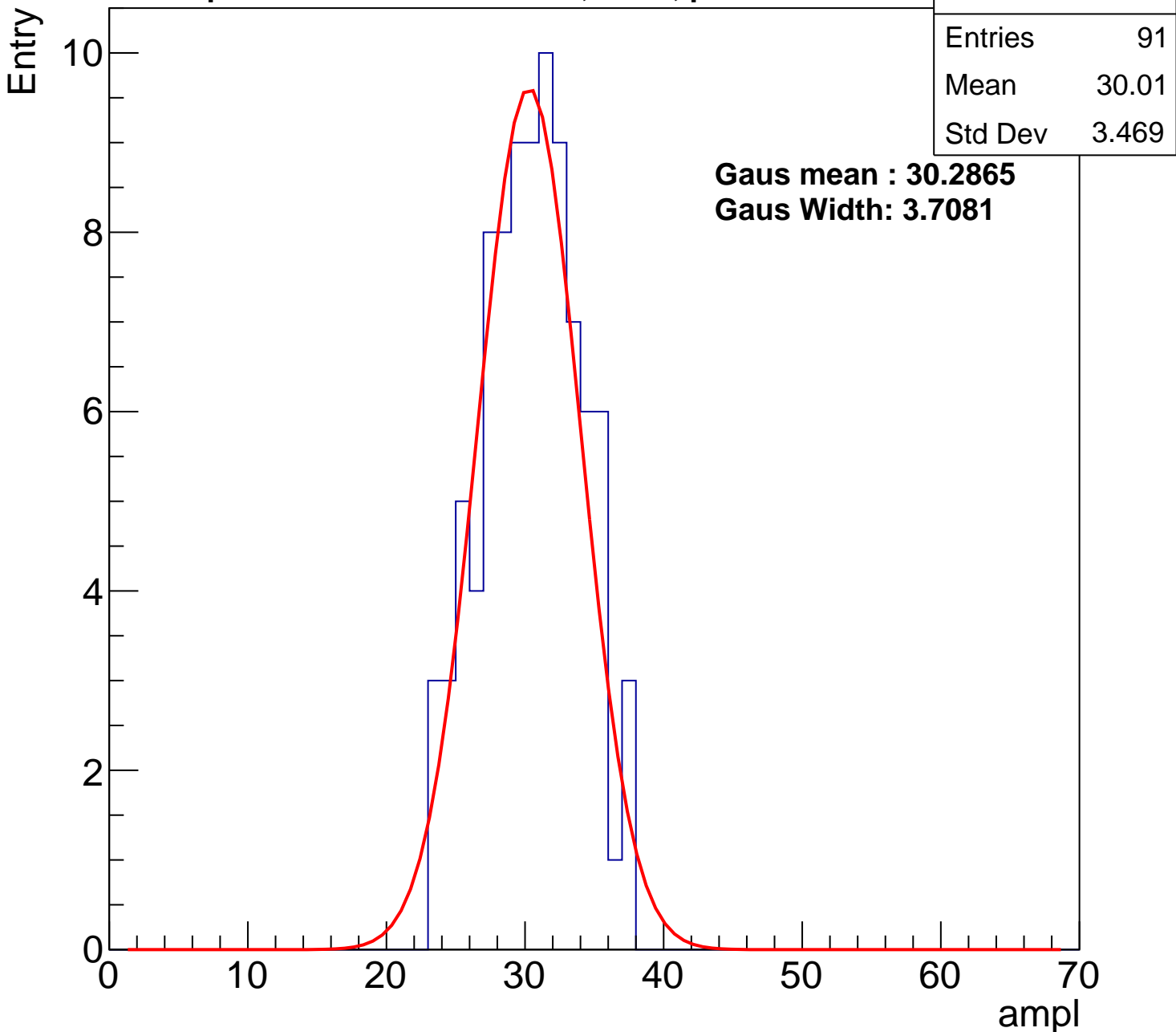
Gaus Width: 3.7081

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch42, adc1

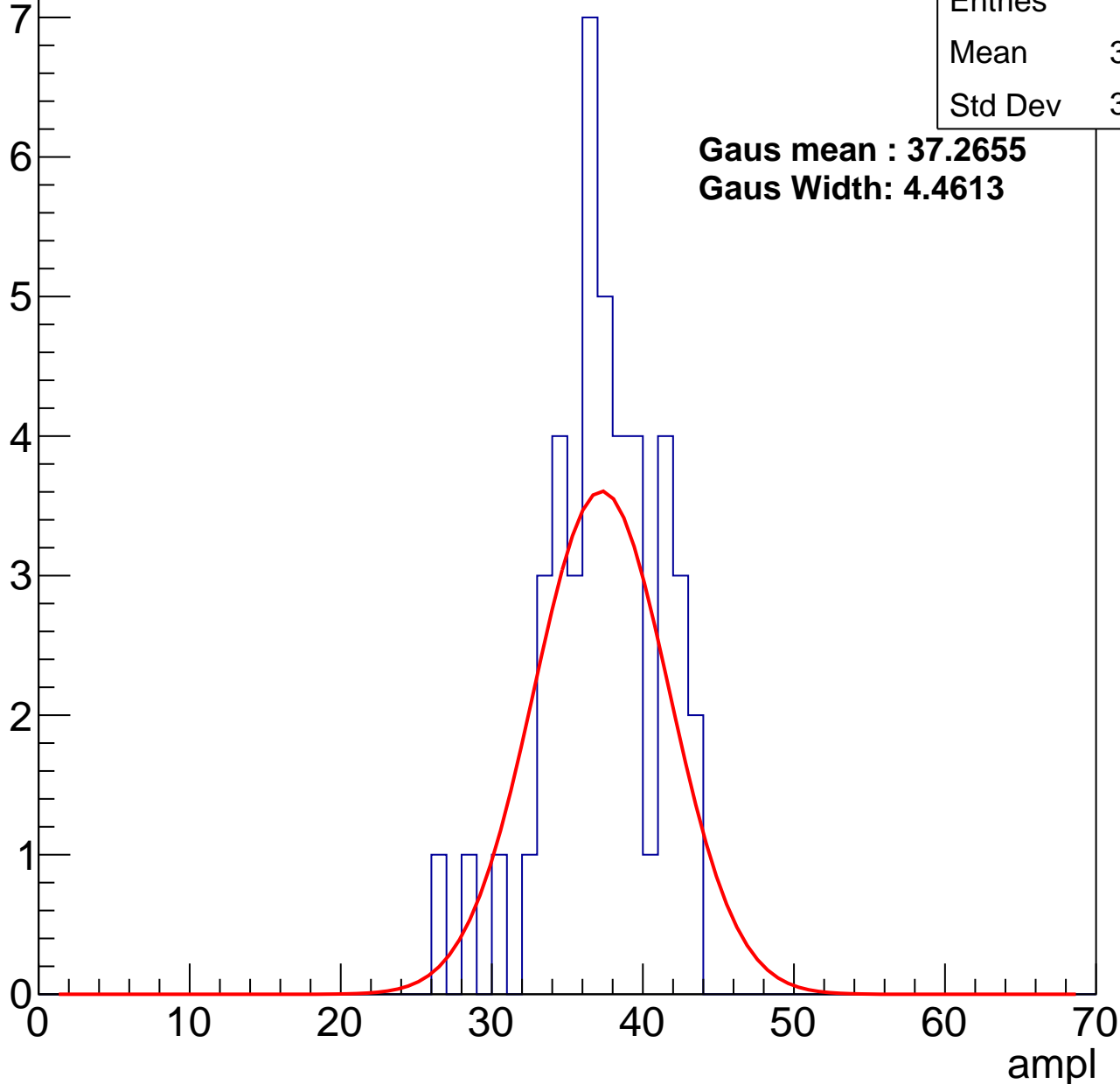
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	36.75
Std Dev	3.742

Gaus mean : 37.2655

Gaus Width: 4.4613



B0L001S, U11-ch42, adc2

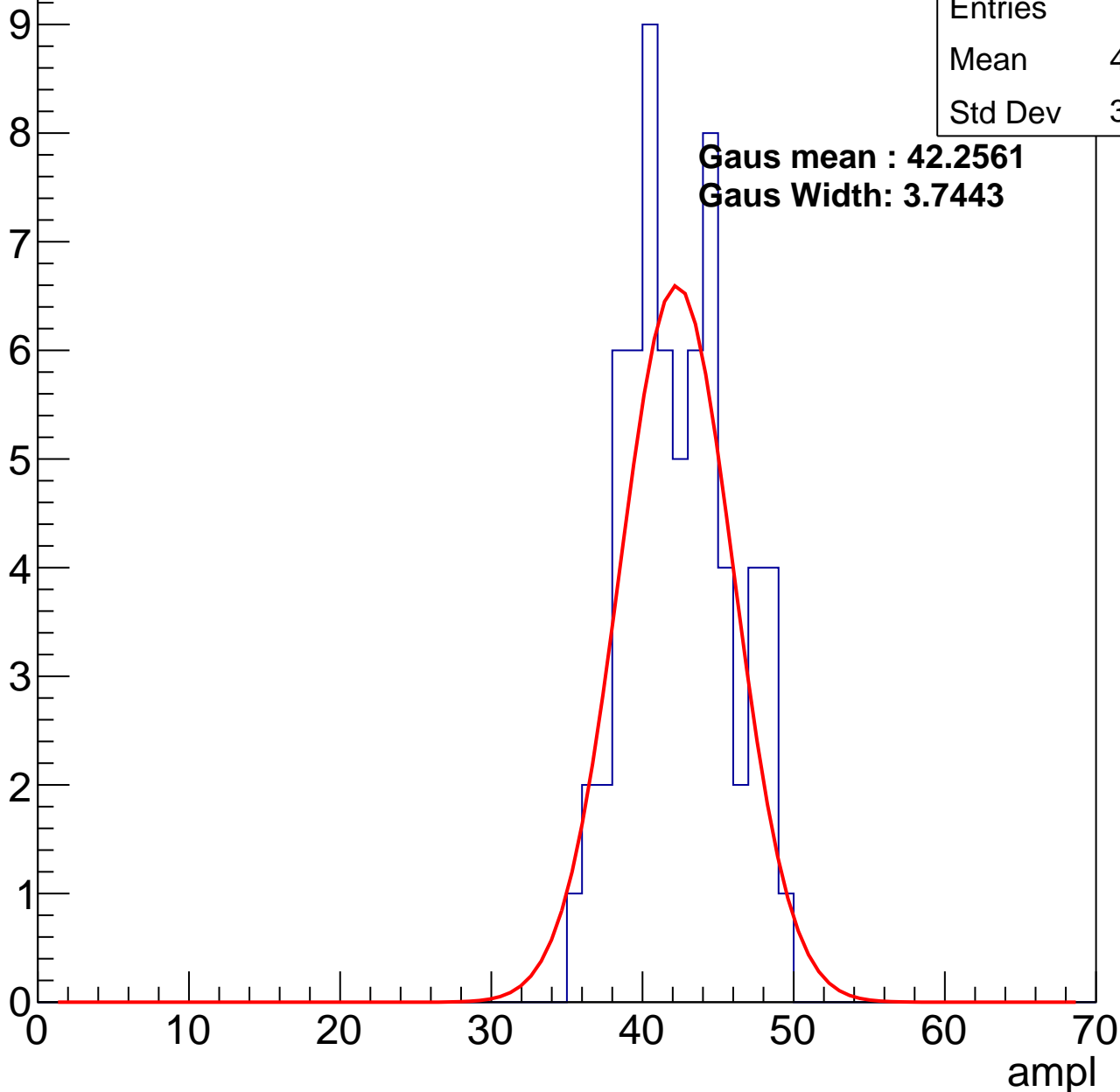
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	41.97
Std Dev	3.398

Gaus mean : 42.2561

Gaus Width: 3.7443

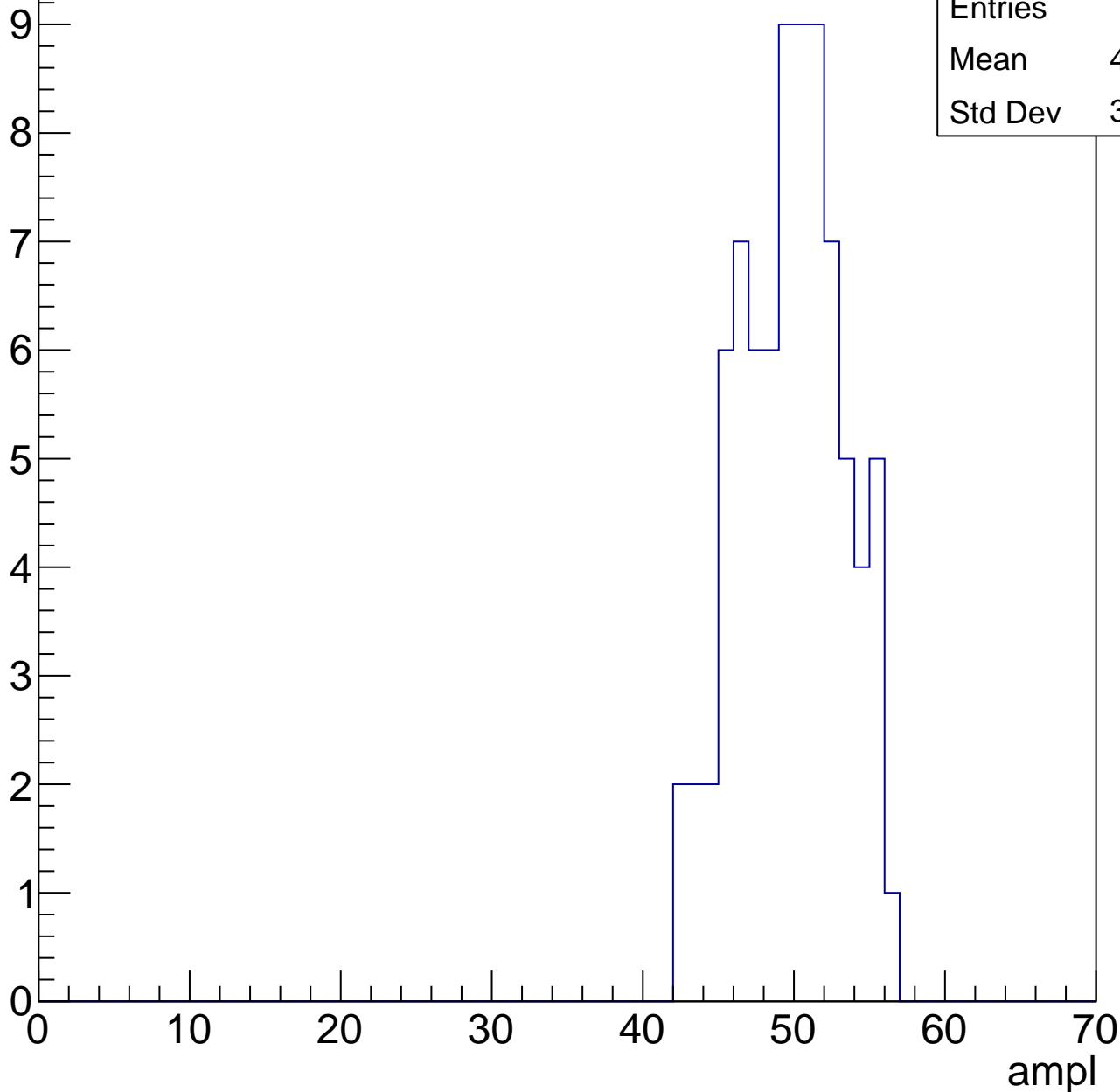


B0L001S, U11-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	49.33
Std Dev	3.387

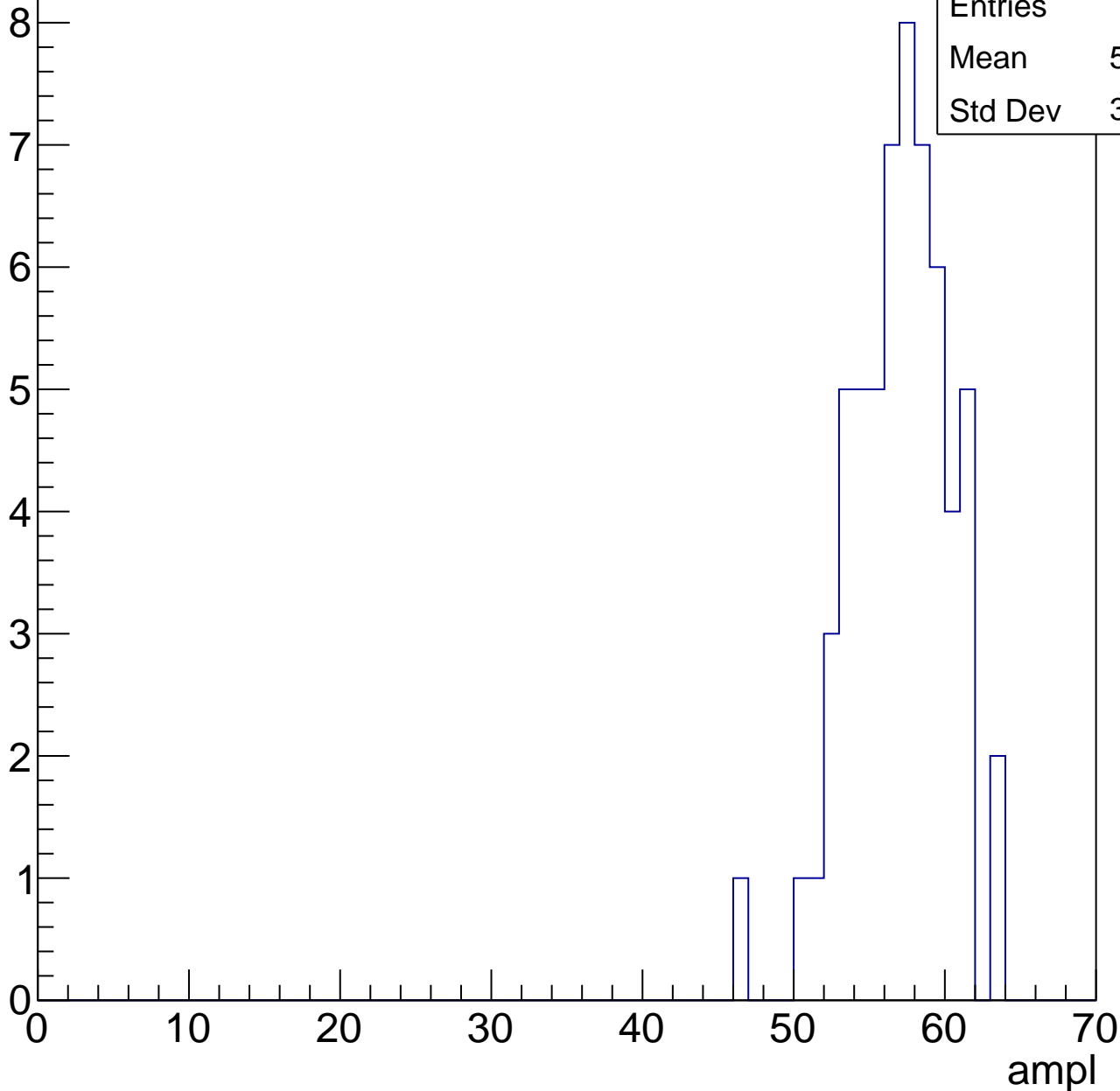


B0L001S, U11-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.53
Std Dev	3.263

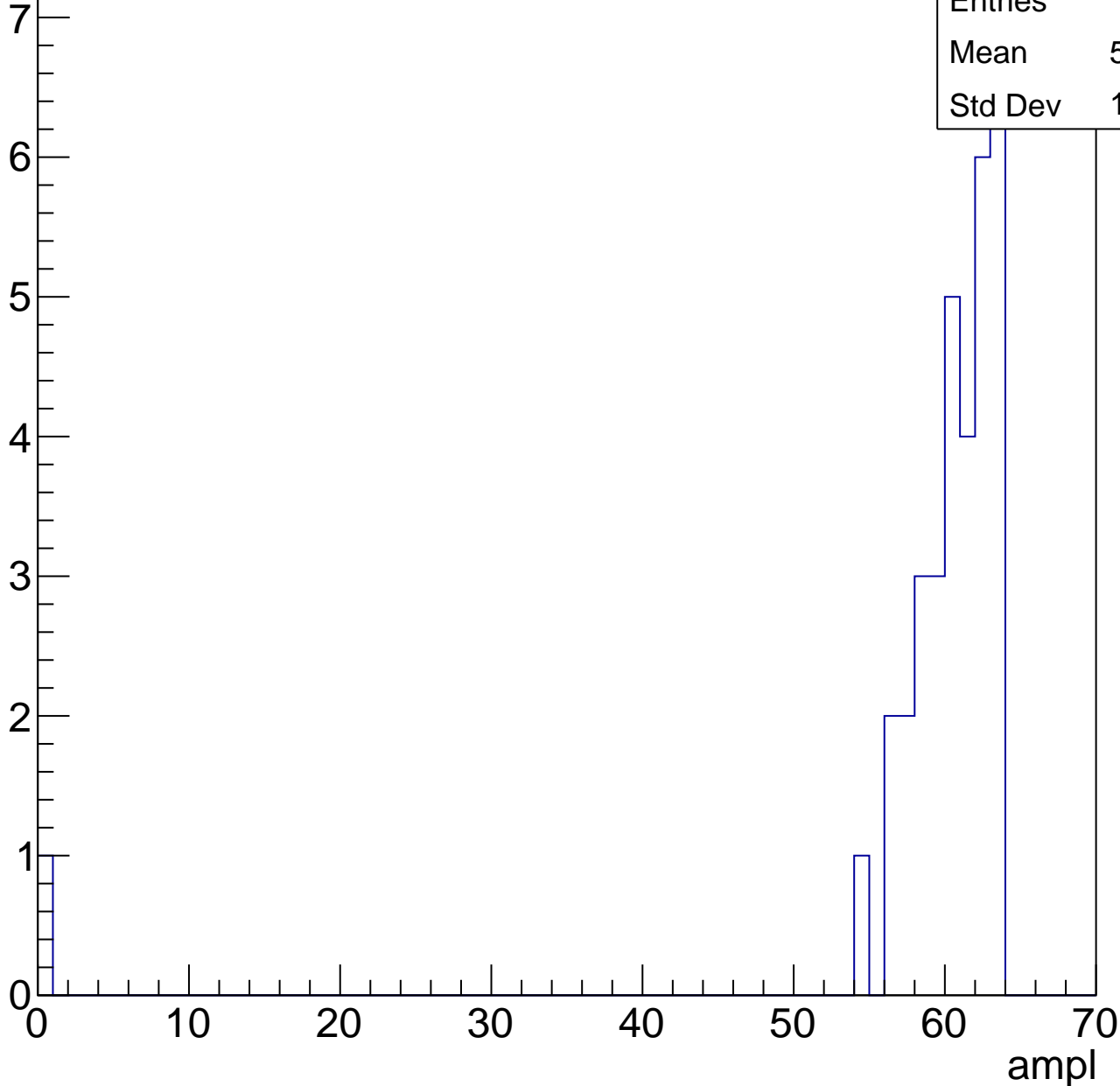


B0L001S, U11-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

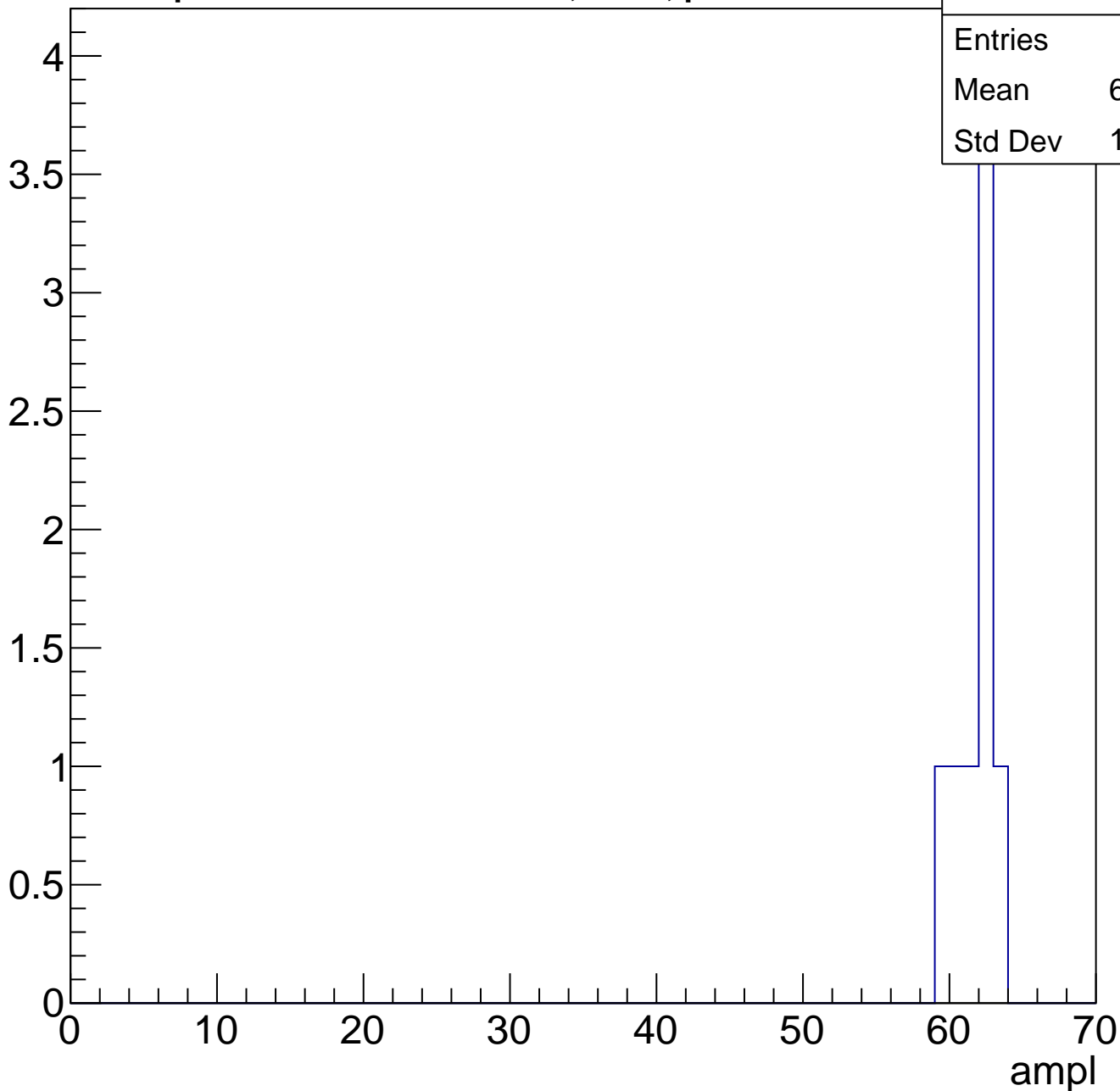
Entries	34
Mean	58.47
Std Dev	10.45



B0L001S, U11-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch43, adc0

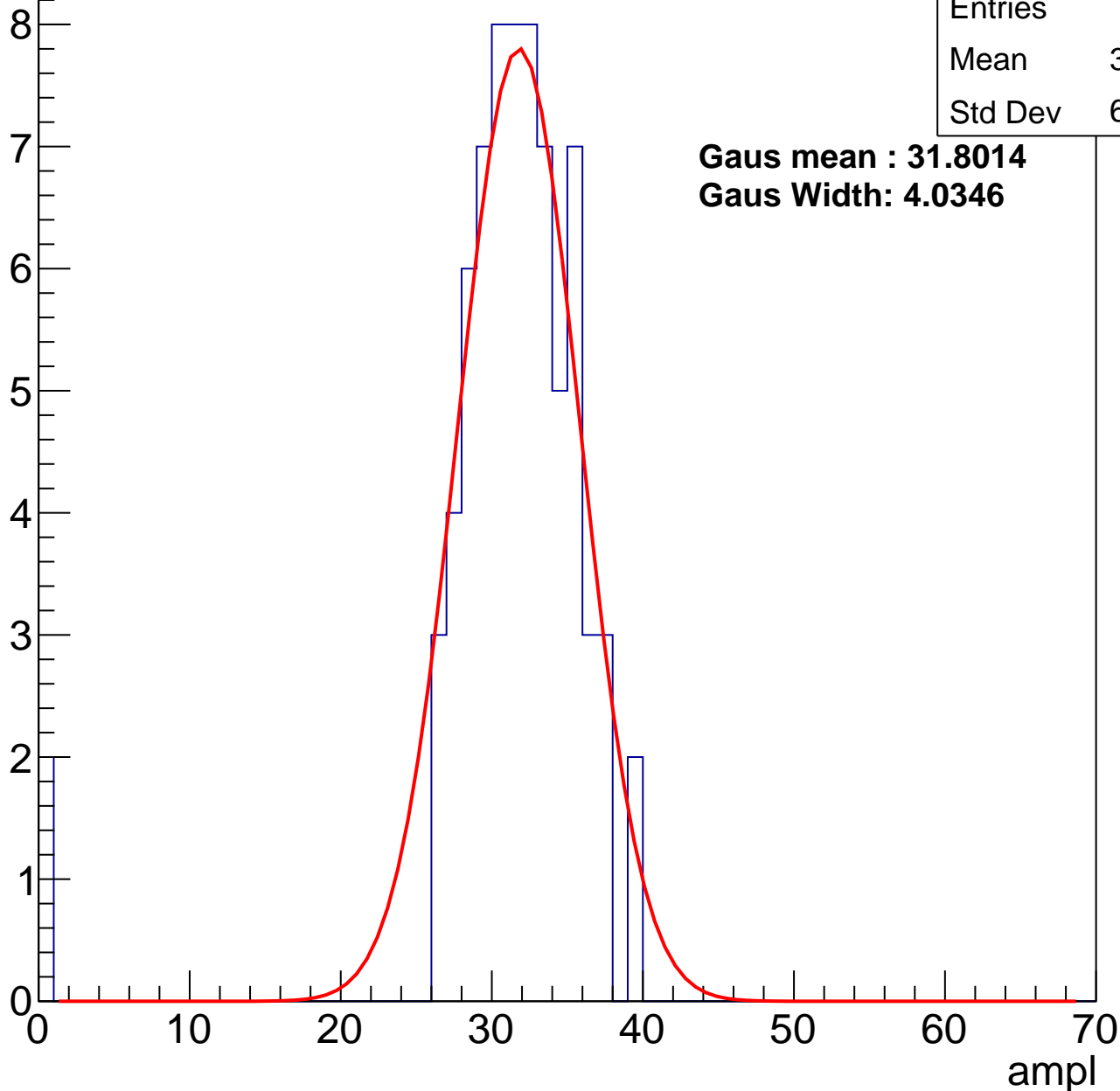
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.74
Std Dev	6.025

Gaus mean : 31.8014

Gaus Width: 4.0346



B0L001S, U11-ch43, adc1

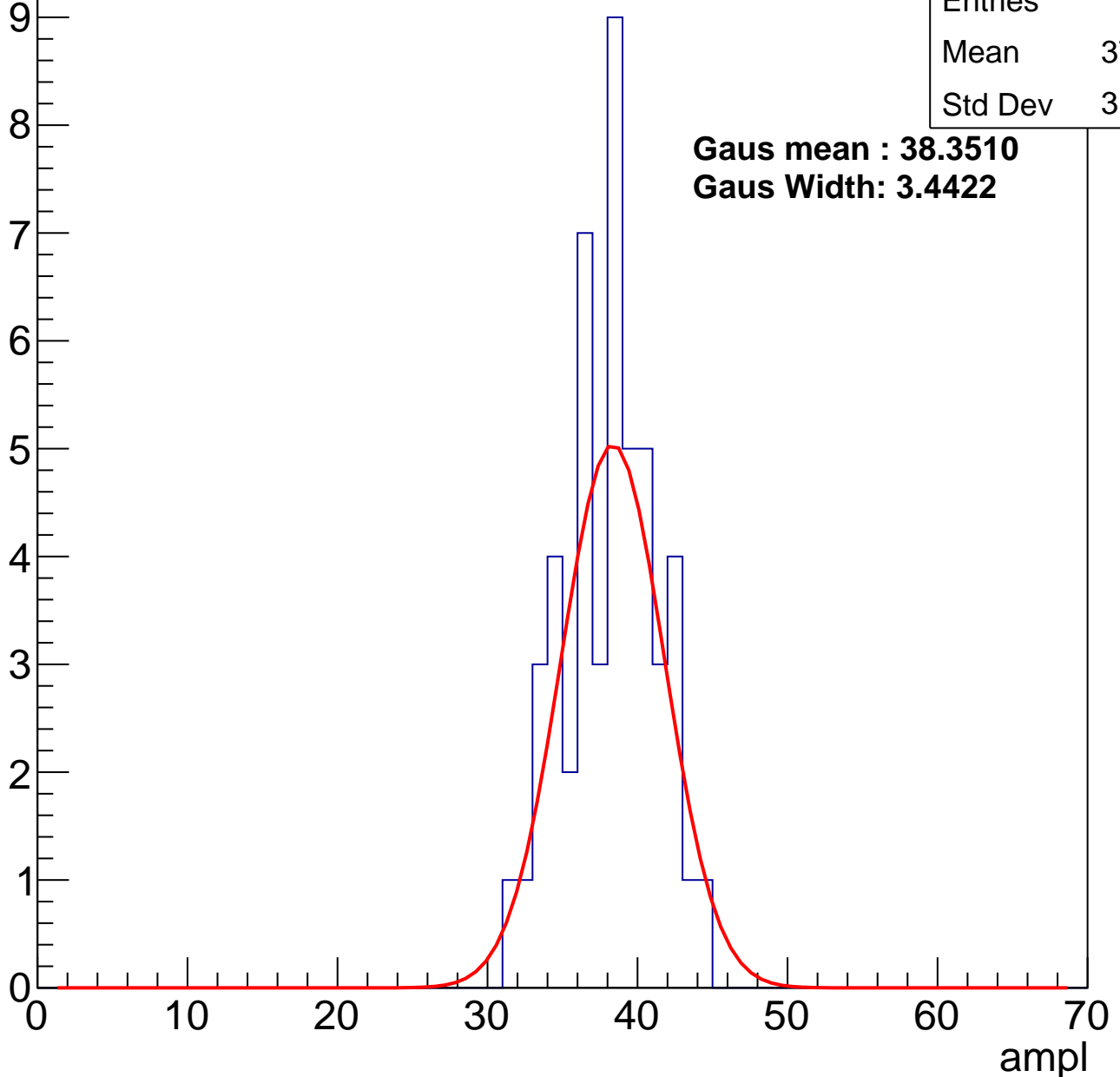
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	37.67
Std Dev	3.013

Gaus mean : 38.3510

Gaus Width: 3.4422



B0L001S, U11-ch43, adc2

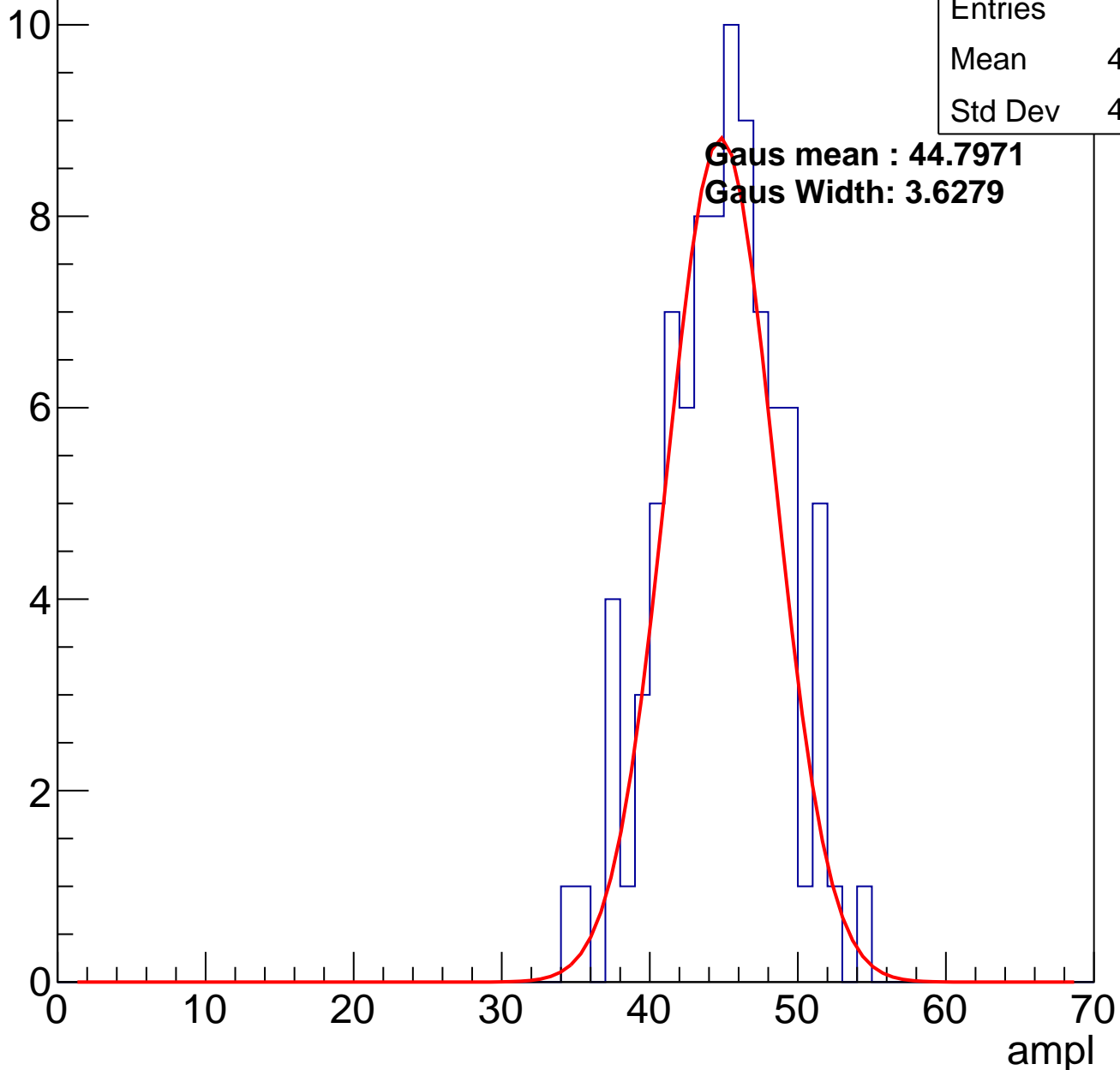
calib_packv5_042523_0143.root, FC#9, port A1

Entries	90
Mean	44.37
Std Dev	4.015

Gaus mean : 44.7971

Gaus Width: 3.6279

Entry

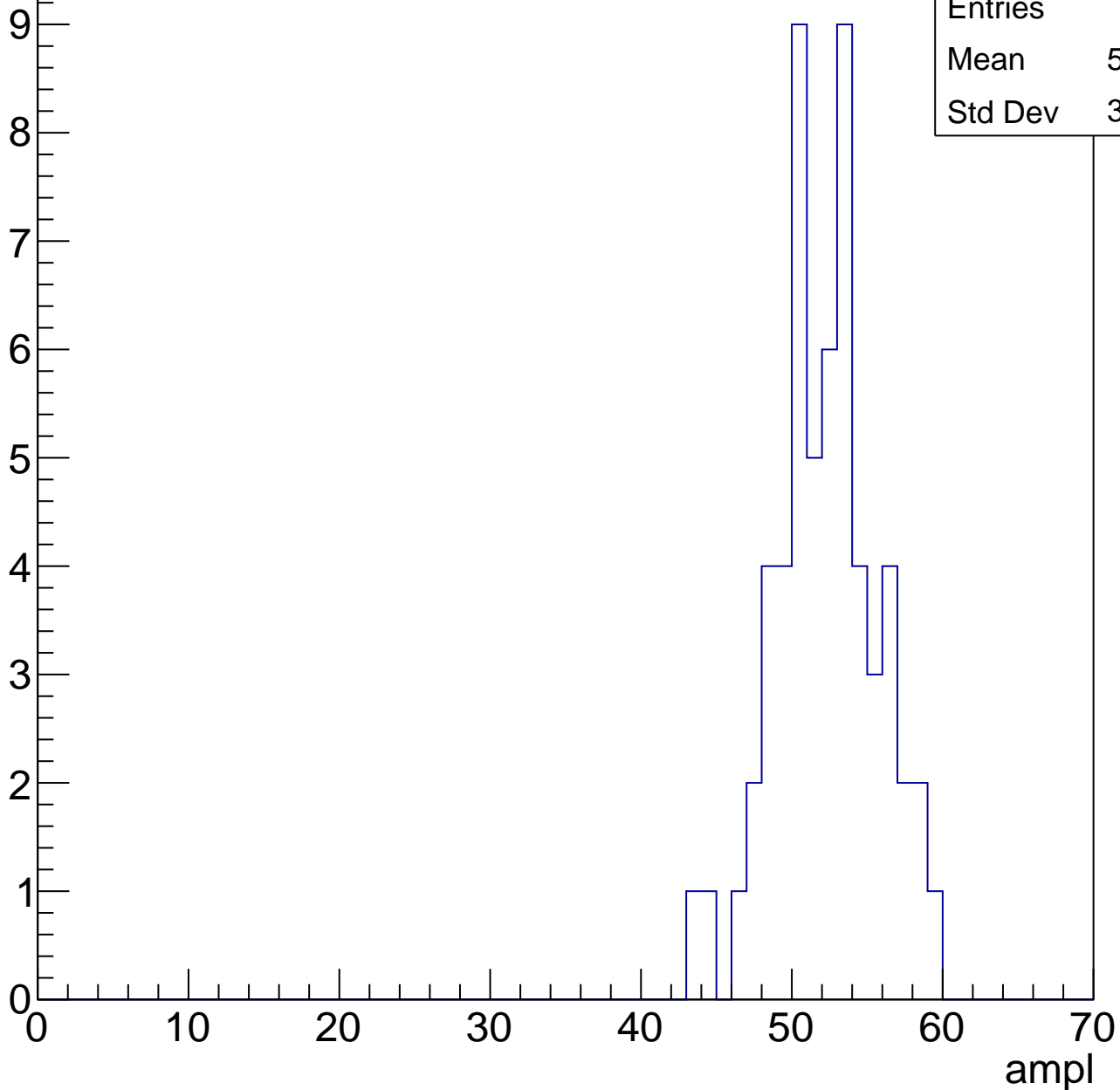


B0L001S, U11-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	51.78
Std Dev	3.363

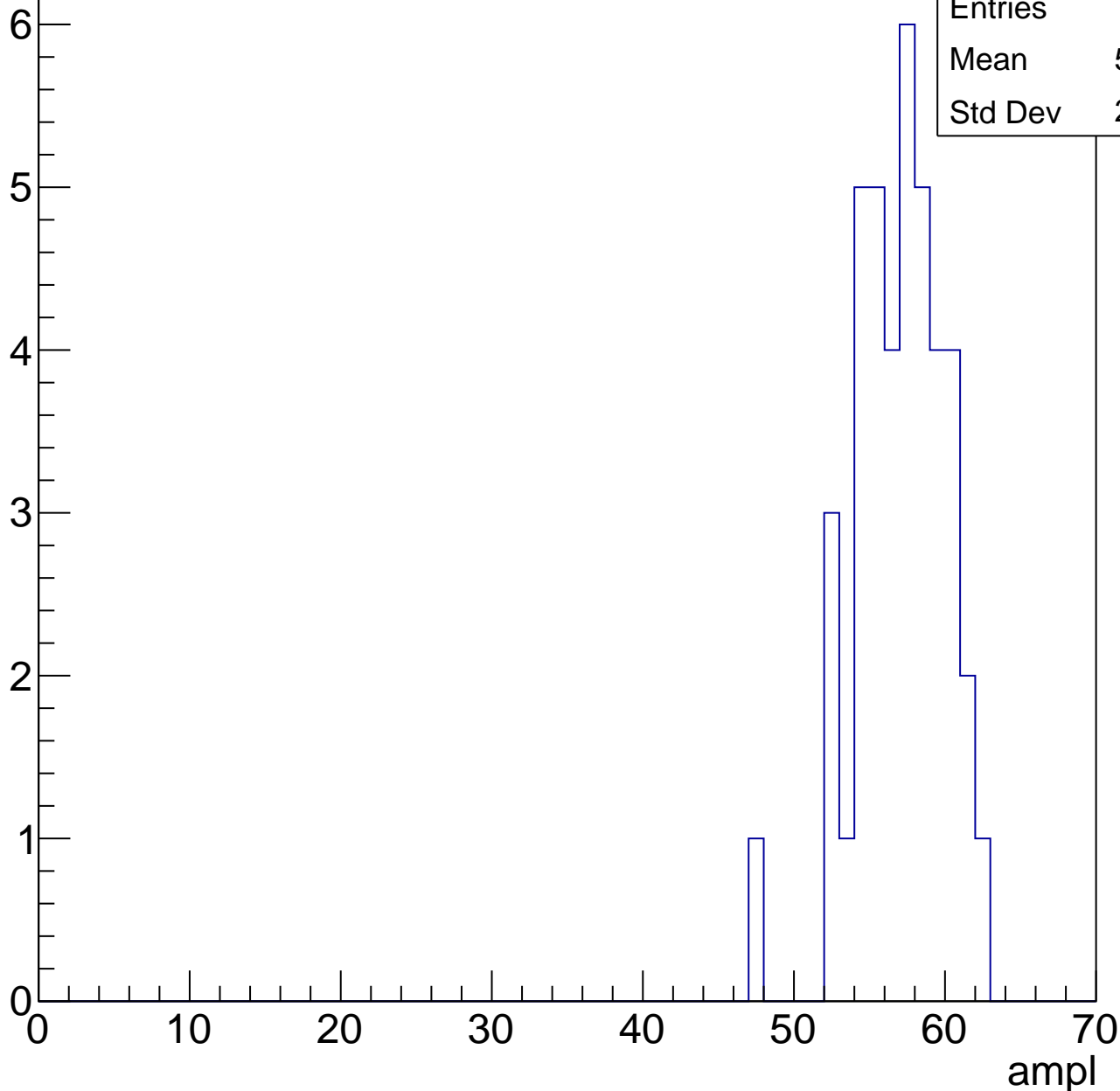


B0L001S, U11-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	56.51
Std Dev	2.981

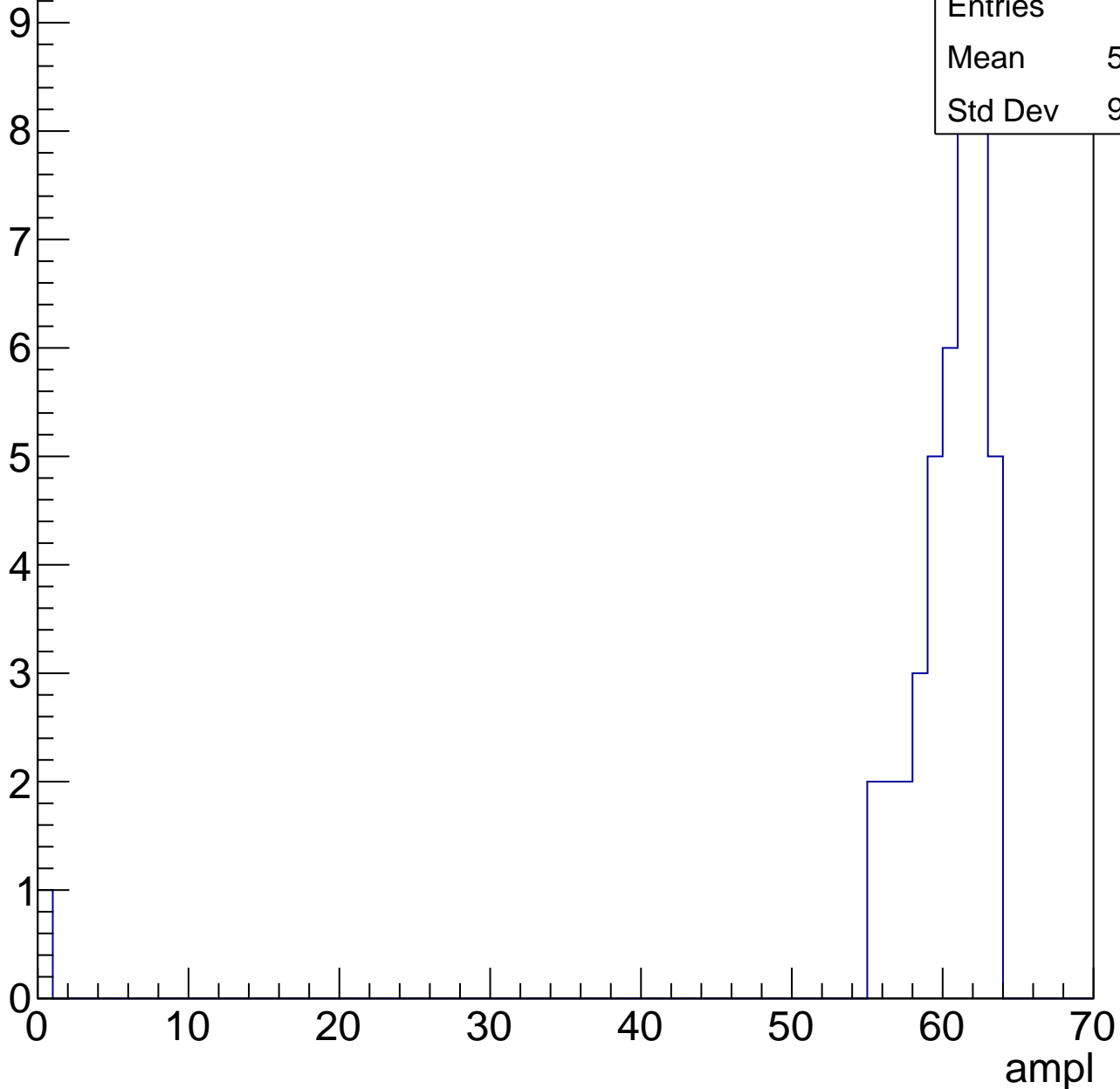


B0L001S, U11-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.74
Std Dev	9.324



B0L001S, U11-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch44, adc0

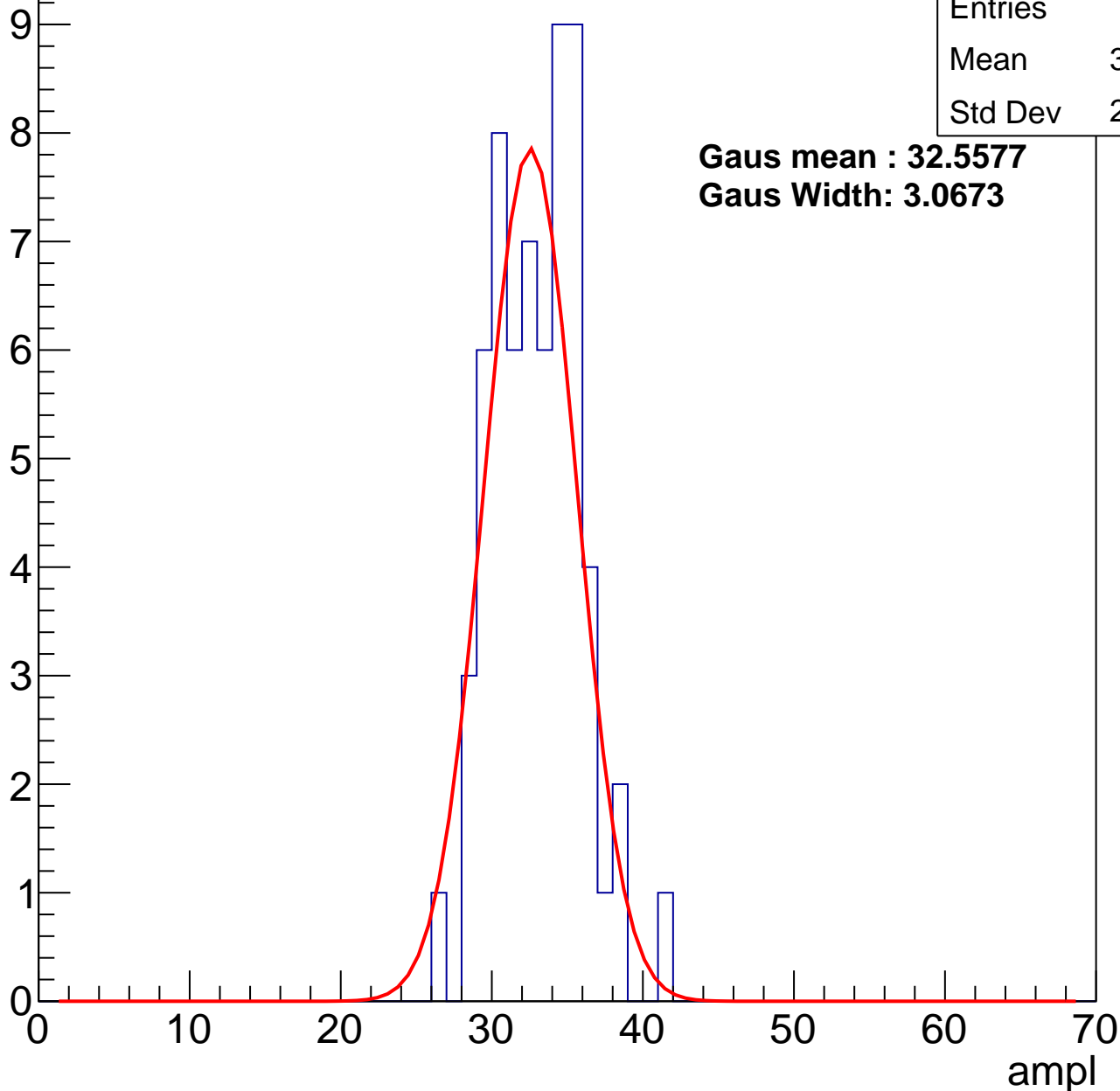
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	32.56
Std Dev	2.877

Gaus mean : 32.5577

Gaus Width: 3.0673



B0L001S, U11-ch44, adc1

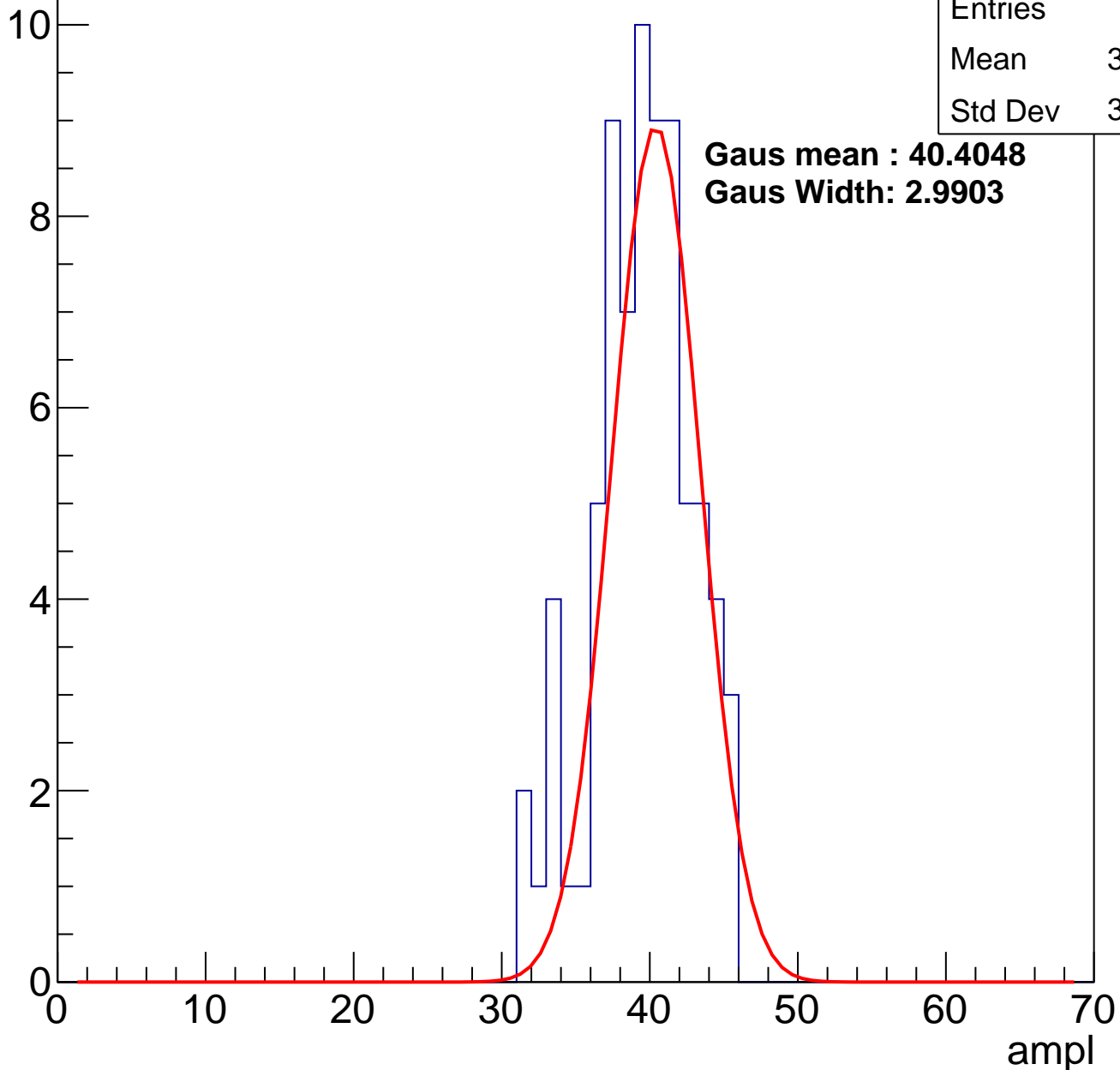
calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	39.05
Std Dev	3.322

Gaus mean : 40.4048

Gaus Width: 2.9903

Entry



B0L001S, U11-ch44, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	46.65
Std Dev	3.087

Gaus mean : 47.1266

Gaus Width: 3.5368

Entry

10

8

6

4

2

0

0

10

20

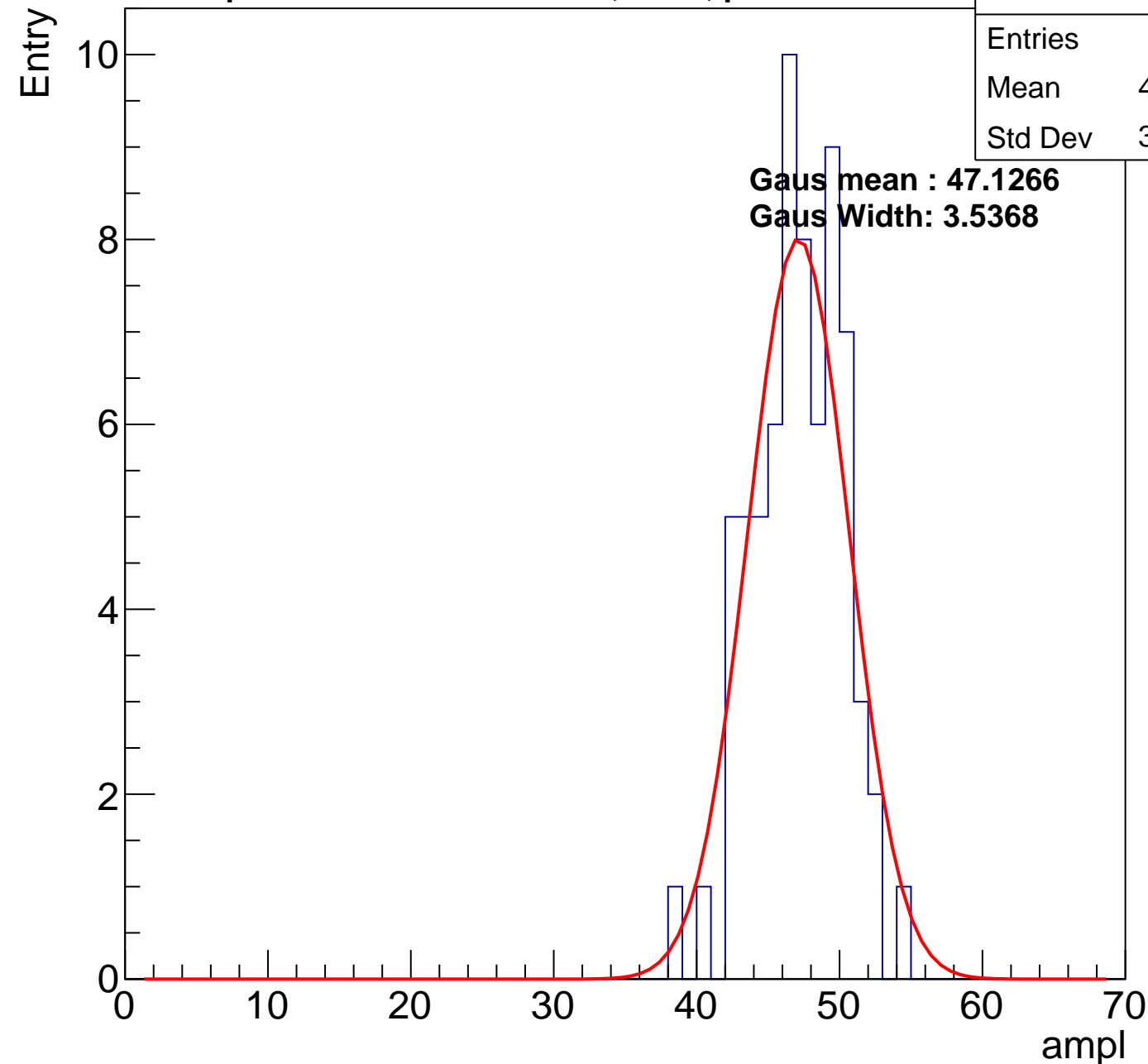
30

40

50

60

ampl

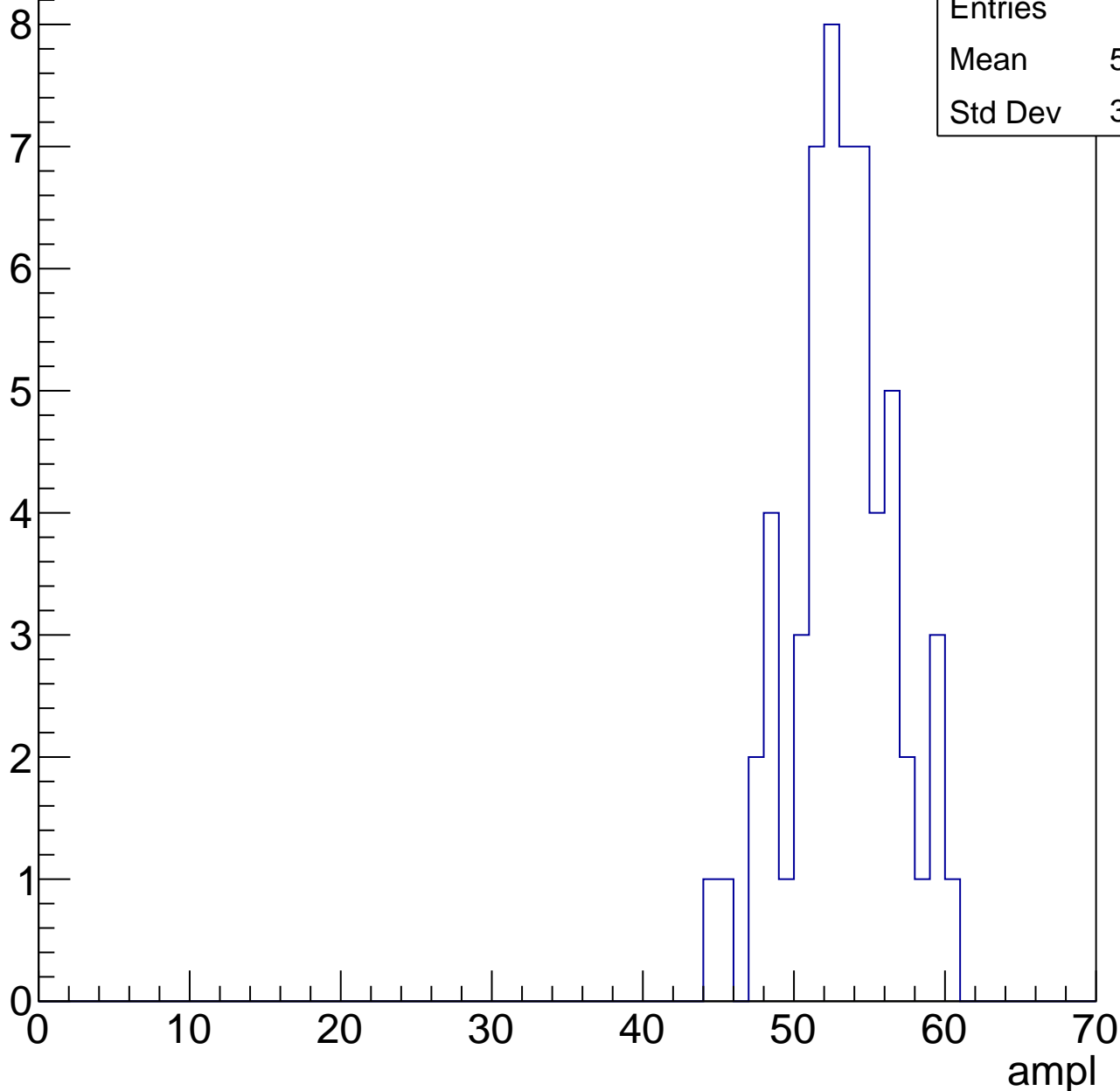


B0L001S, U11-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	52.72
Std Dev	3.437

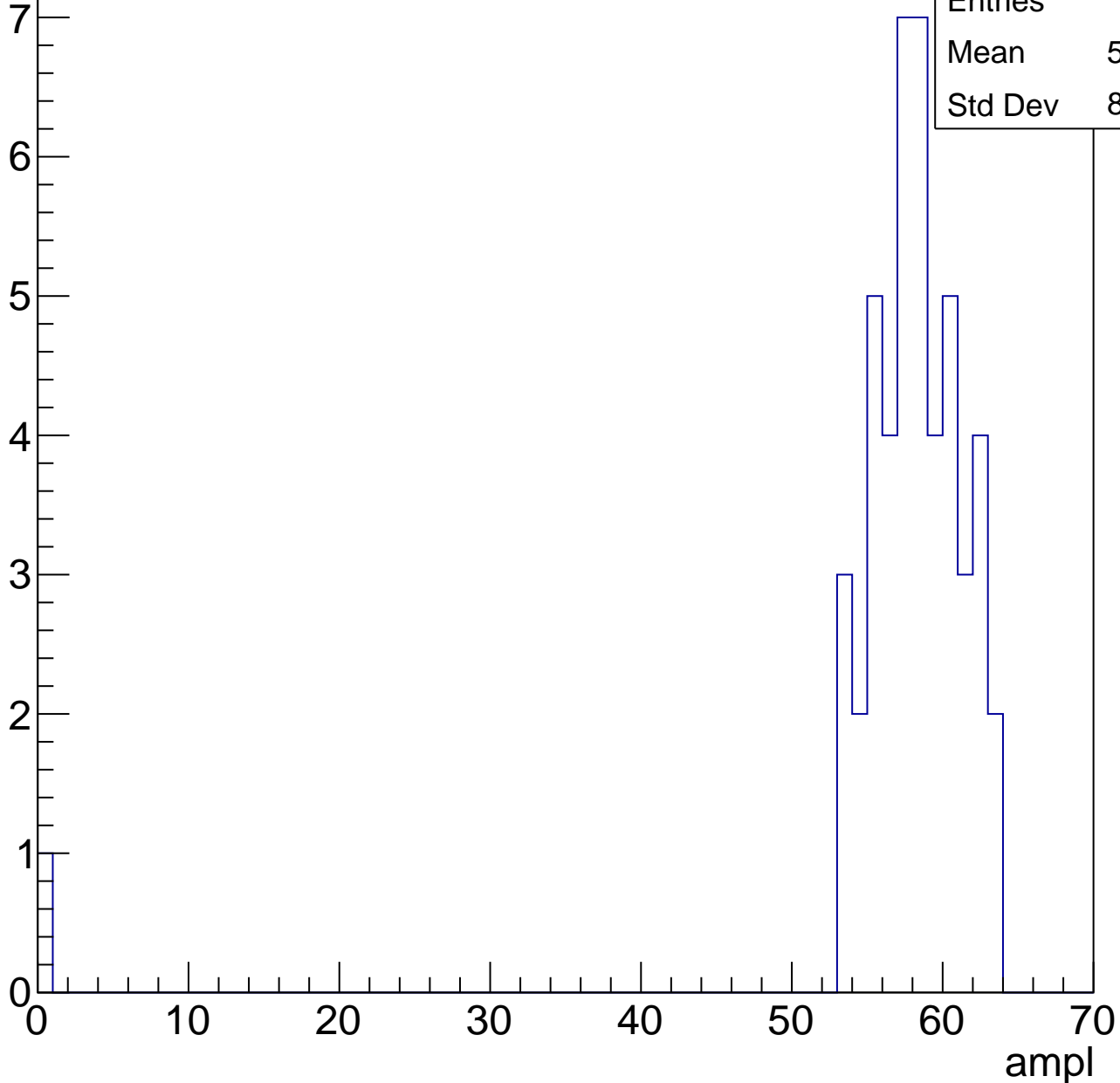


B0L001S, U11-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

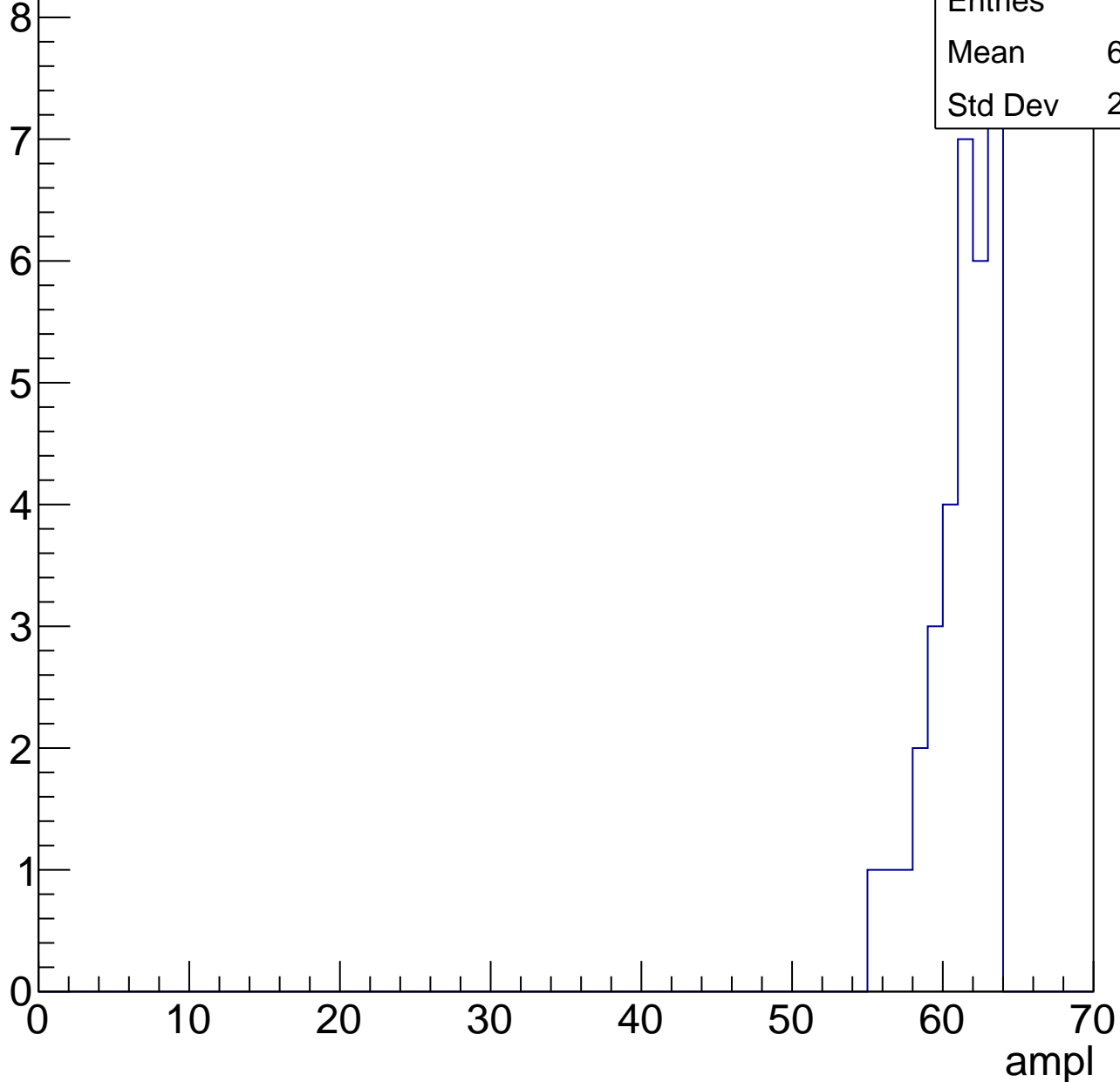
Entries	47
Mean	56.68
Std Dev	8.779



B0L001S, U11-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

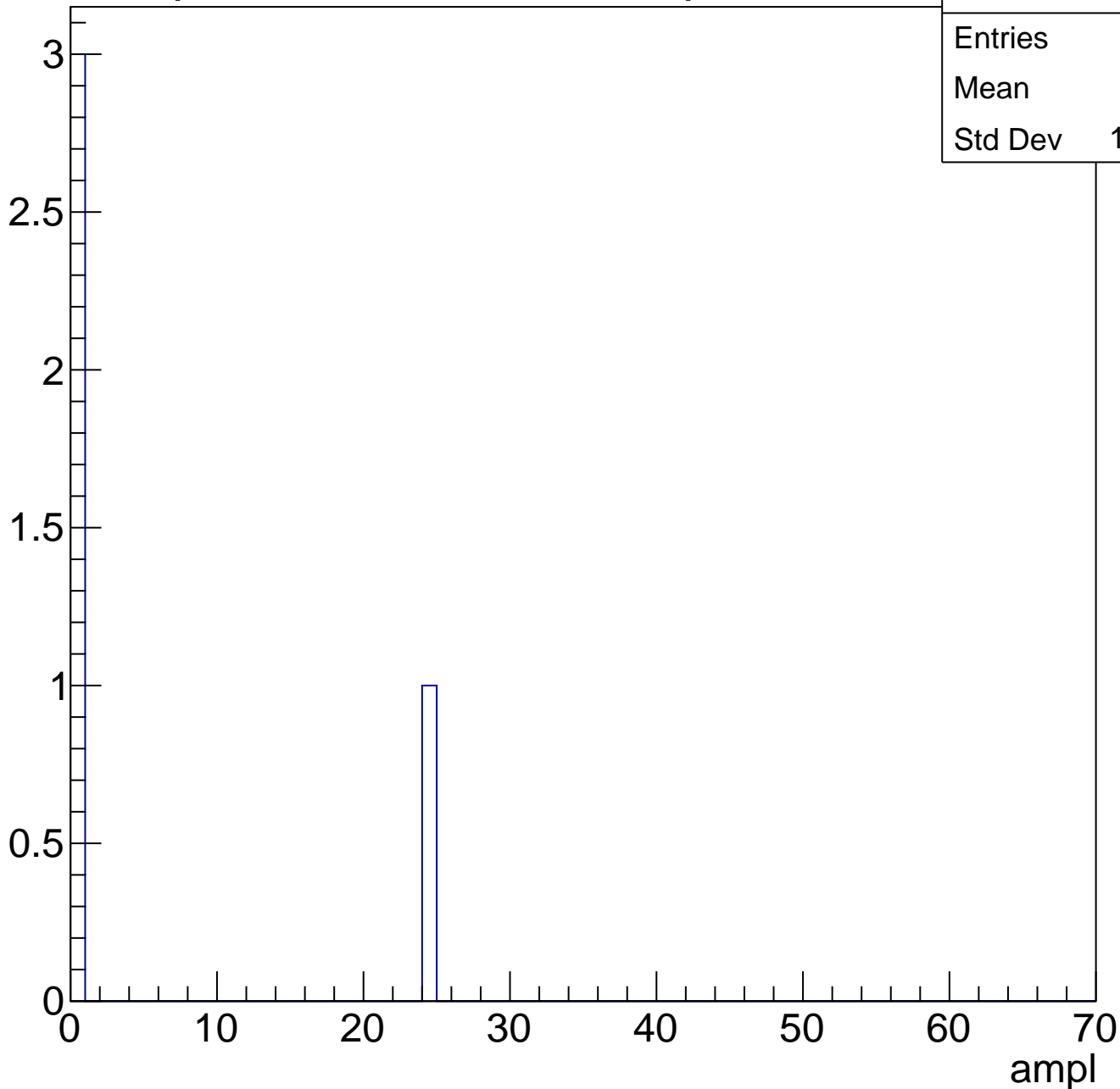


Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch45, adc0

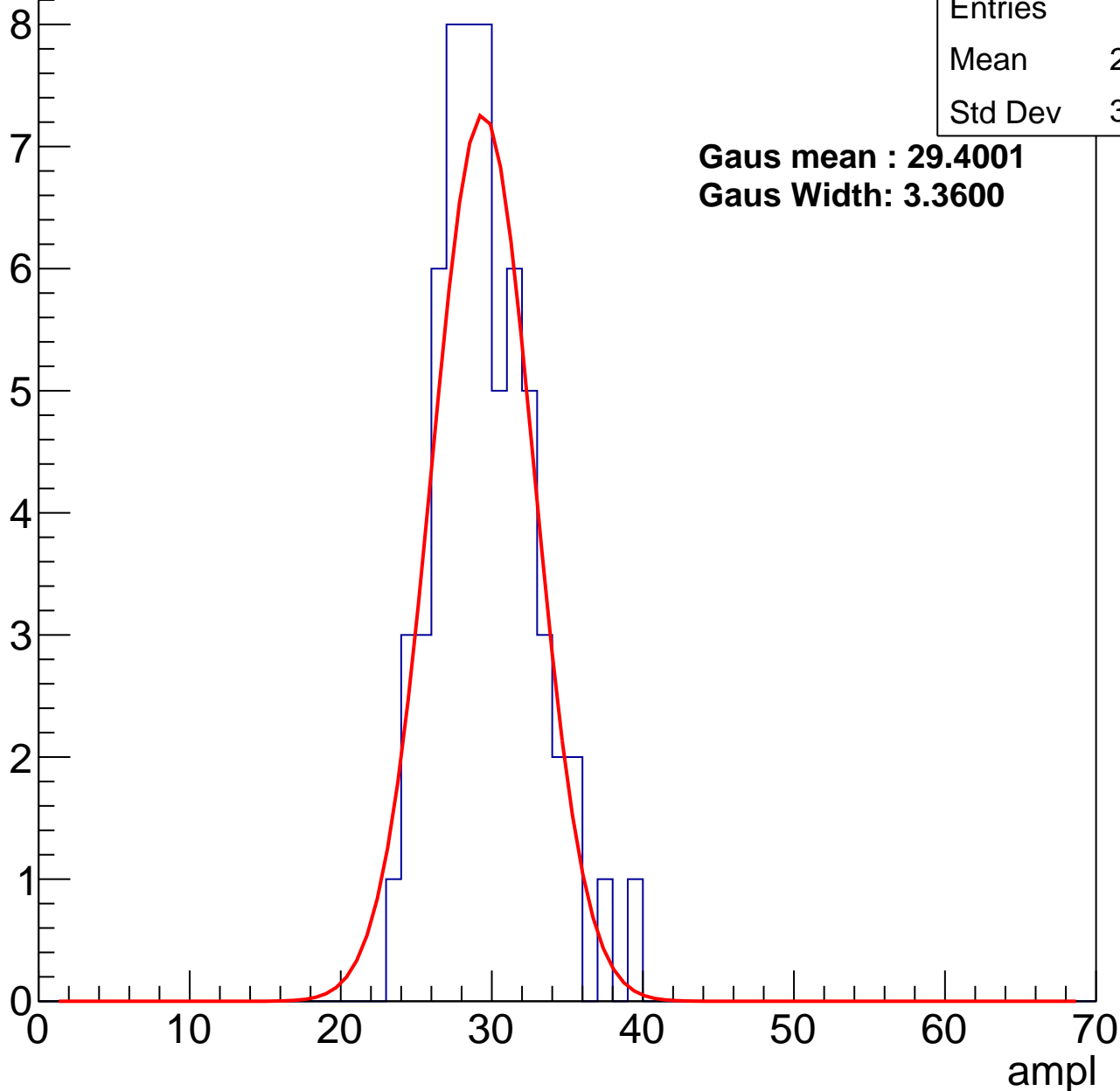
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.15
Std Dev	3.262

Gaus mean : 29.4001

Gaus Width: 3.3600



B0L001S, U11-ch45, adc1

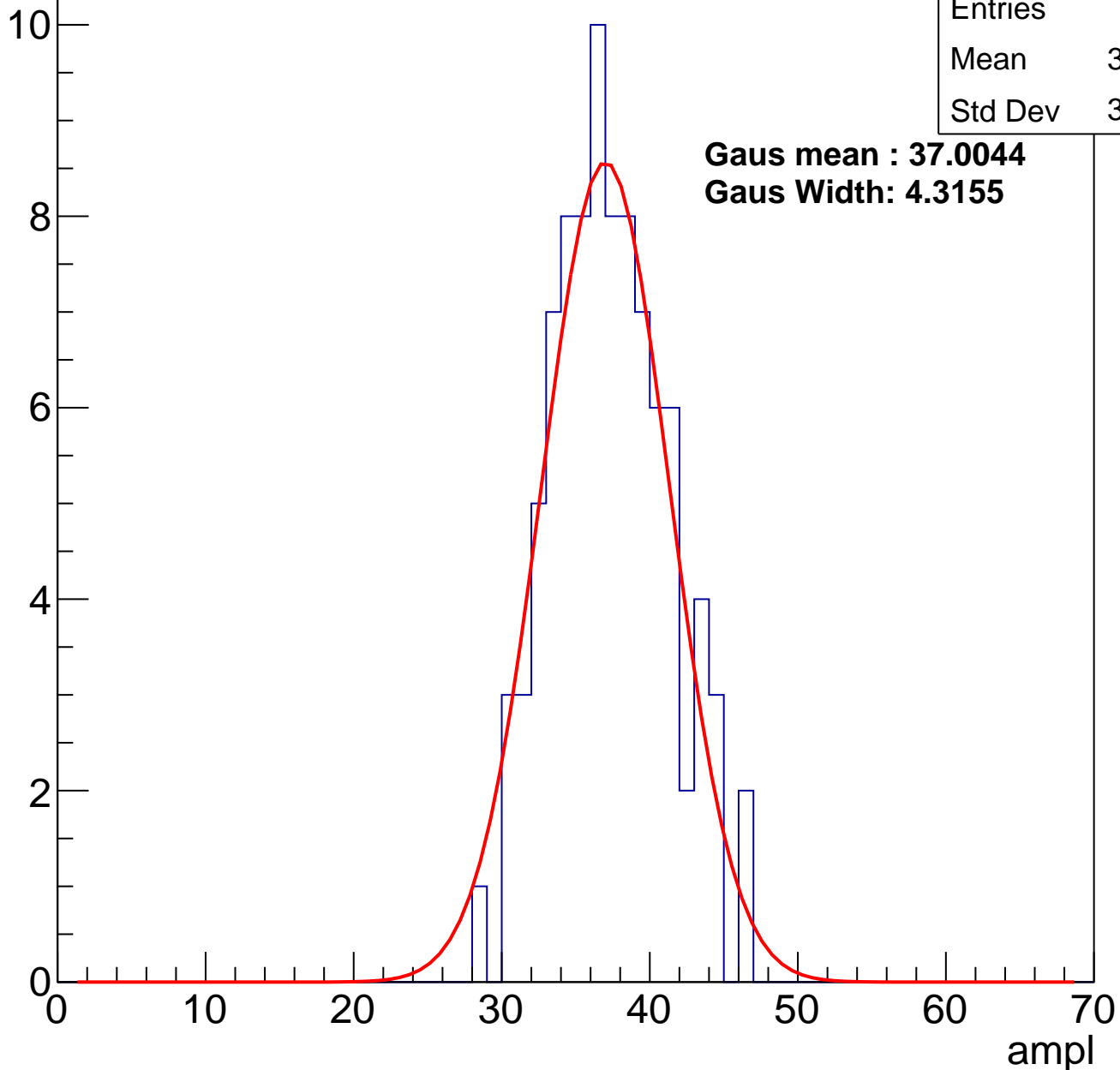
calib_packv5_042523_0143.root, FC#9, port A1

Entries	91
Mean	36.85
Std Dev	3.886

Gaus mean : 37.0044

Gaus Width: 4.3155

Entry



B0L001S, U11-ch45, adc2

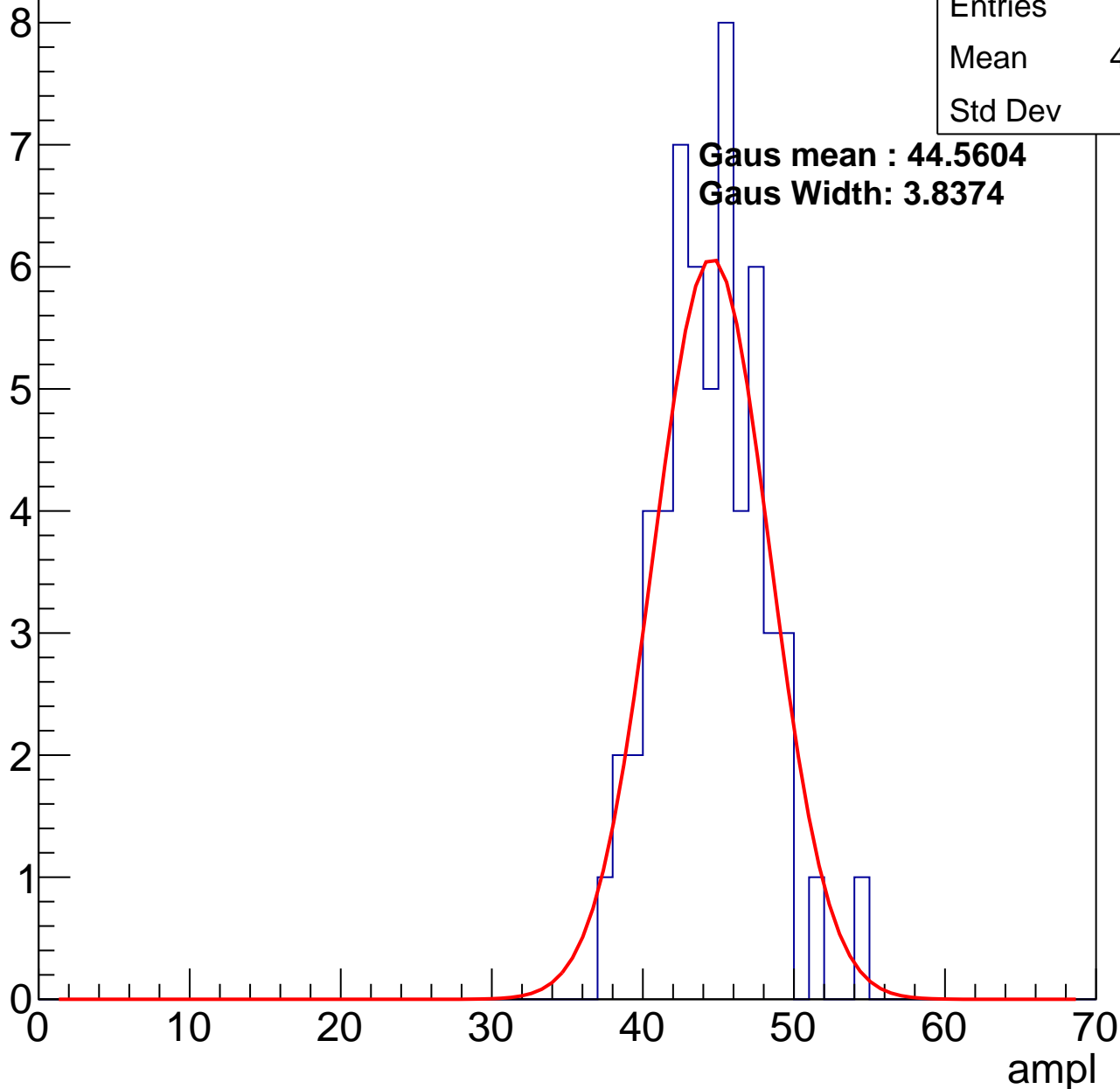
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.02
Std Dev	3.39

Gaus mean : 44.5604

Gaus Width: 3.8374

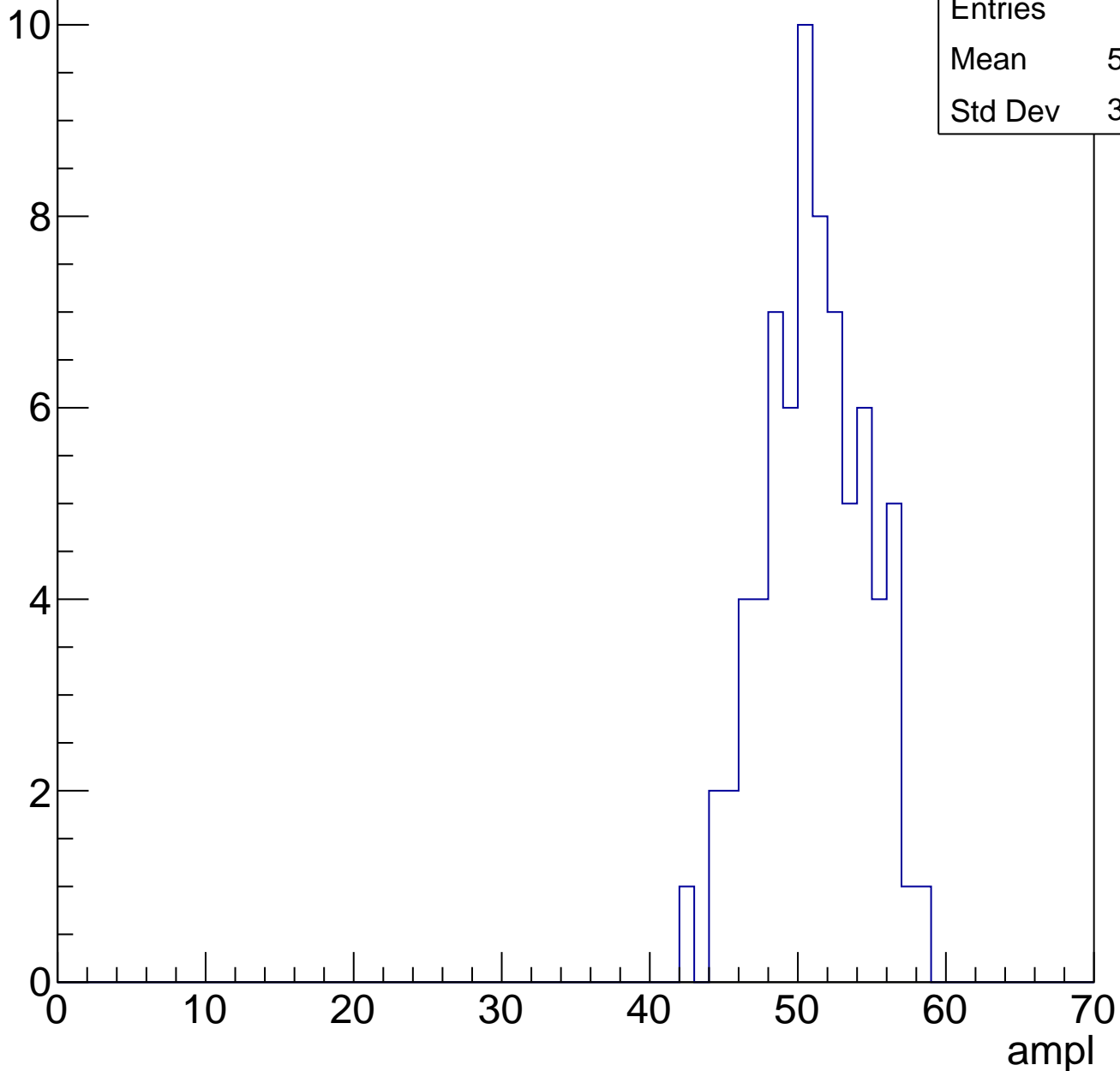


B0L001S, U11-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	50.66
Std Dev	3.429

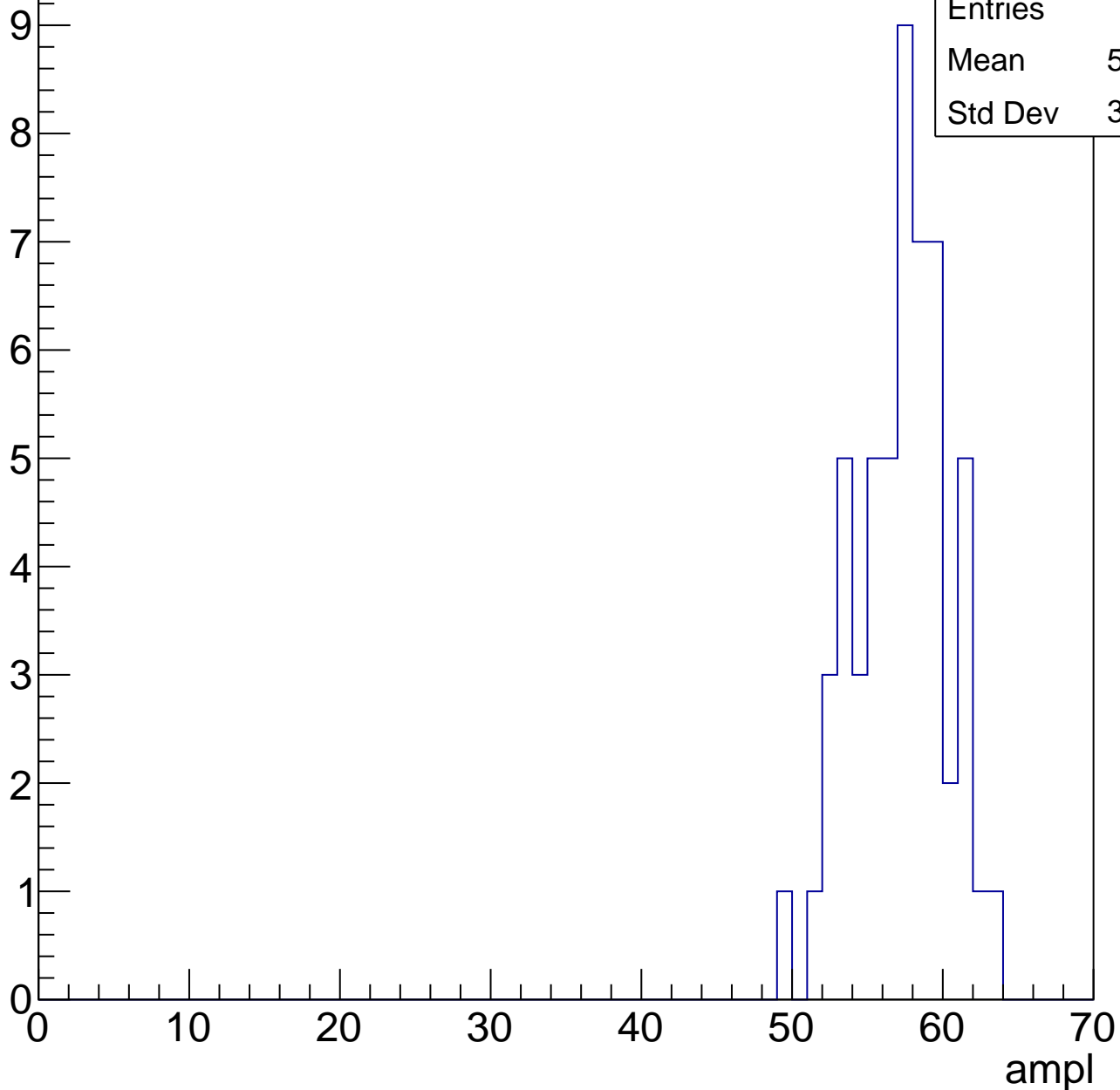
Entry



B0L001S, U11-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

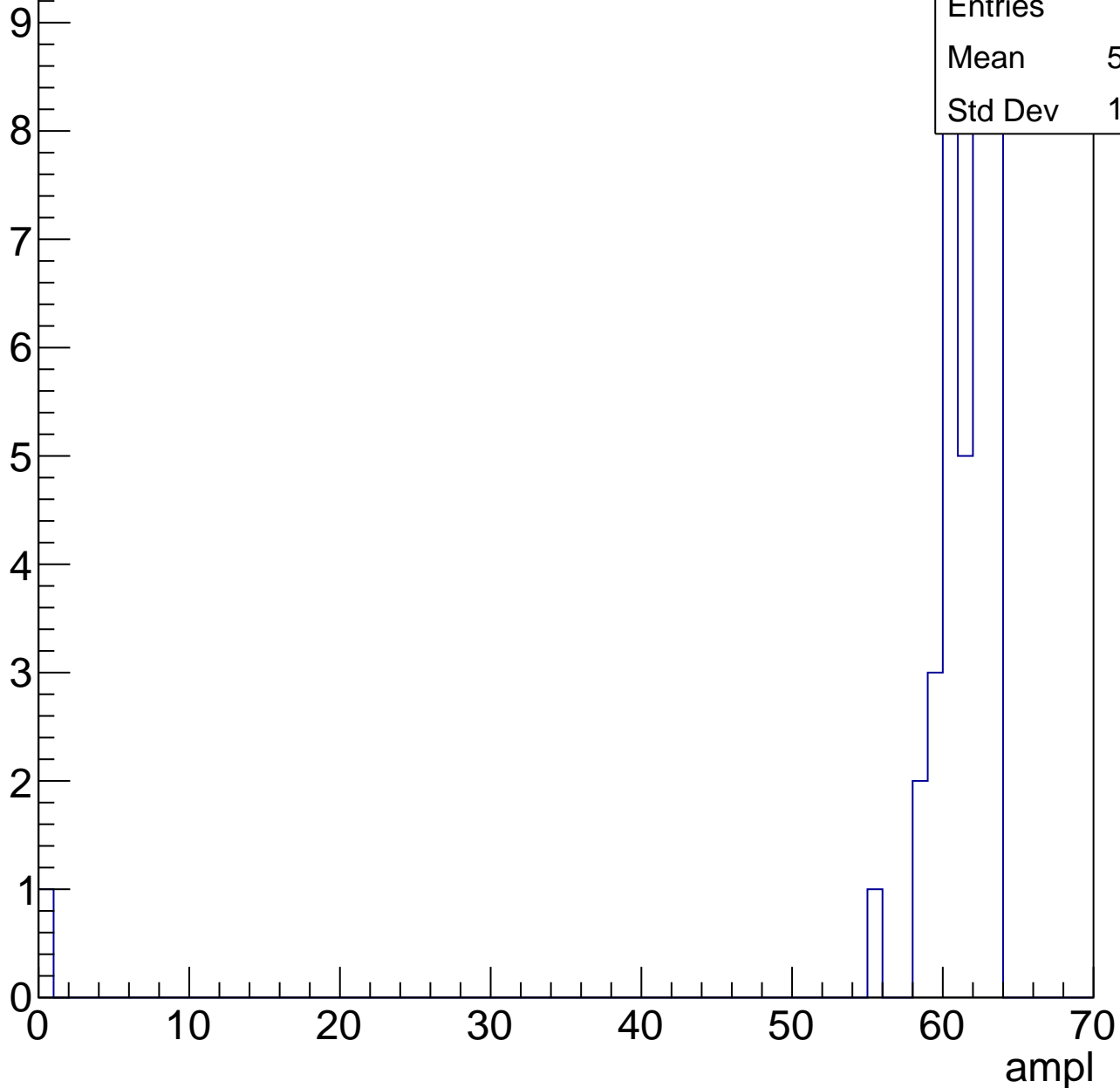
Entry



B0L001S, U11-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch46, adc0

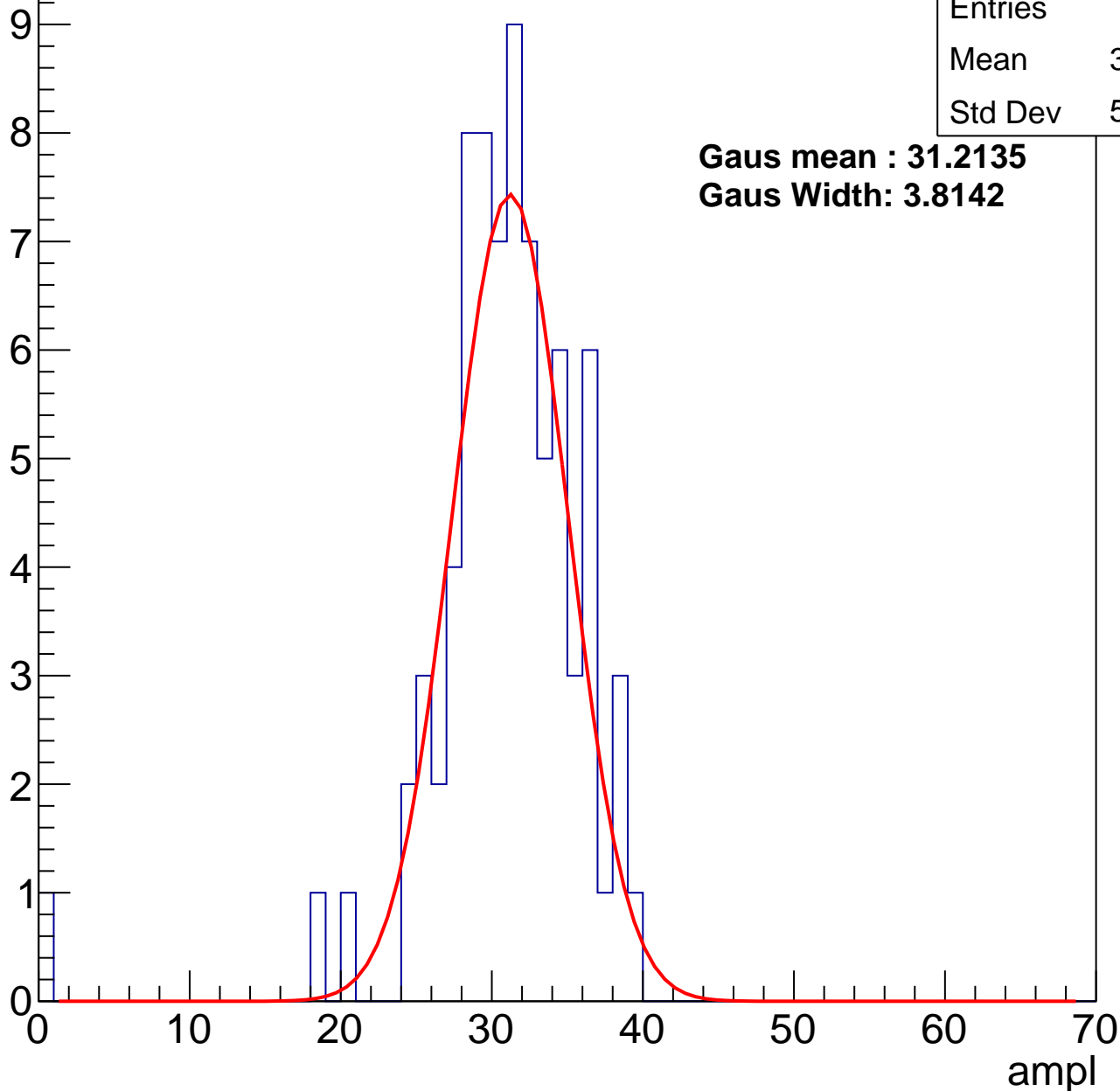
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.38
Std Dev	5.285

Gaus mean : 31.2135

Gaus Width: 3.8142



B0L001S, U11-ch46, adc1

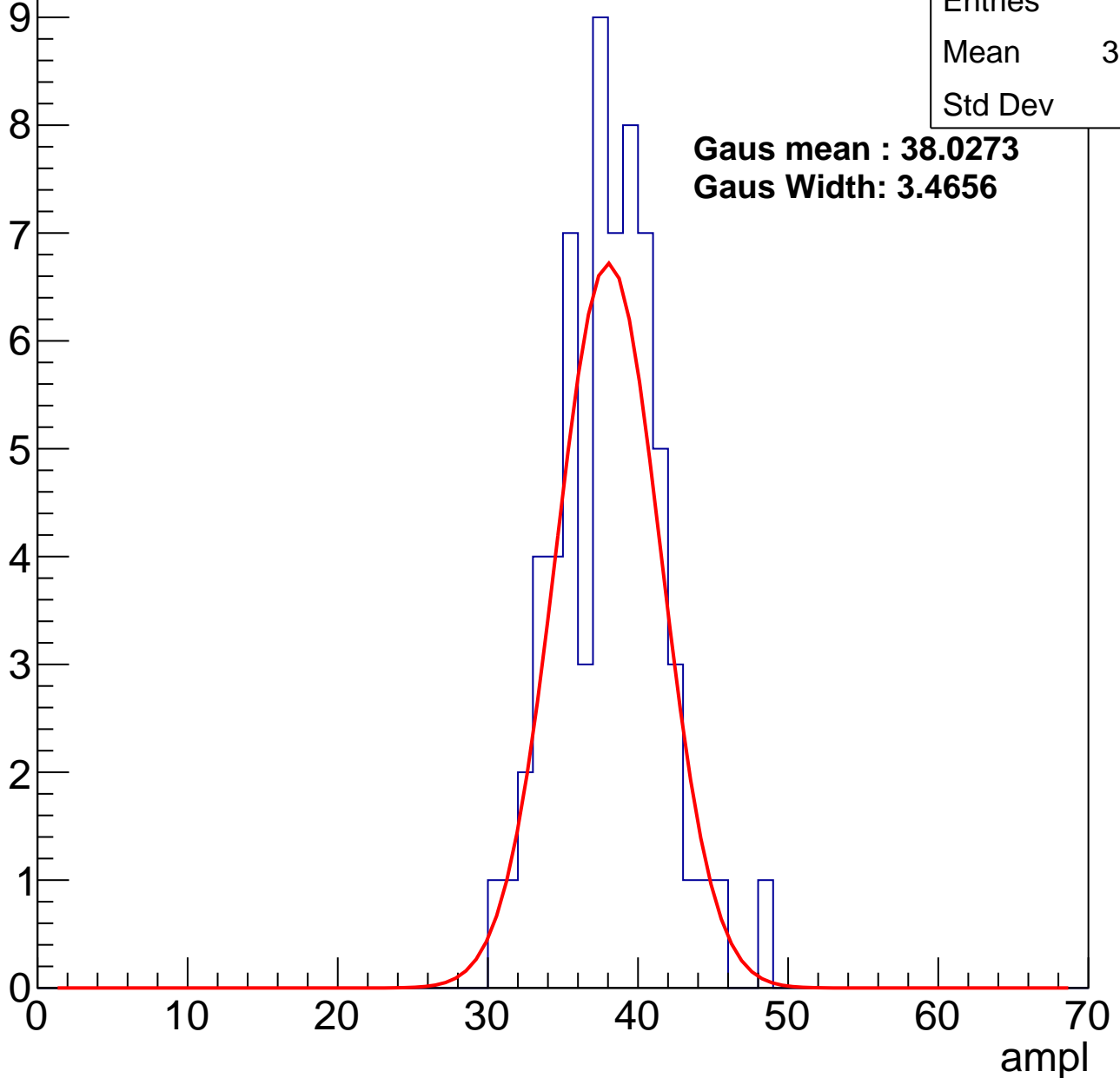
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.66
Std Dev	3.43

Gaus mean : 38.0273

Gaus Width: 3.4656



B0L001S, U11-ch46, adc2

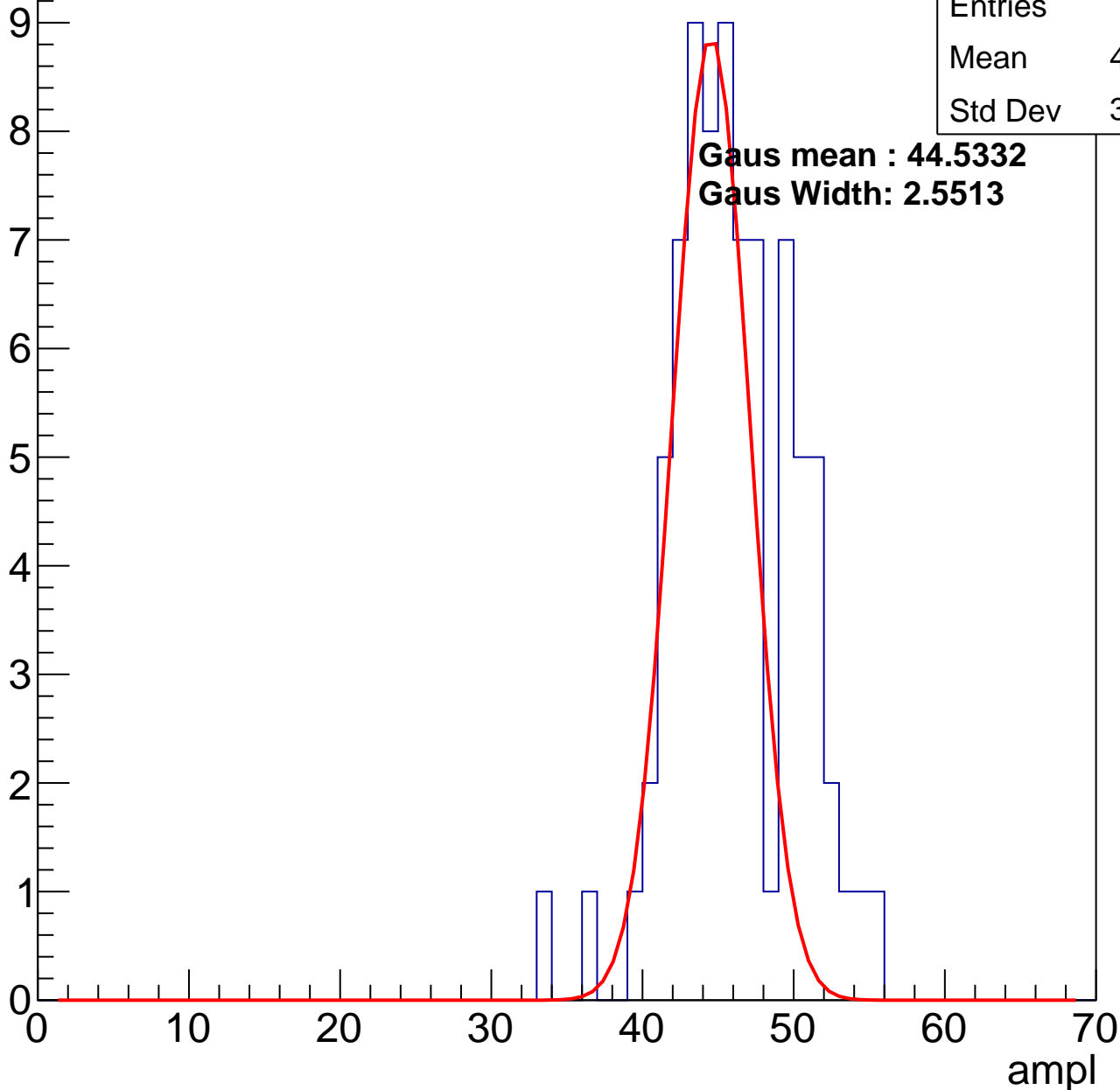
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	45.55
Std Dev	3.997

Gaus mean : 44.5332

Gaus Width: 2.5513

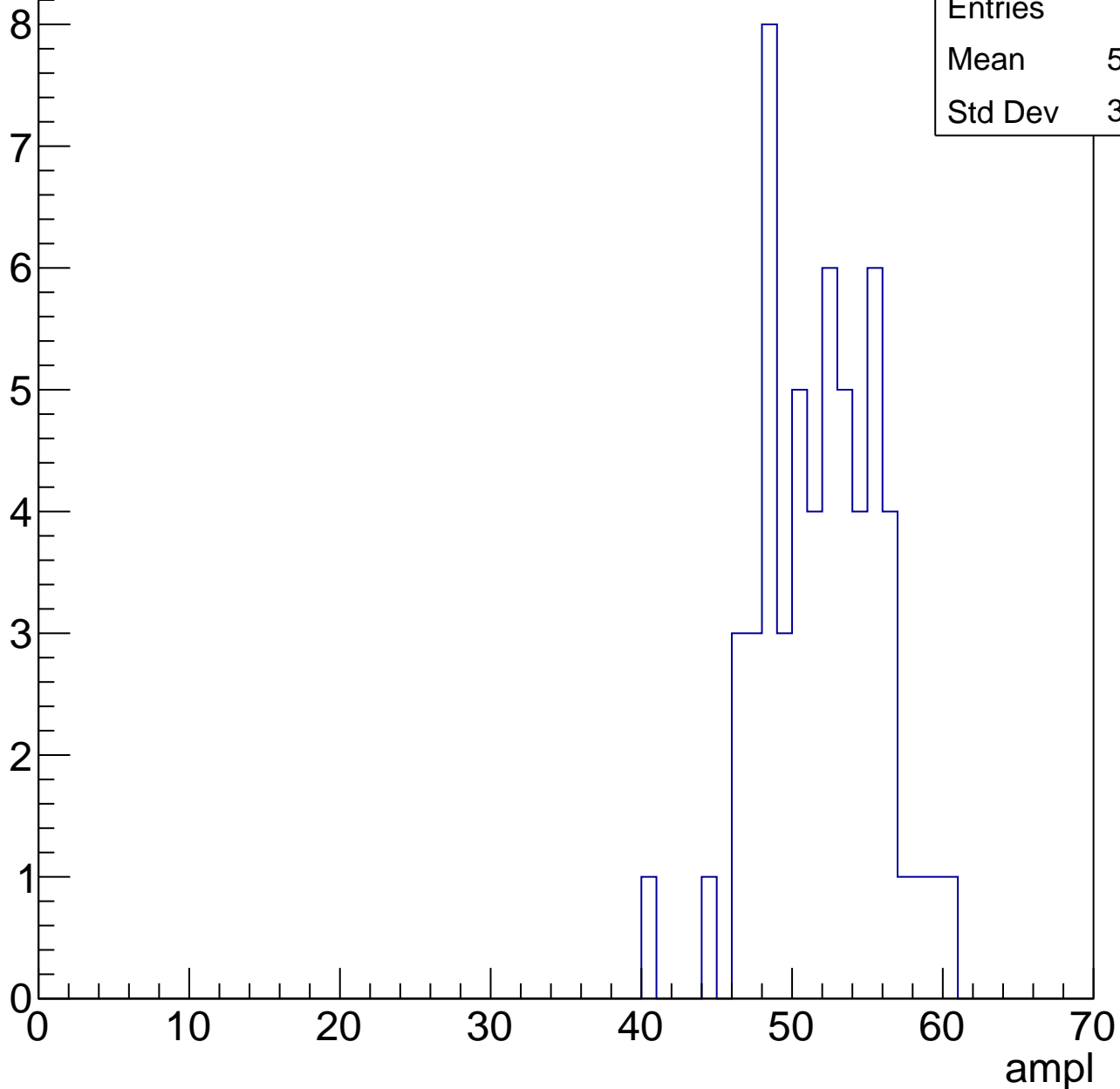


B0L001S, U11-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	51.39
Std Dev	3.888

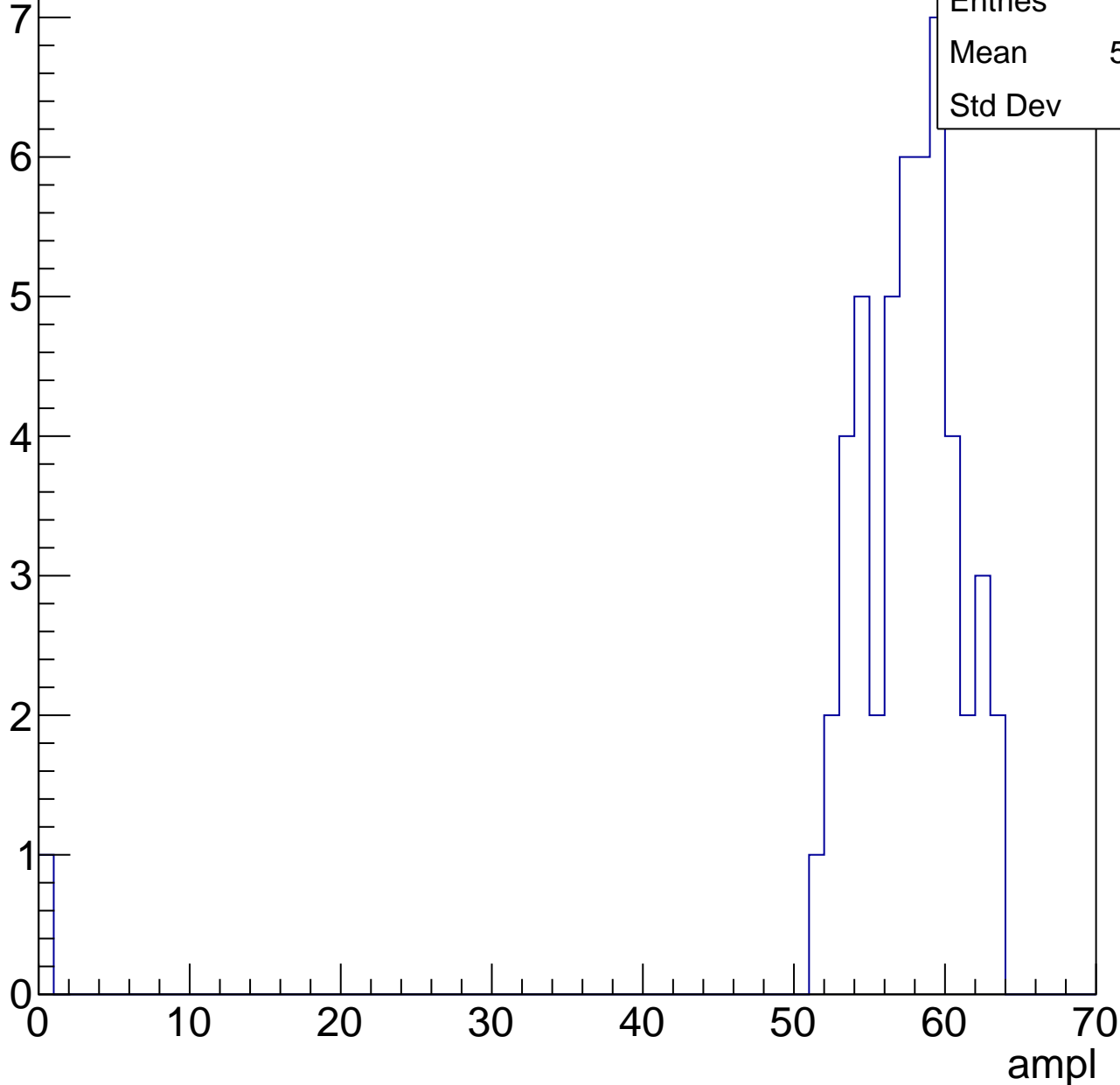


B0L001S, U11-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.08
Std Dev	8.56

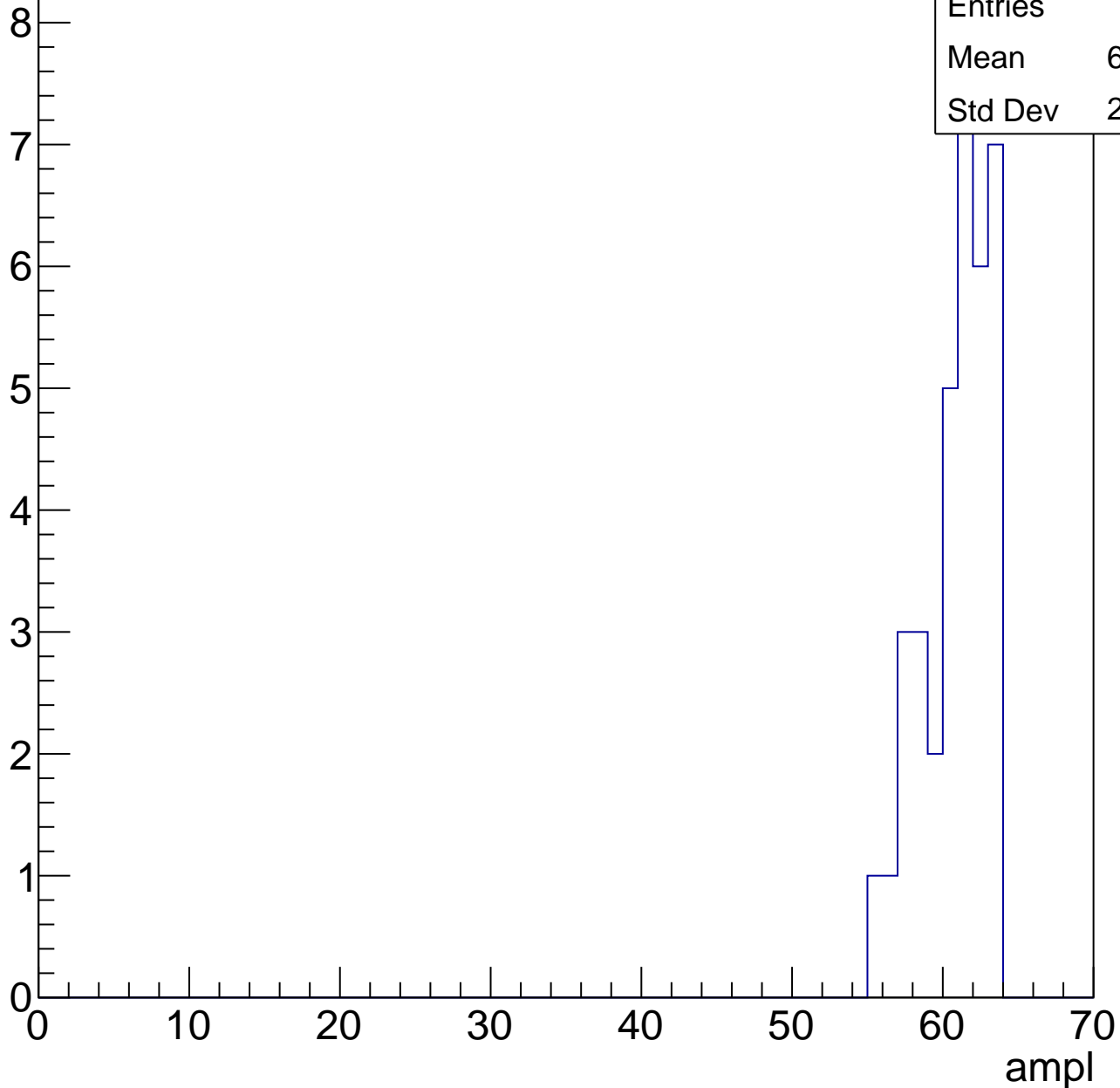


B0L001S, U11-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	60.42
Std Dev	2.178



B0L001S, U11-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch47, adc0

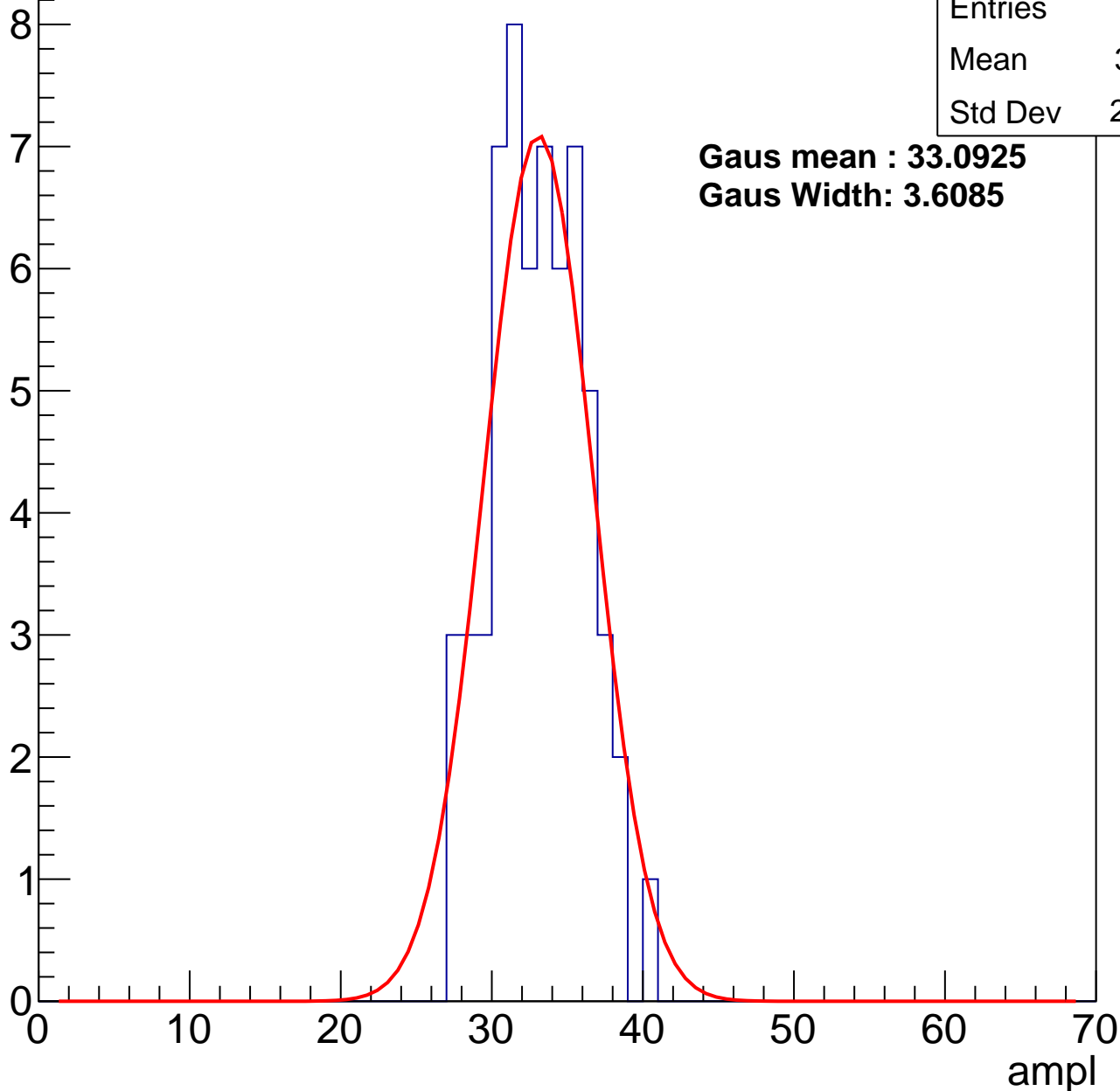
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	32.61
Std Dev	2.999

Gaus mean : 33.0925

Gaus Width: 3.6085



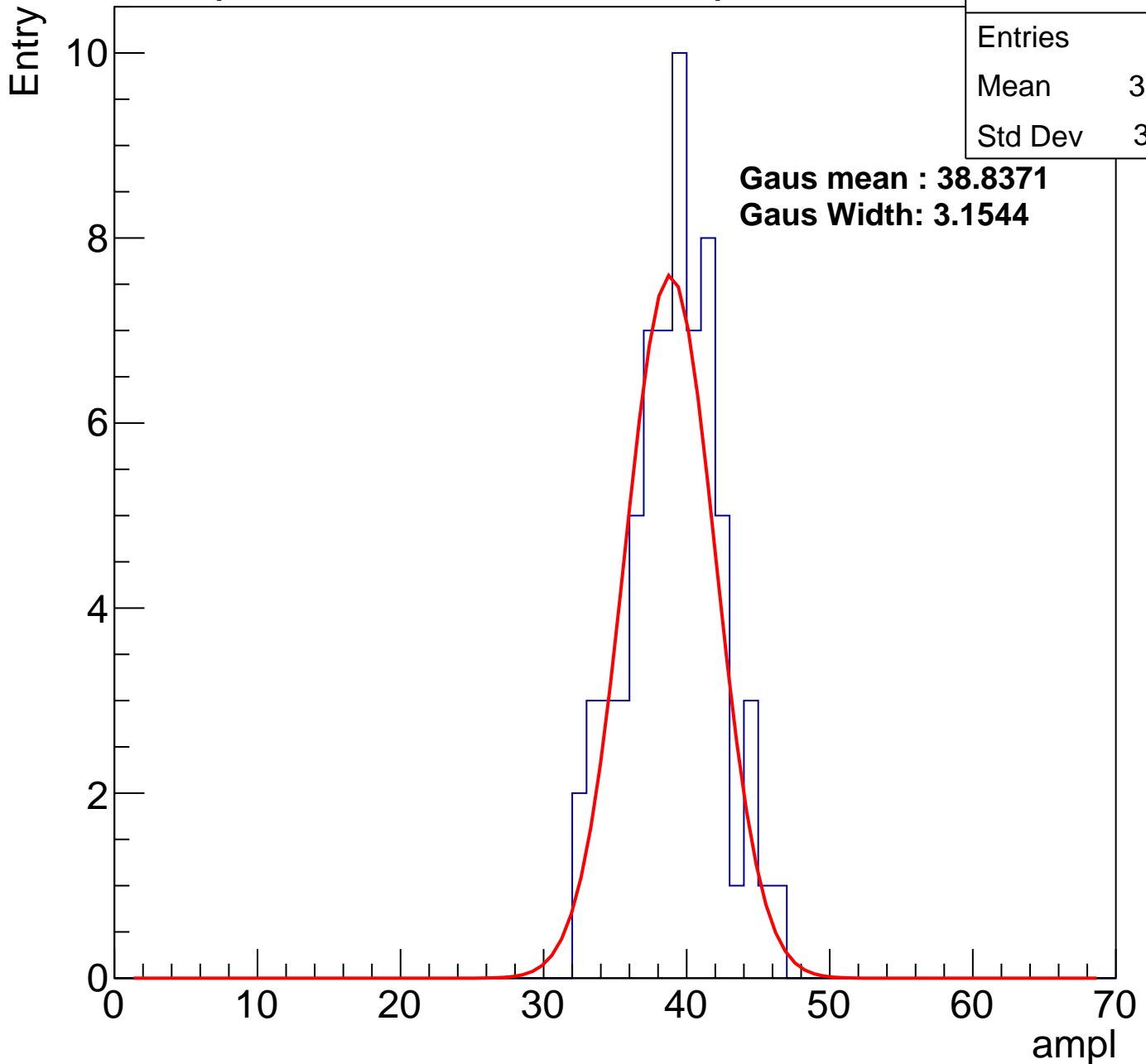
B0L001S, U11-ch47, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	38.62
Std Dev	3.161

Gaus mean : 38.8371

Gaus Width: 3.1544



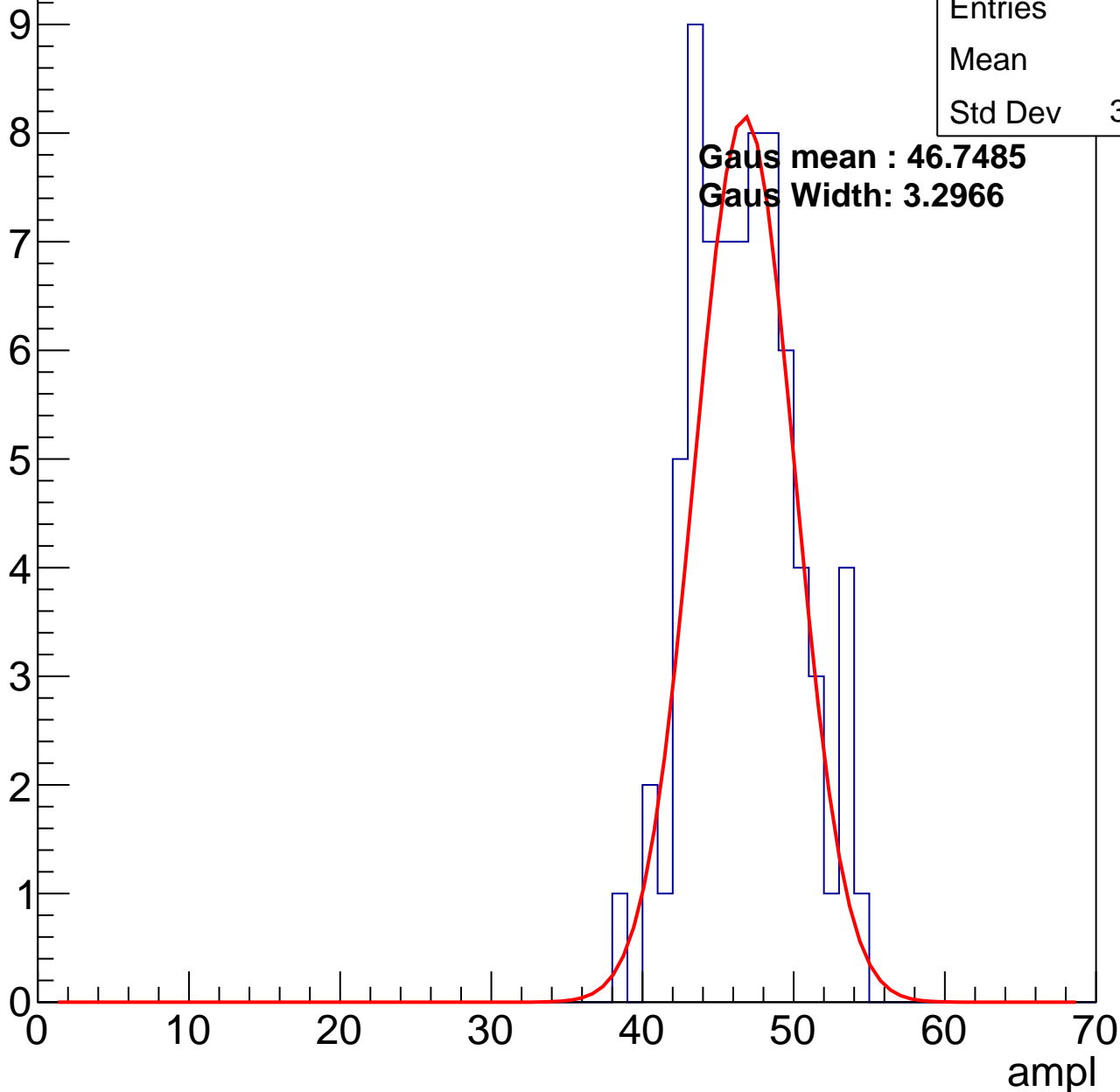
B0L001S, U11-ch47, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	46.3
Std Dev	3.447

Gaus mean : 46.7485
Gaus Width: 3.2966

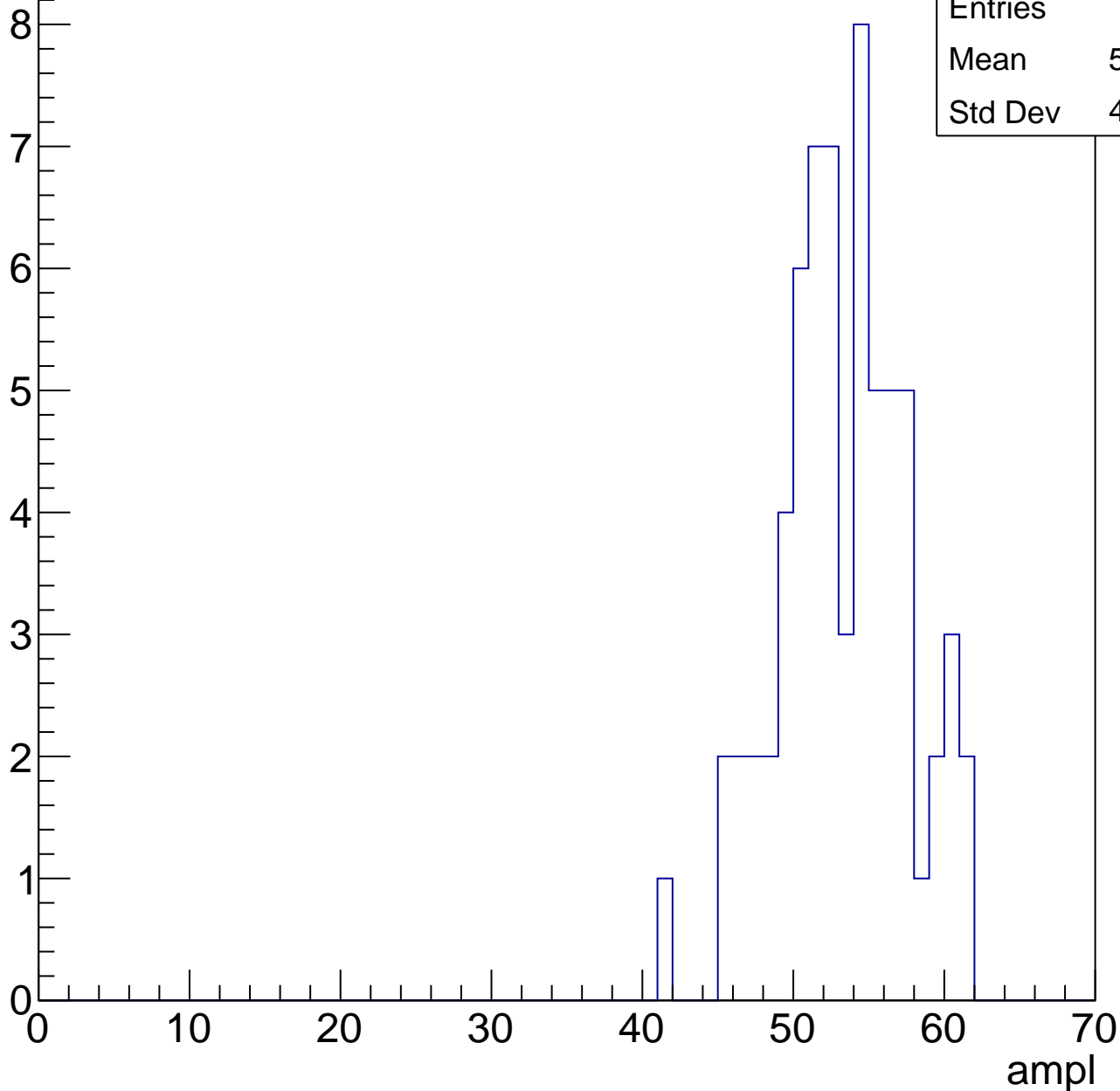


B0L001S, U11-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

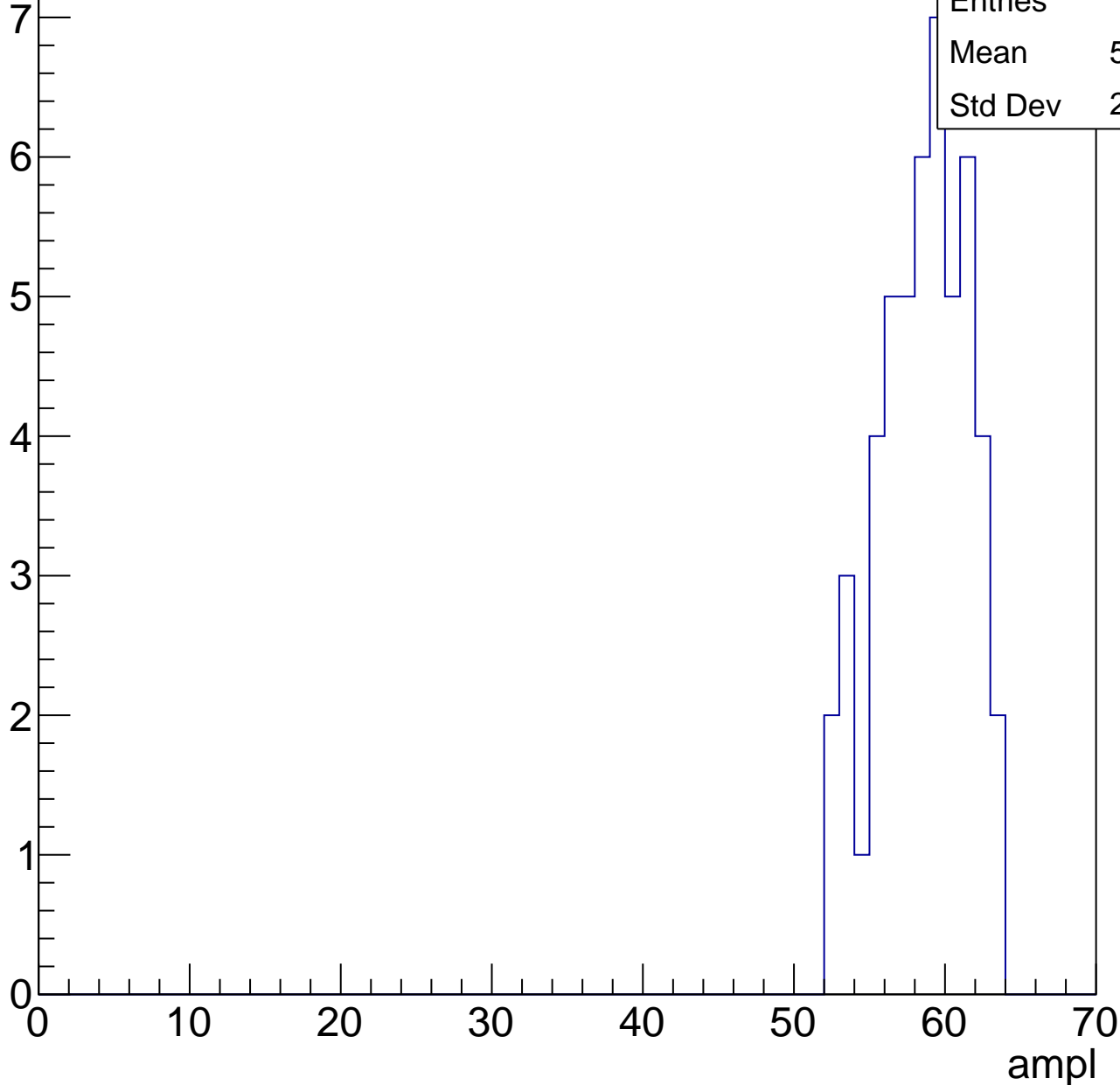
Entries	67
Mean	52.82
Std Dev	4.175



B0L001S, U11-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

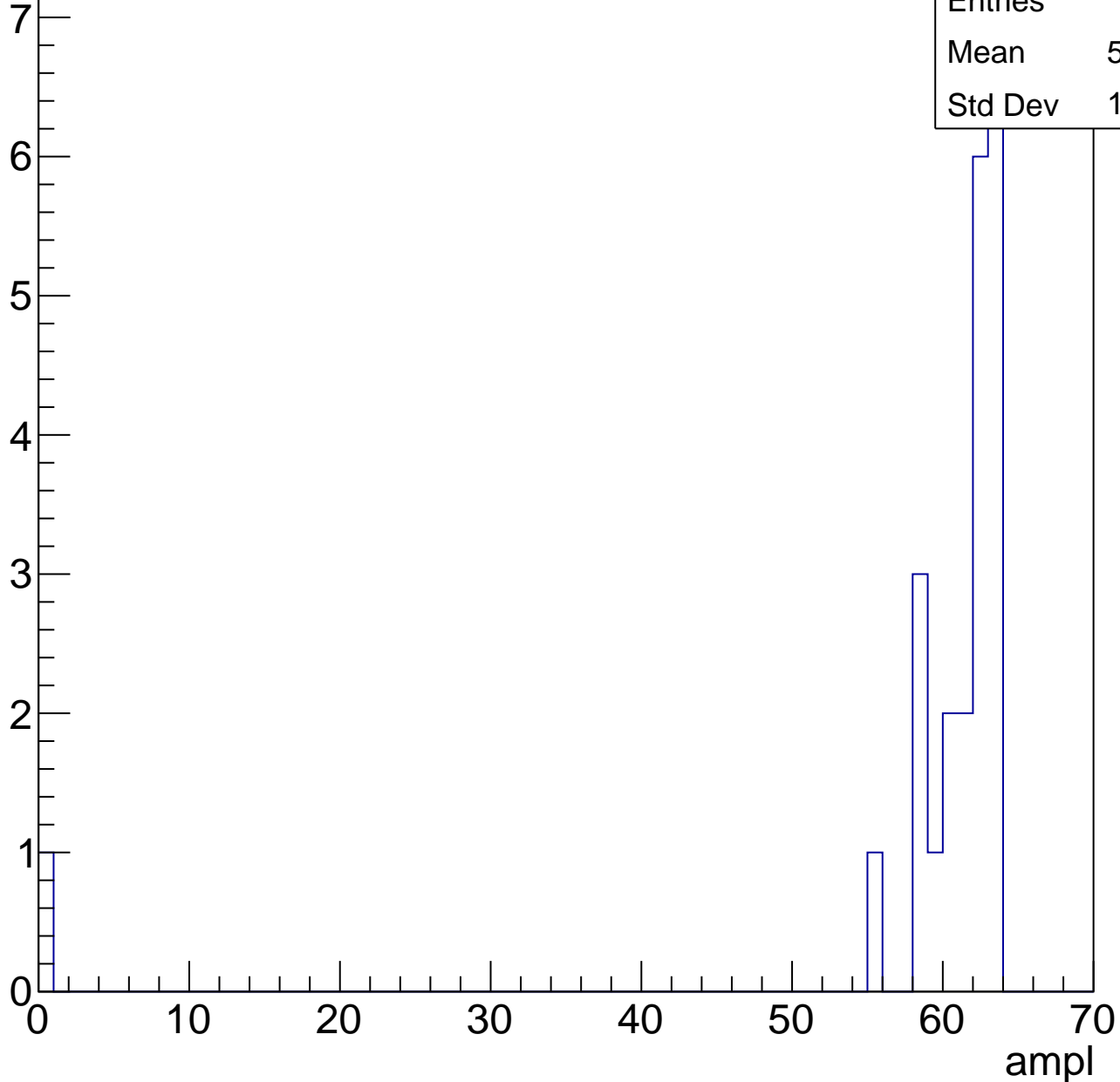


B0L001S, U11-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.39
Std Dev	12.63



B0L001S, U11-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch48, adc0

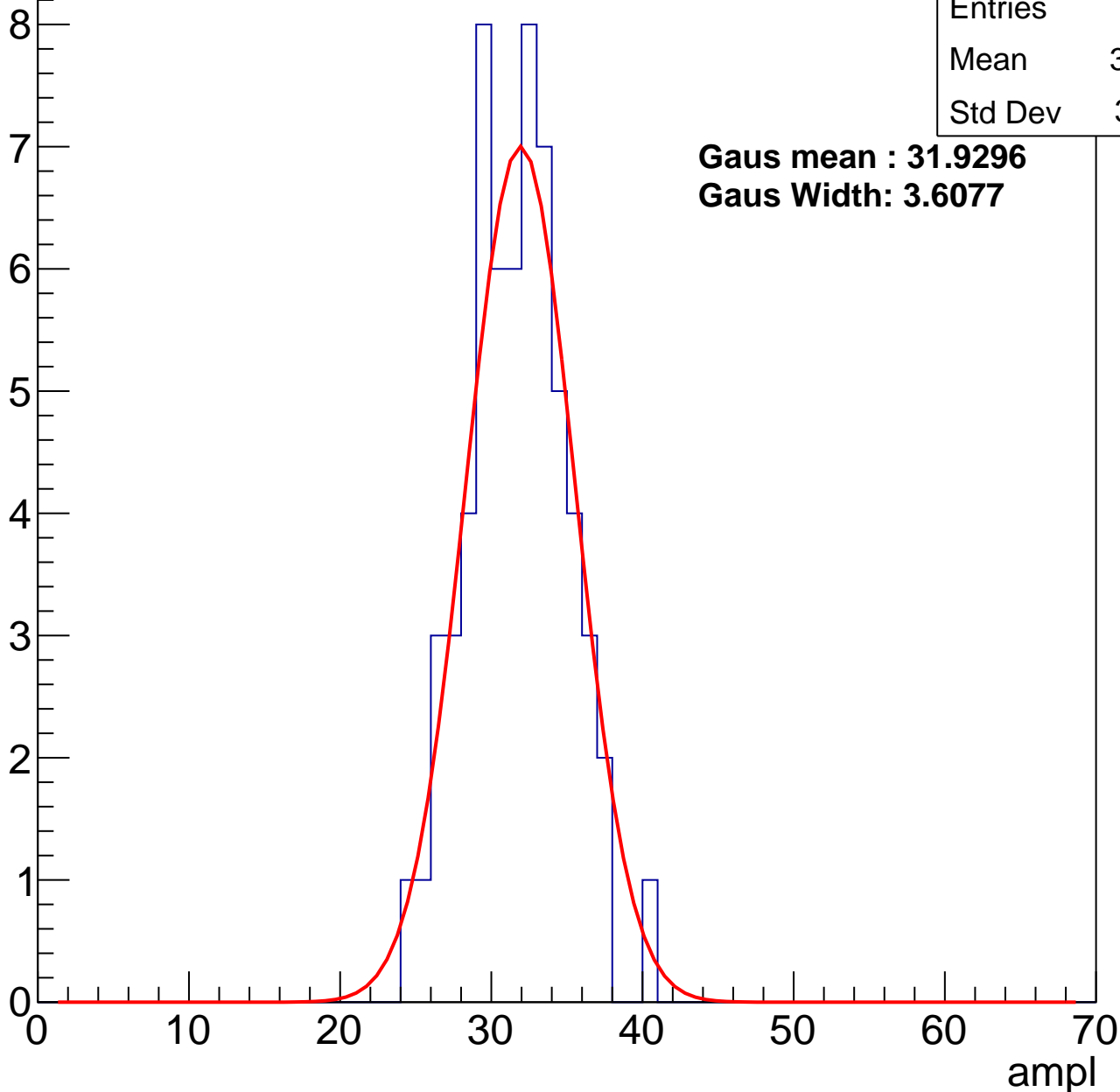
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.24
Std Dev	3.241

Gaus mean : 31.9296

Gaus Width: 3.6077



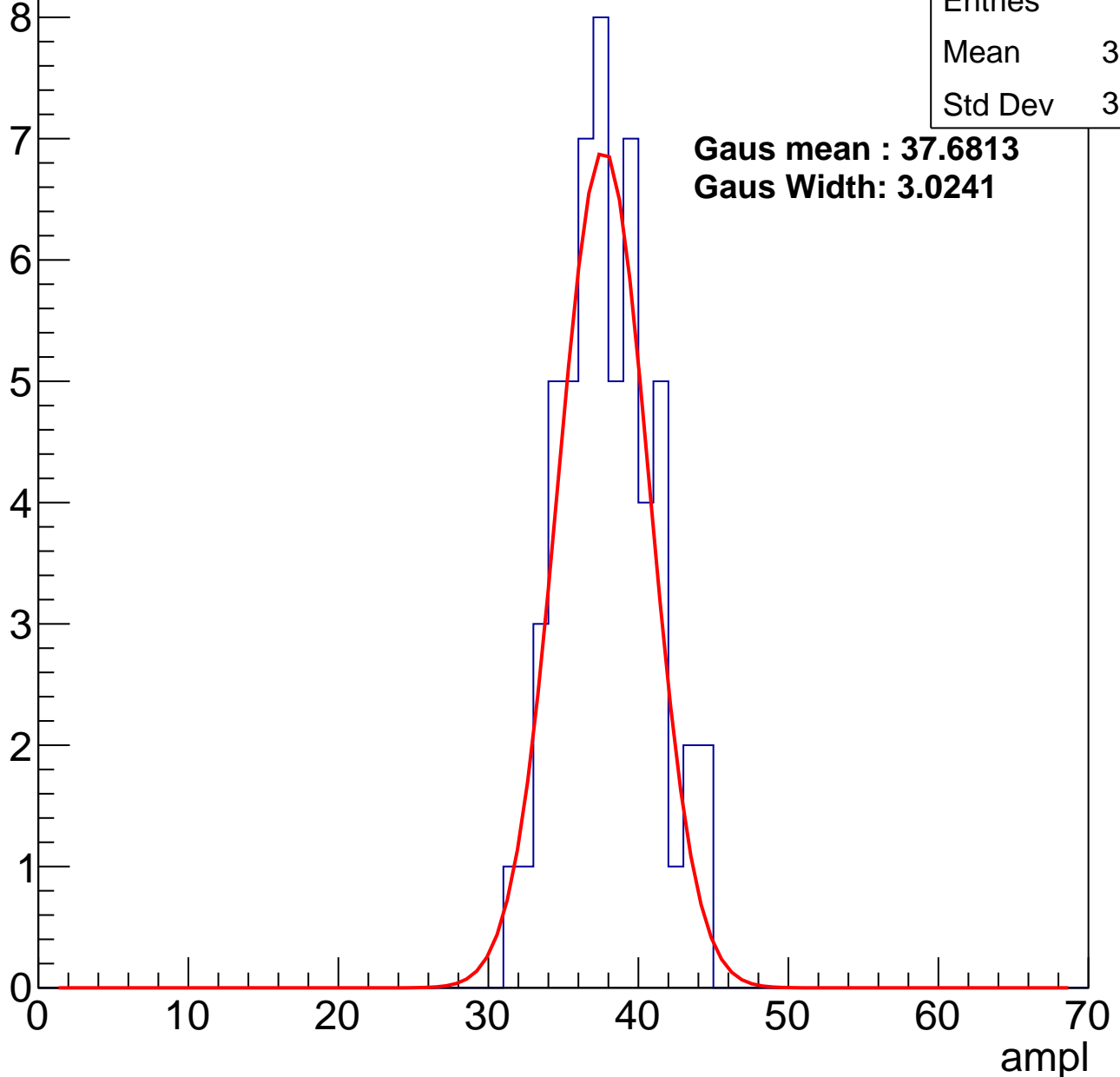
B0L001S, U11-ch48, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	37.48
Std Dev	3.024

Gaus mean : 37.6813
Gaus Width: 3.0241



B0L001S, U11-ch48, adc2

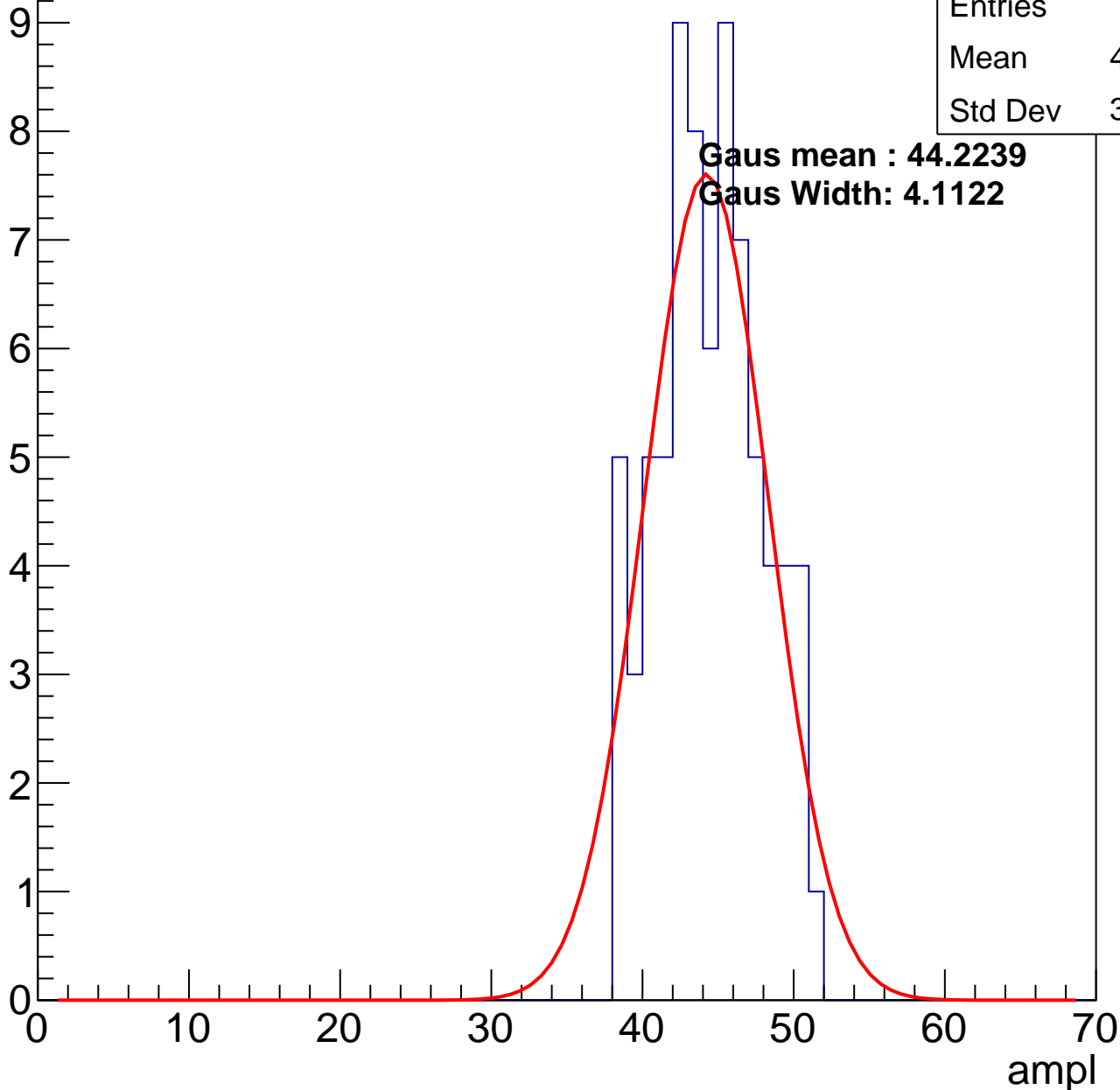
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.99
Std Dev	3.392

Gaus mean : 44.2239

Gaus Width: 4.1122

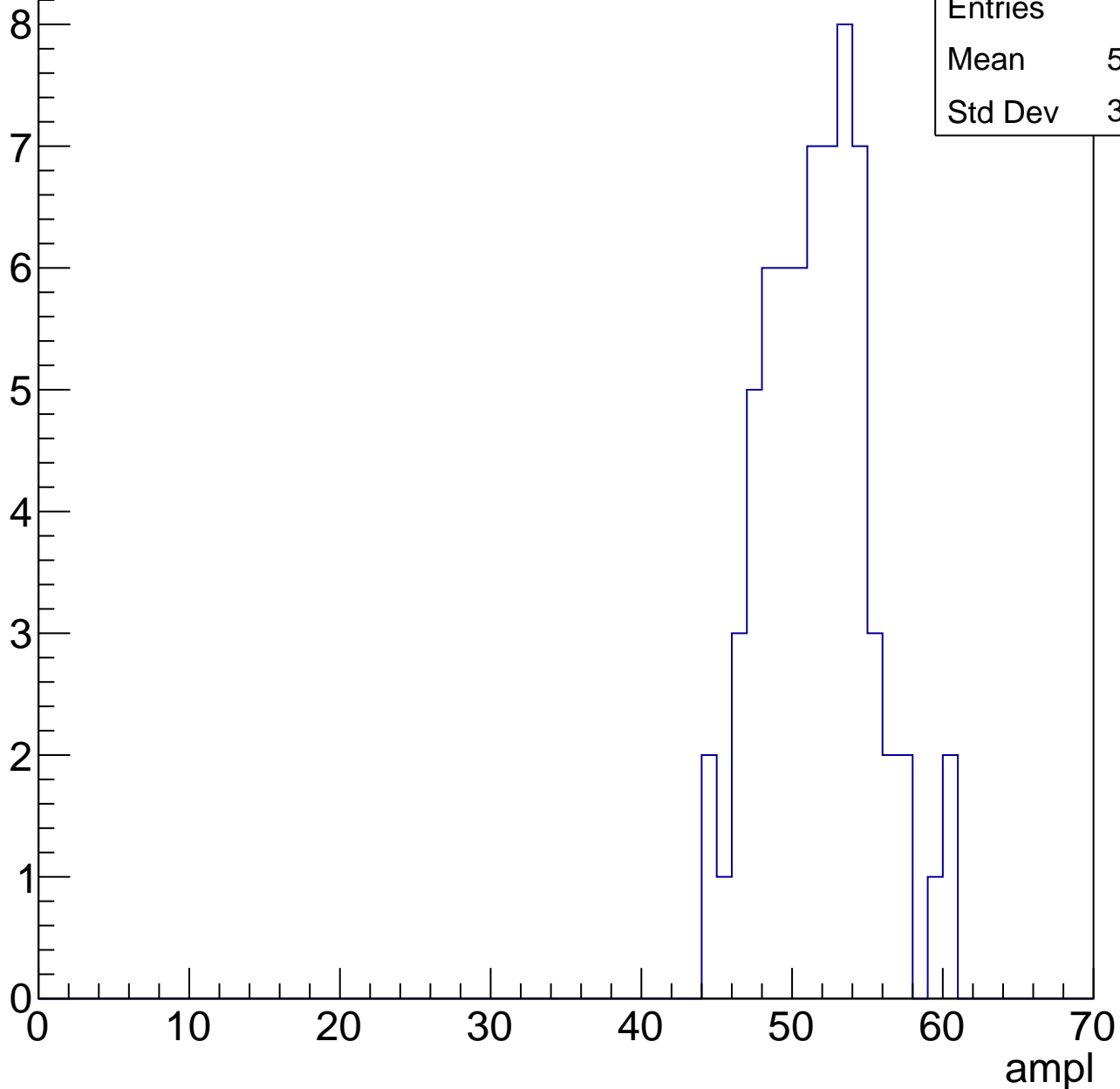


B0L001S, U11-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.19
Std Dev	3.574

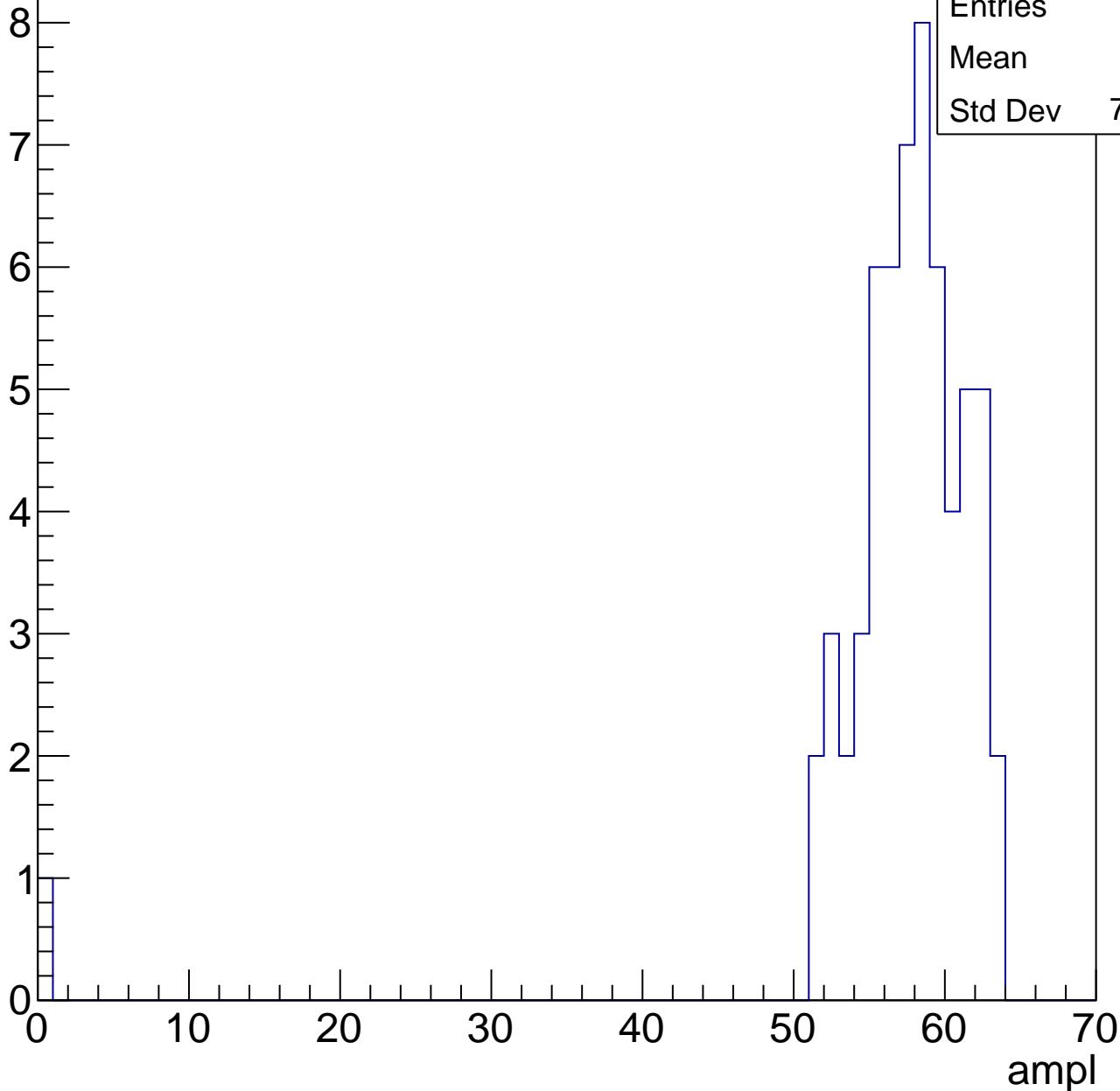


B0L001S, U11-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.5
Std Dev	7.974

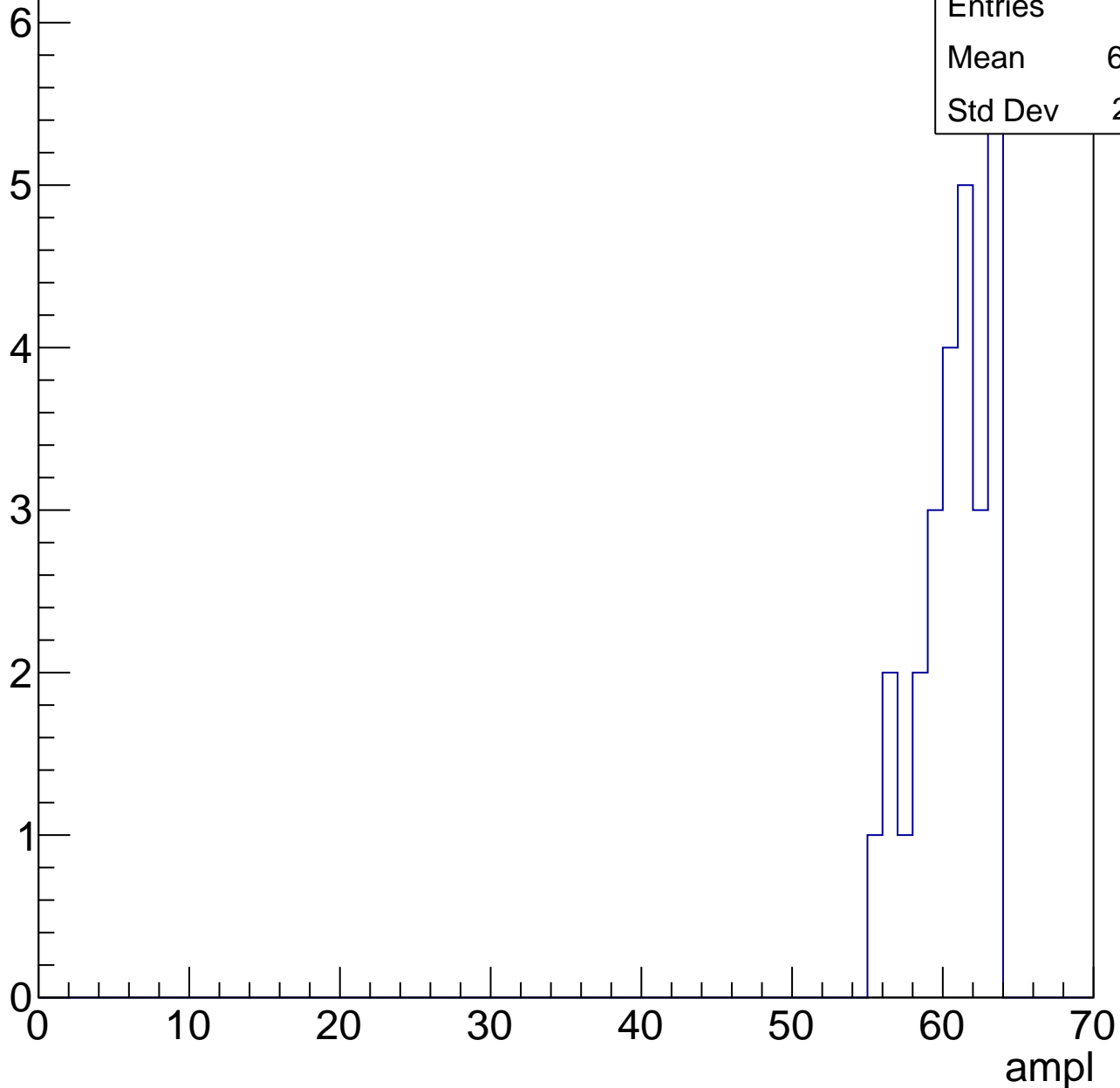


B0L001S, U11-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	60.22
Std Dev	2.331



B0L001S, U11-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch49, adc0

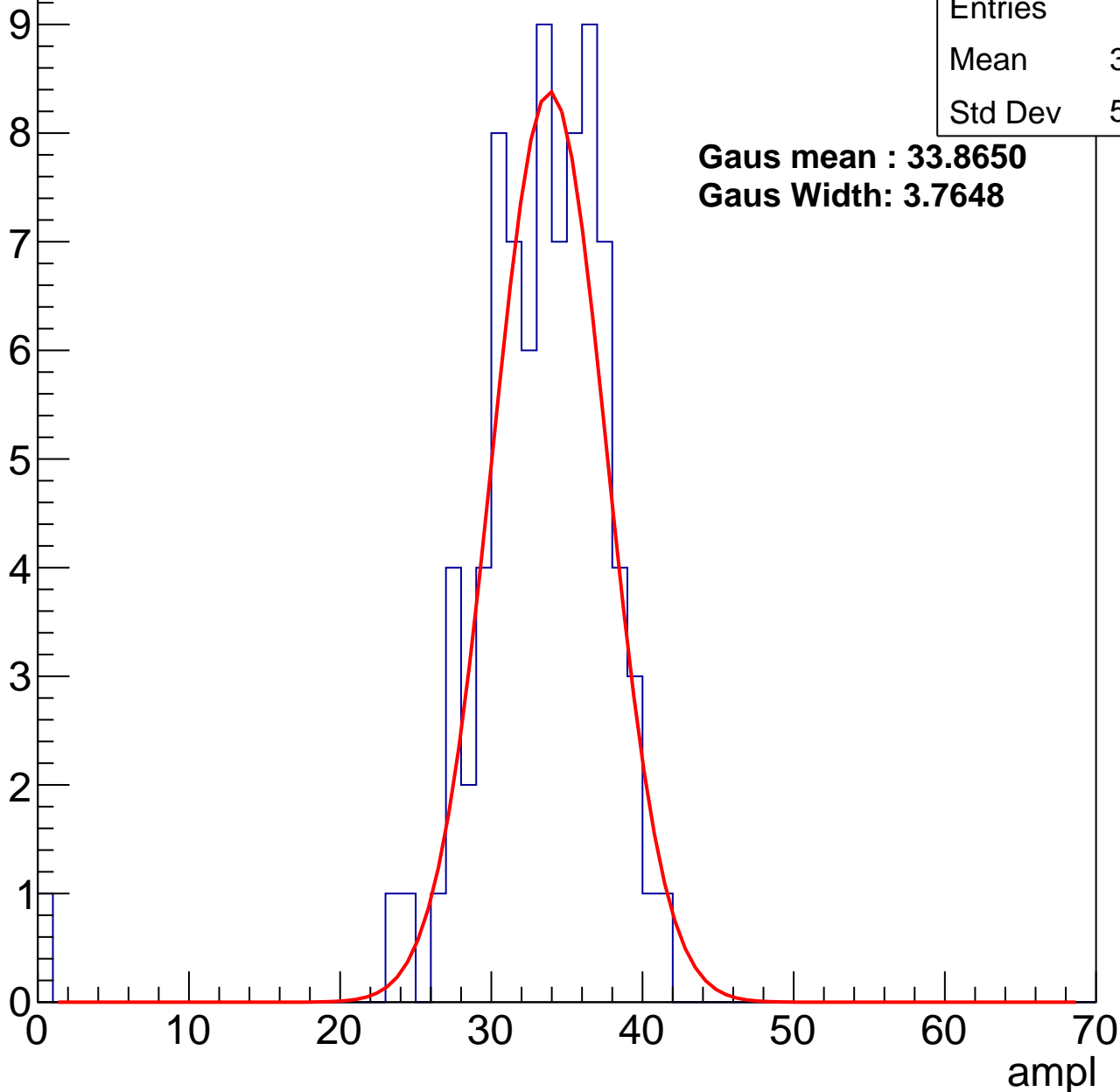
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	32.74
Std Dev	5.153

Gaus mean : 33.8650

Gaus Width: 3.7648



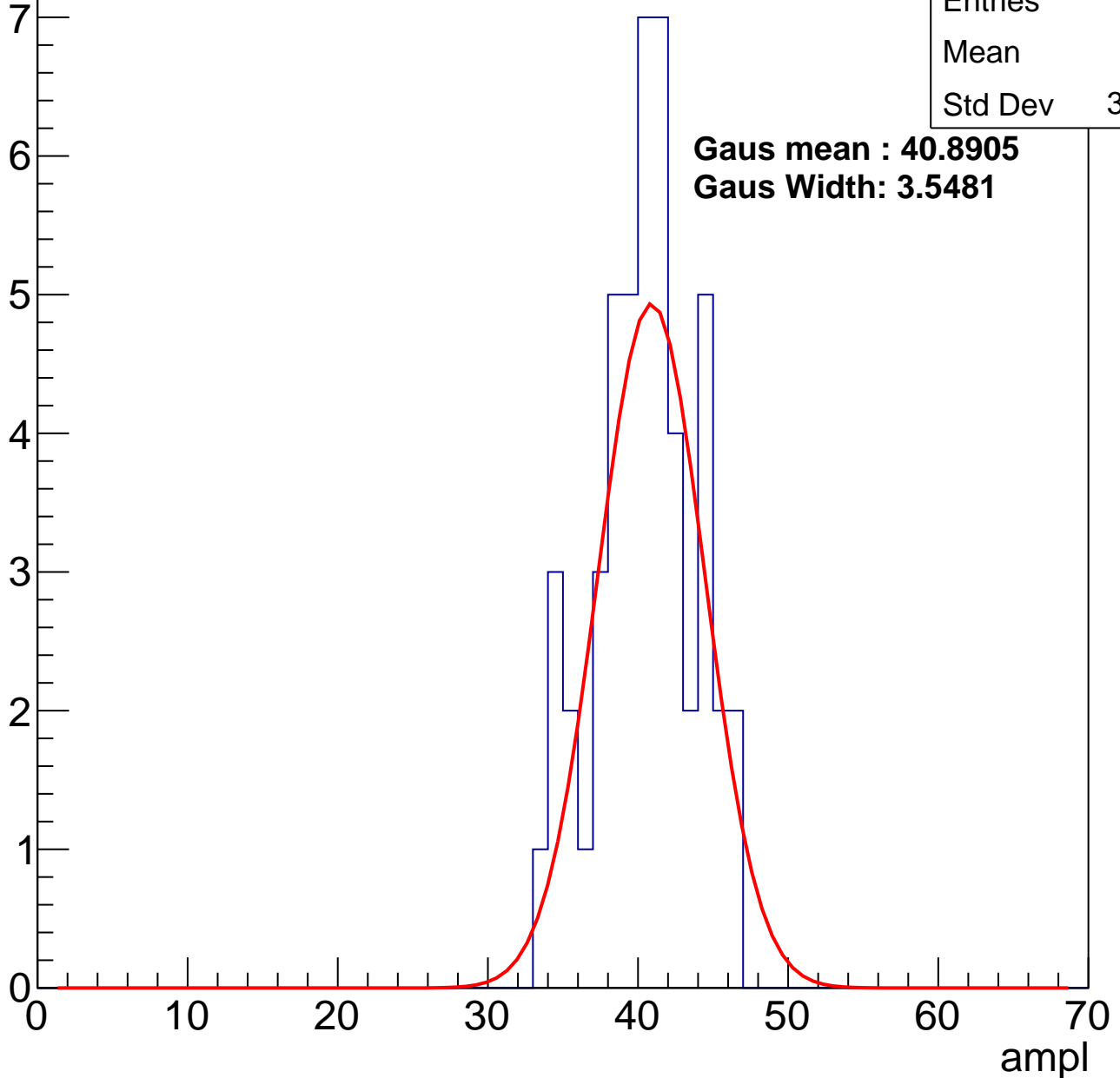
B0L001S, U11-ch49, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	40
Std Dev	3.251

Gaus mean : 40.8905
Gaus Width: 3.5481



B0L001S, U11-ch49, adc2

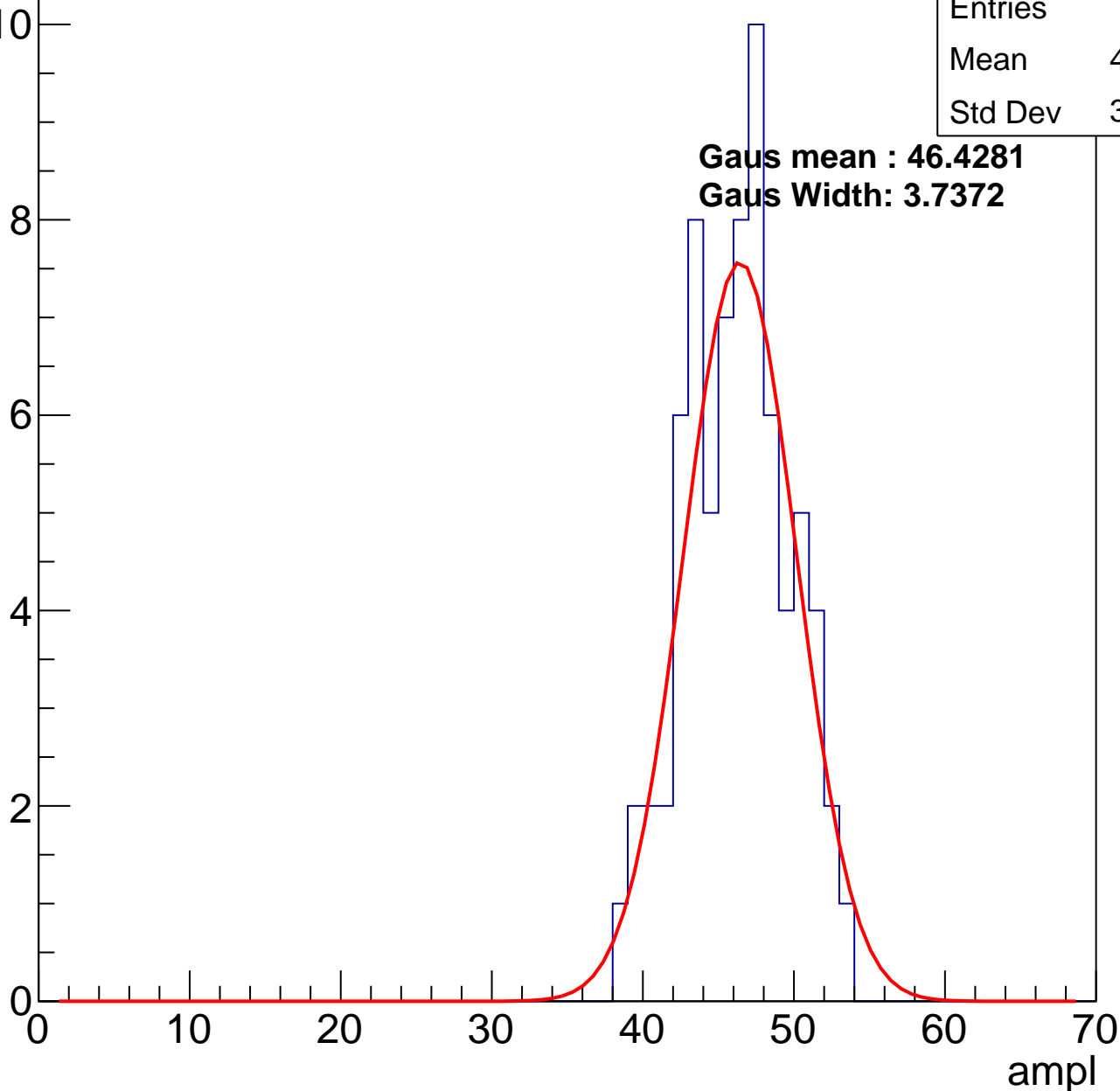
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.78
Std Dev	3.405

Gaus mean : 46.4281

Gaus Width: 3.7372

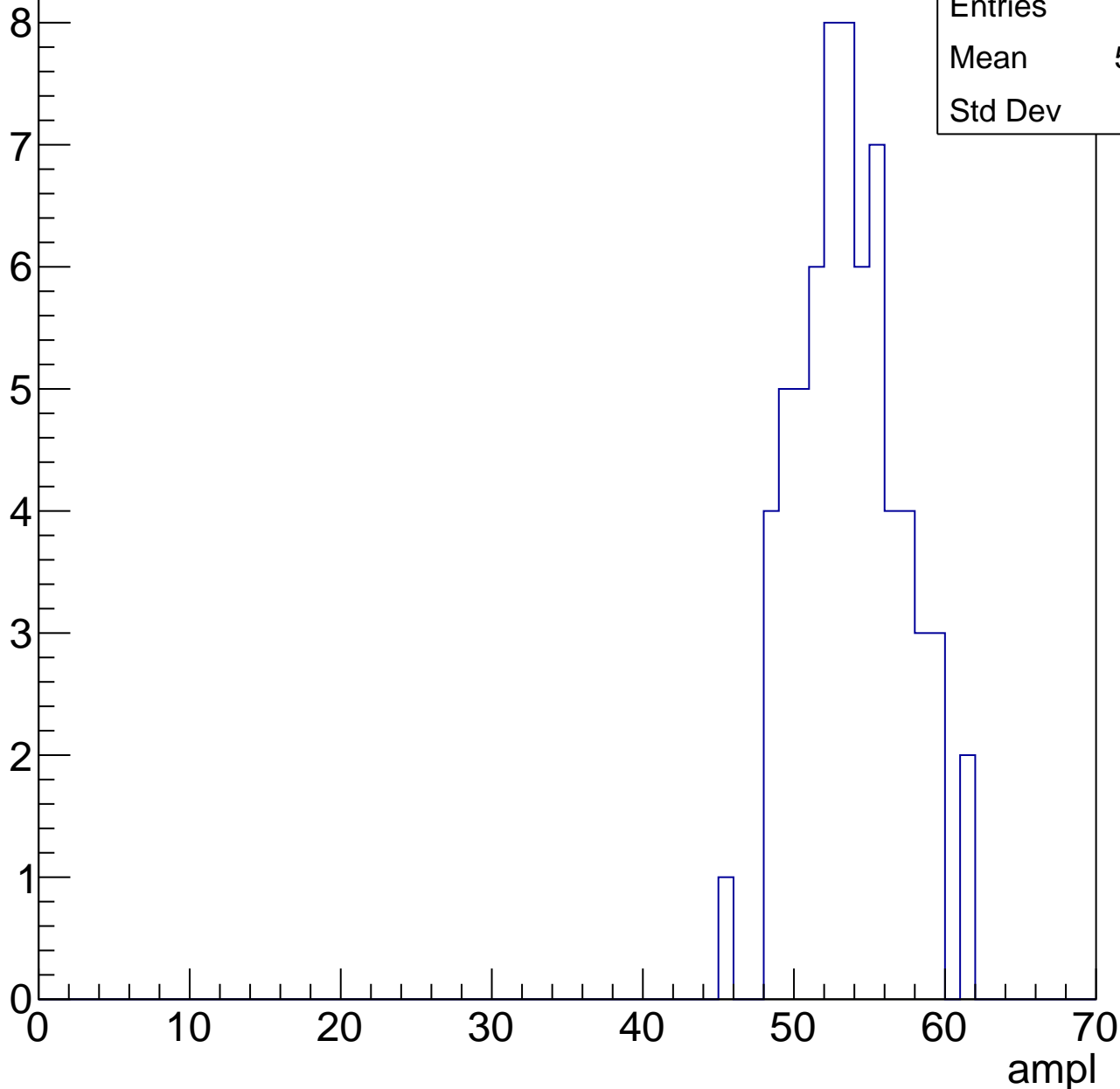


B0L001S, U11-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	53.21
Std Dev	3.4

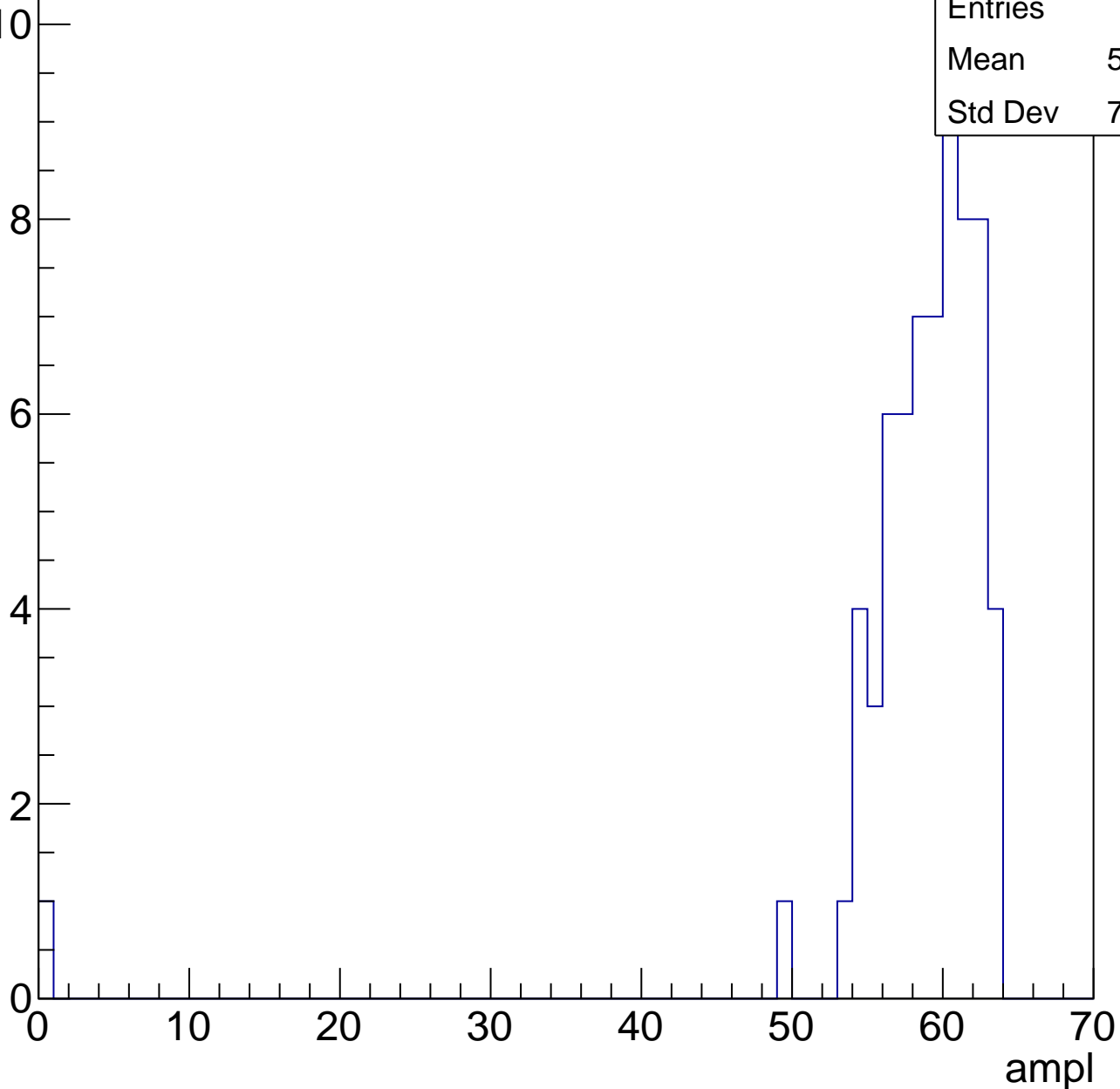


B0L001S, U11-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	57.82
Std Dev	7.722

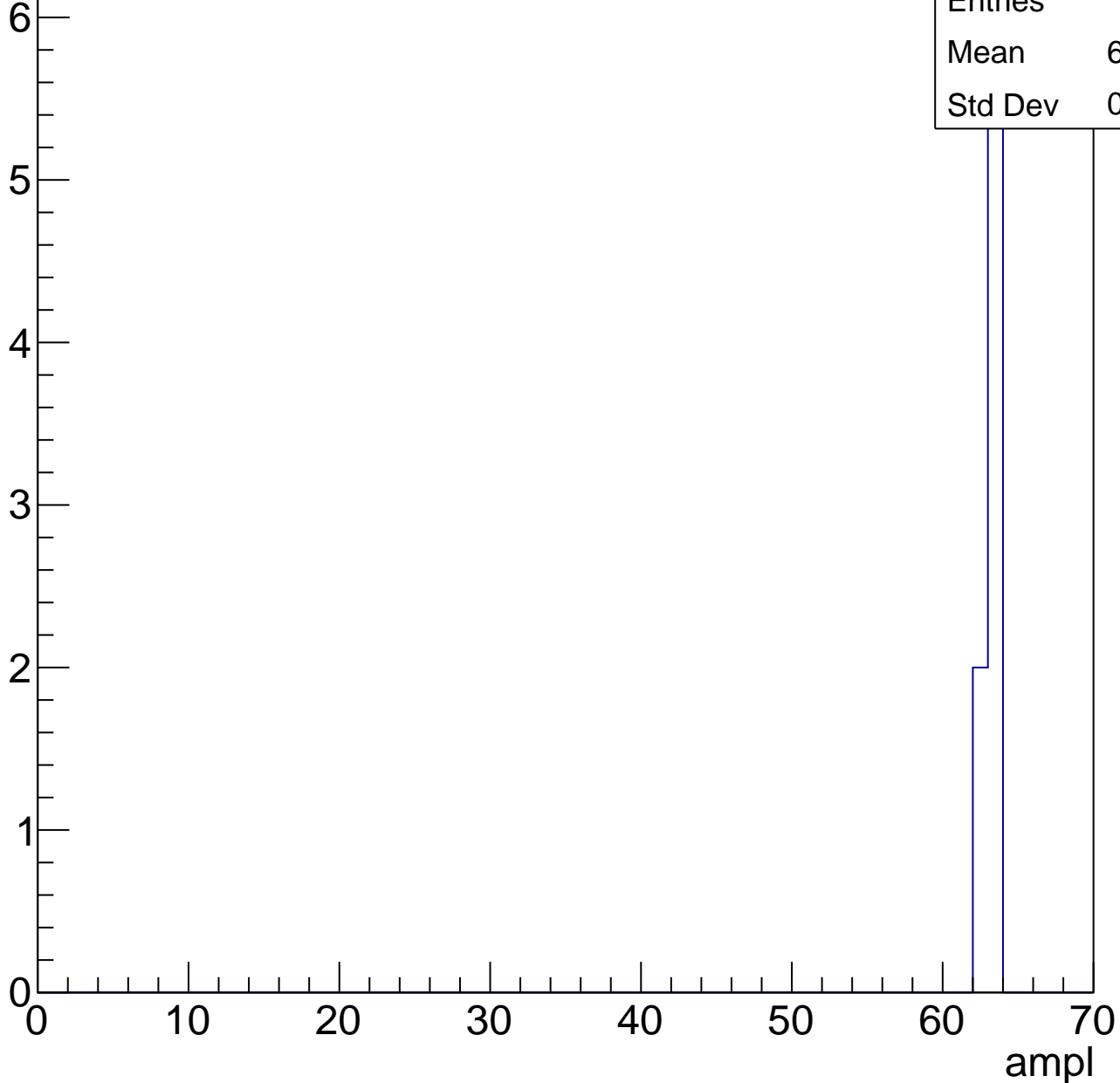


B0L001S, U11-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B0L001S, U11-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch50, adc0

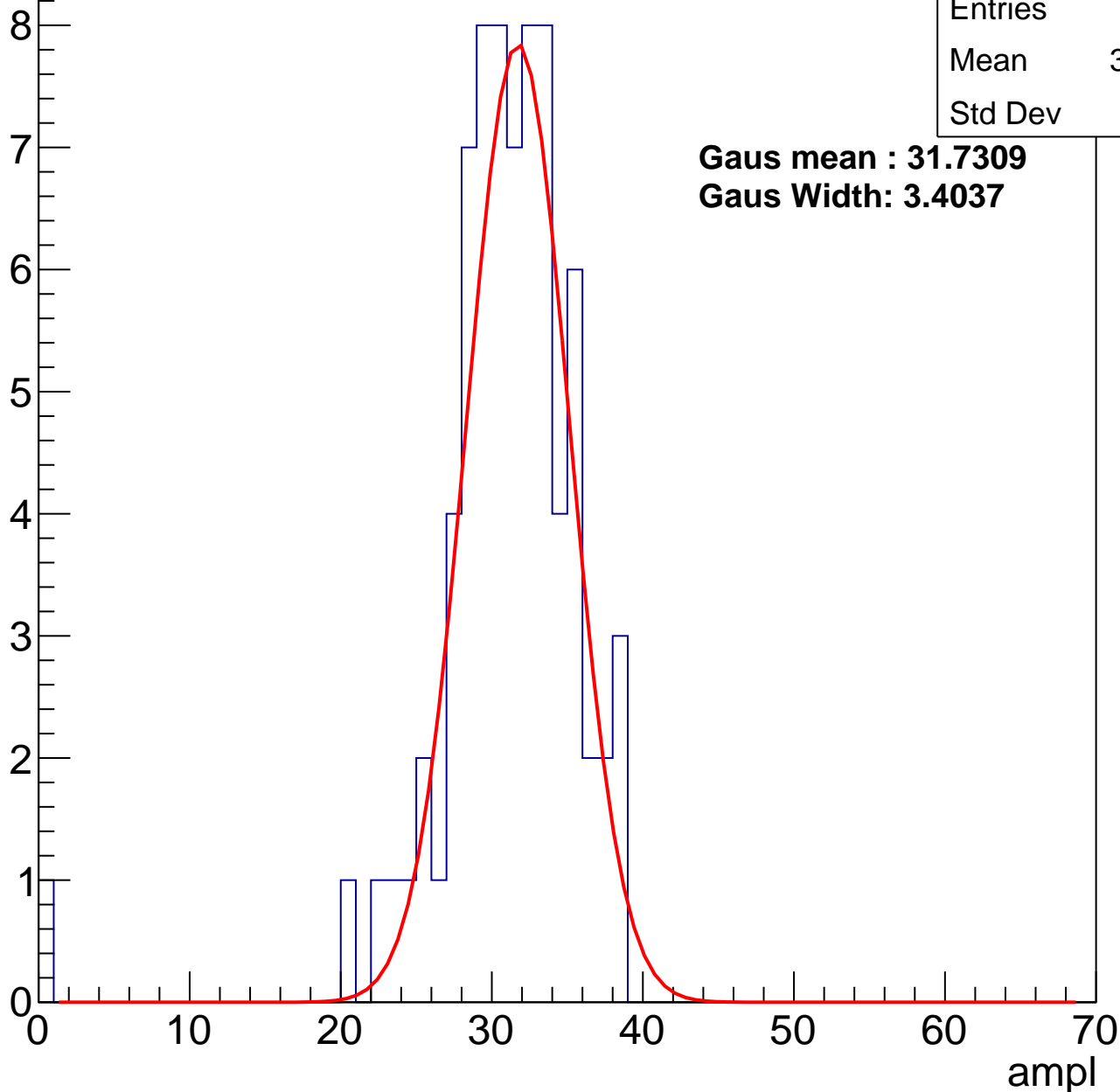
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	30.45
Std Dev	5.11

Gaus mean : 31.7309

Gaus Width: 3.4037



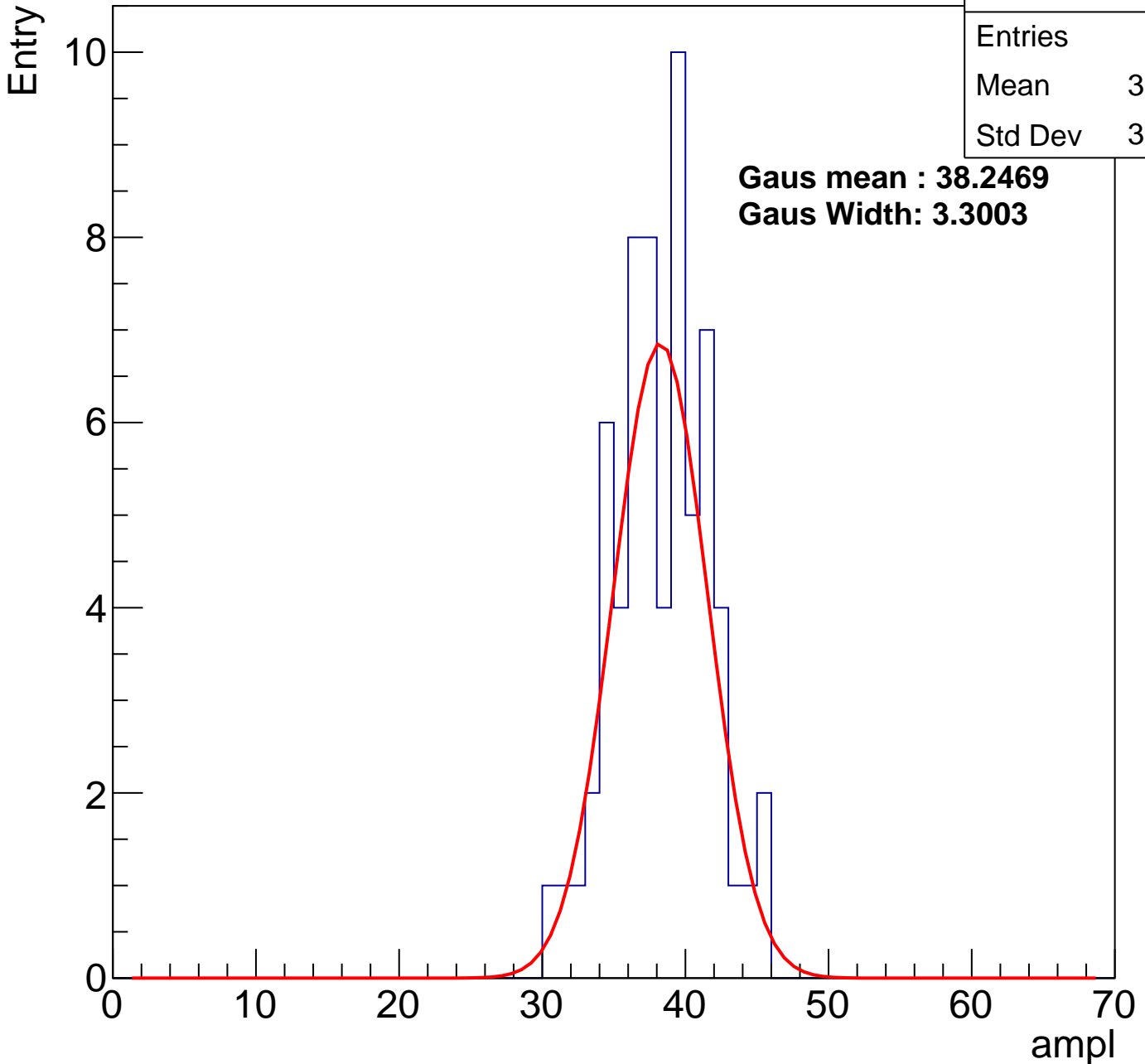
B0L001S, U11-ch50, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	37.86
Std Dev	3.248

Gaus mean : 38.2469

Gaus Width: 3.3003



B0L001S, U11-ch50, adc2

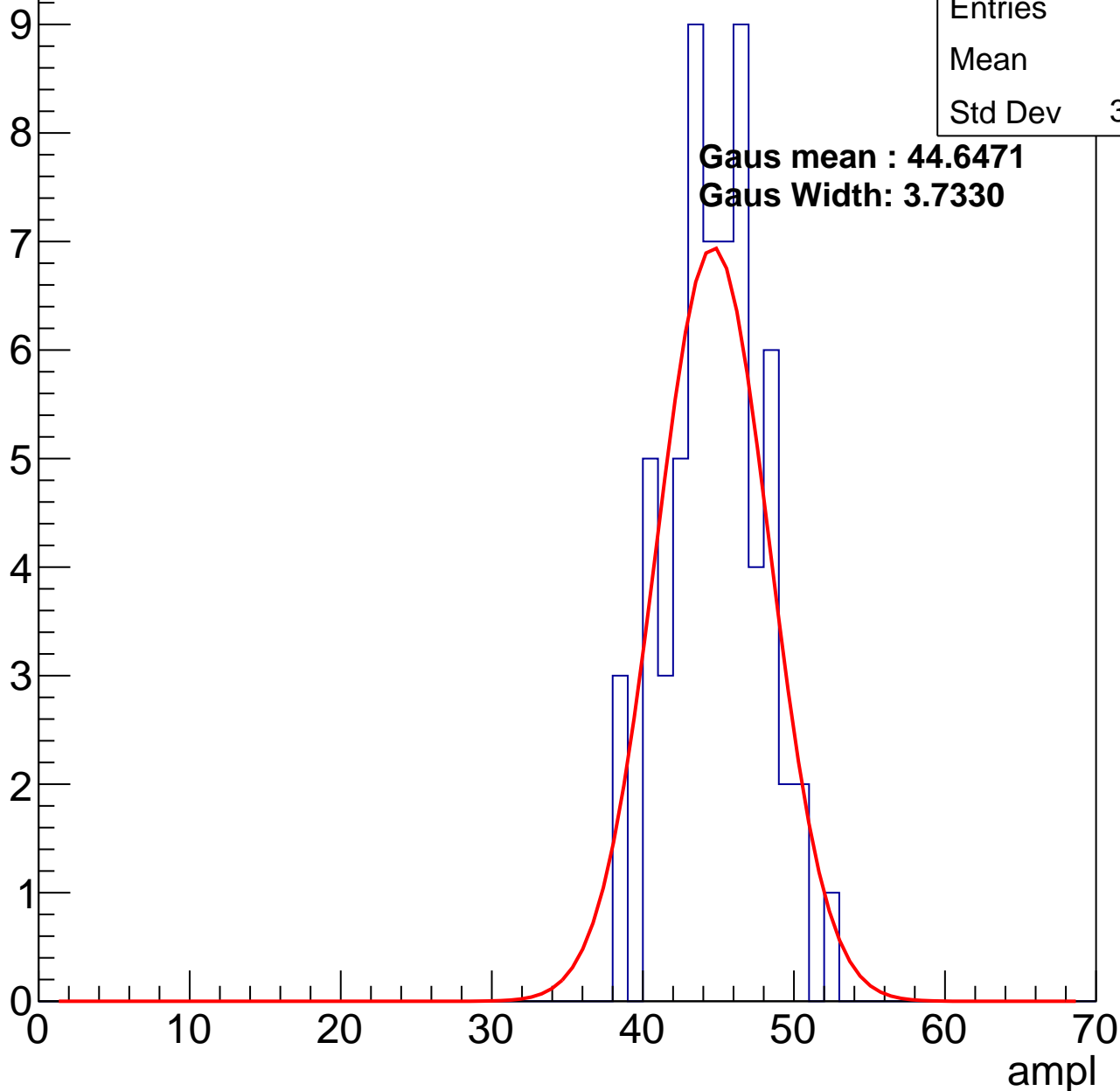
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.4
Std Dev	3.073

Gaus mean : 44.6471

Gaus Width: 3.7330

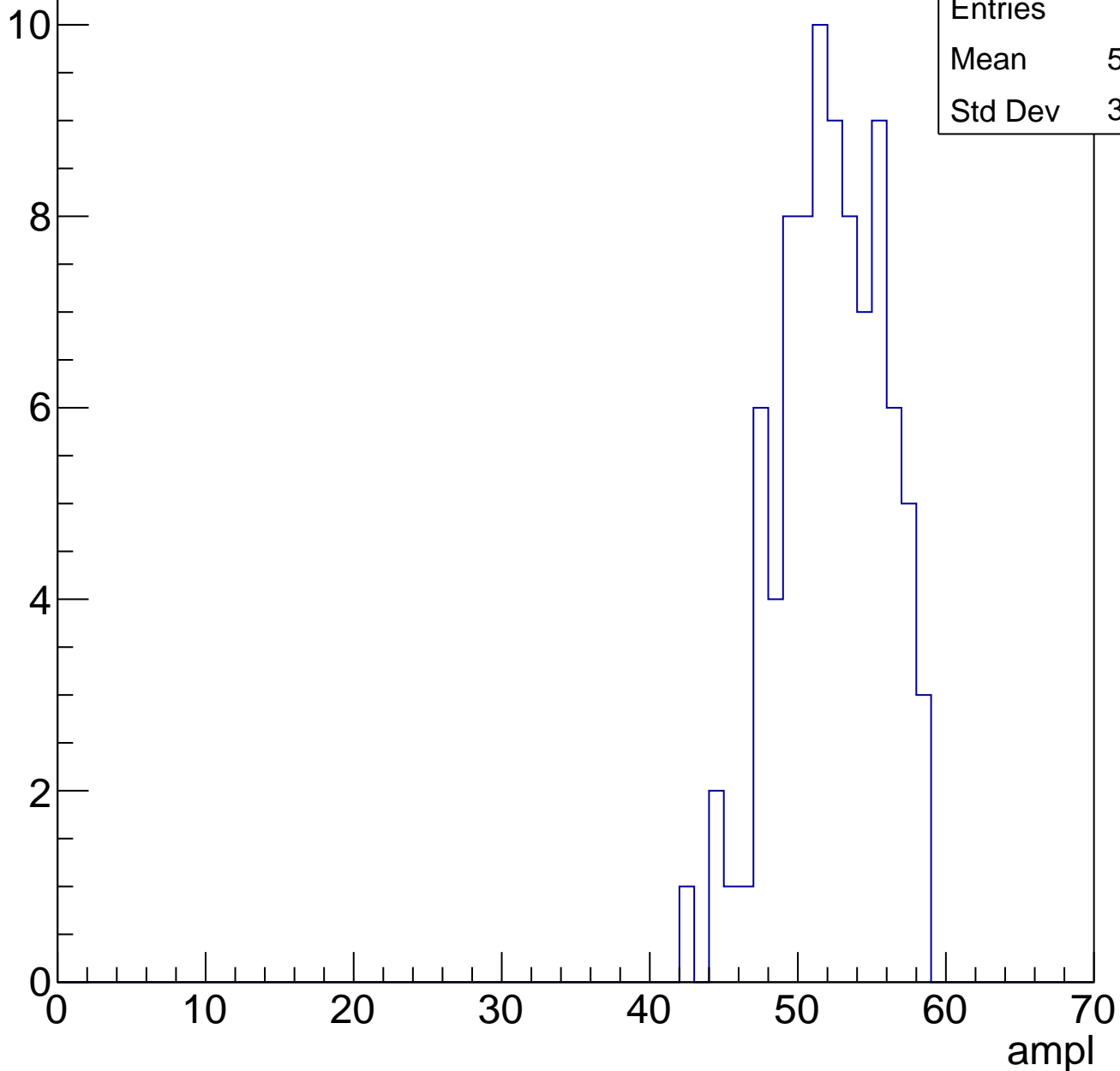


B0L001S, U11-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	51.78
Std Dev	3.508

Entry

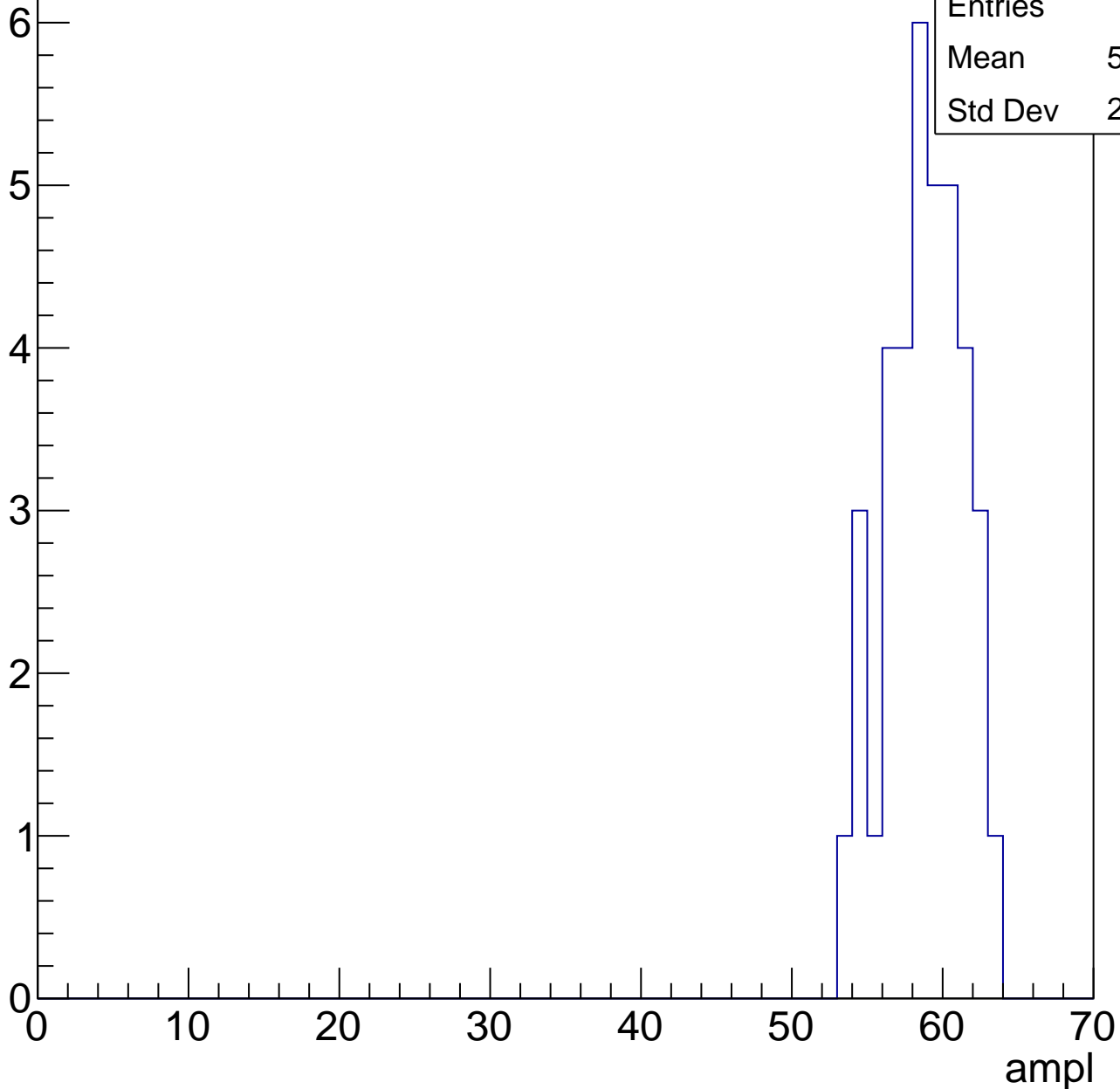


B0L001S, U11-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.32
Std Dev	2.505

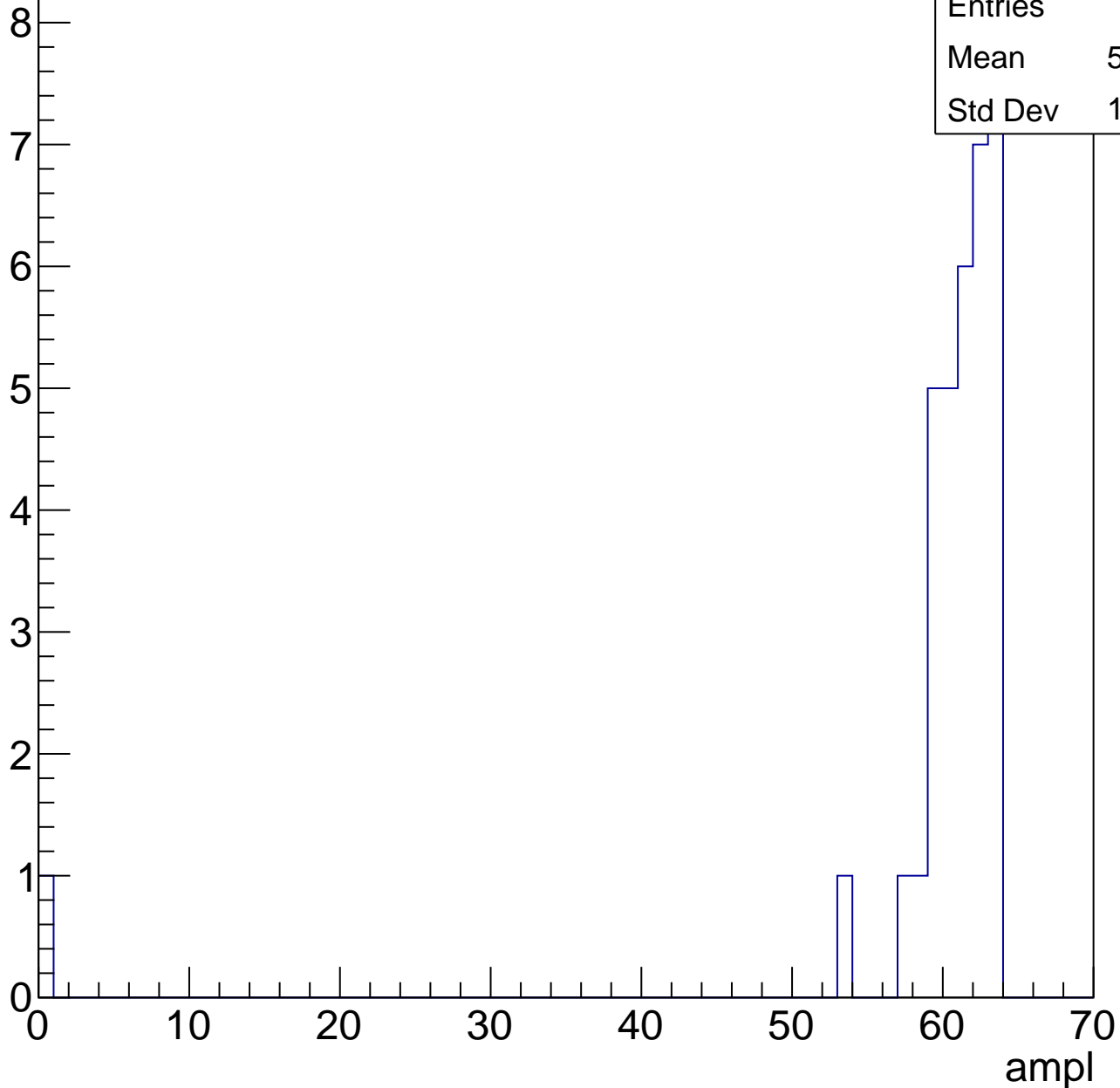


B0L001S, U11-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	59.06
Std Dev	10.34



B0L001S, U11-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch51, adc0

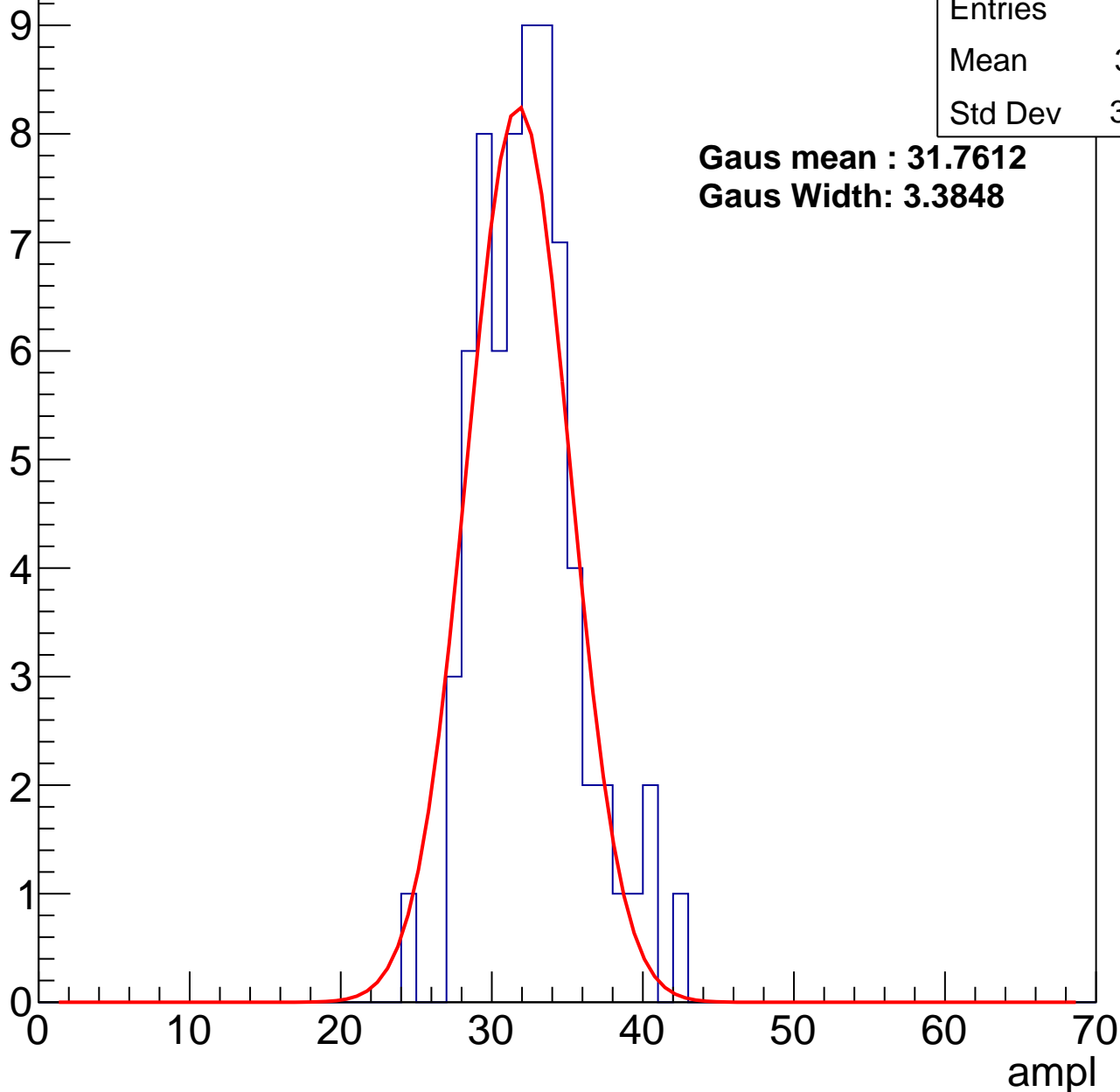
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	32.01
Std Dev	3.395

Gaus mean : 31.7612

Gaus Width: 3.3848



B0L001S, U11-ch51, adc1

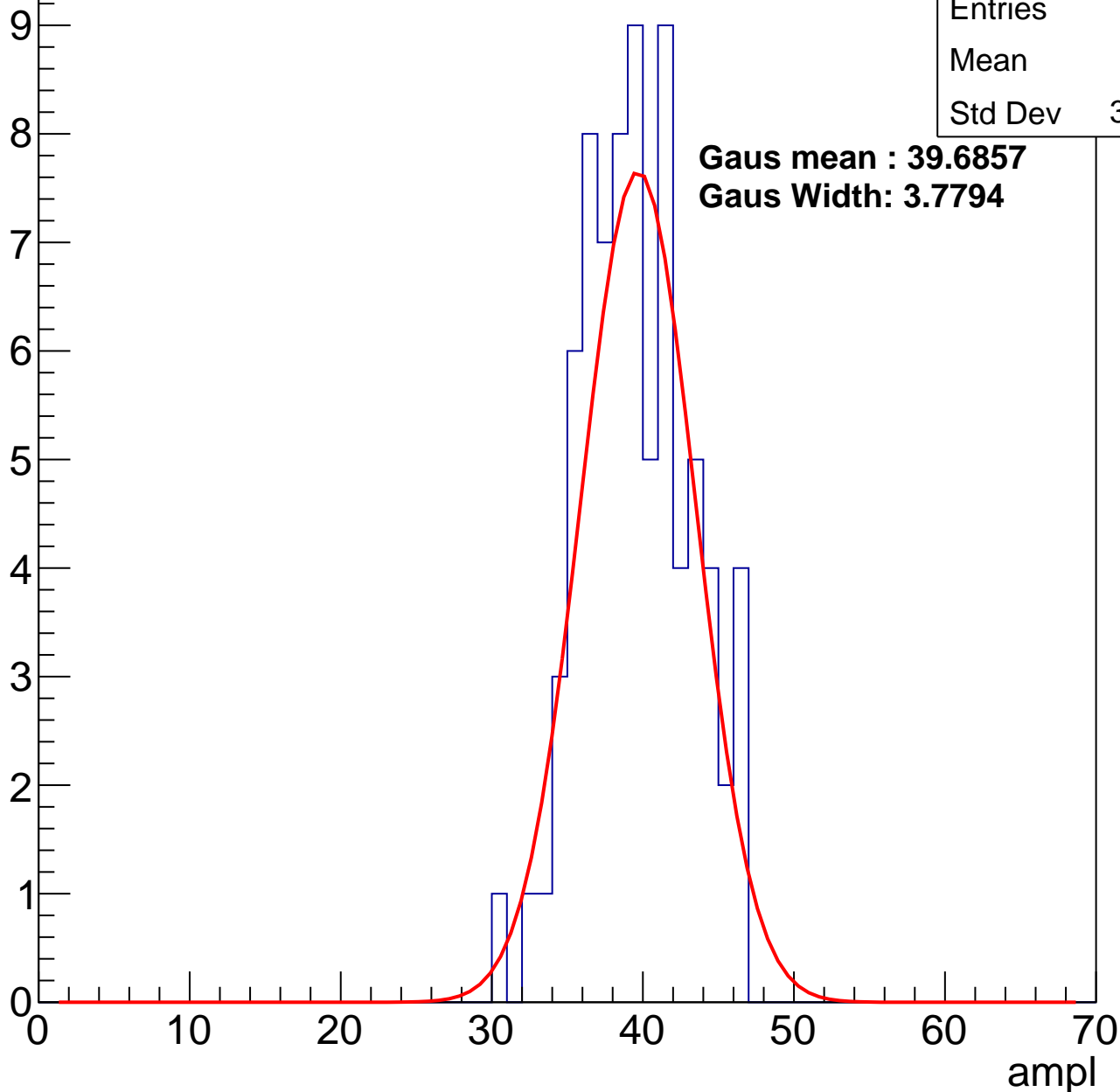
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	39.1
Std Dev	3.548

Gaus mean : 39.6857

Gaus Width: 3.7794



B0L001S, U11-ch51, adc2

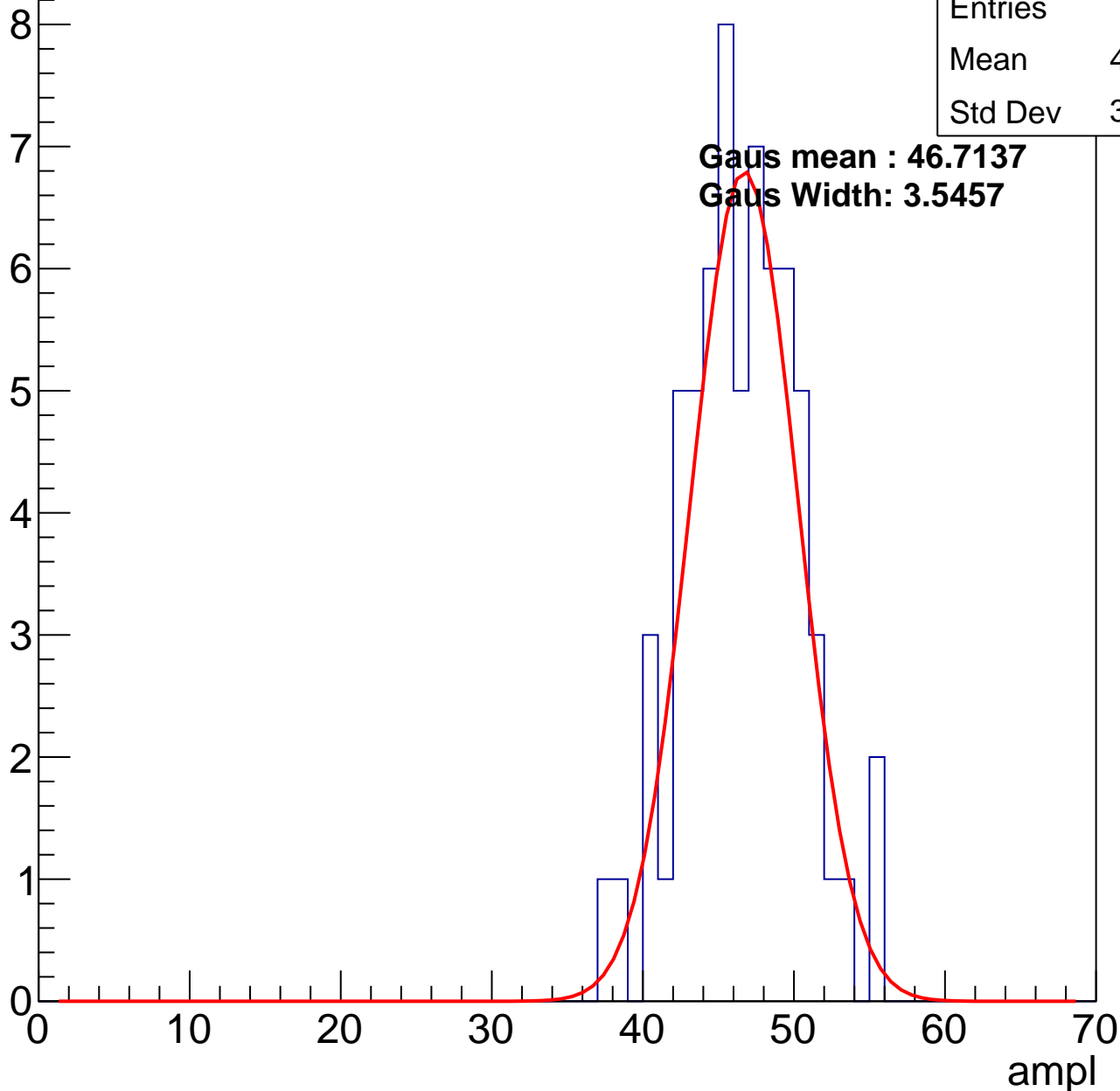
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	46.12
Std Dev	3.736

Gaus mean : 46.7137

Gaus Width: 3.5457

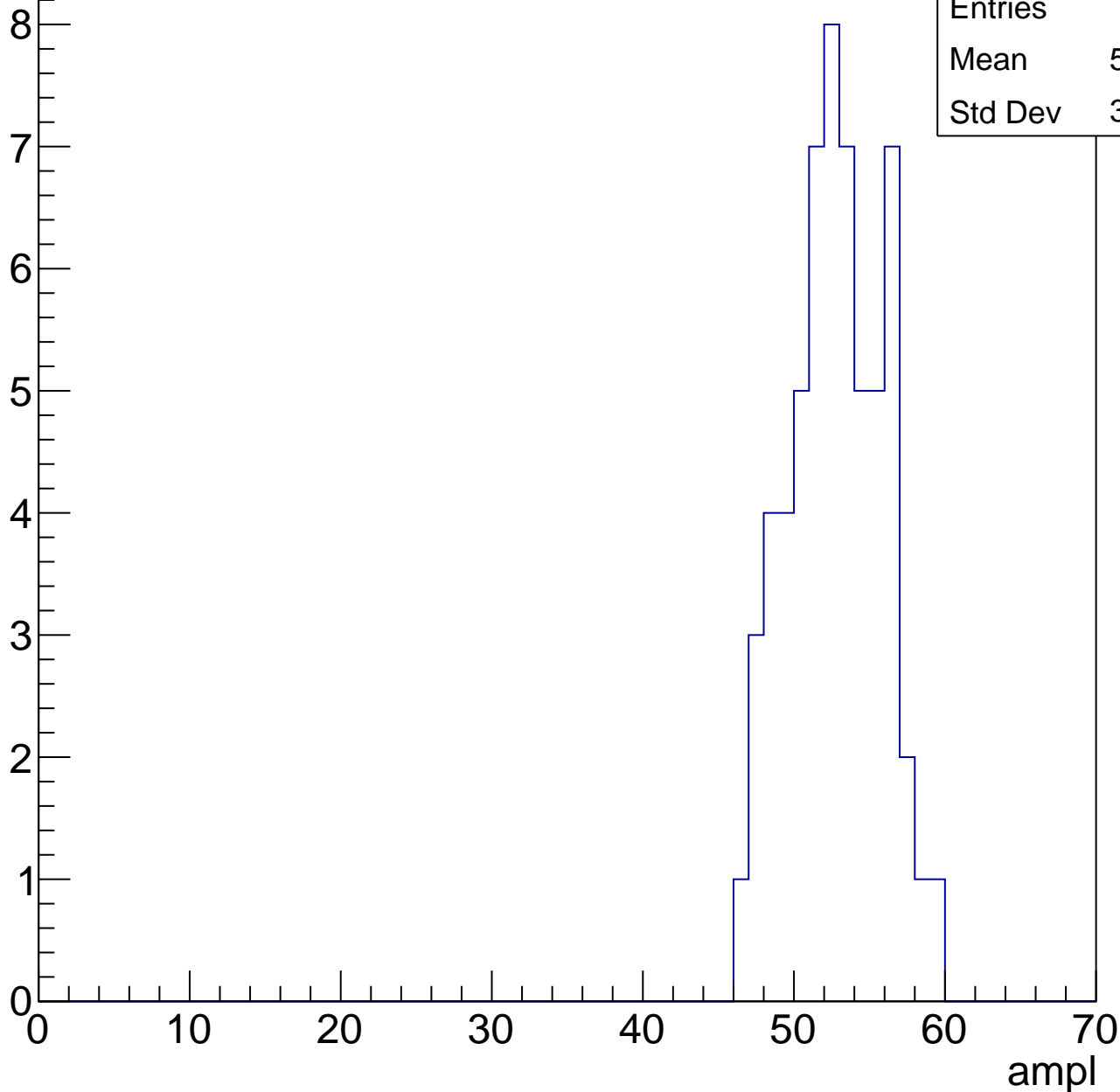


B0L001S, U11-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

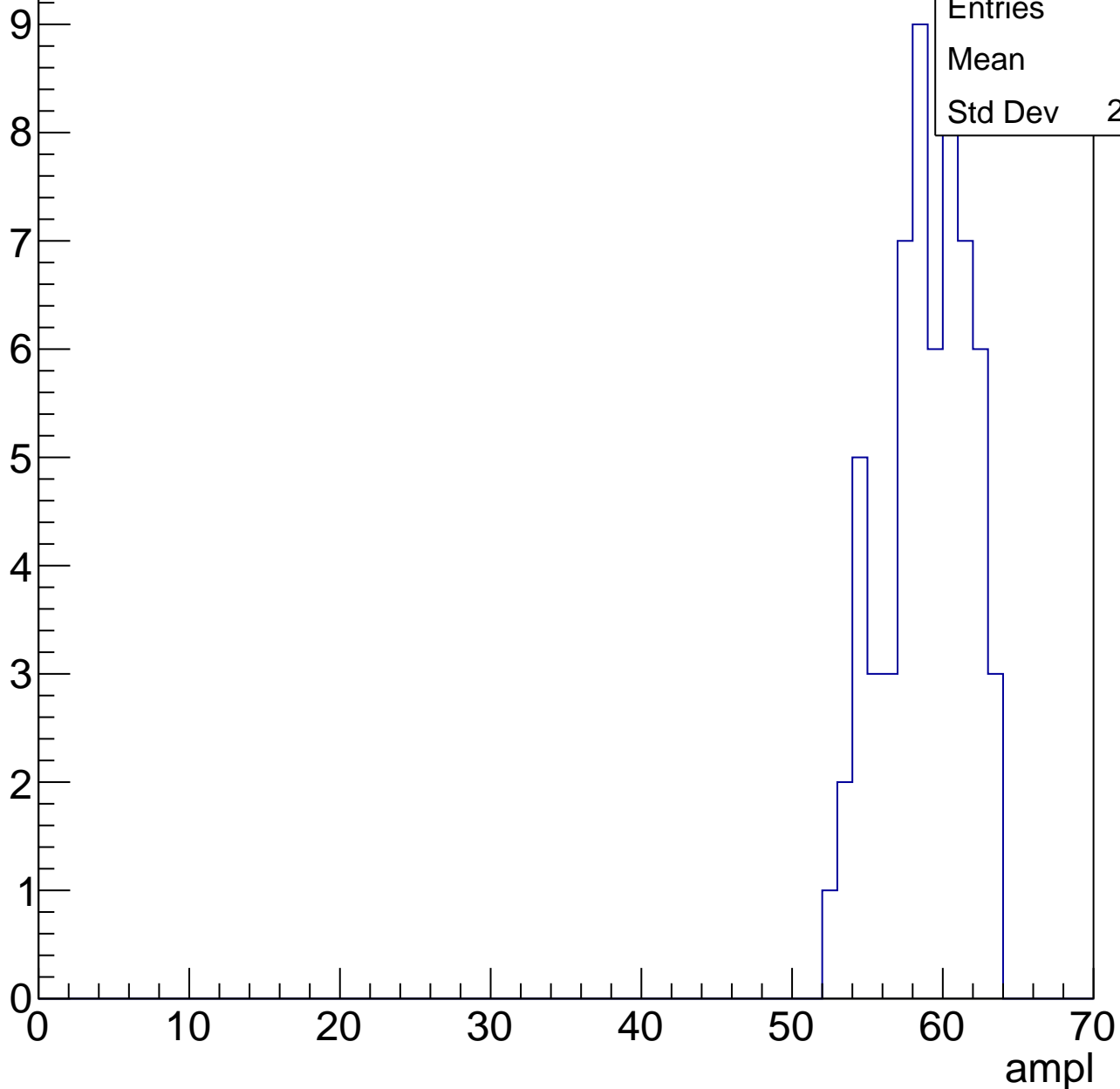
Entries	60
Mean	52.28
Std Dev	3.034



B0L001S, U11-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

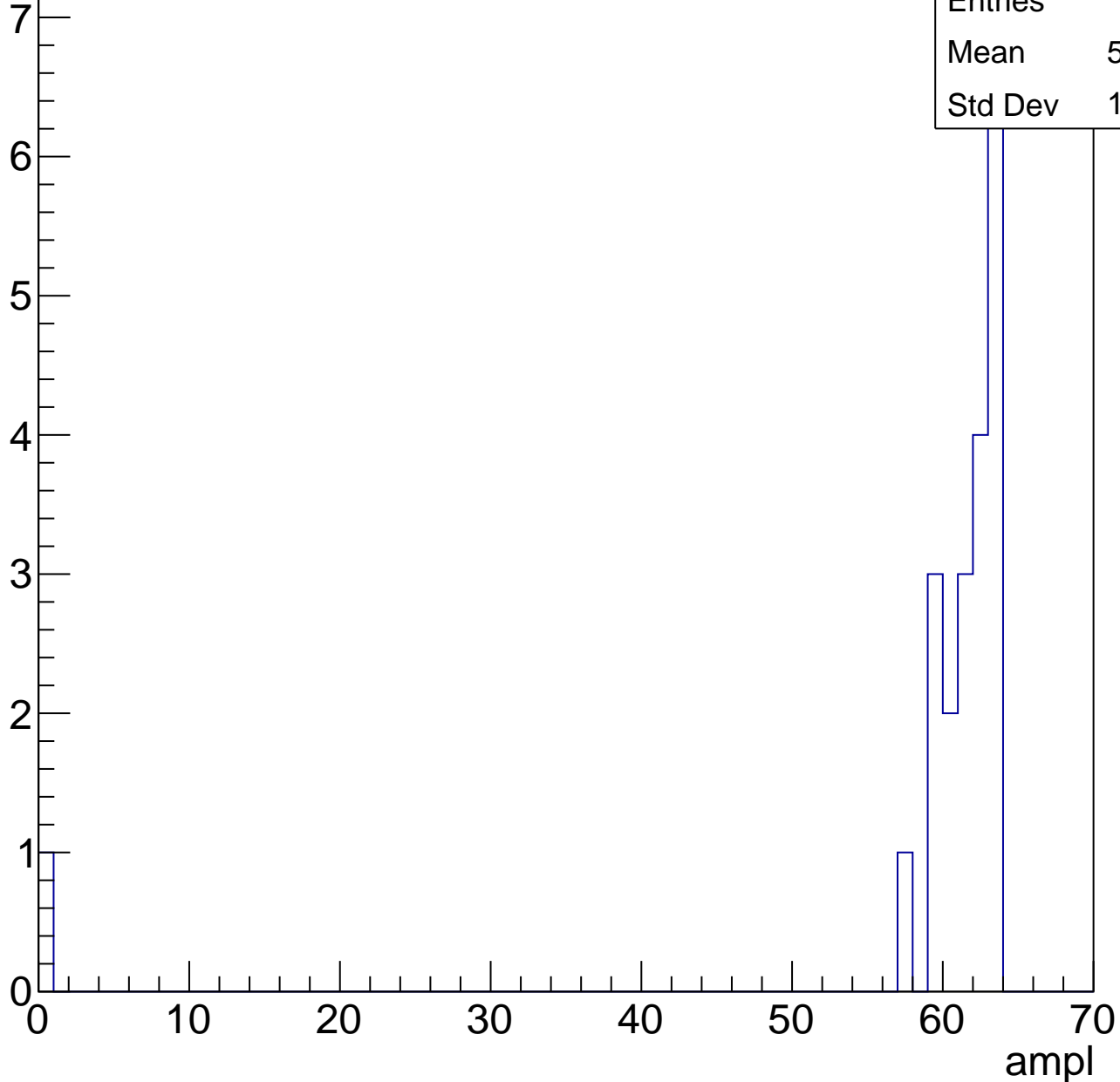


B0L001S, U11-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.38
Std Dev	13.16



B0L001S, U11-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch52, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	60
Mean	29.23
Std Dev	6.235

Gaus mean : 30.7608

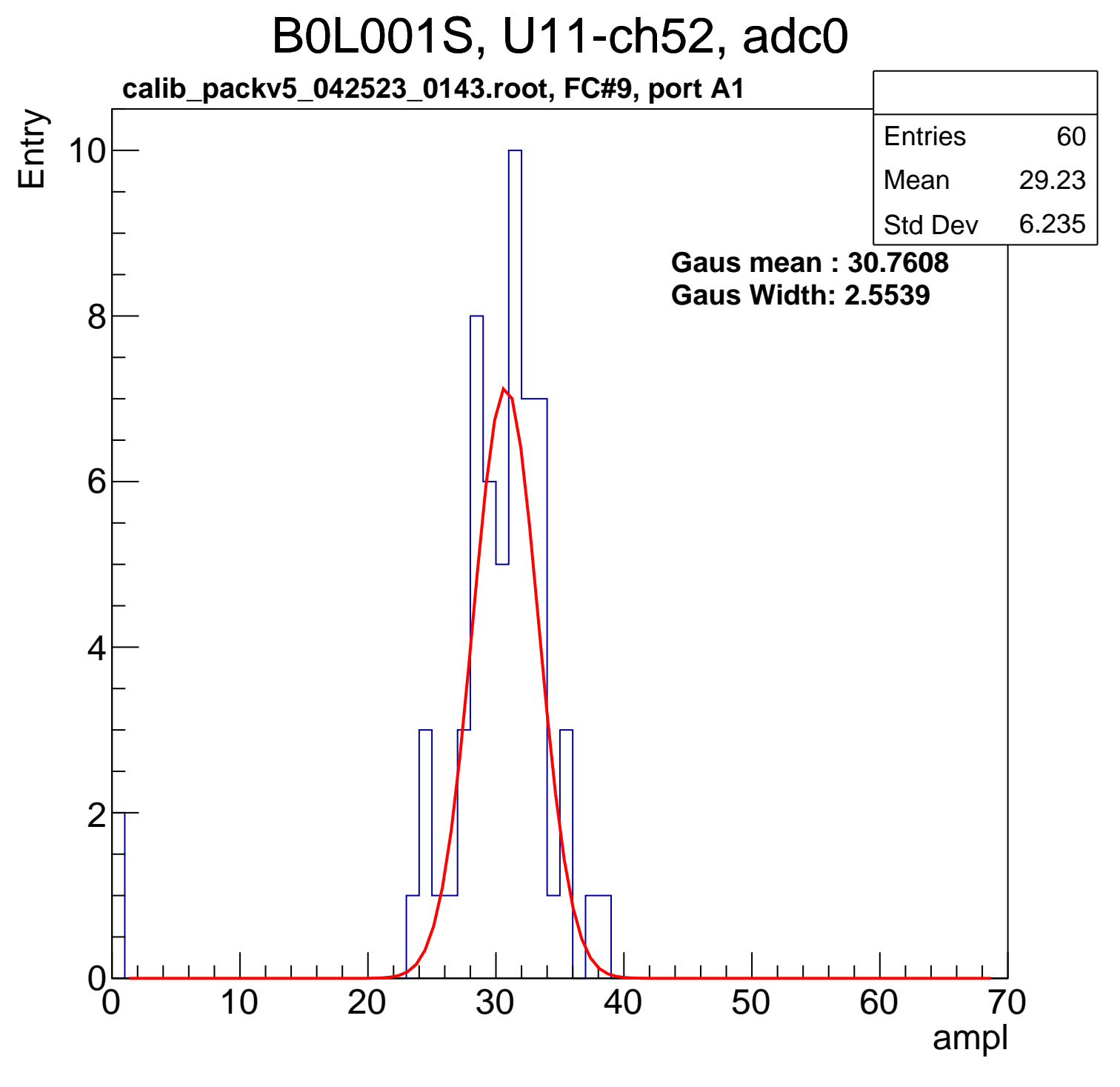
Gaus Width: 2.5539

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch52, adc1

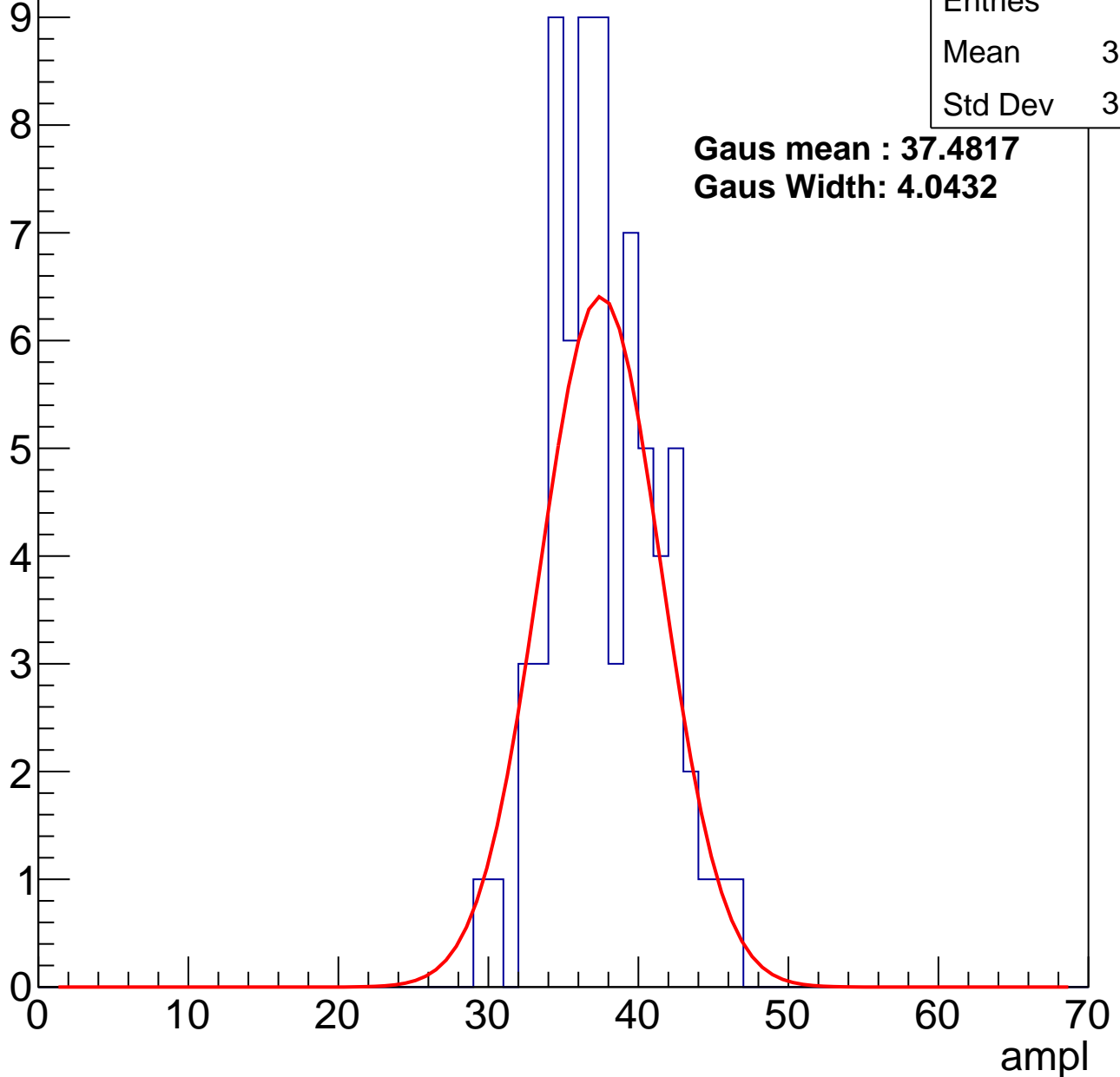
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.27
Std Dev	3.537

Gaus mean : 37.4817

Gaus Width: 4.0432



B0L001S, U11-ch52, adc2

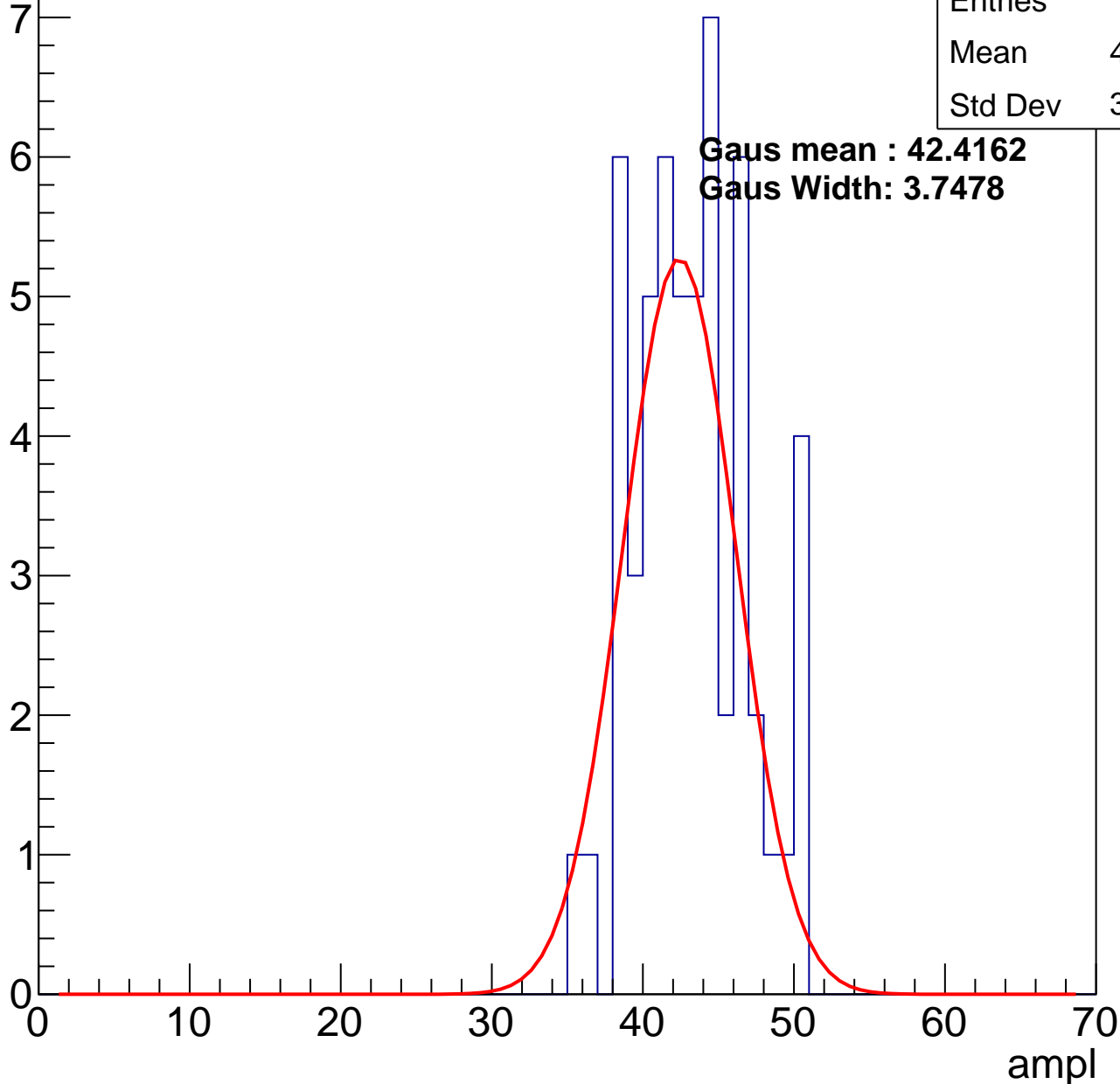
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.76
Std Dev	3.668

Gaus mean : 42.4162

Gaus Width: 3.7478

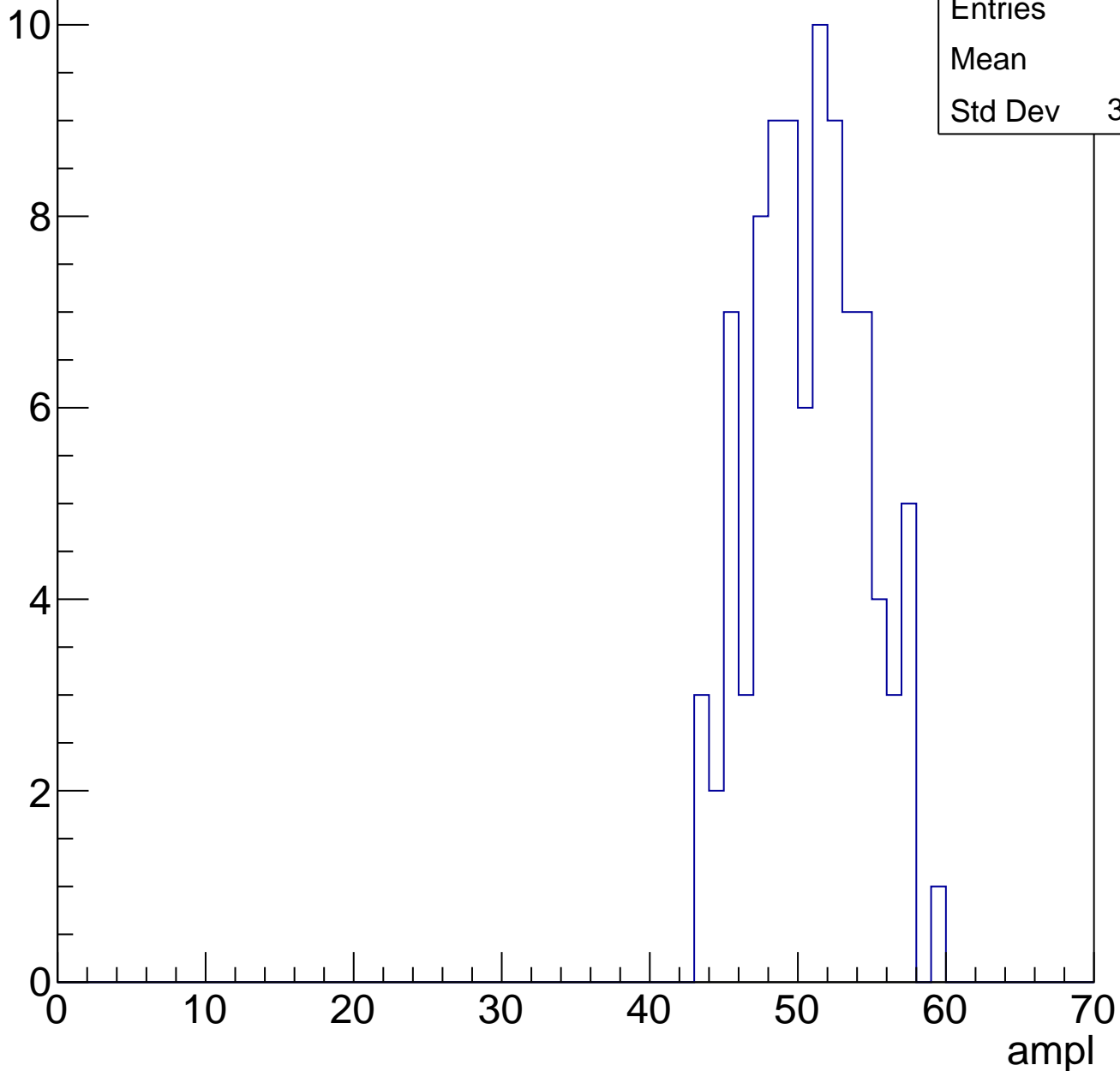


B0L001S, U11-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	93
Mean	50.3
Std Dev	3.747

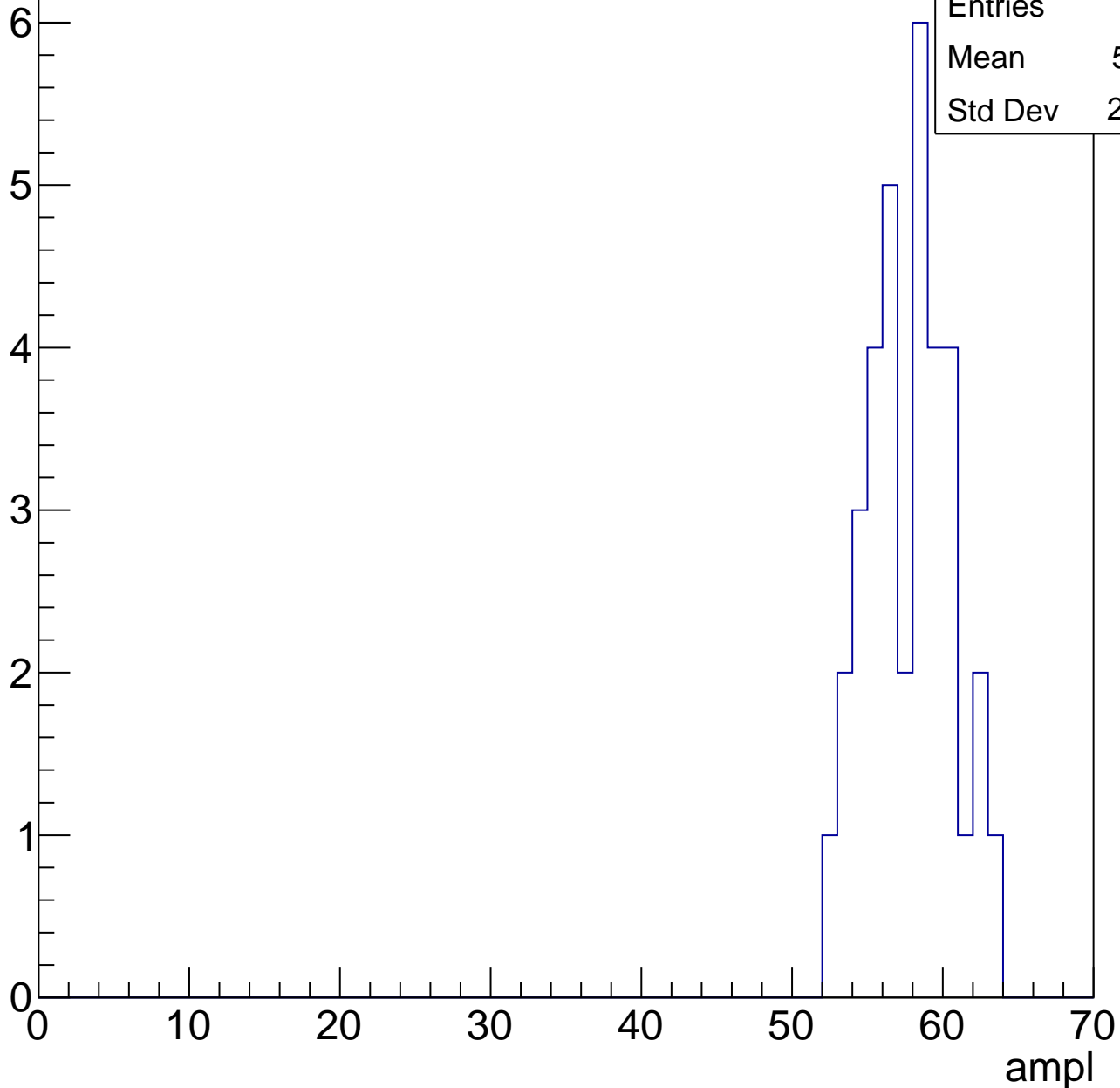
Entry



B0L001S, U11-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



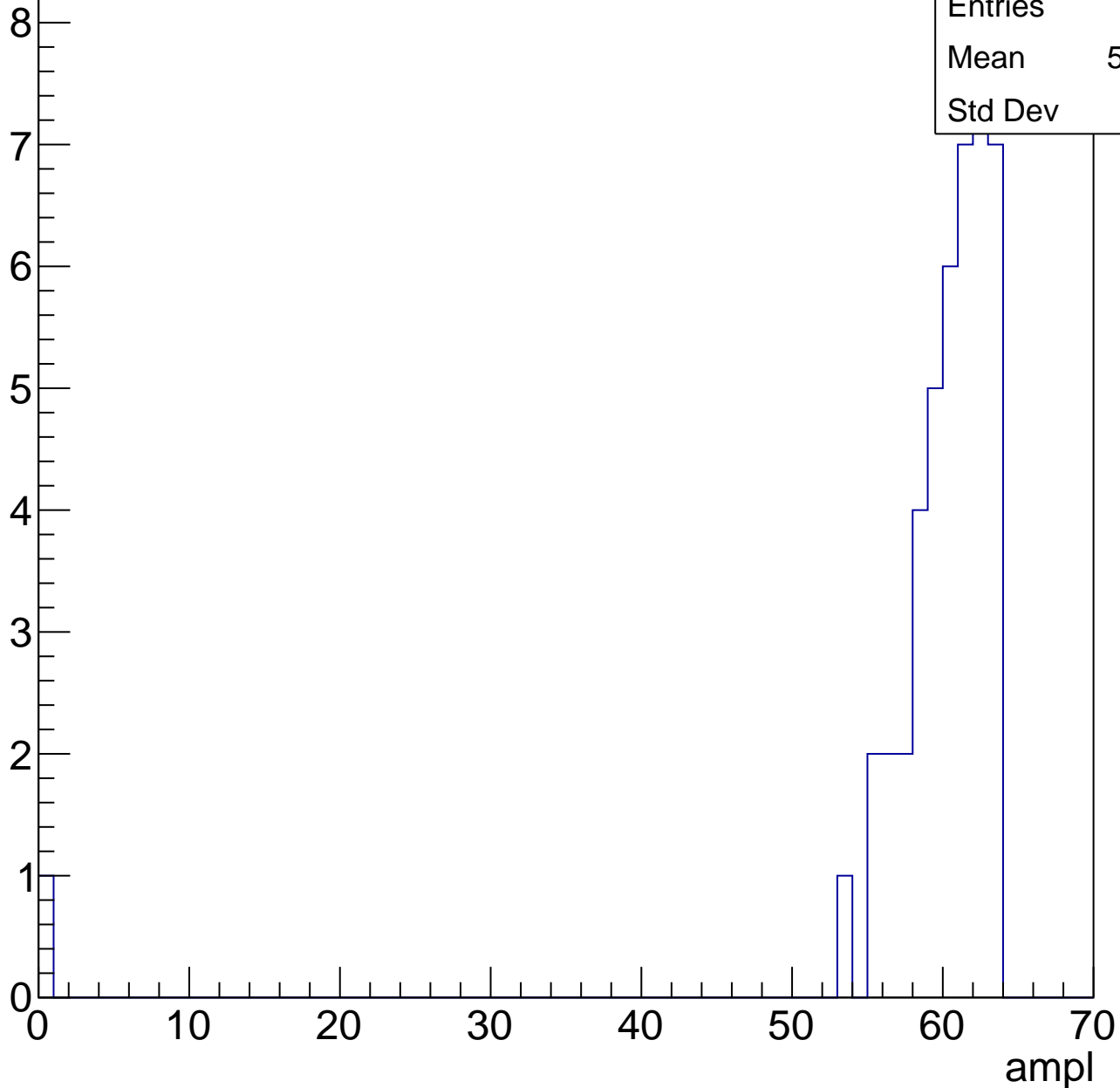
Entries	35
Mean	57.31
Std Dev	2.734

B0L001S, U11-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

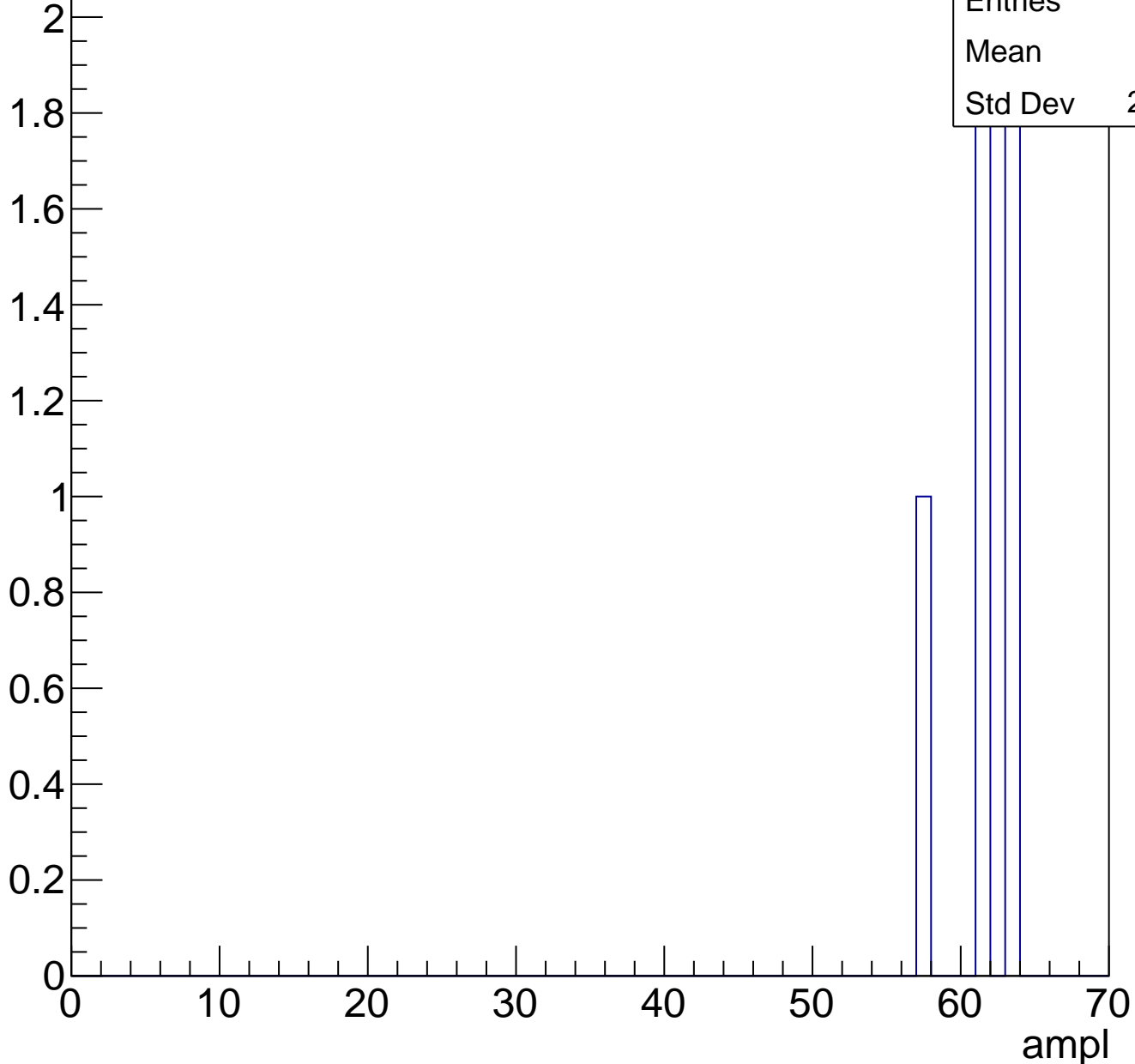
Entries	45
Mean	58.67
Std Dev	9.18



B0L001S, U11-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch53, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	31.51
Std Dev	3.655

Gaus mean : 32.4586

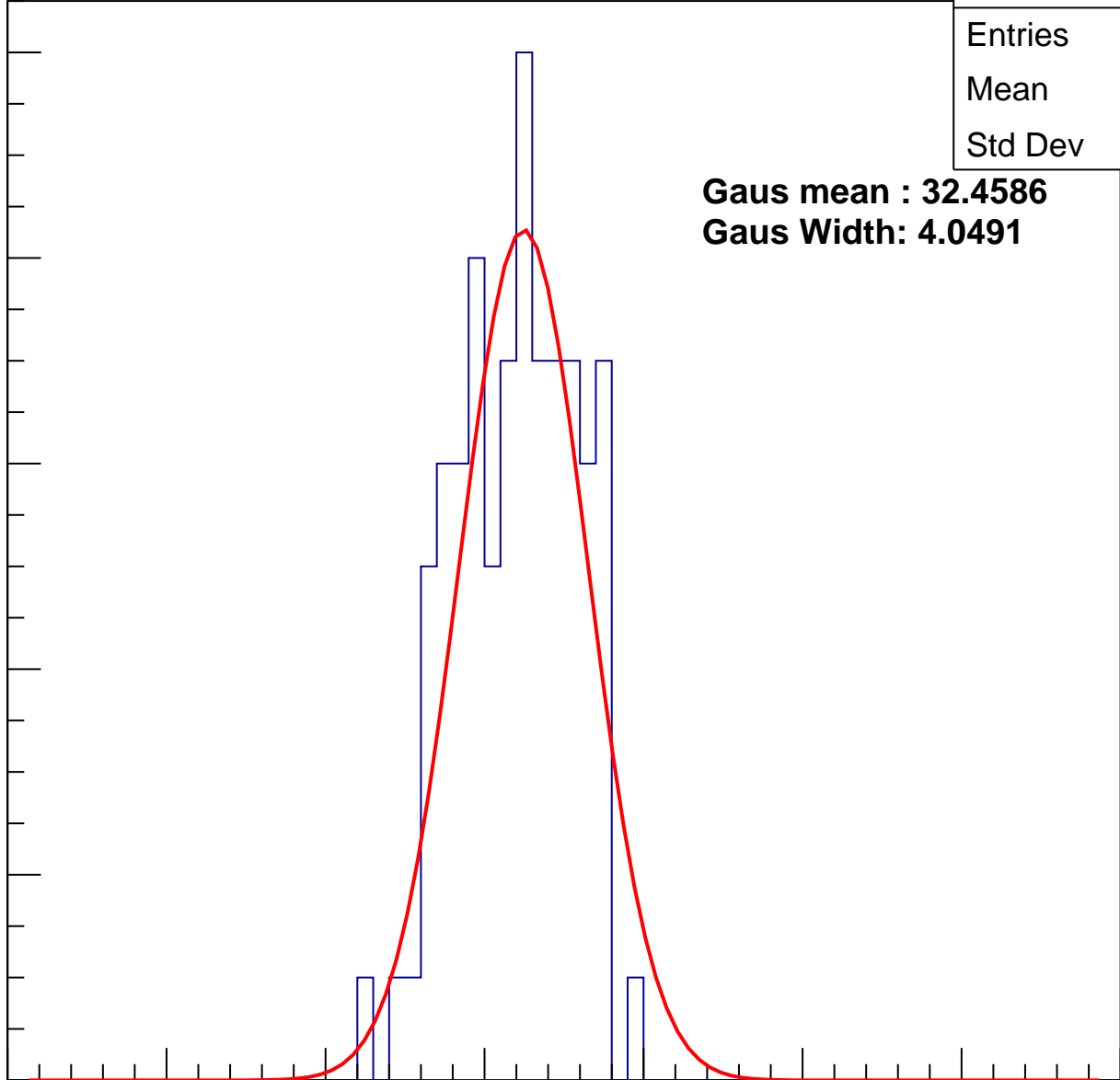
Gaus Width: 4.0491

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch53, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	38.94
Std Dev	3.383

Gaus mean : 40.0104

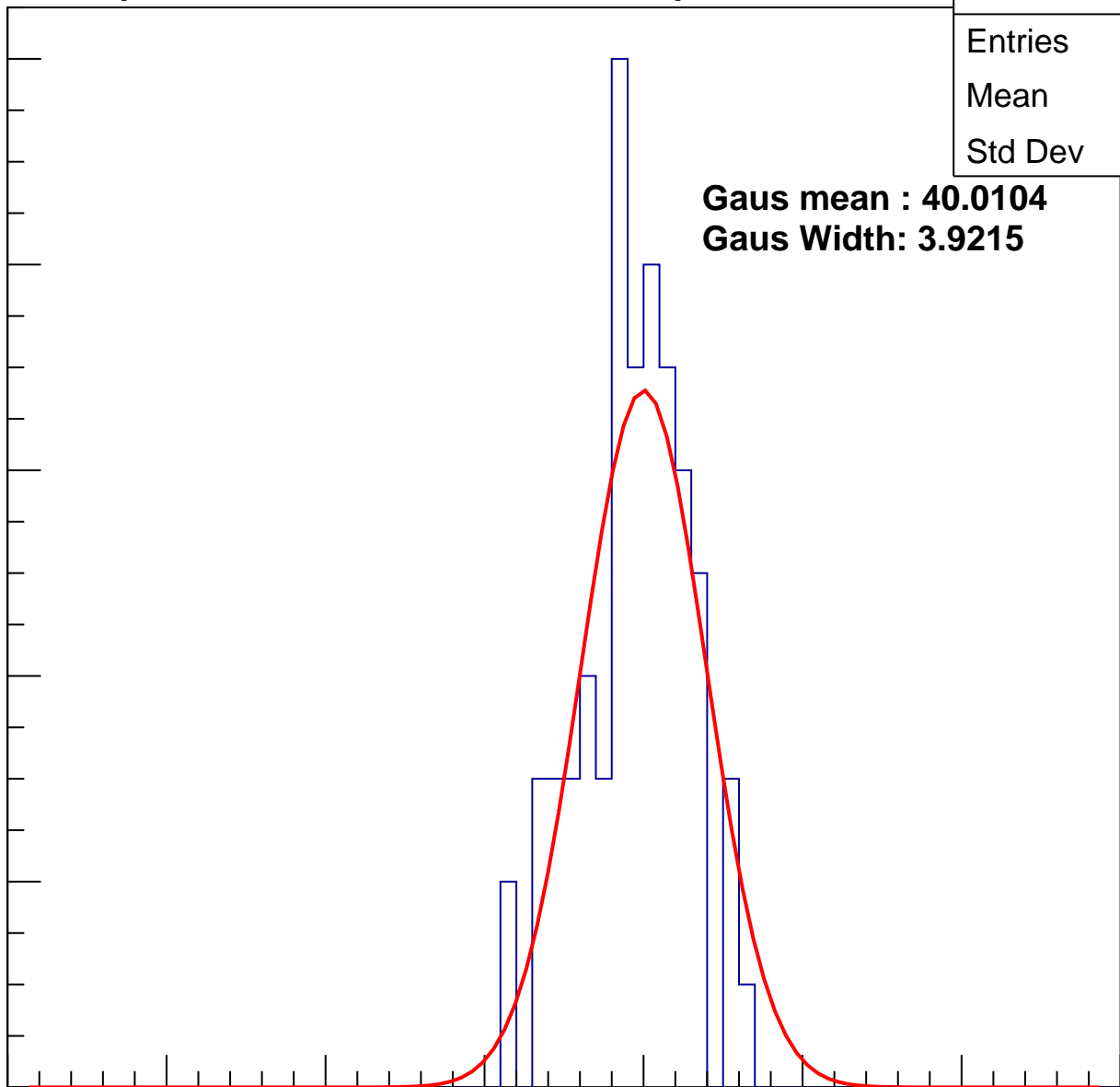
Gaus Width: 3.9215

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch53, adc2

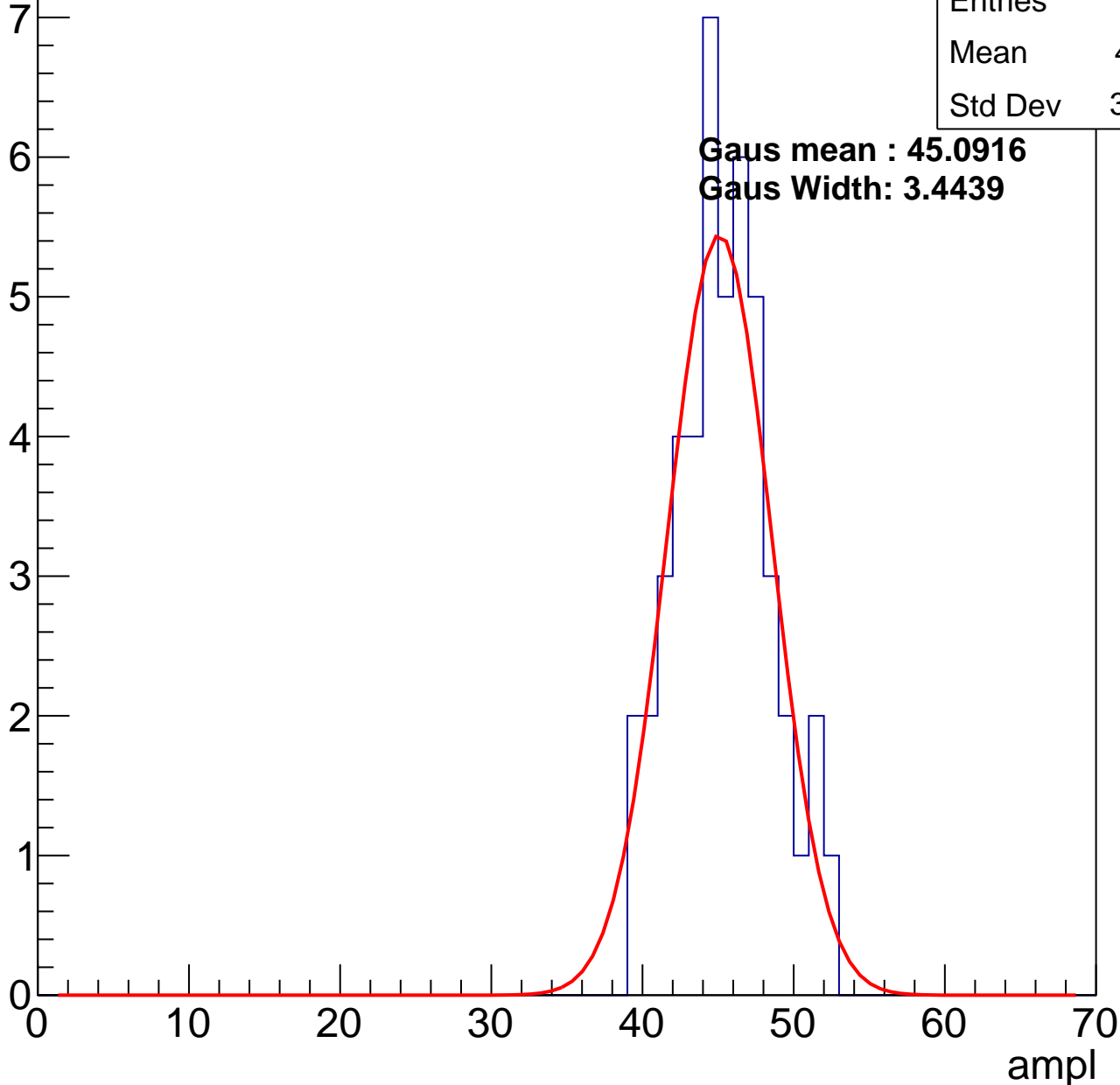
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	44.91
Std Dev	3.127

Gaus mean : 45.0916

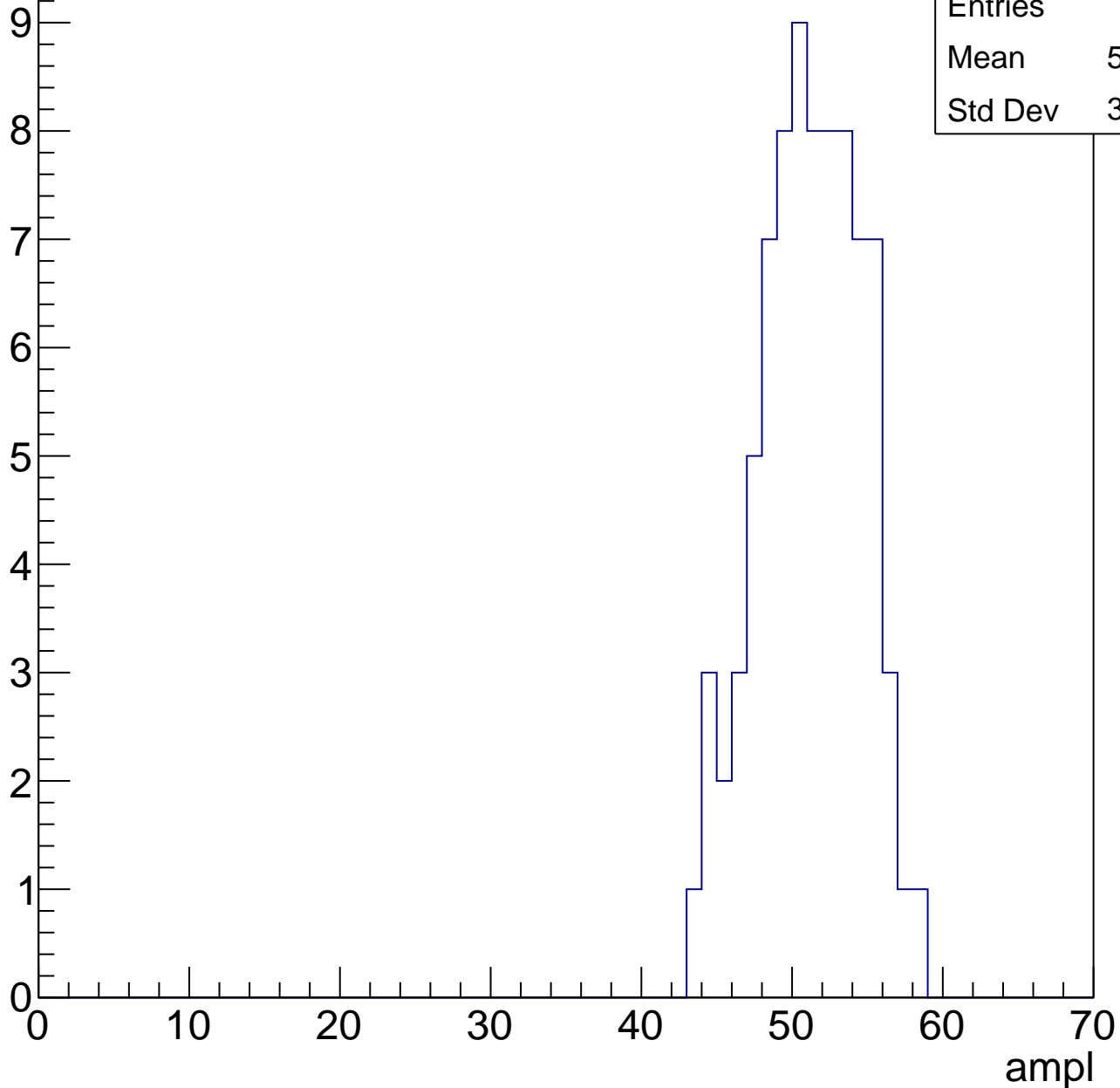
Gaus Width: 3.4439



B0L001S, U11-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

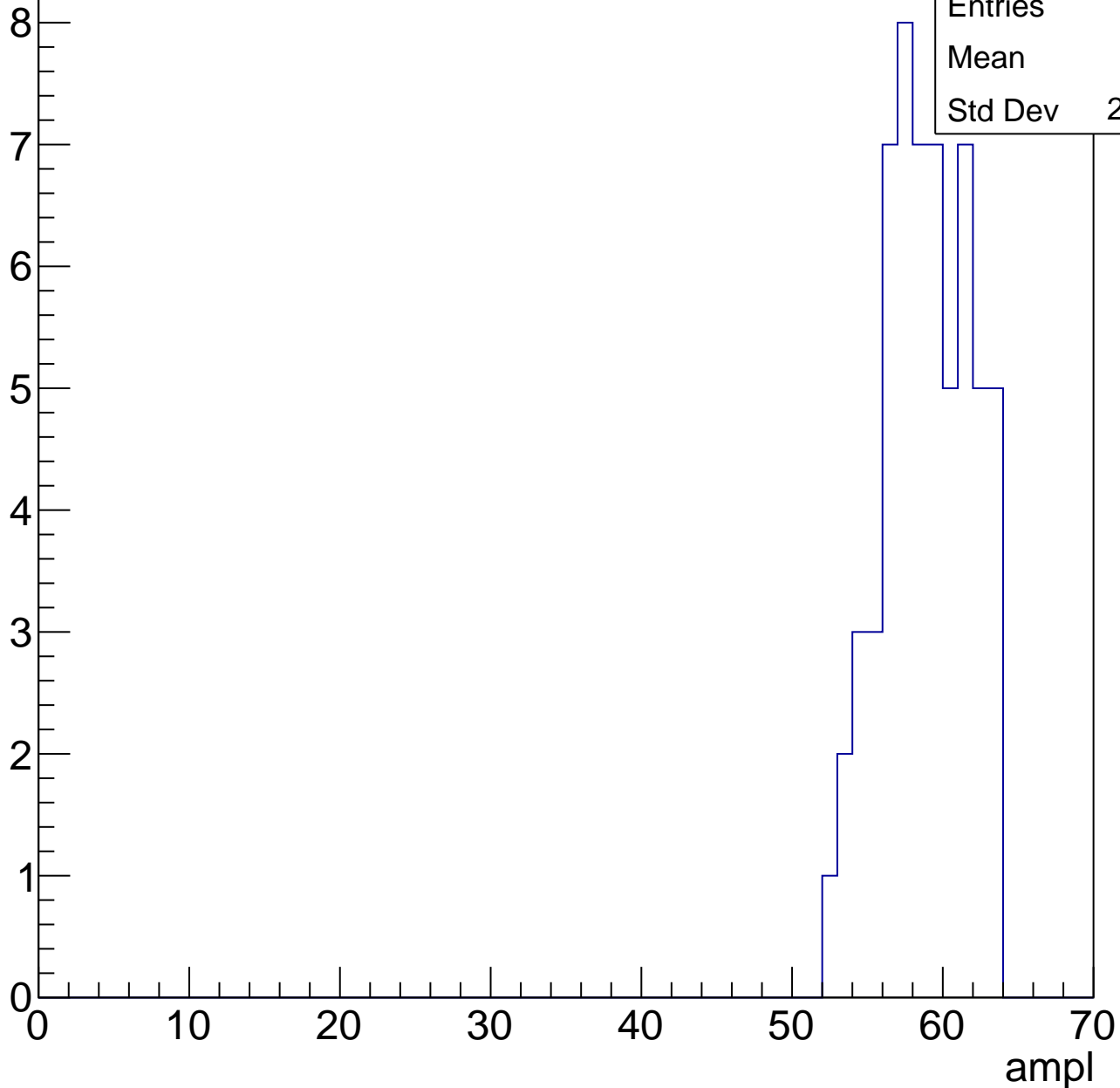


Entries	81
Mean	50.74
Std Dev	3.355

B0L001S, U11-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

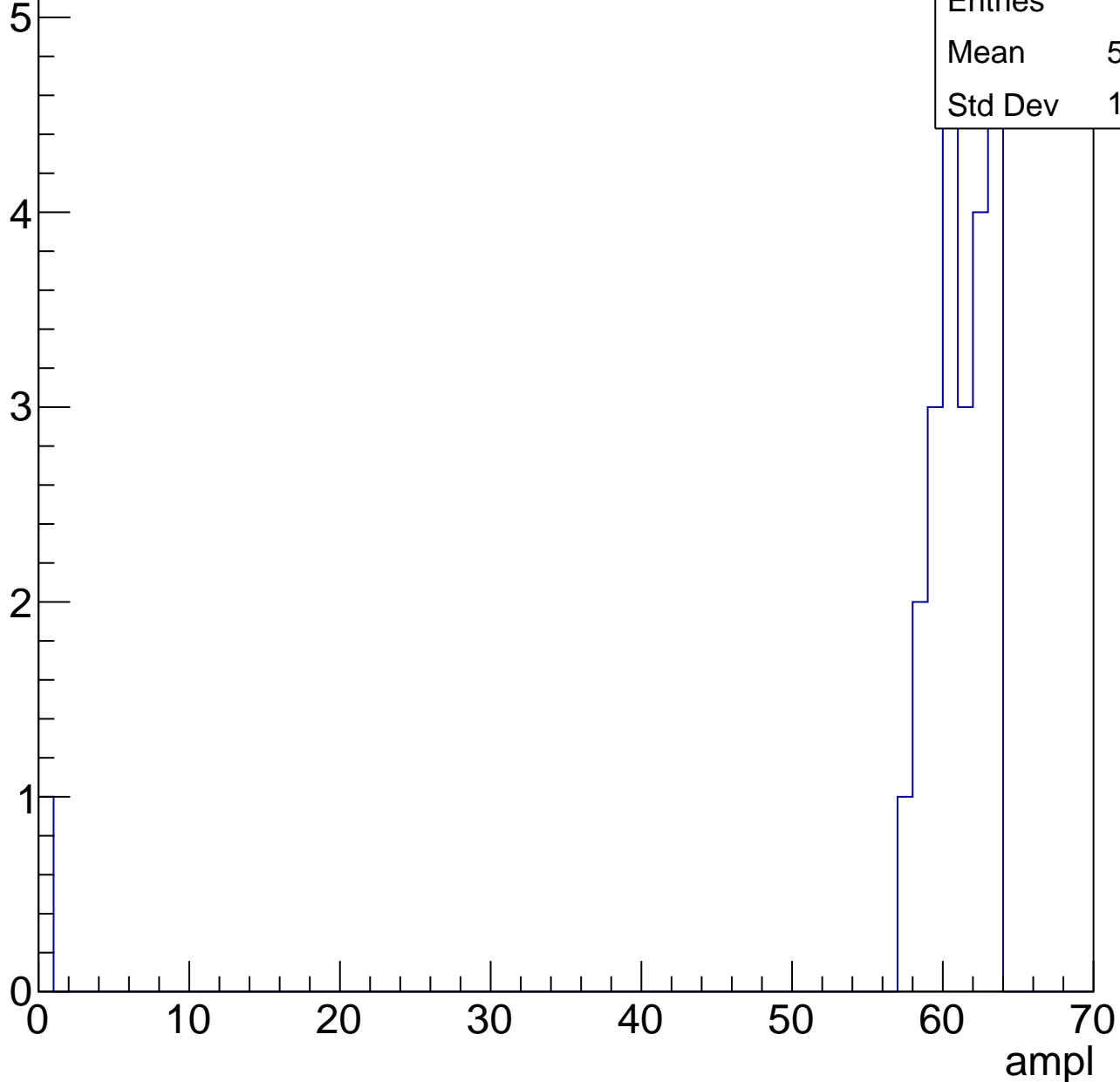


Entries	60
Mean	58.4
Std Dev	2.835

B0L001S, U11-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch54, adc0

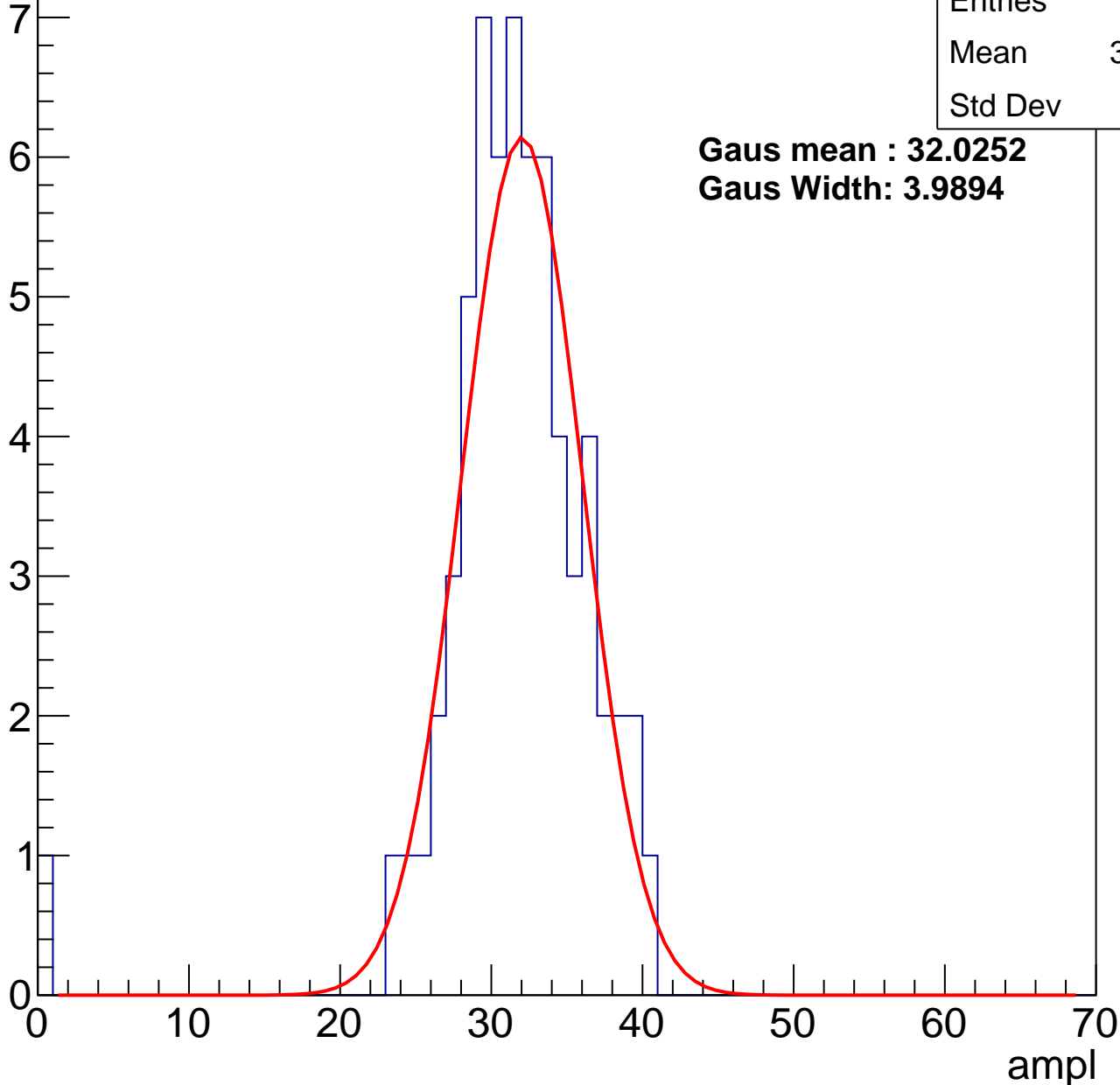
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.06
Std Dev	5.42

Gaus mean : 32.0252

Gaus Width: 3.9894



B0L001S, U11-ch54, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	67
Mean	37.45
Std Dev	3.546

Gaus mean : 37.3922

Gaus Width: 3.7420

0

10

20

30

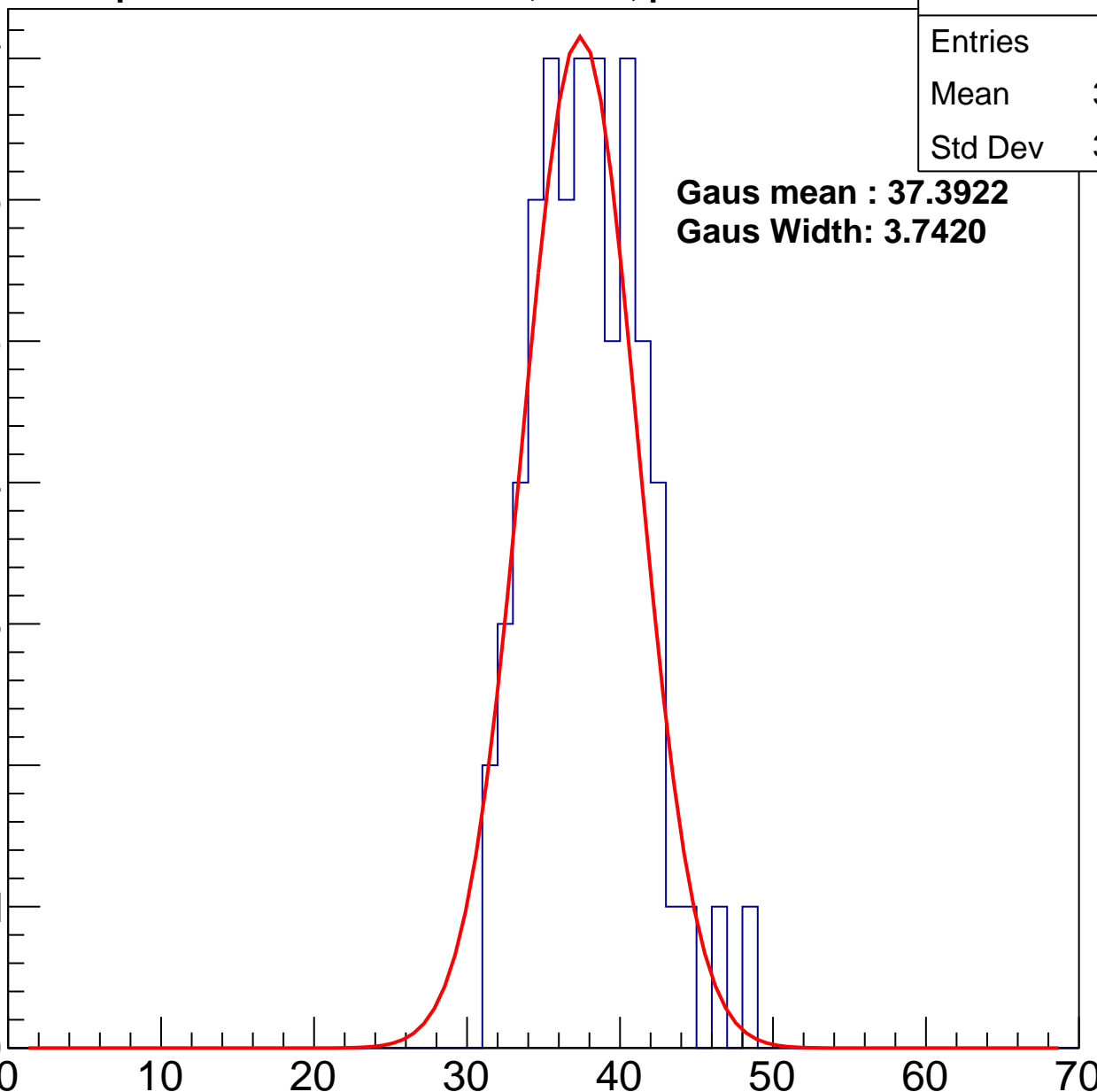
40

50

60

70

ampl



B0L001S, U11-ch54, adc2

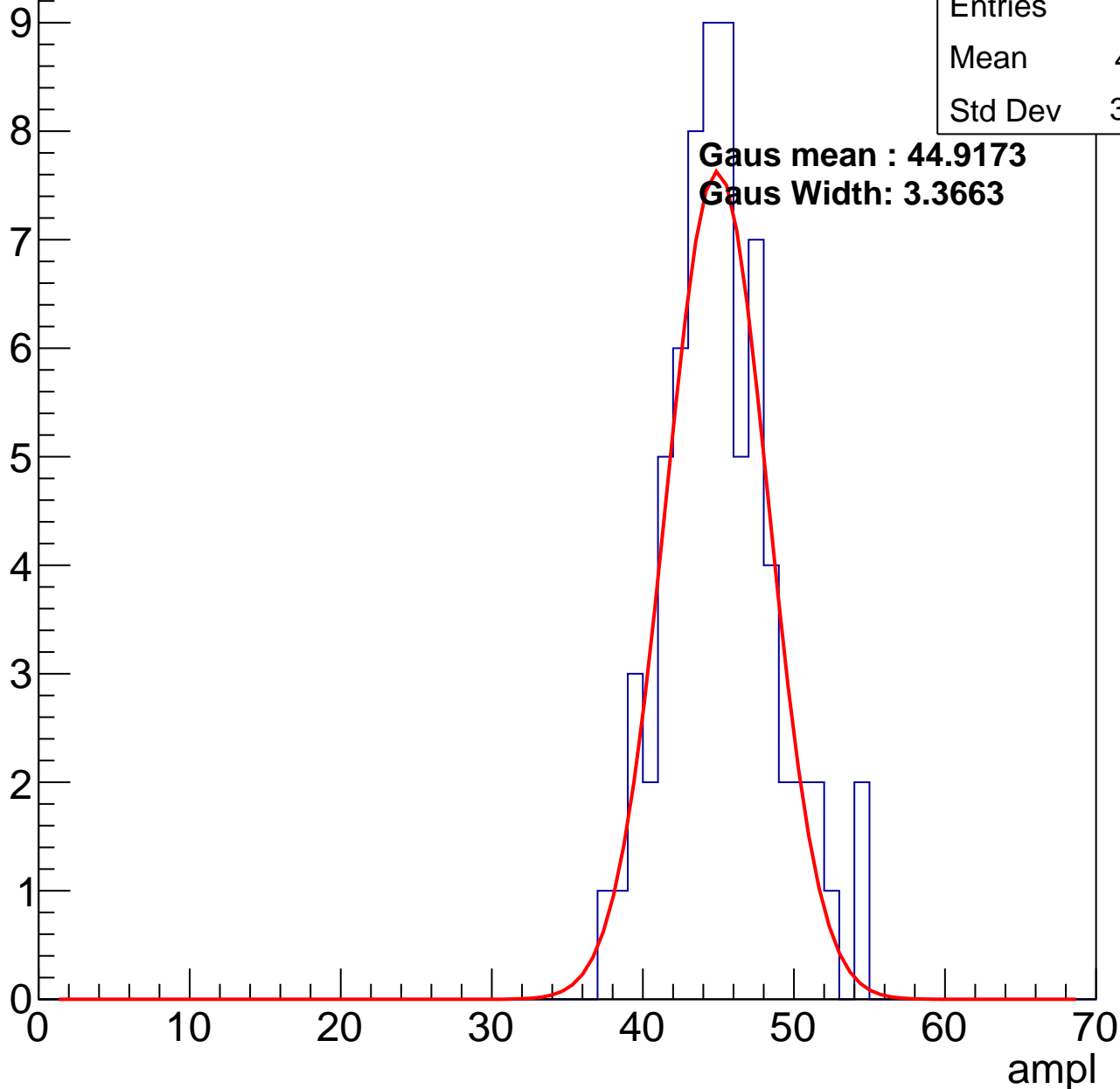
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.71
Std Dev	3.559

Gaus mean : 44.9173

Gaus Width: 3.3663

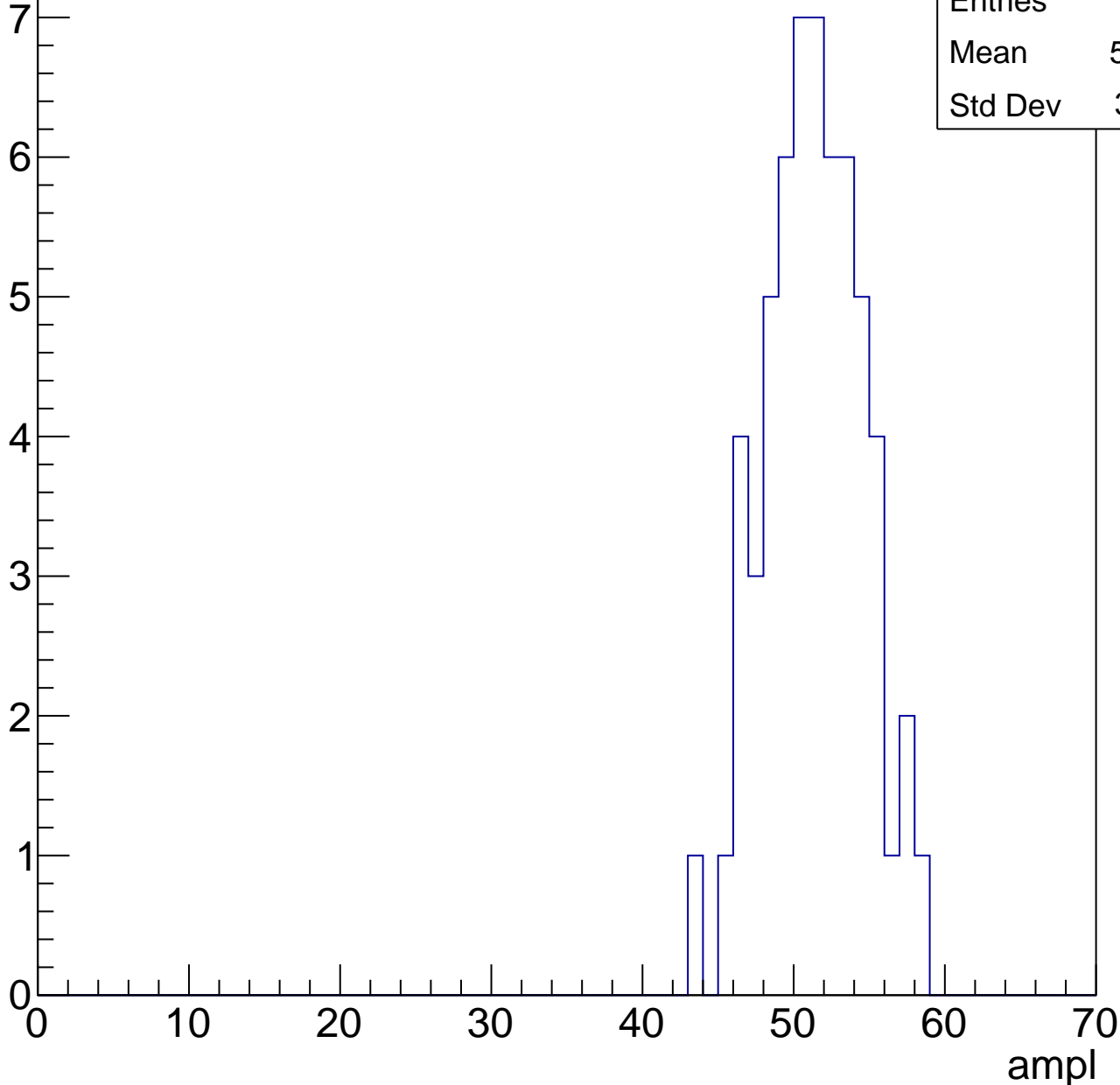


B0L001S, U11-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.88
Std Dev	3.211

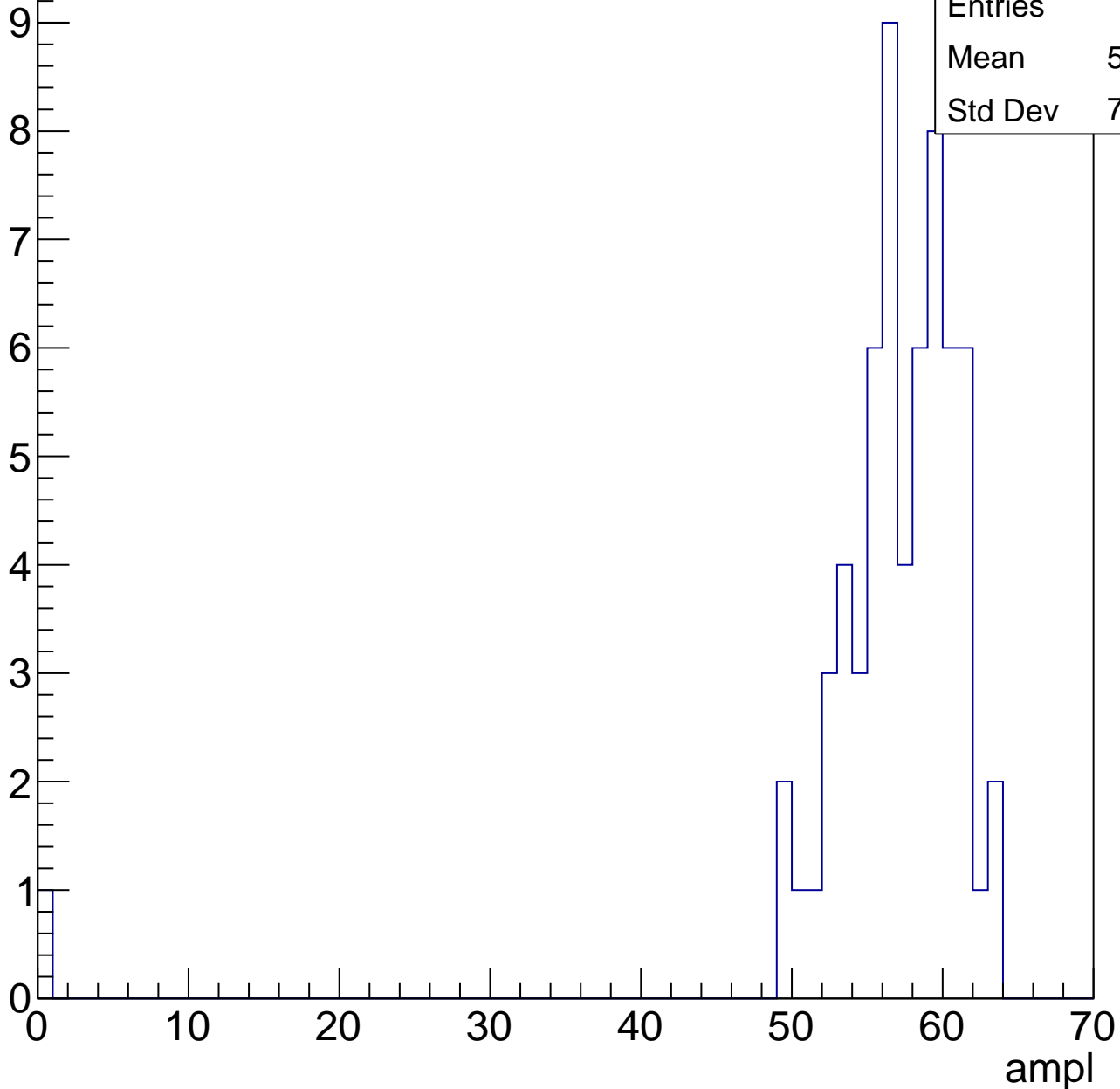


B0L001S, U11-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

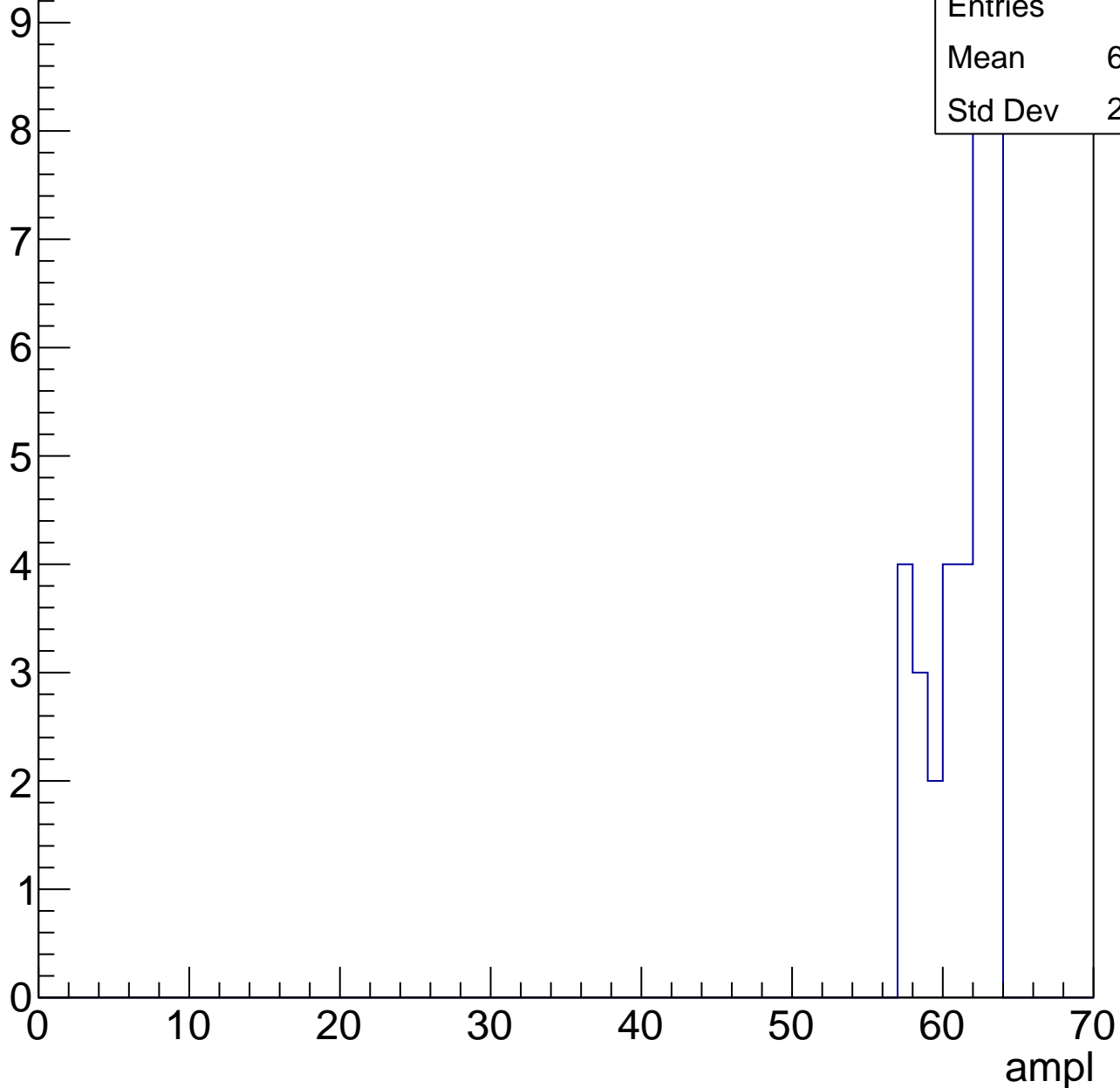
Entries	63
Mean	55.95
Std Dev	7.845



B0L001S, U11-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch55, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	30.48
Std Dev	4.836

Gaus mean : 31.0286

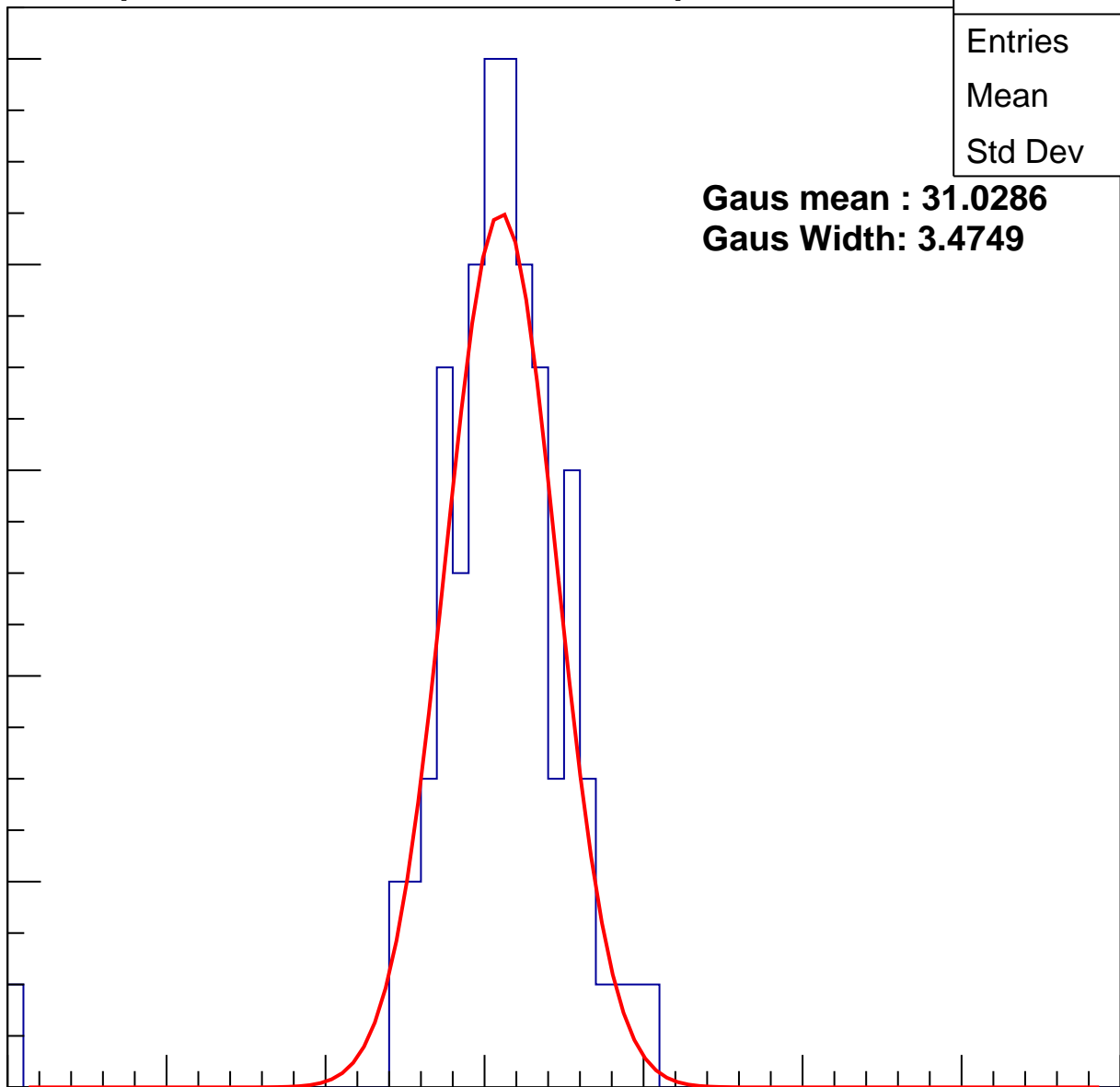
Gaus Width: 3.4749

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch55, adc1

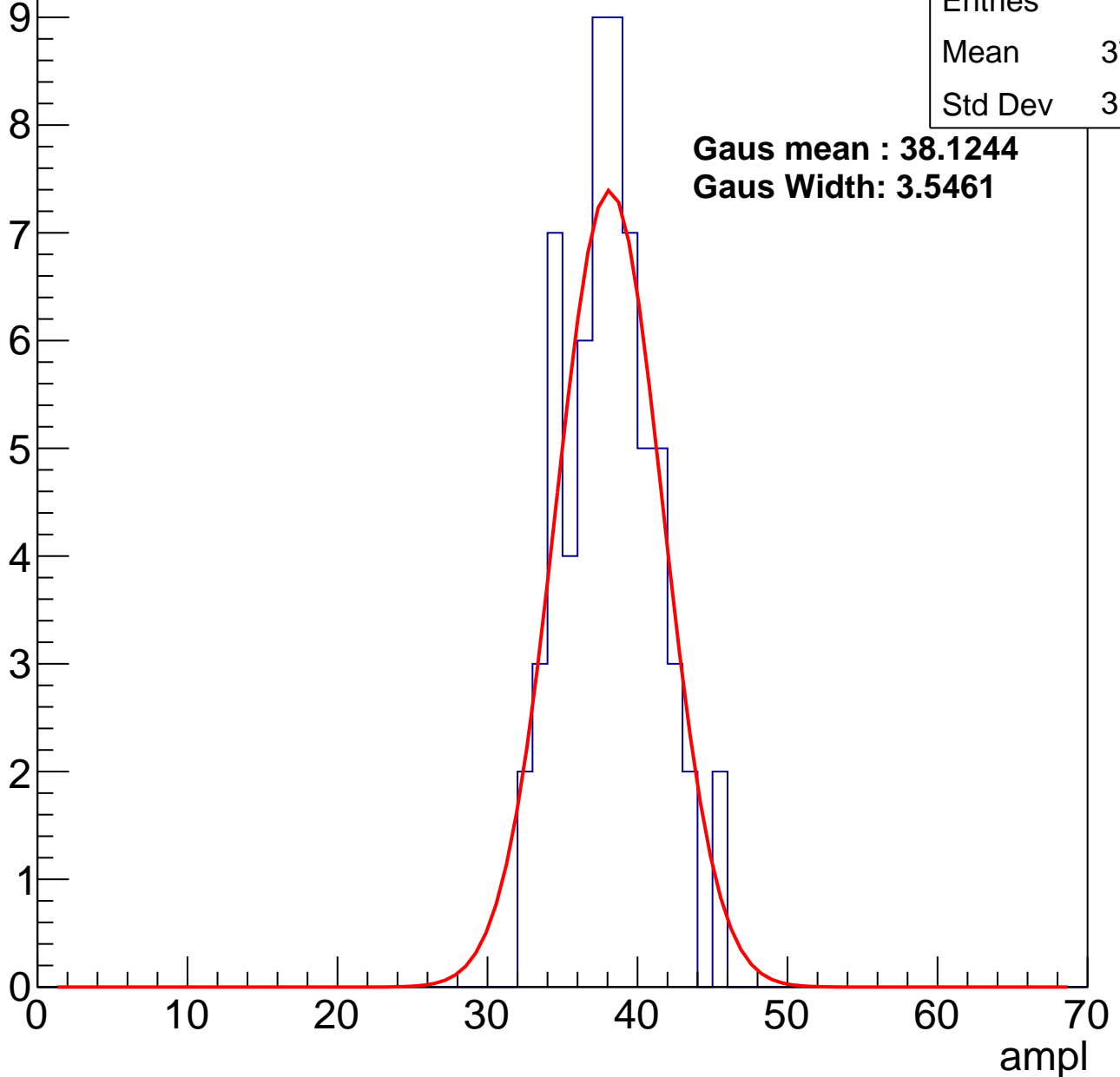
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.69
Std Dev	3.036

Gaus mean : 38.1244

Gaus Width: 3.5461



B0L001S, U11-ch55, adc2

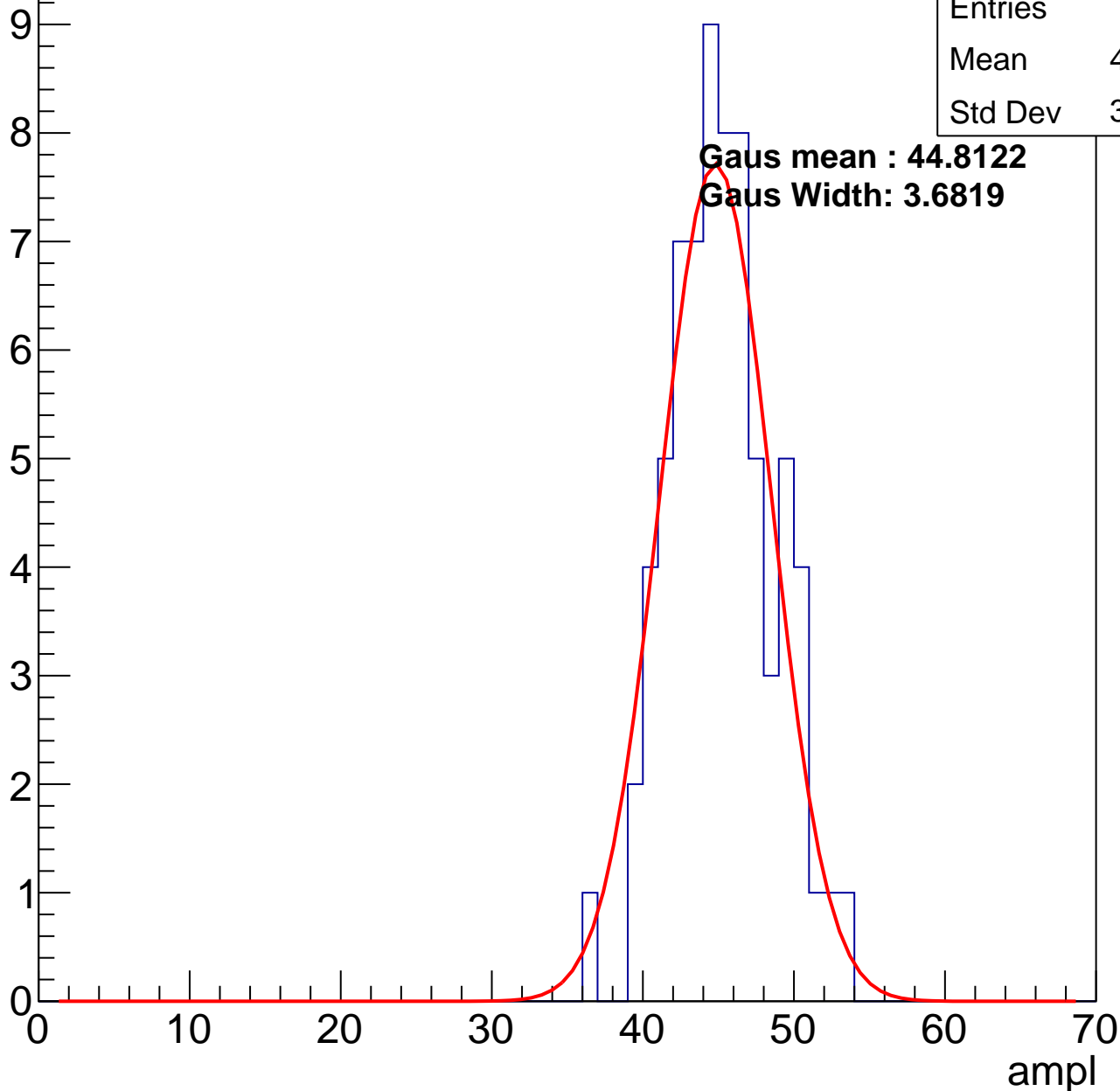
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.76
Std Dev	3.384

Gaus mean : 44.8122

Gaus Width: 3.6819

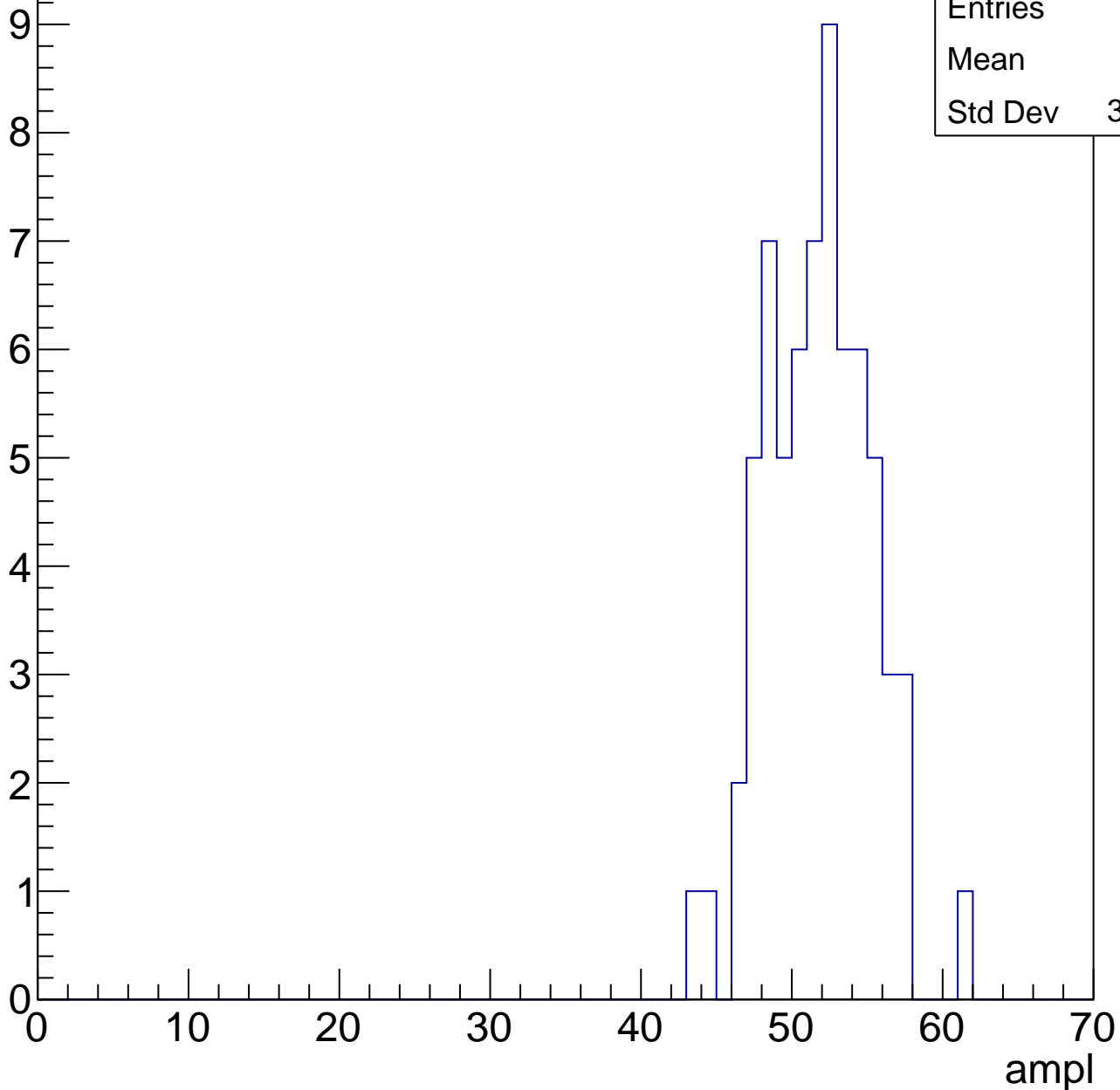


B0L001S, U11-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	51.3
Std Dev	3.403

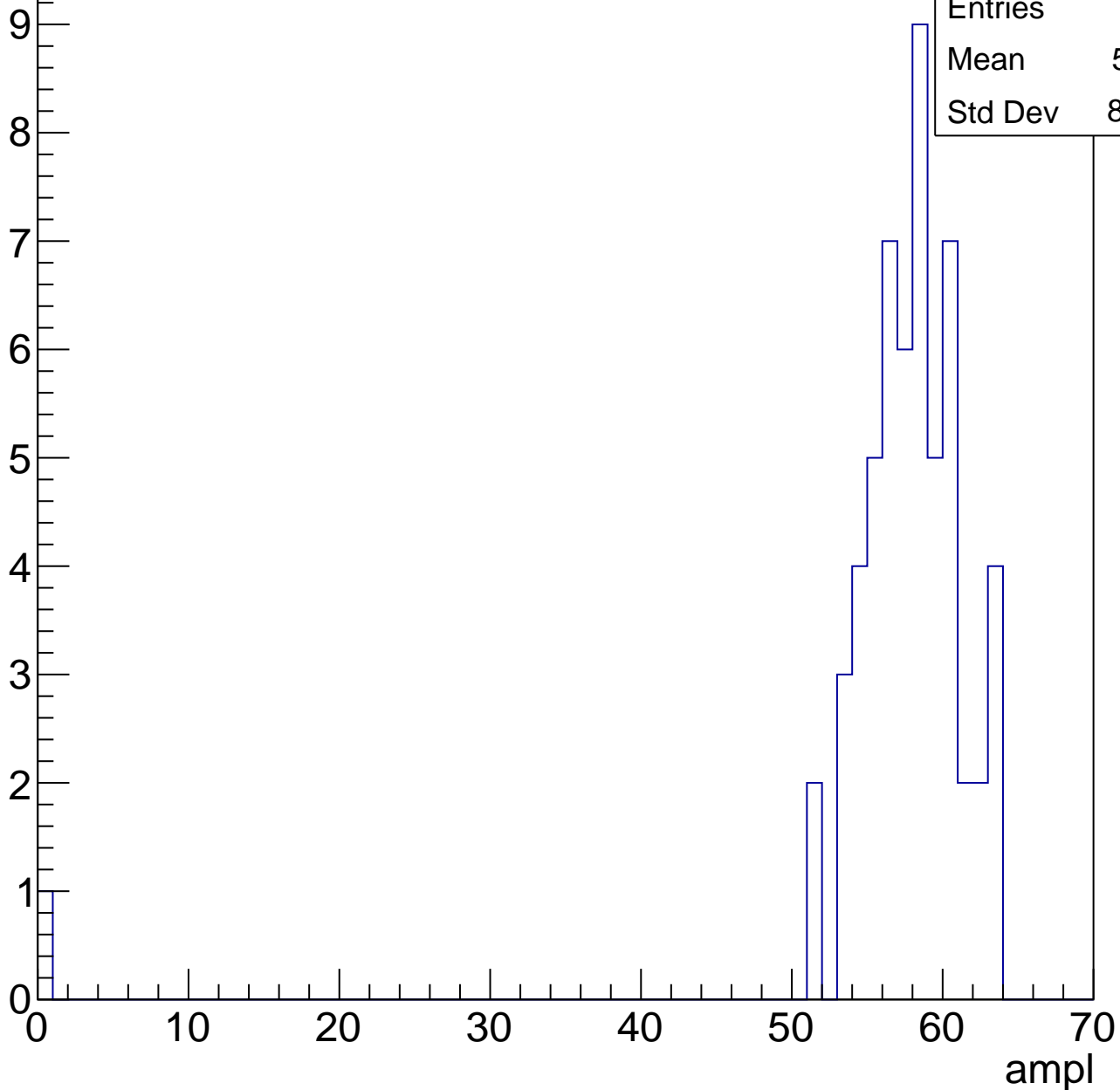


B0L001S, U11-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.51
Std Dev	8.096

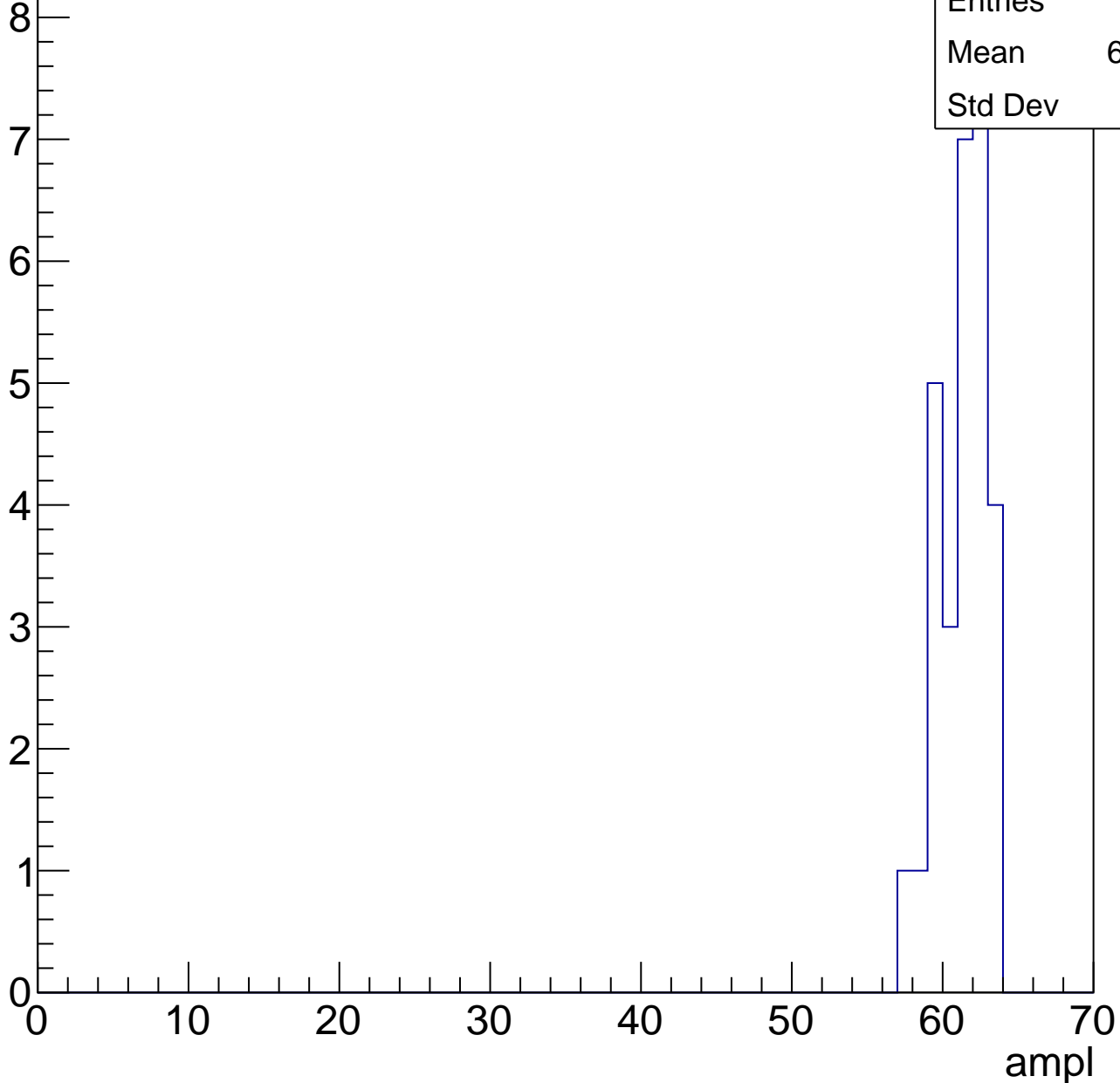


B0L001S, U11-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	60.86
Std Dev	1.57



B0L001S, U11-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch56, adc0

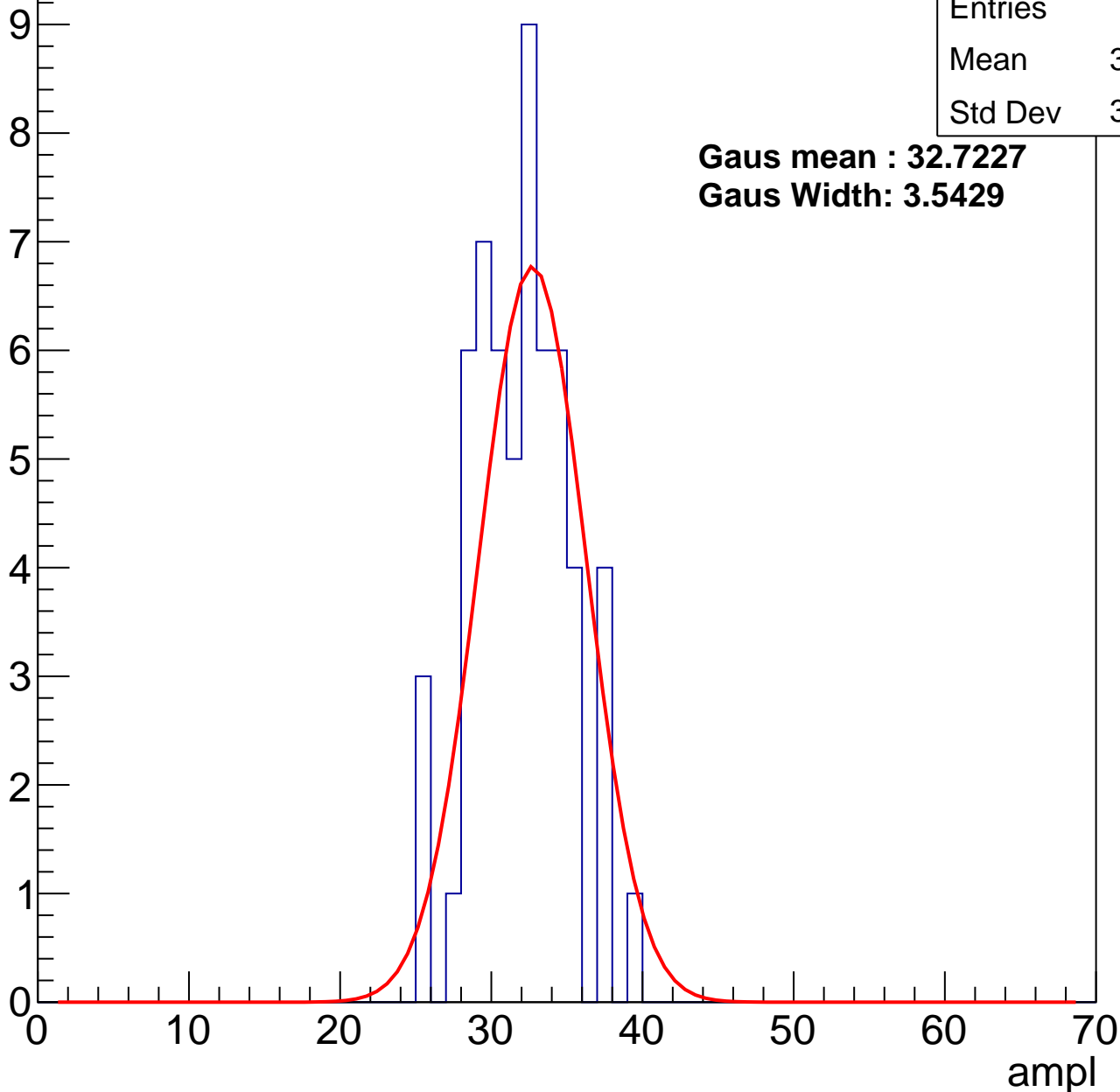
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.47
Std Dev	3.103

Gaus mean : 32.7227

Gaus Width: 3.5429



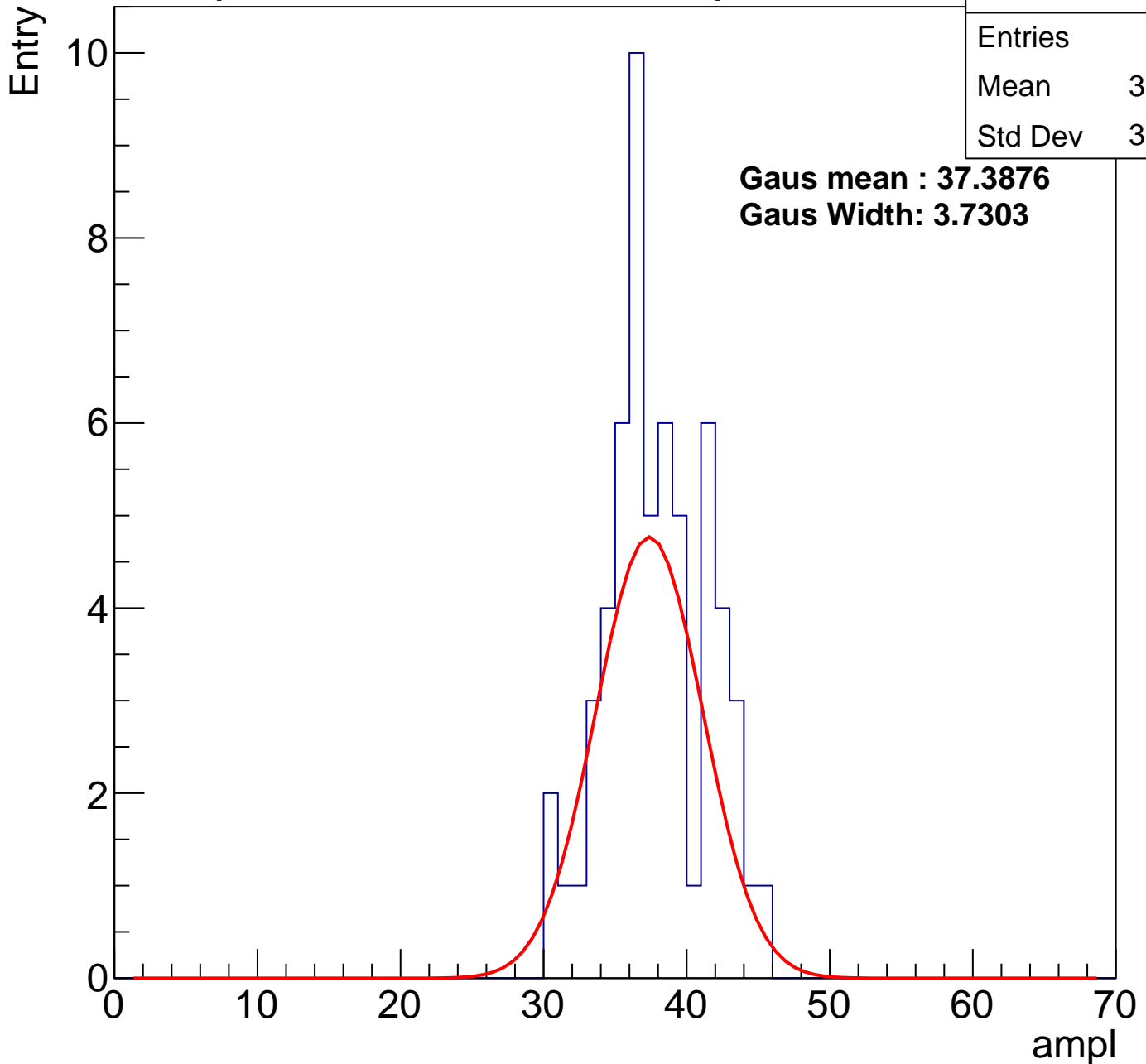
B0L001S, U11-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	37.42
Std Dev	3.489

Gaus mean : 37.3876

Gaus Width: 3.7303



B0L001S, U11-ch56, adc2

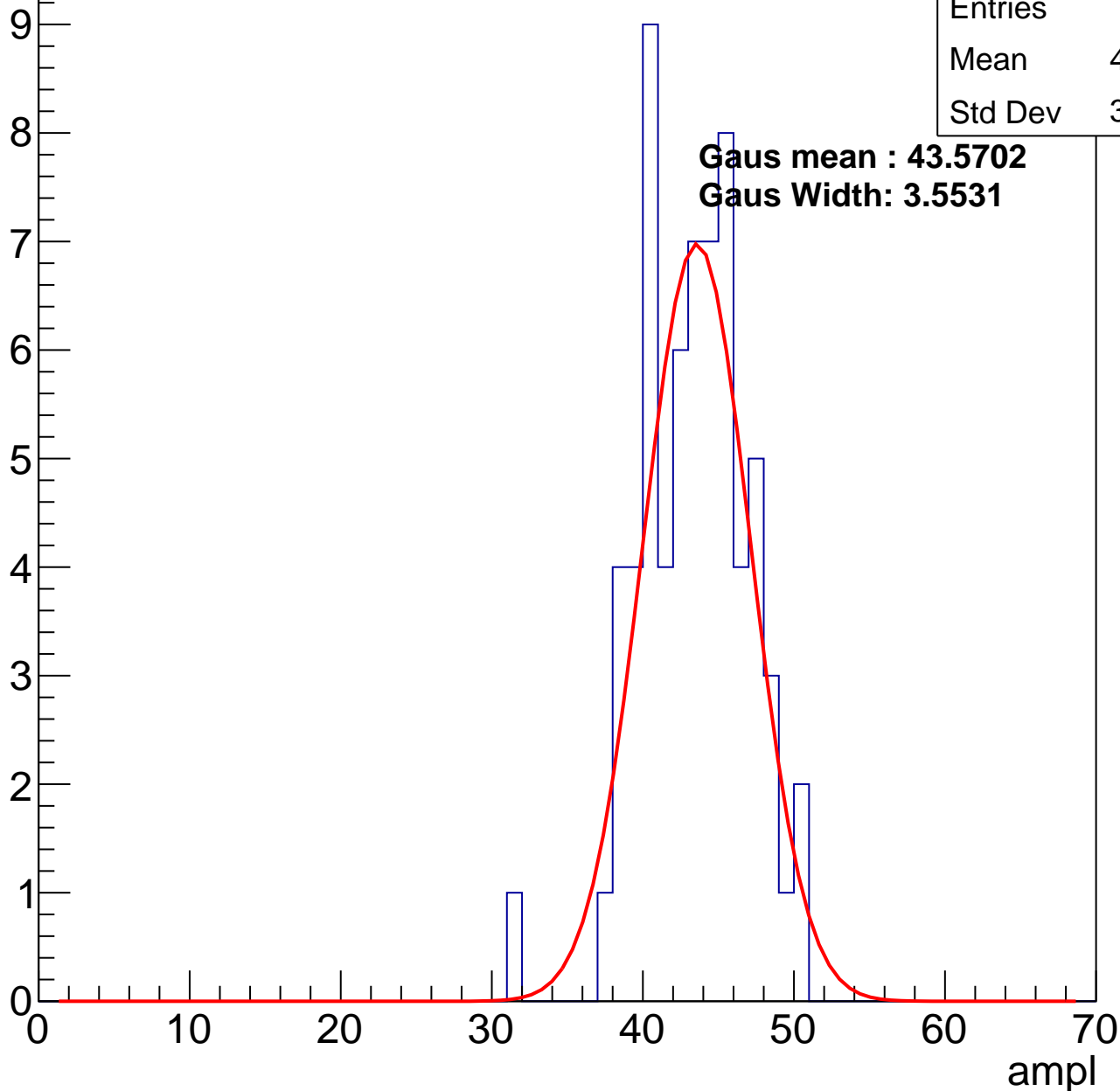
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	42.92
Std Dev	3.505

Gaus mean : 43.5702

Gaus Width: 3.5531

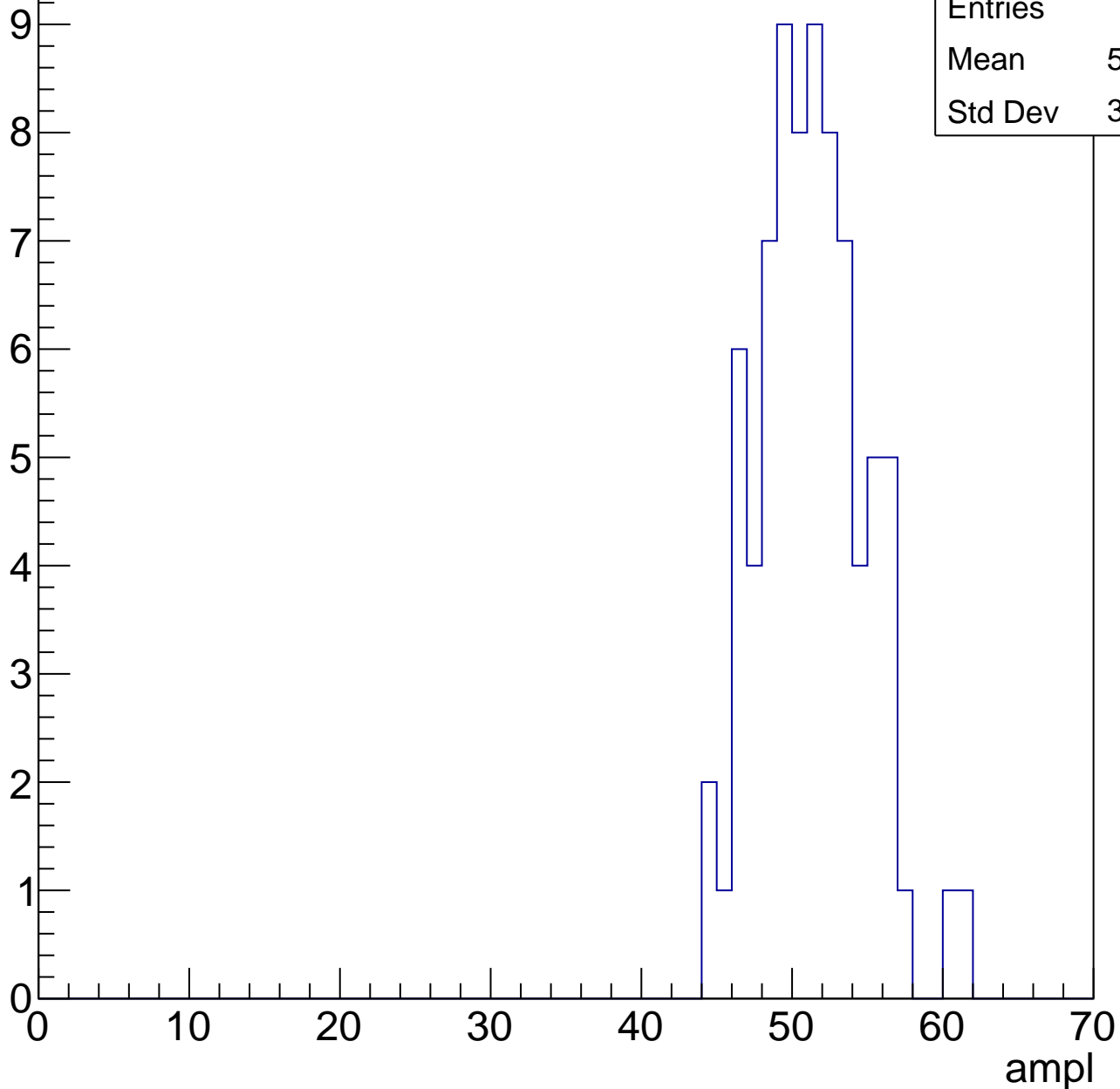


B0L001S, U11-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	50.88
Std Dev	3.486

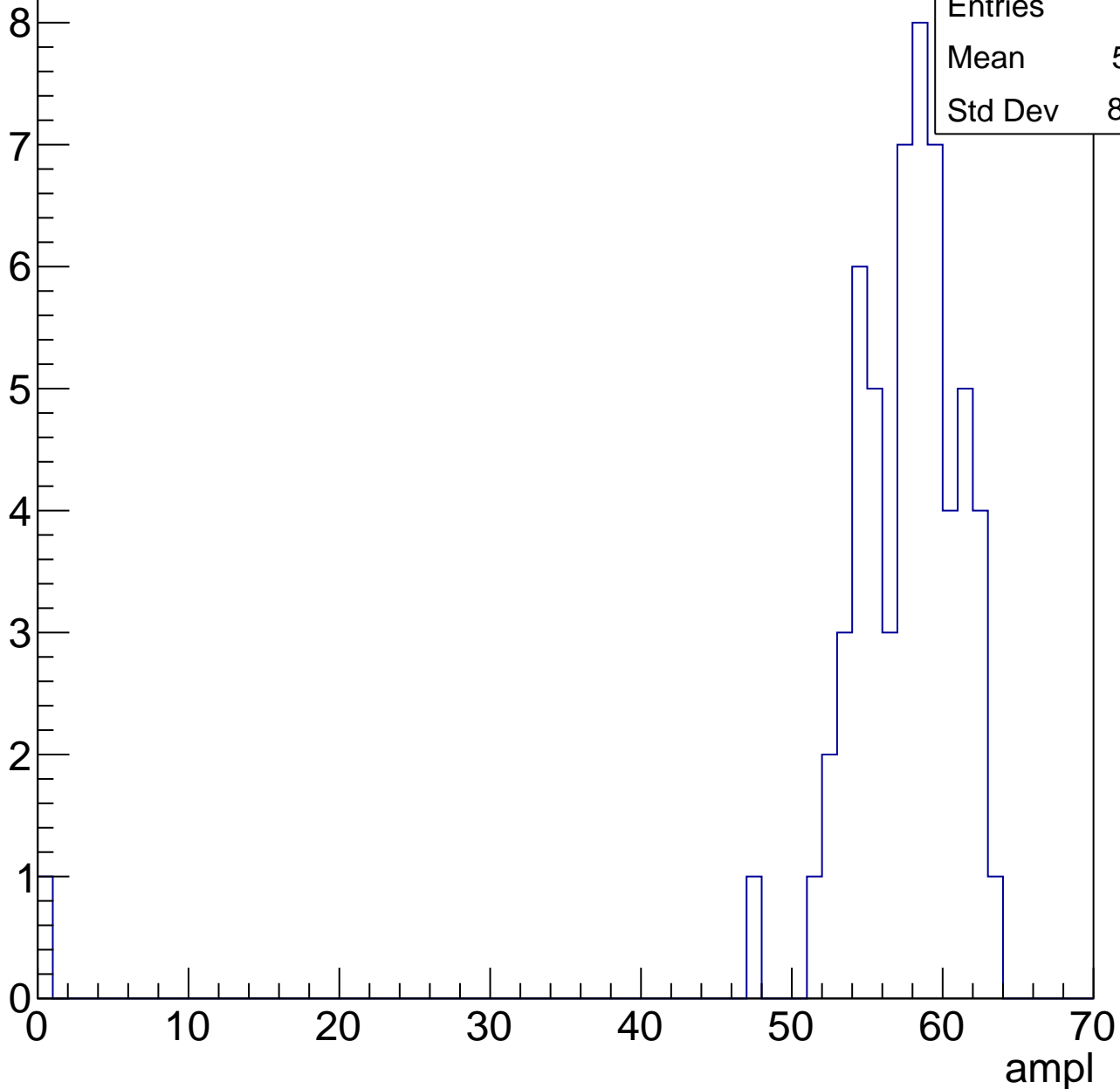


B0L001S, U11-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.21
Std Dev	8.107

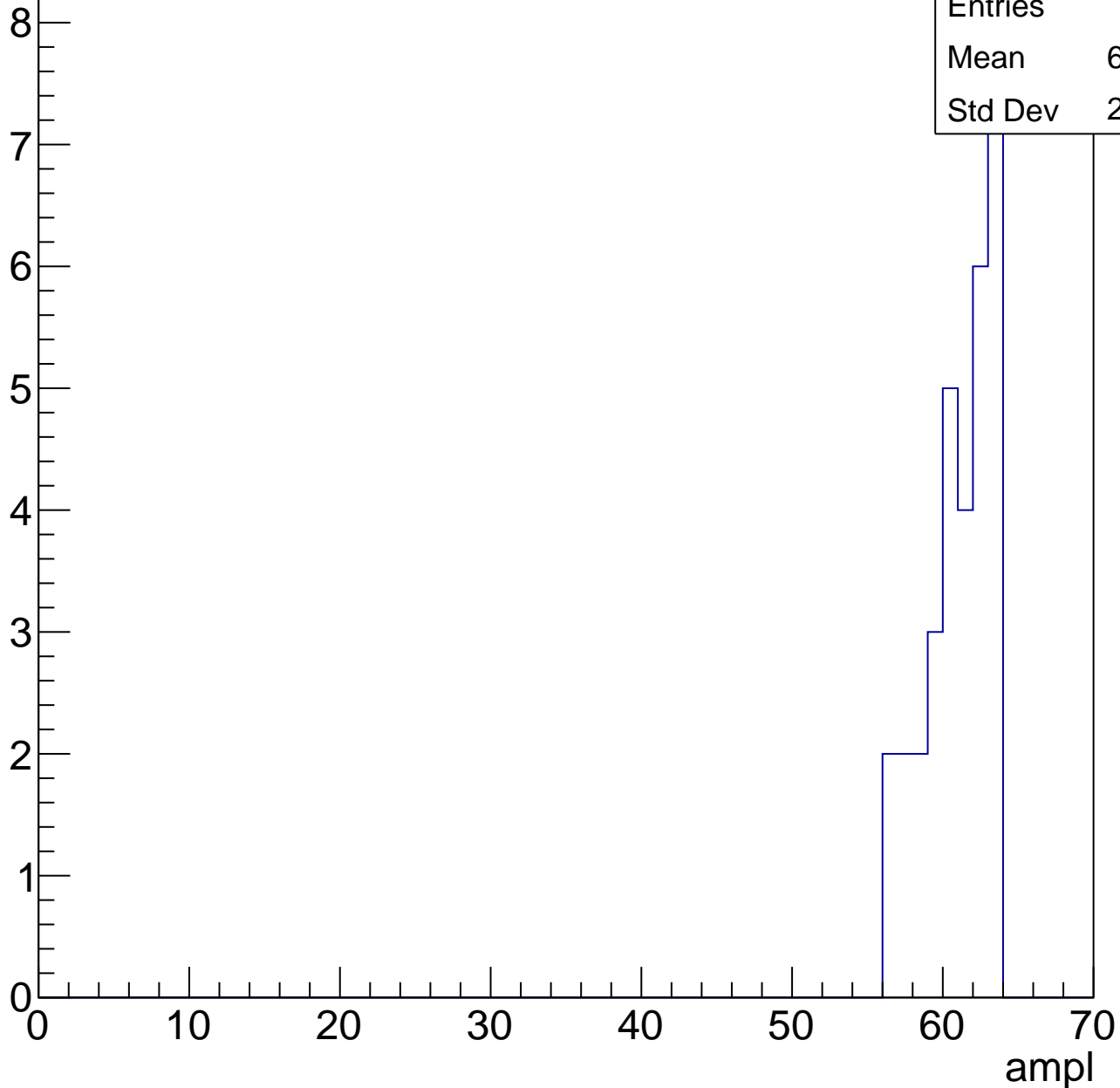


B0L001S, U11-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.59
Std Dev	2.163



B0L001S, U11-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch57, adc0

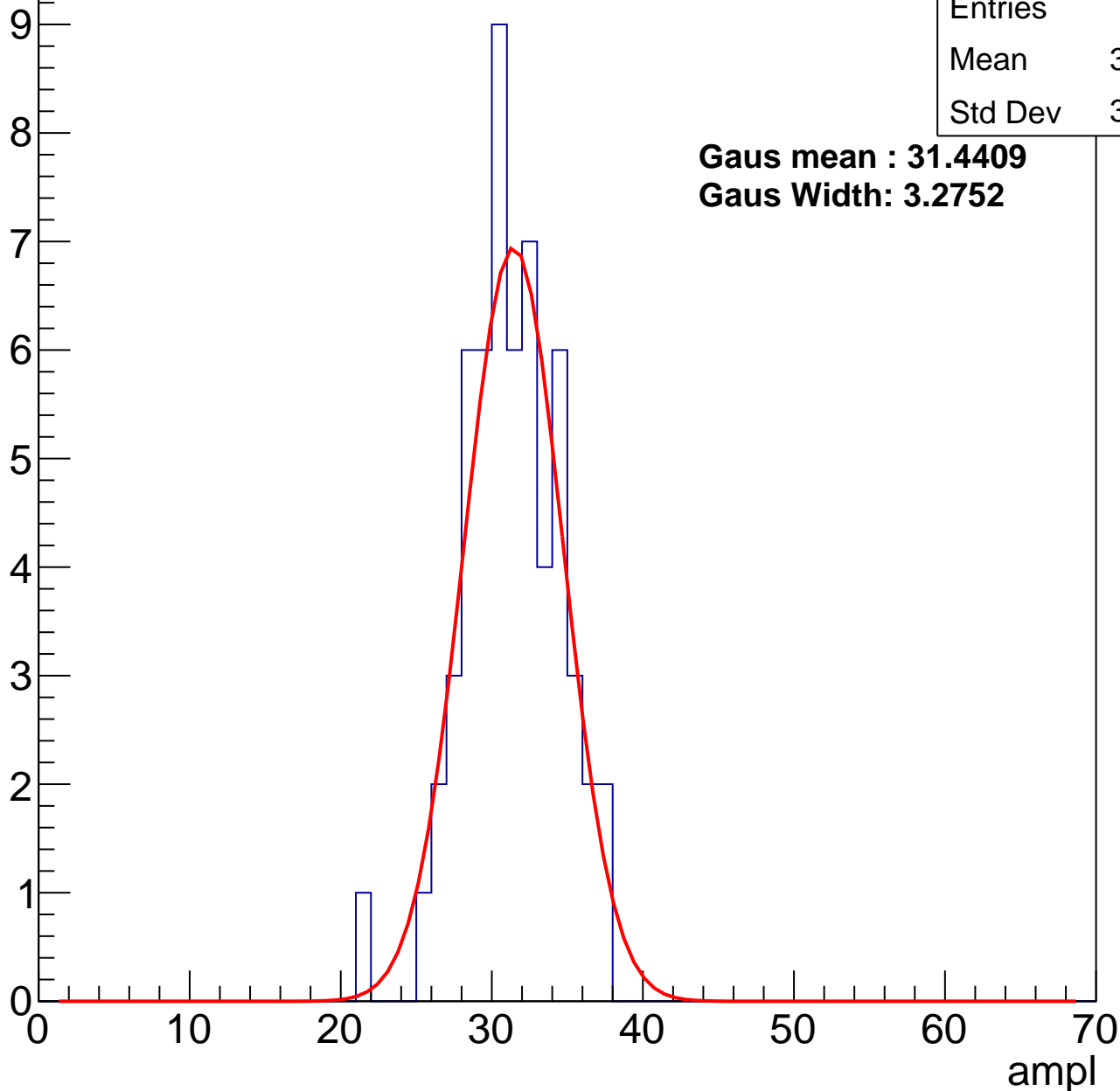
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	30.83
Std Dev	3.125

Gaus mean : 31.4409

Gaus Width: 3.2752



B0L001S, U11-ch57, adc1

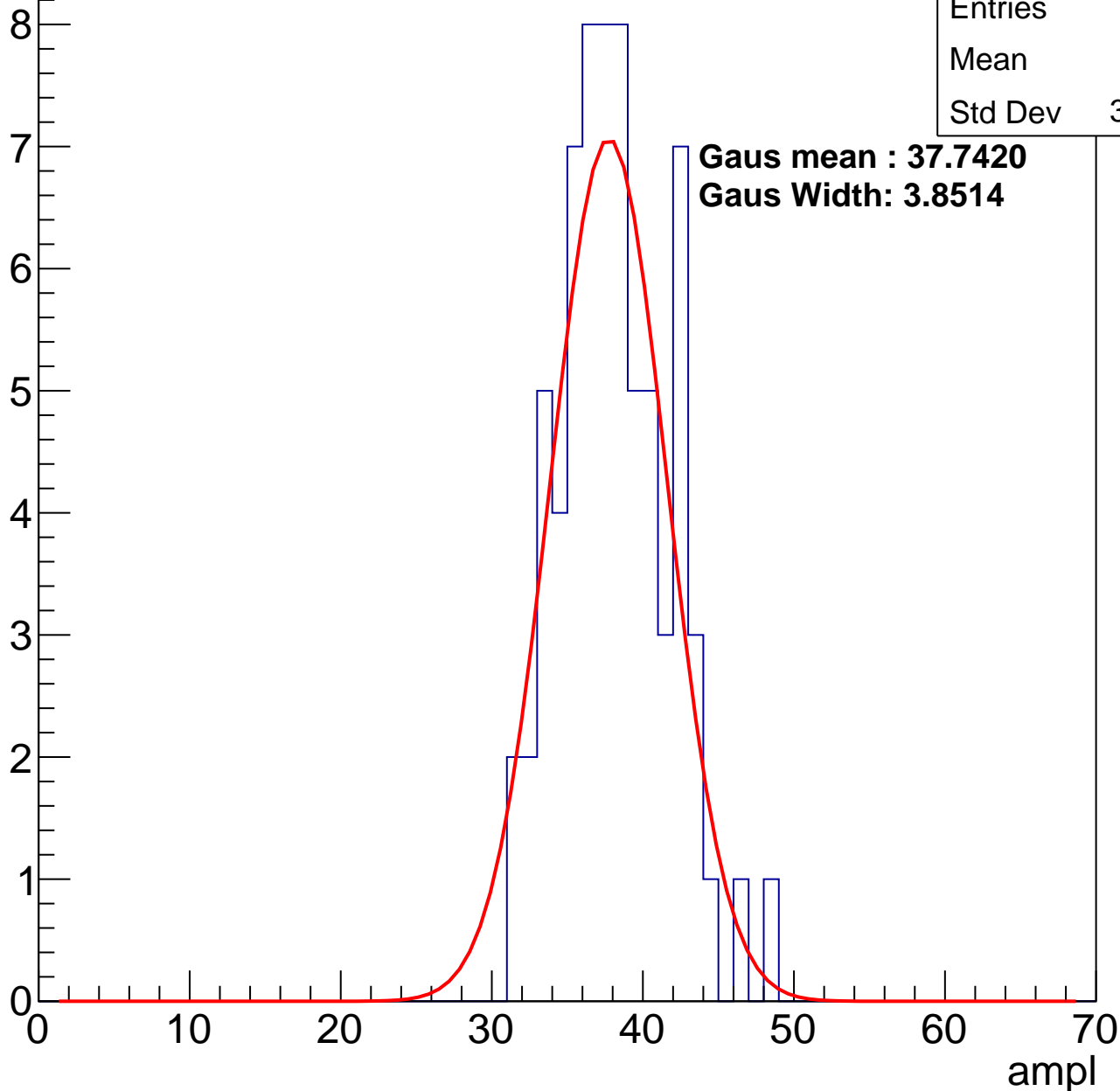
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.7
Std Dev	3.575

Gaus mean : 37.7420

Gaus Width: 3.8514



B0L001S, U11-ch57, adc2

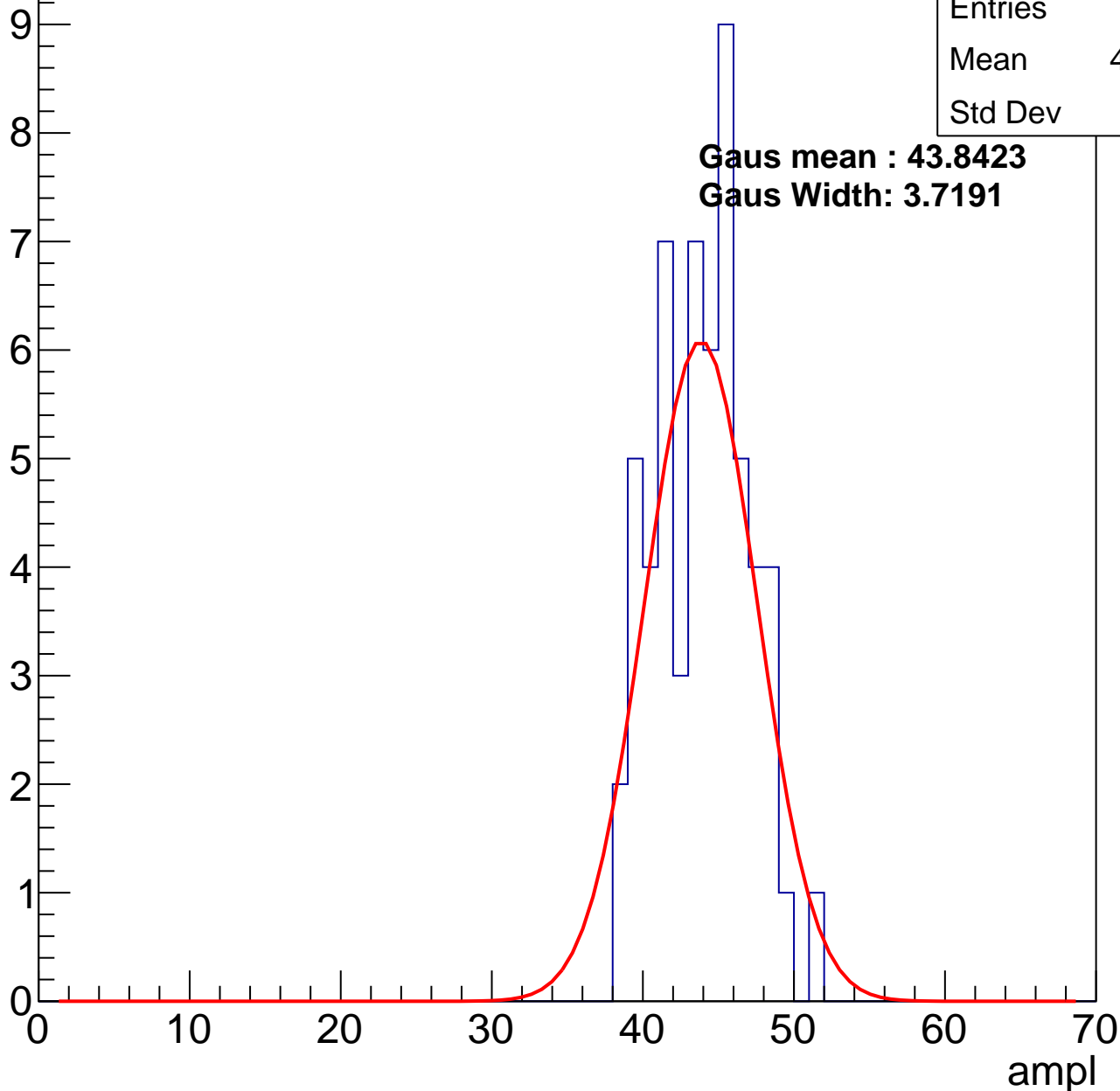
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.52
Std Dev	3.03

Gaus mean : 43.8423

Gaus Width: 3.7191

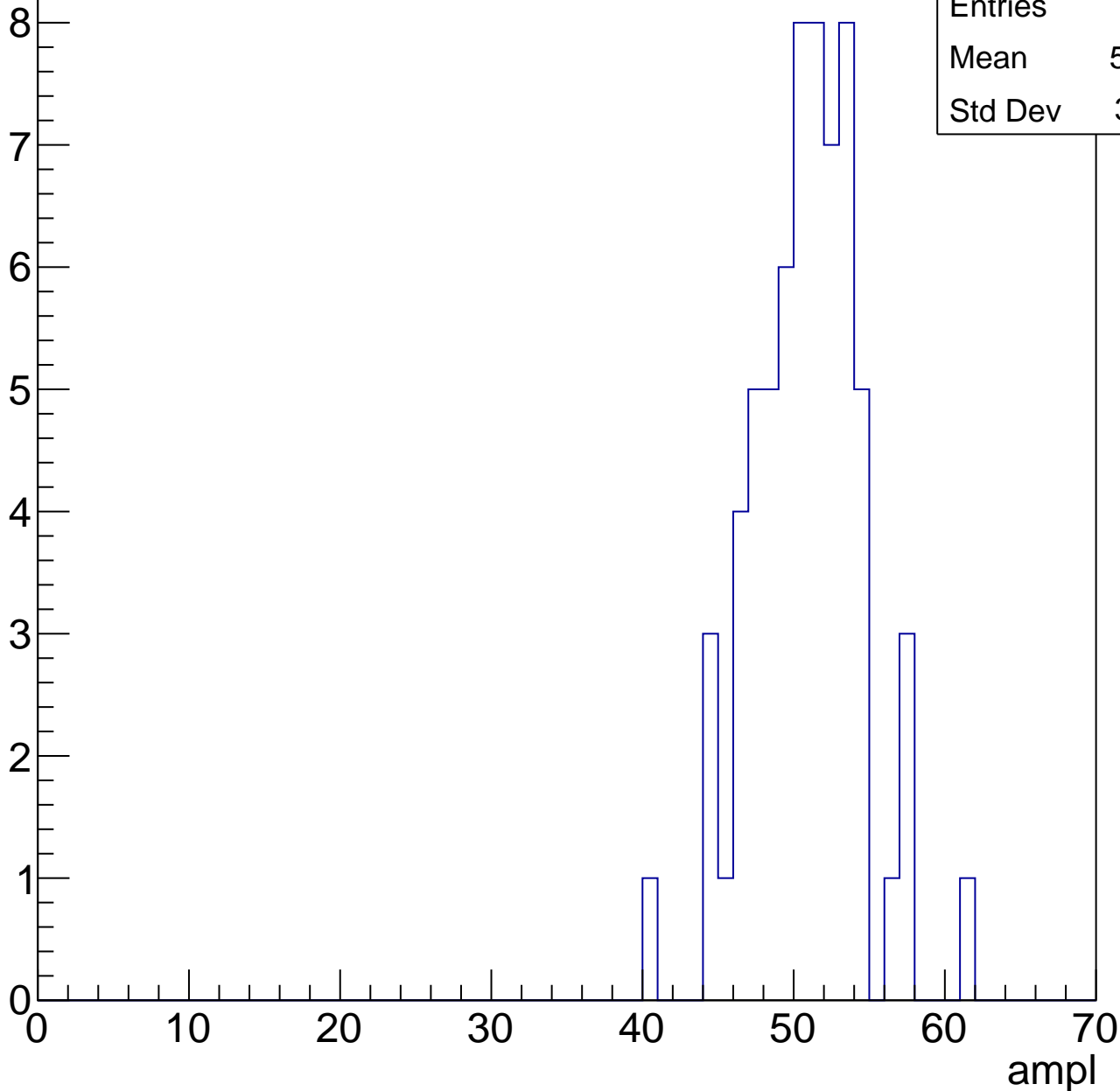


B0L001S, U11-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.36
Std Dev	3.591

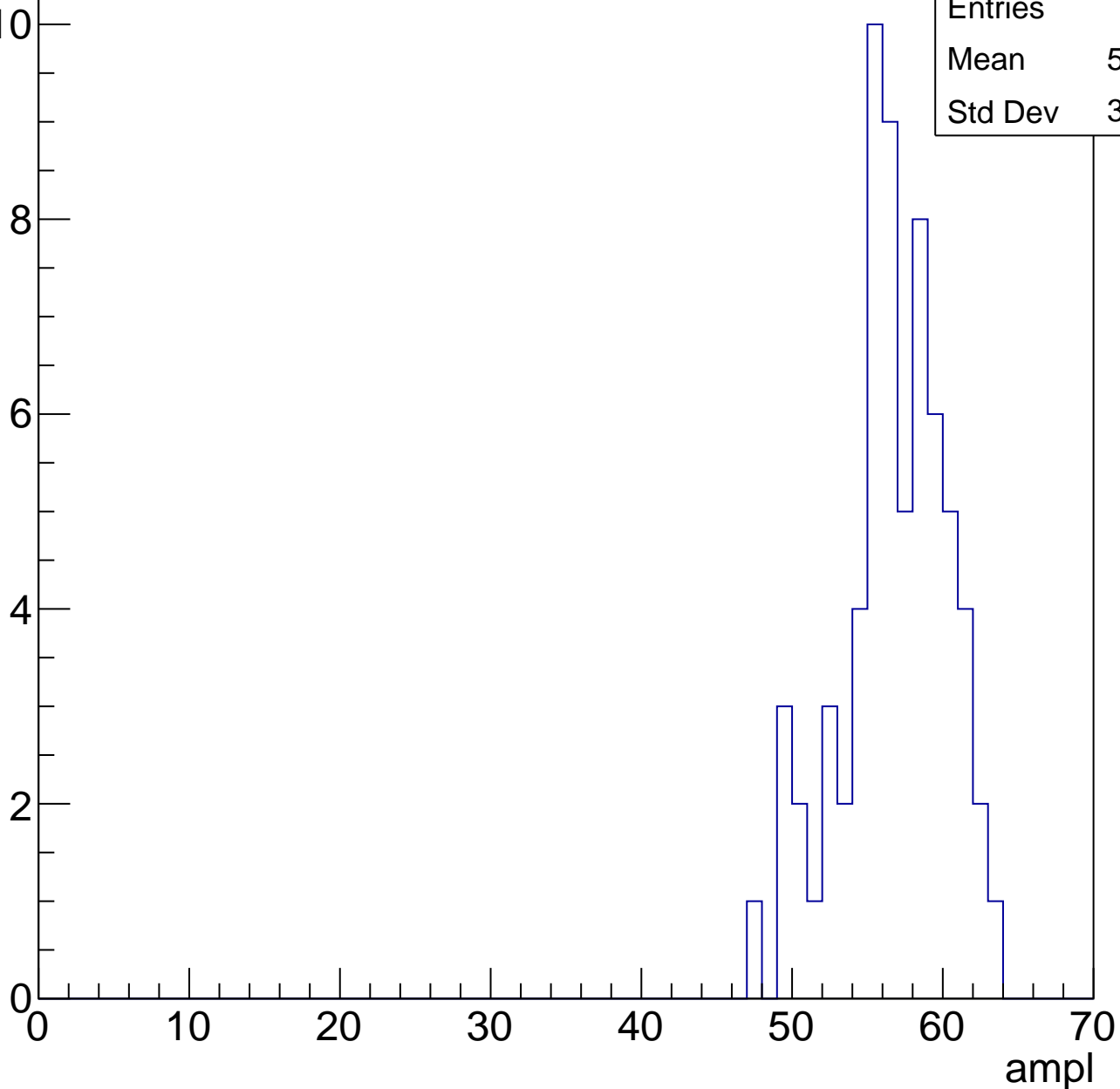


B0L001S, U11-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.23
Std Dev	3.494

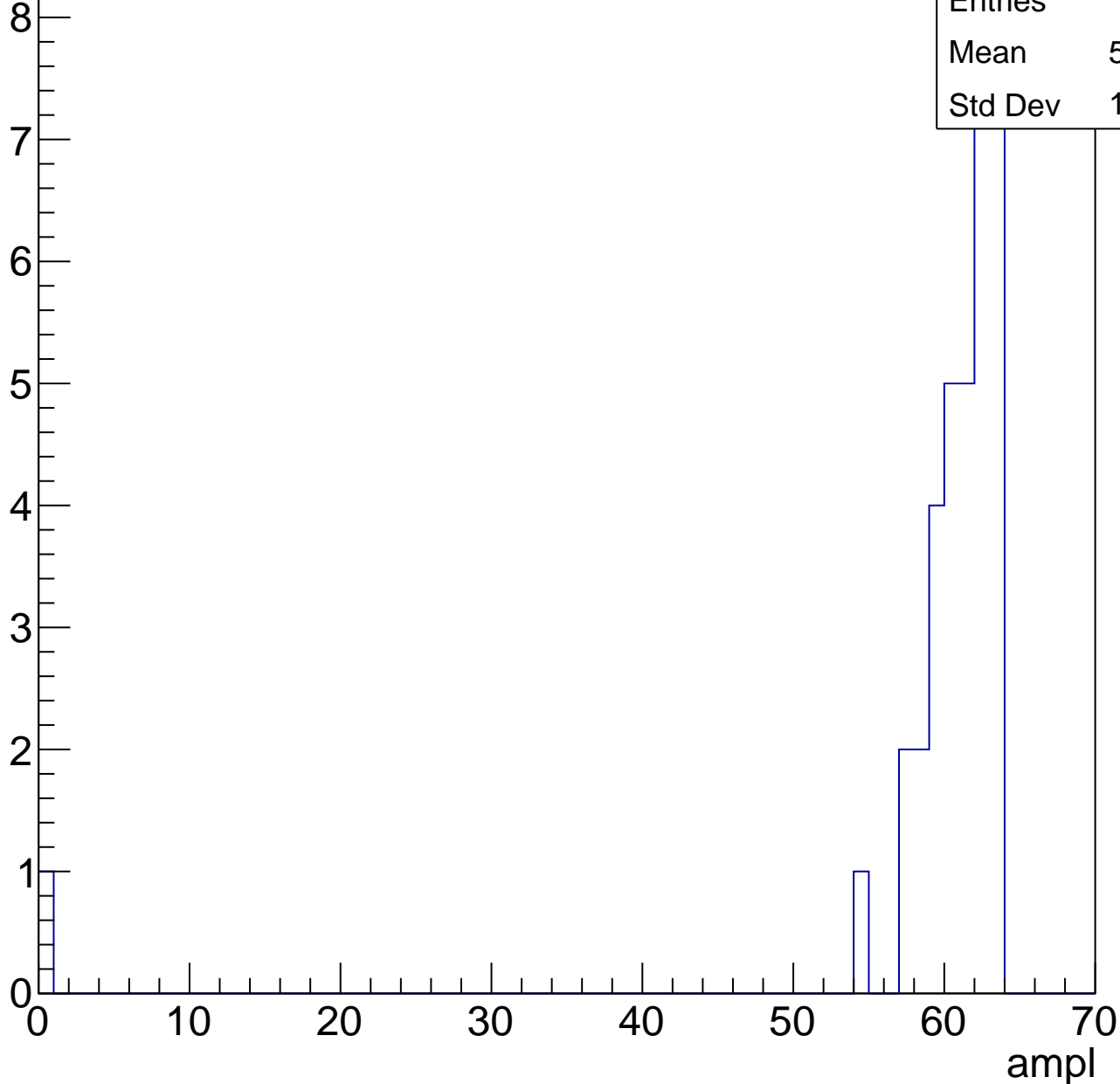


B0L001S, U11-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.03
Std Dev	10.19



B0L001S, U11-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U11-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch58, adc0

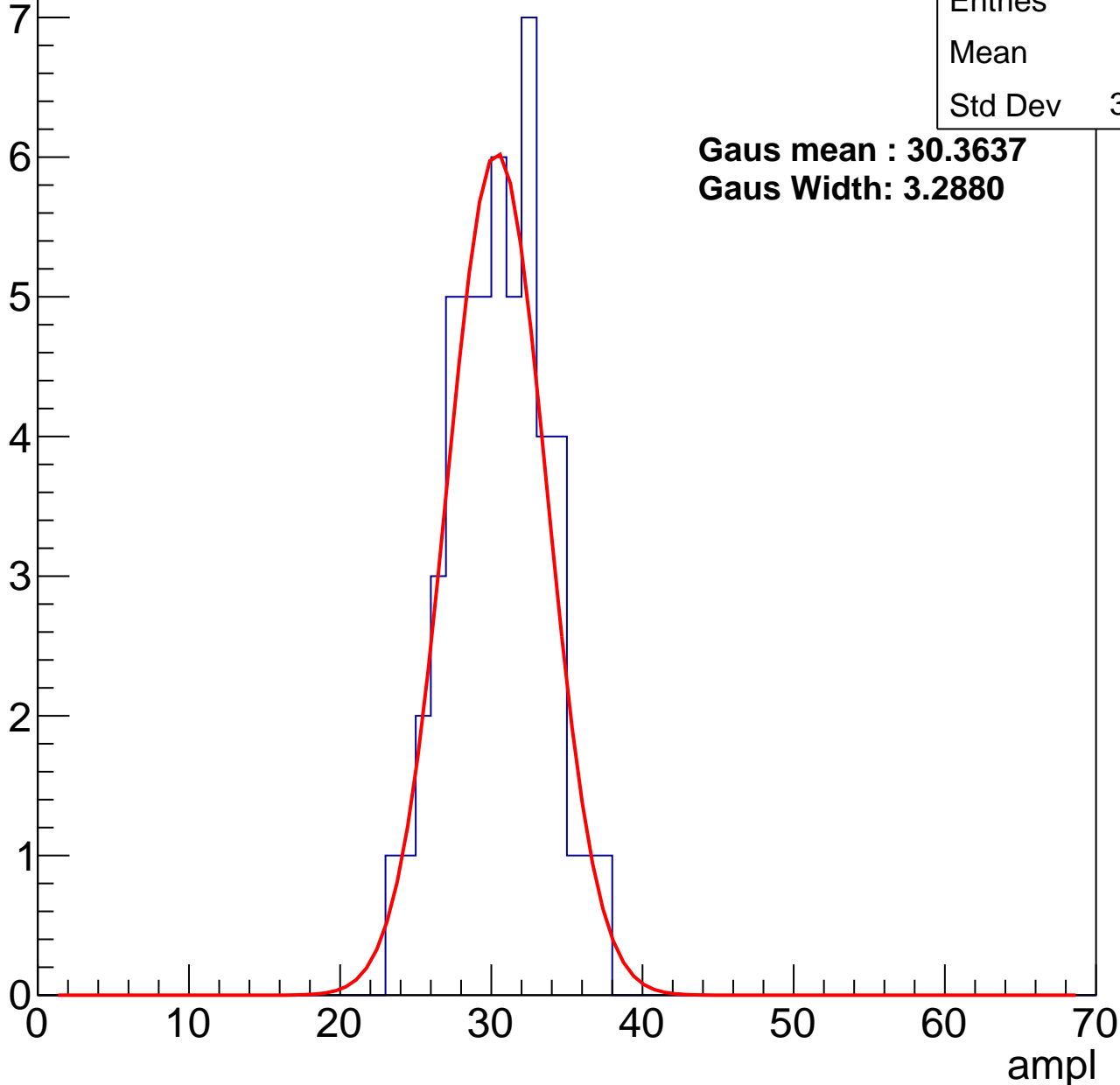
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30
Std Dev	3.119

Gaus mean : 30.3637

Gaus Width: 3.2880



B0L001S, U11-ch58, adc1

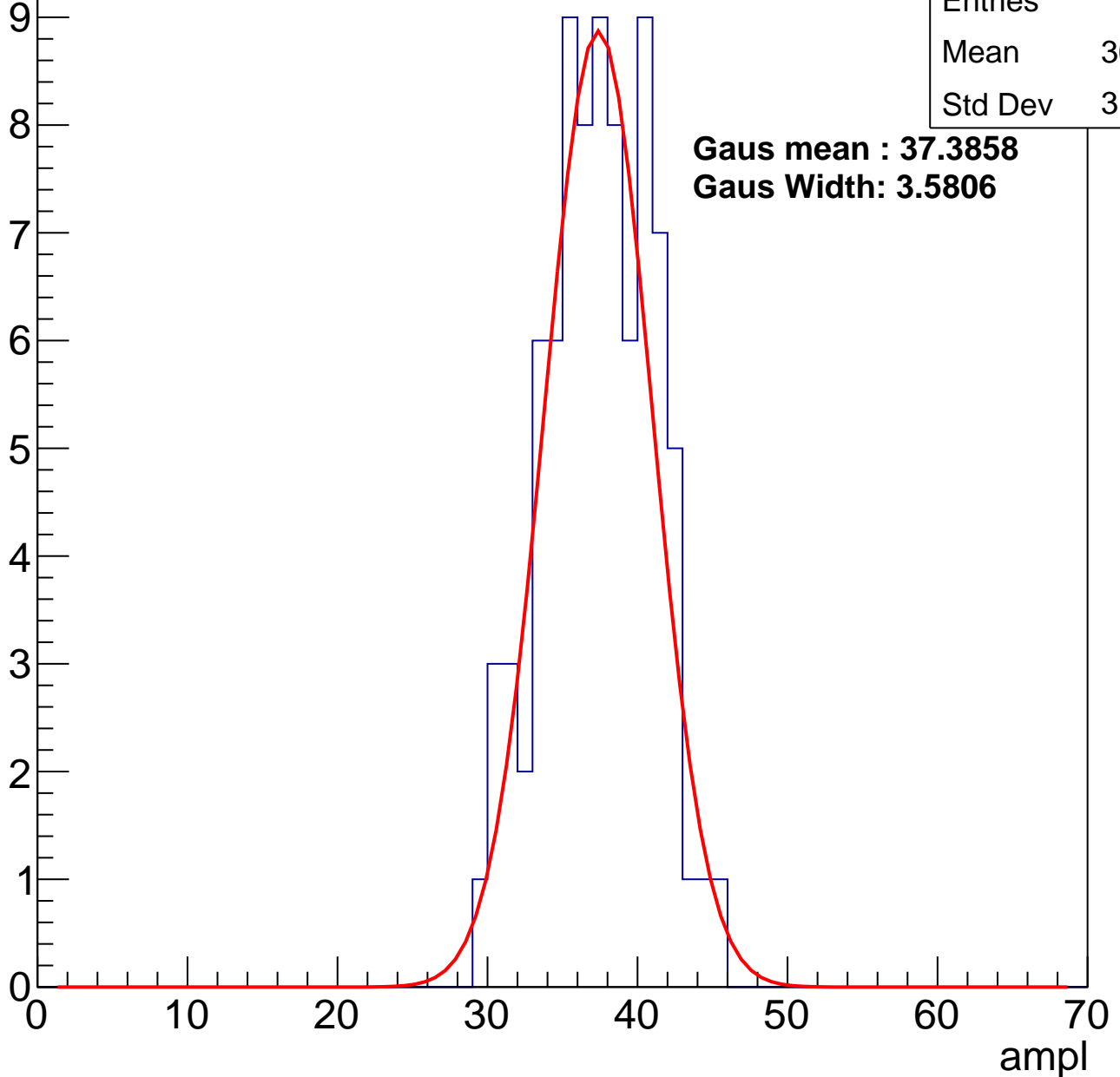
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	36.95
Std Dev	3.534

Gaus mean : 37.3858

Gaus Width: 3.5806



B0L001S, U11-ch58, adc2

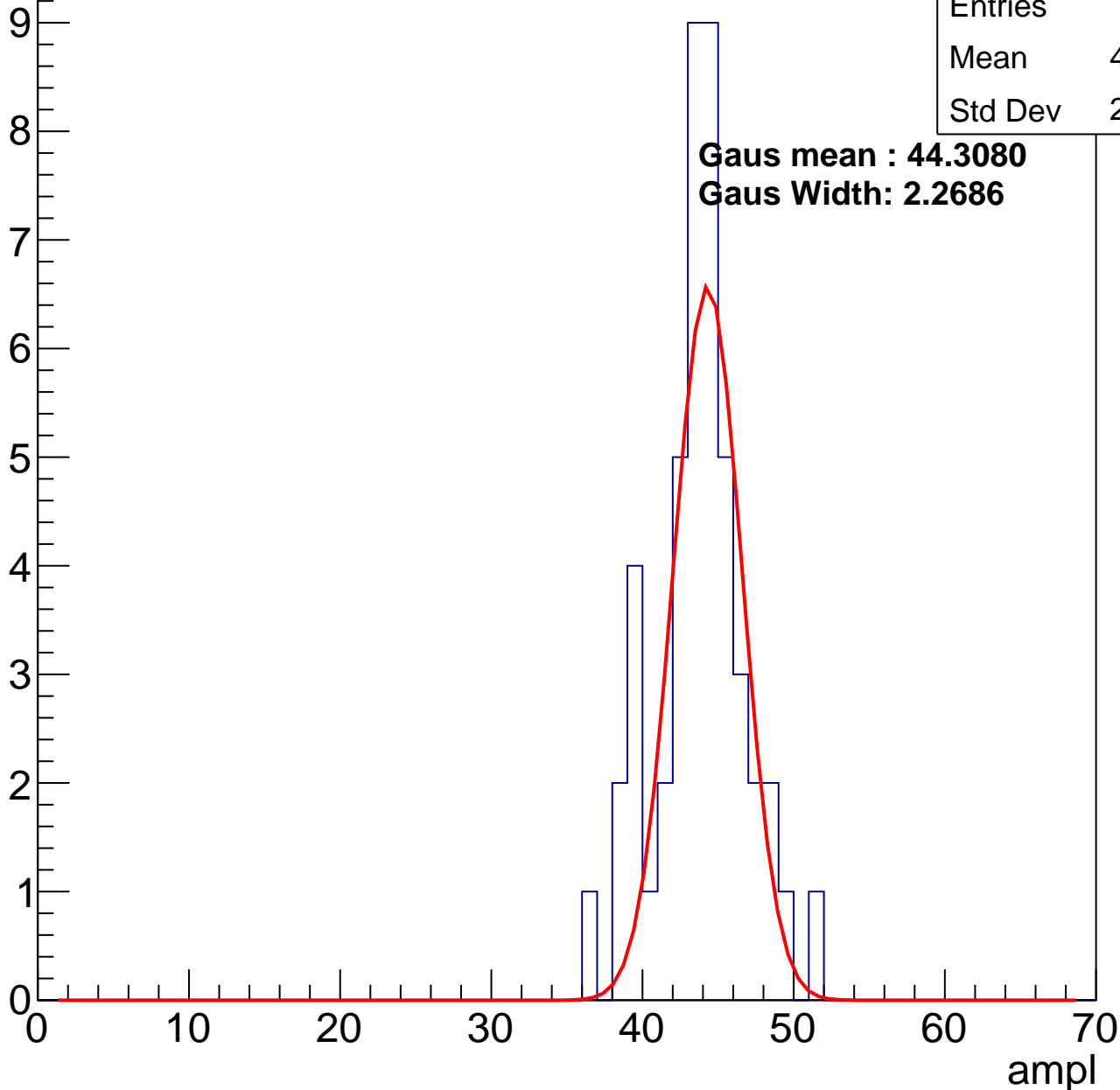
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	43.32
Std Dev	2.983

Gaus mean : 44.3080

Gaus Width: 2.2686



B0L001S, U11-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	50.16
Std Dev	3.32

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

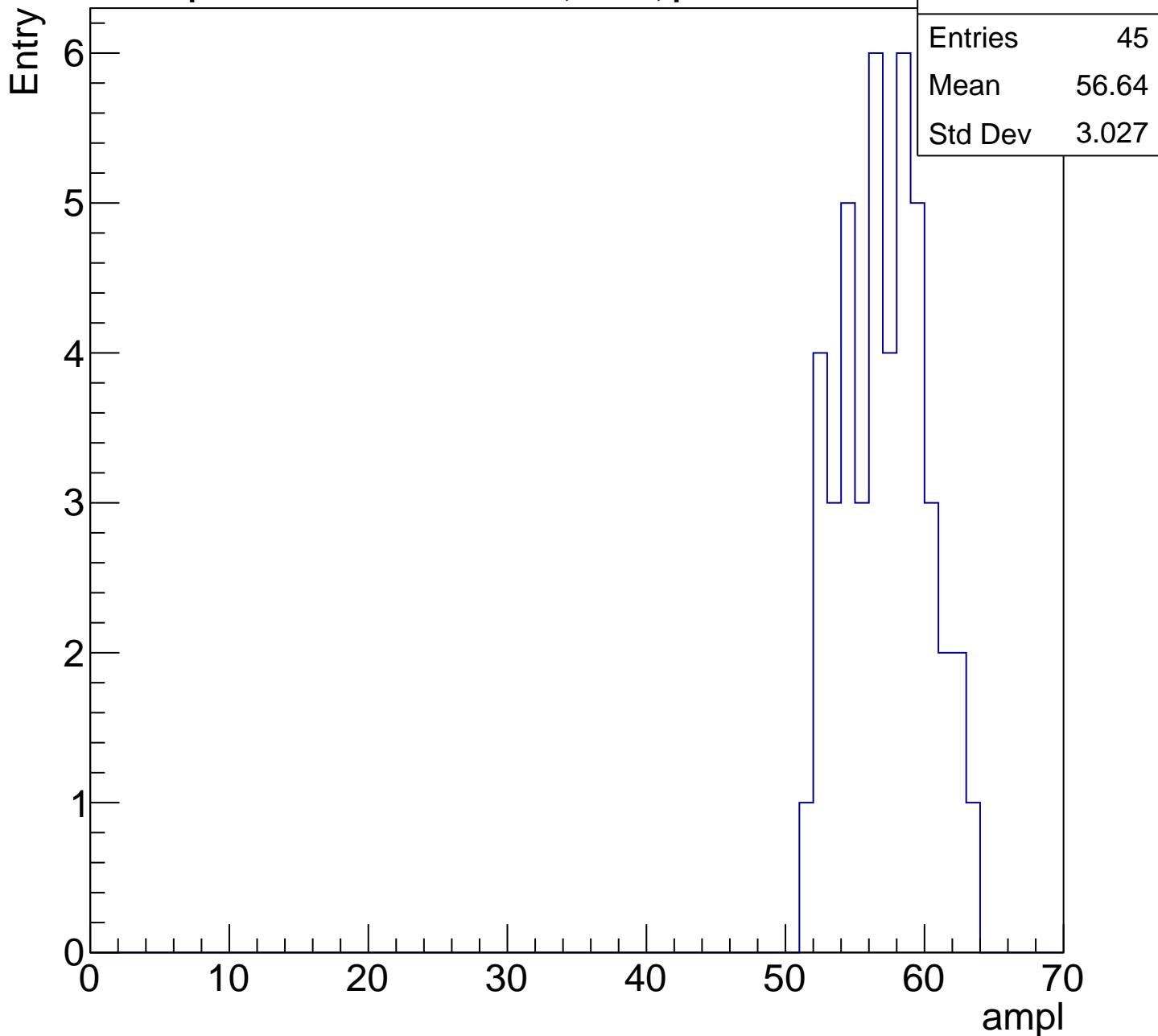
6

8

10

B0L001S, U11-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

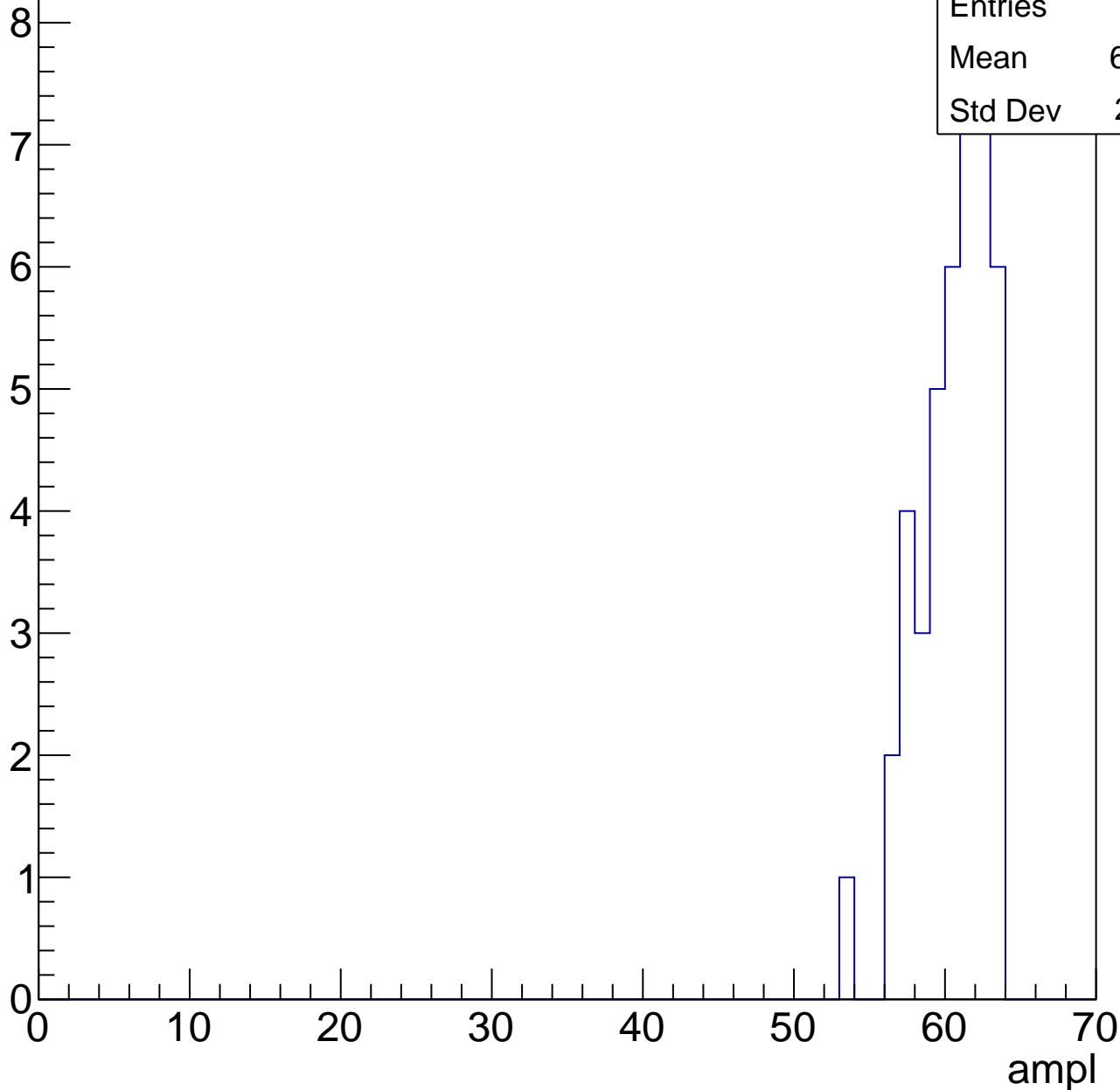


B0L001S, U11-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

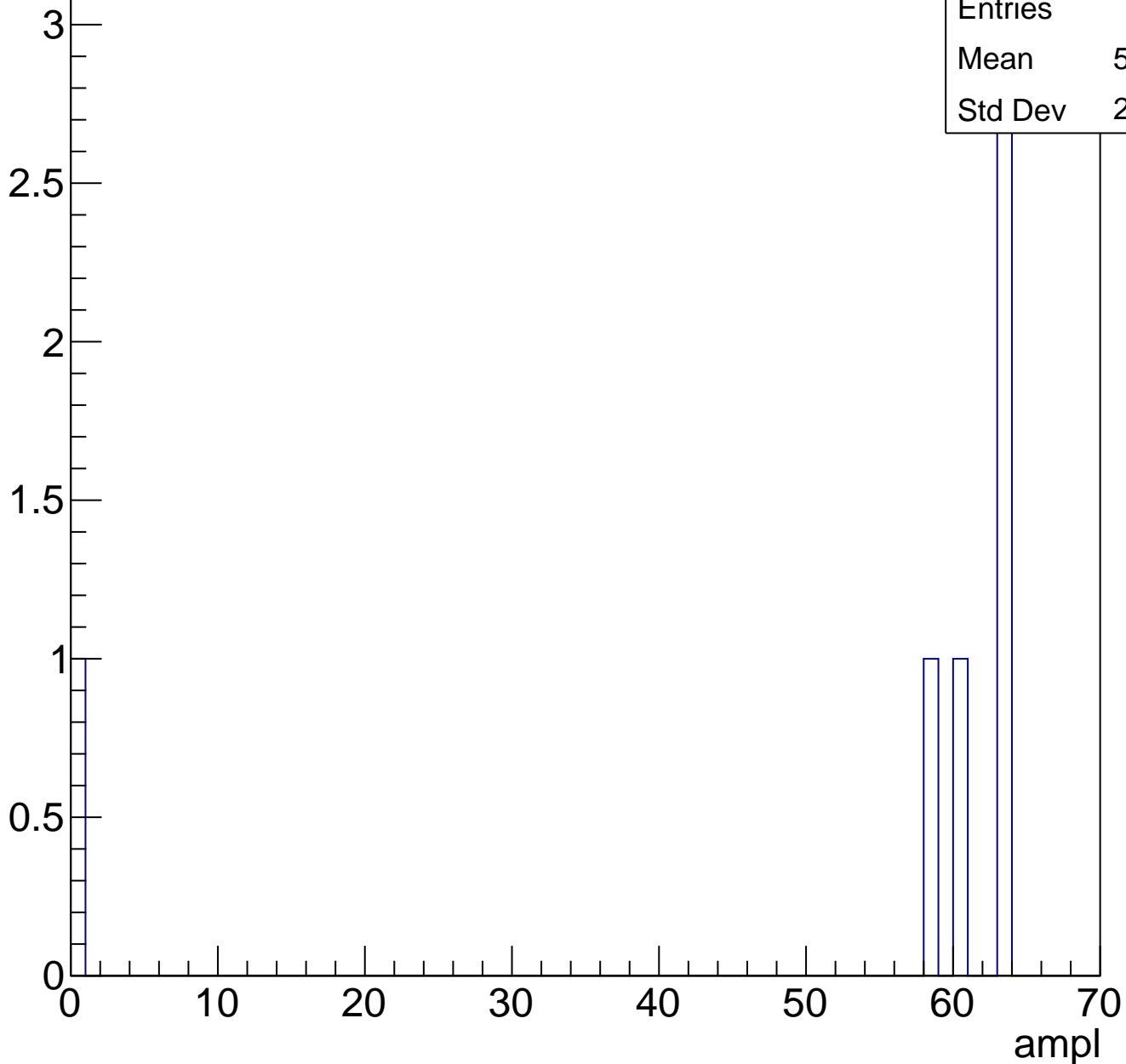
Entries	43
Mean	60.09
Std Dev	2.301



B0L001S, U11-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch59, adc0

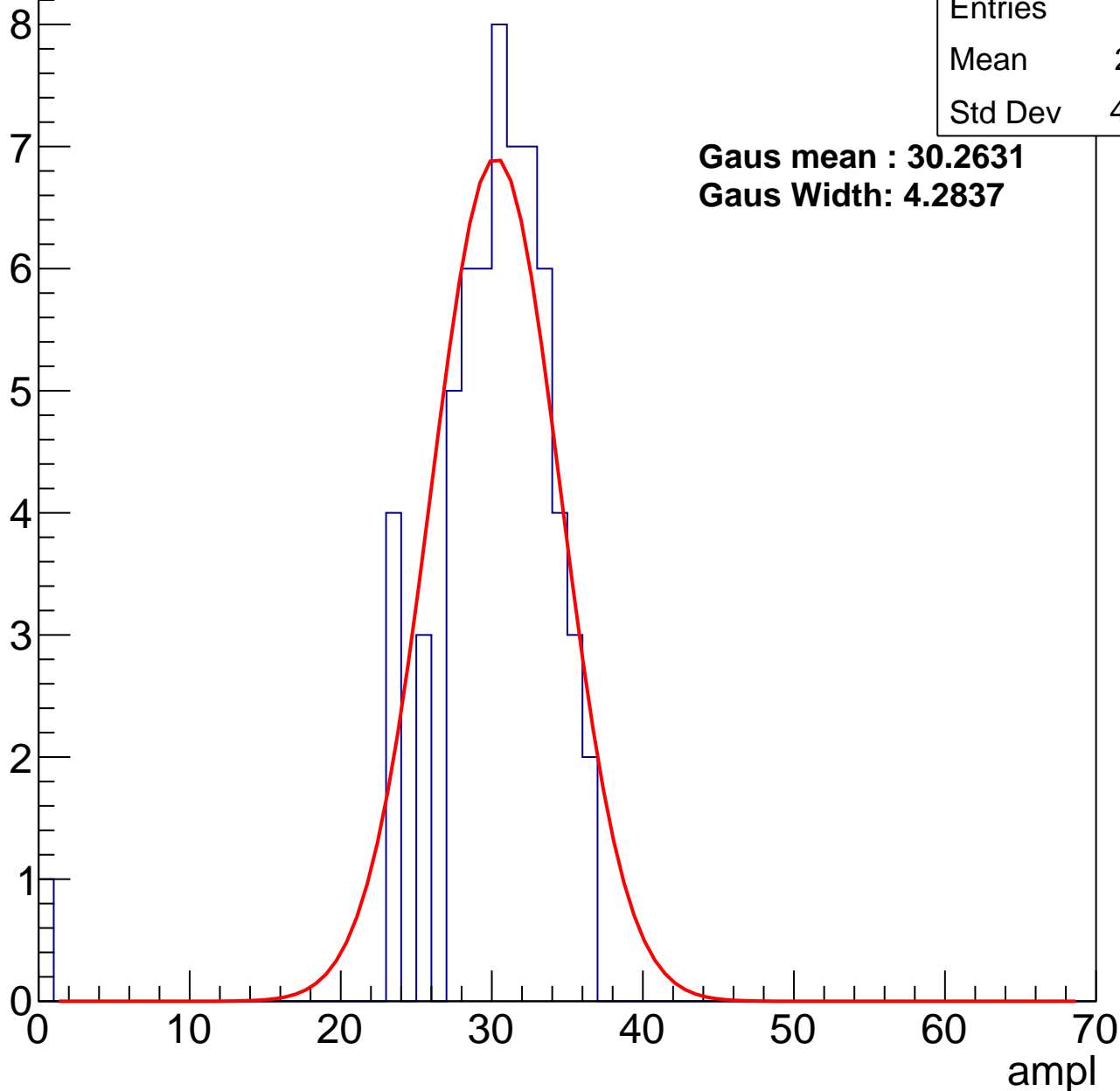
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.61
Std Dev	4.979

Gaus mean : 30.2631

Gaus Width: 4.2837



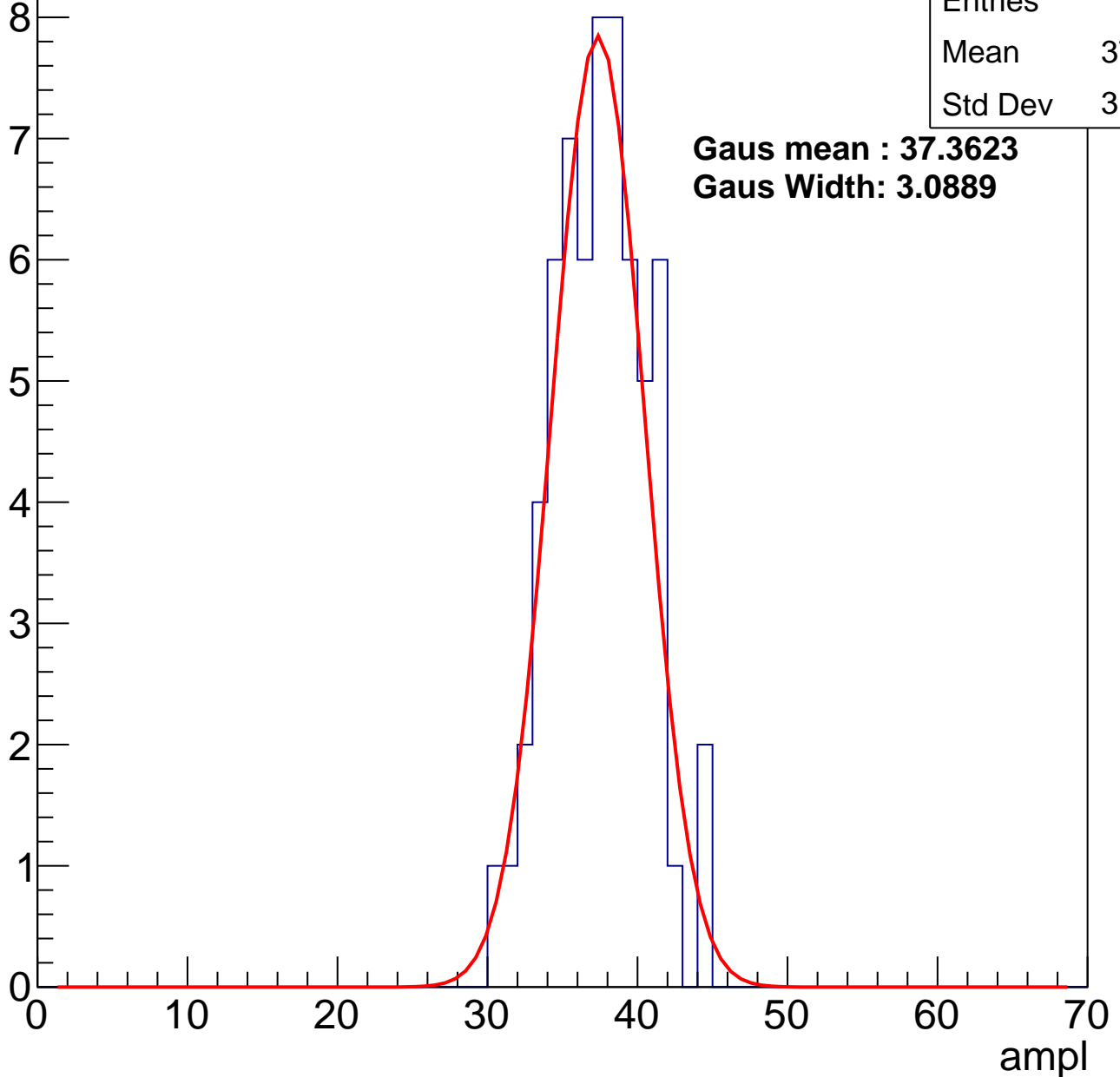
B0L001S, U11-ch59, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.02
Std Dev	3.042

Gaus mean : 37.3623
Gaus Width: 3.0889



B0L001S, U11-ch59, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	44.12
Std Dev	3.941

Gaus mean : 43.9512

Gaus Width: 3.8015

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

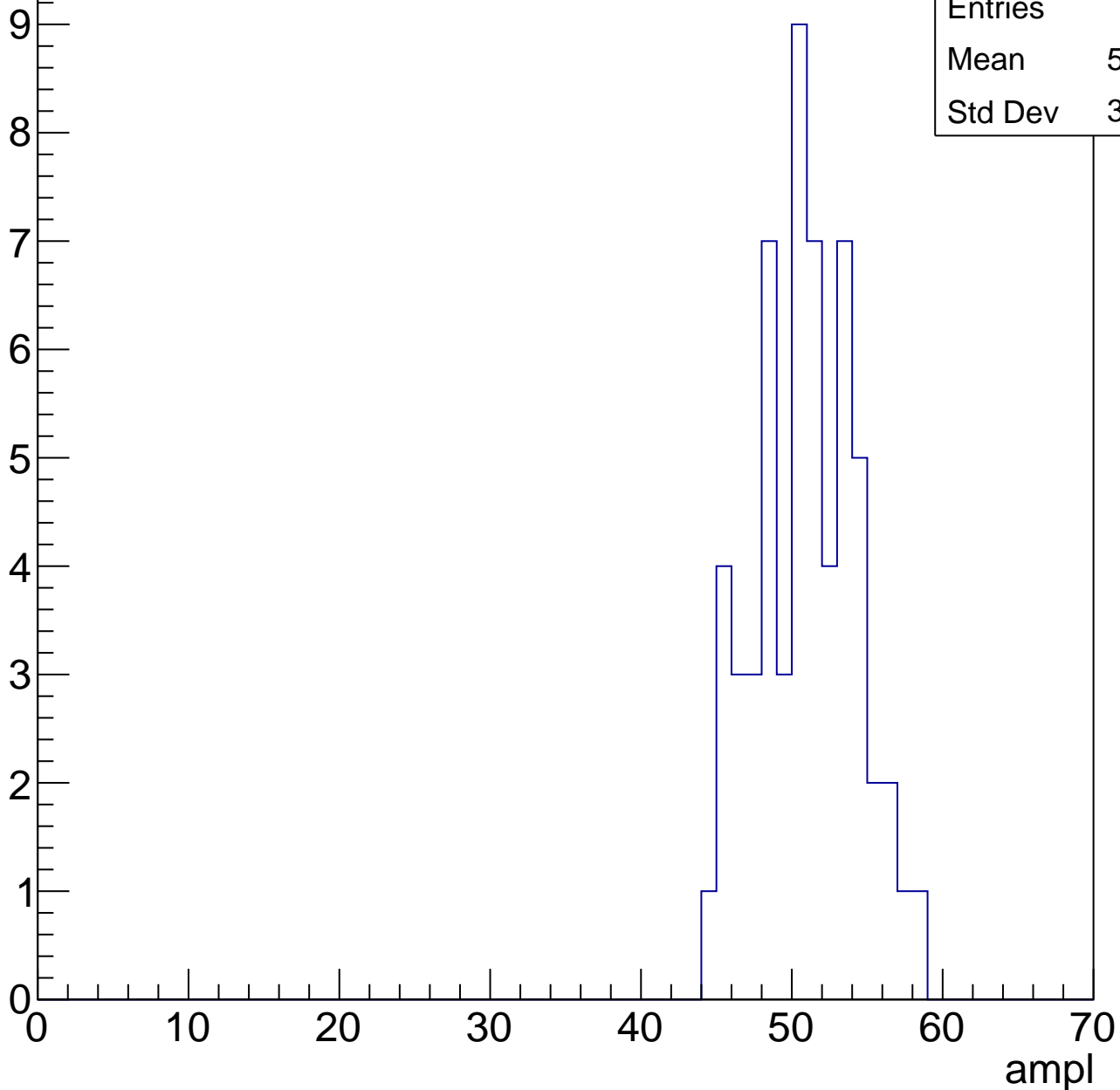


B0L001S, U11-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.49
Std Dev	3.264

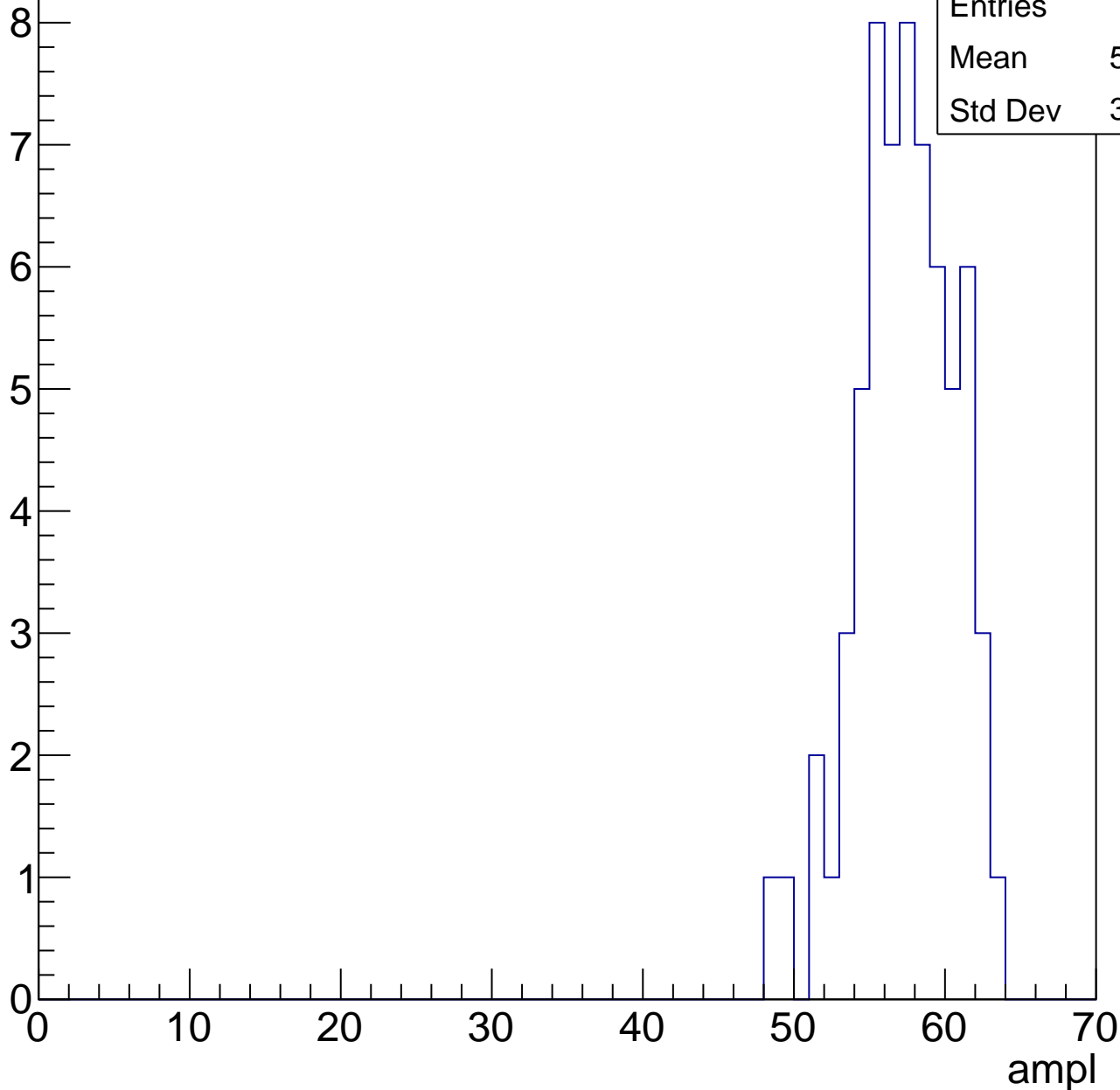


B0L001S, U11-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.92
Std Dev	3.203

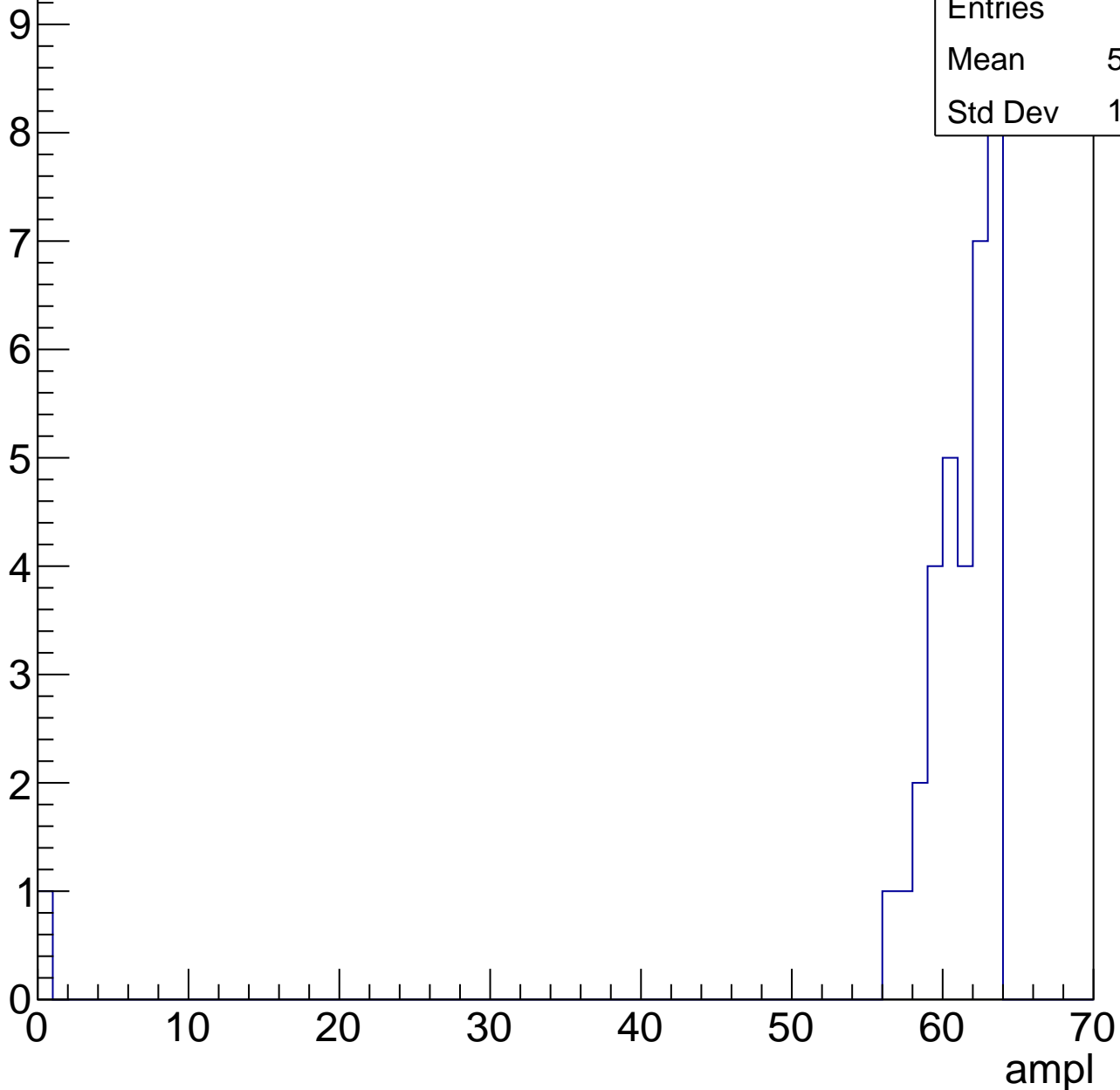


B0L001S, U11-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	59.12
Std Dev	10.46



B0L001S, U11-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch60, adc0

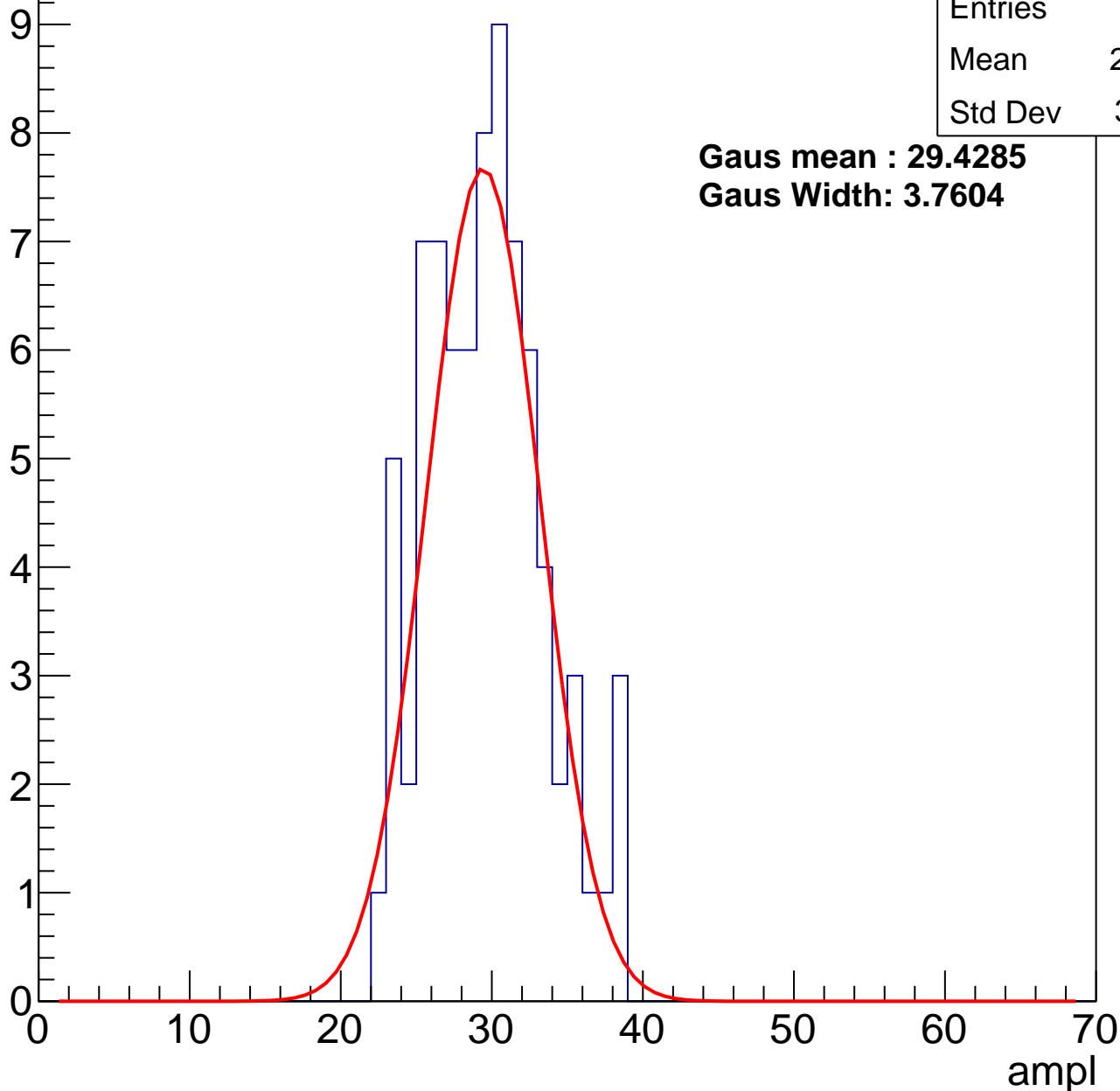
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	29.17
Std Dev	3.841

Gaus mean : 29.4285

Gaus Width: 3.7604



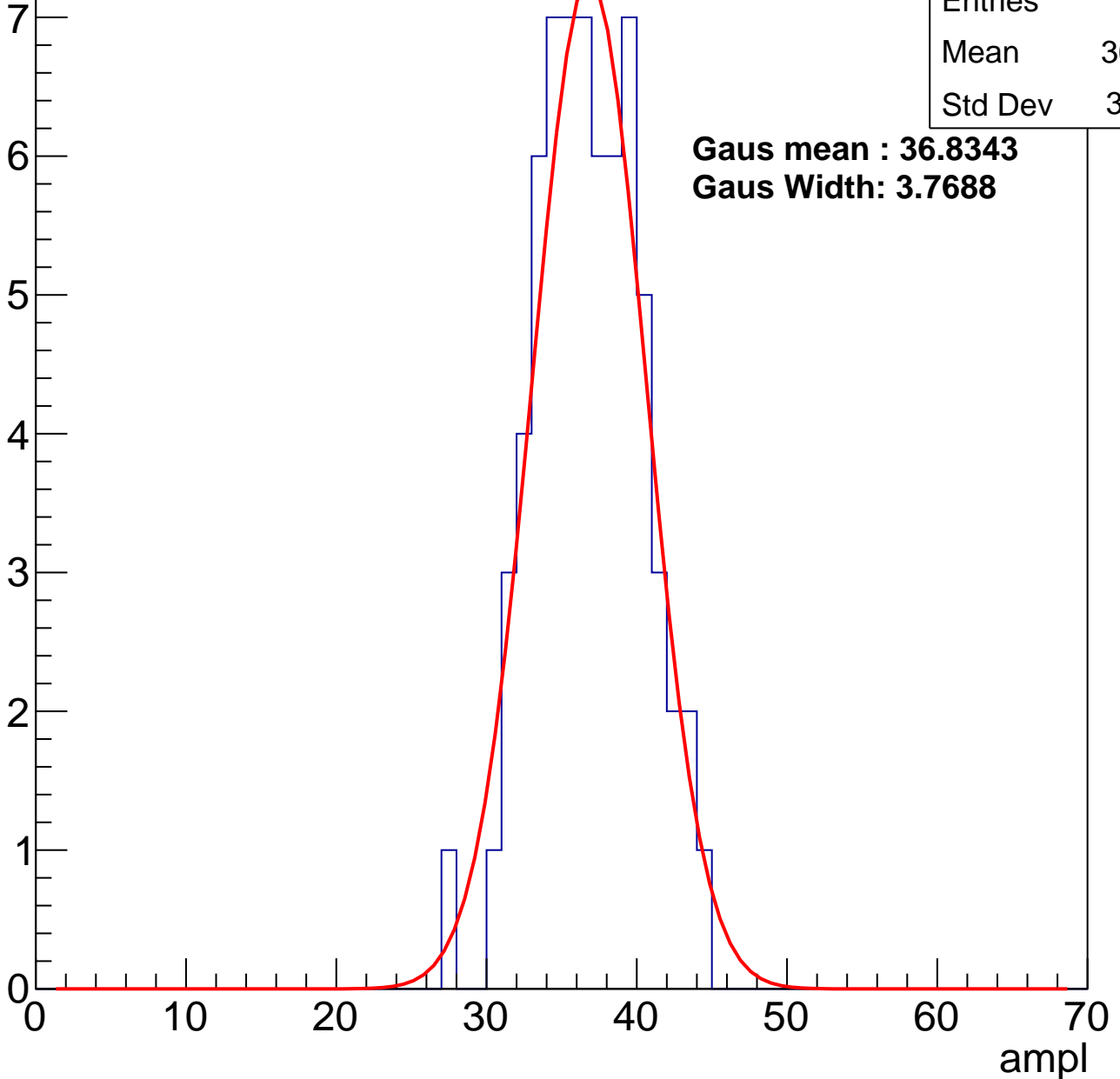
B0L001S, U11-ch60, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.34
Std Dev	3.471

Gaus mean : 36.8343
Gaus Width: 3.7688



B0L001S, U11-ch60, adc2

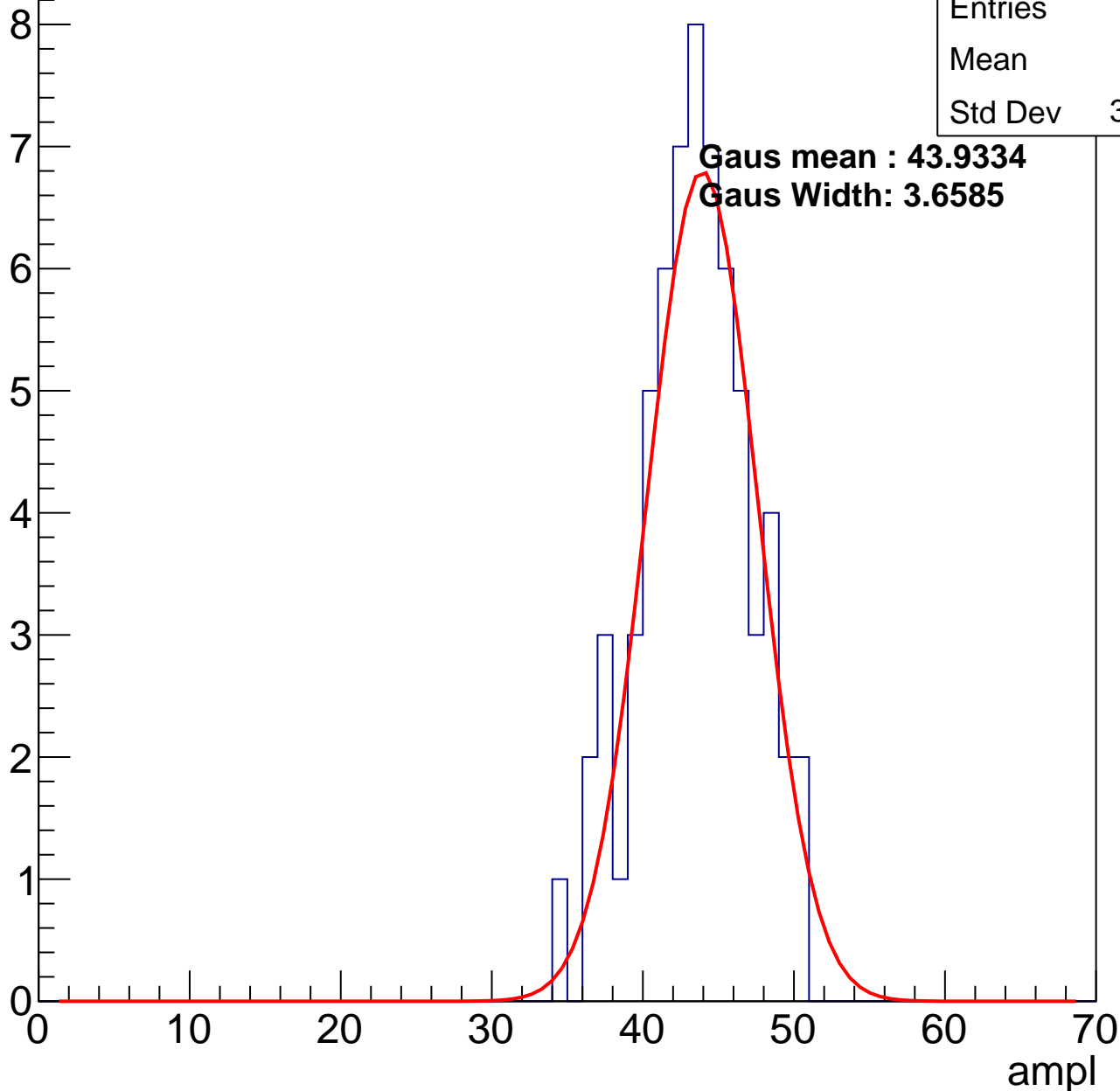
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43
Std Dev	3.573

Gaus mean : 43.9334

Gaus Width: 3.6585

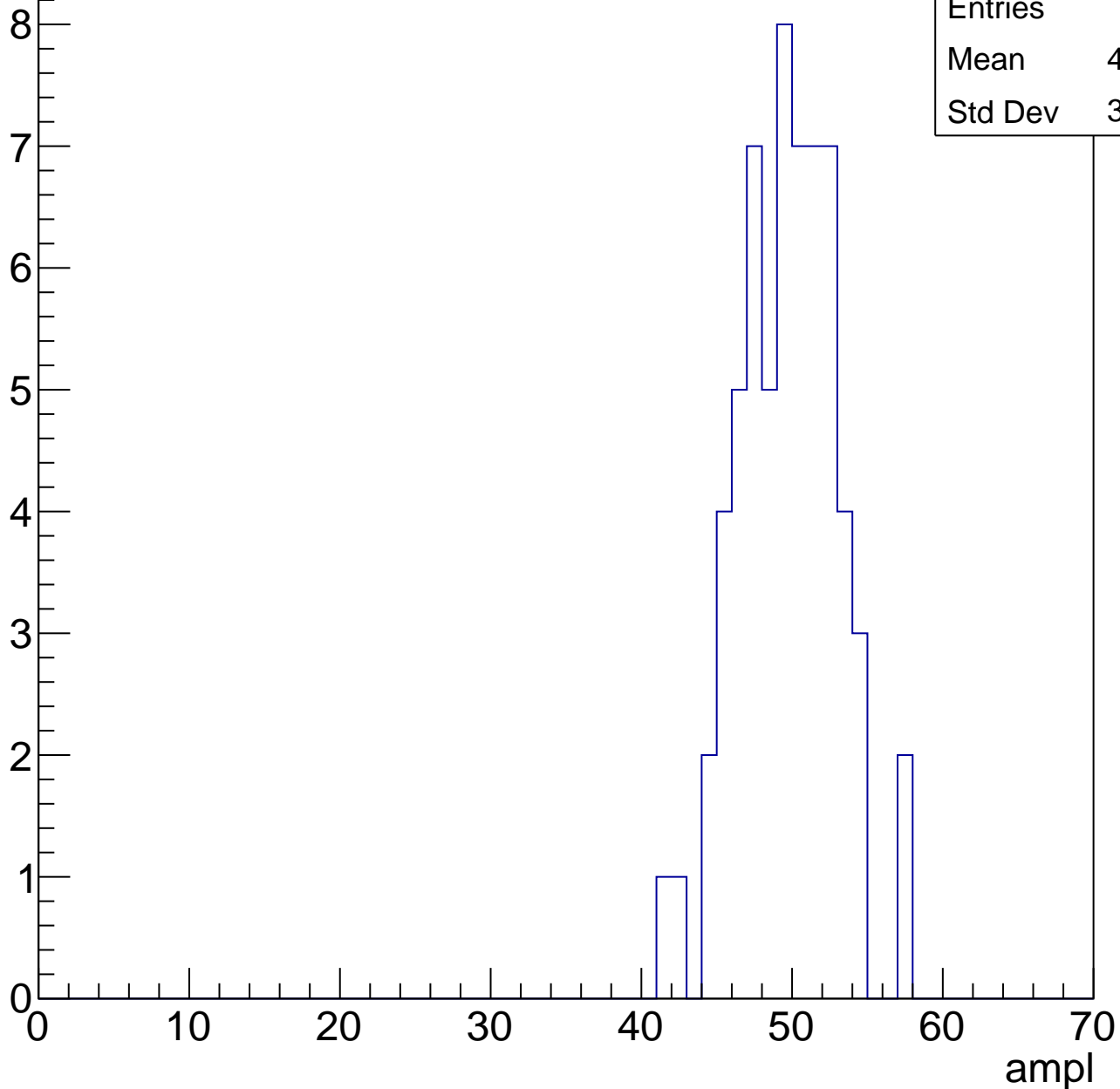


B0L001S, U11-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	49.22
Std Dev	3.254

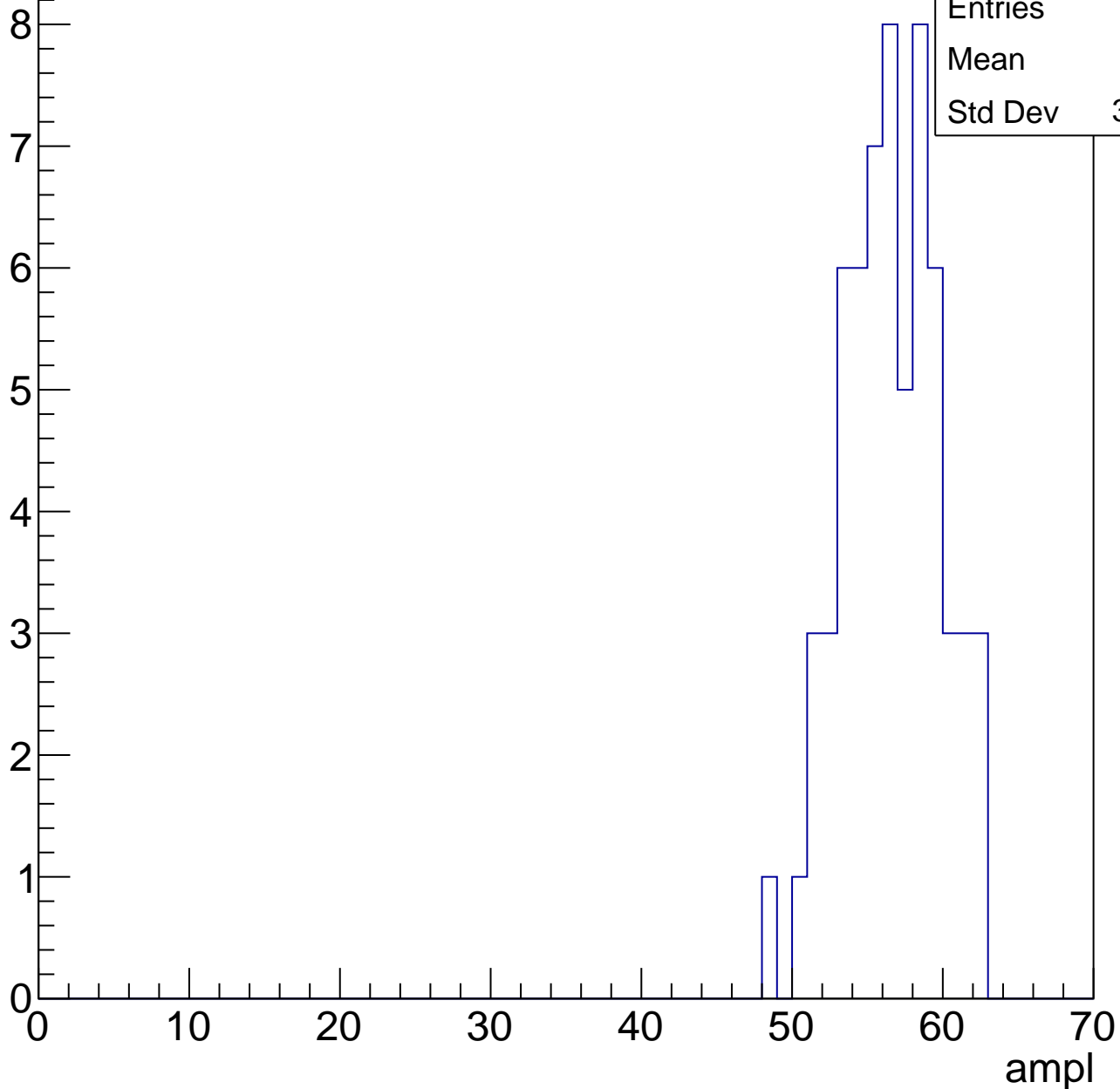


B0L001S, U11-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	56.1
Std Dev	3.161

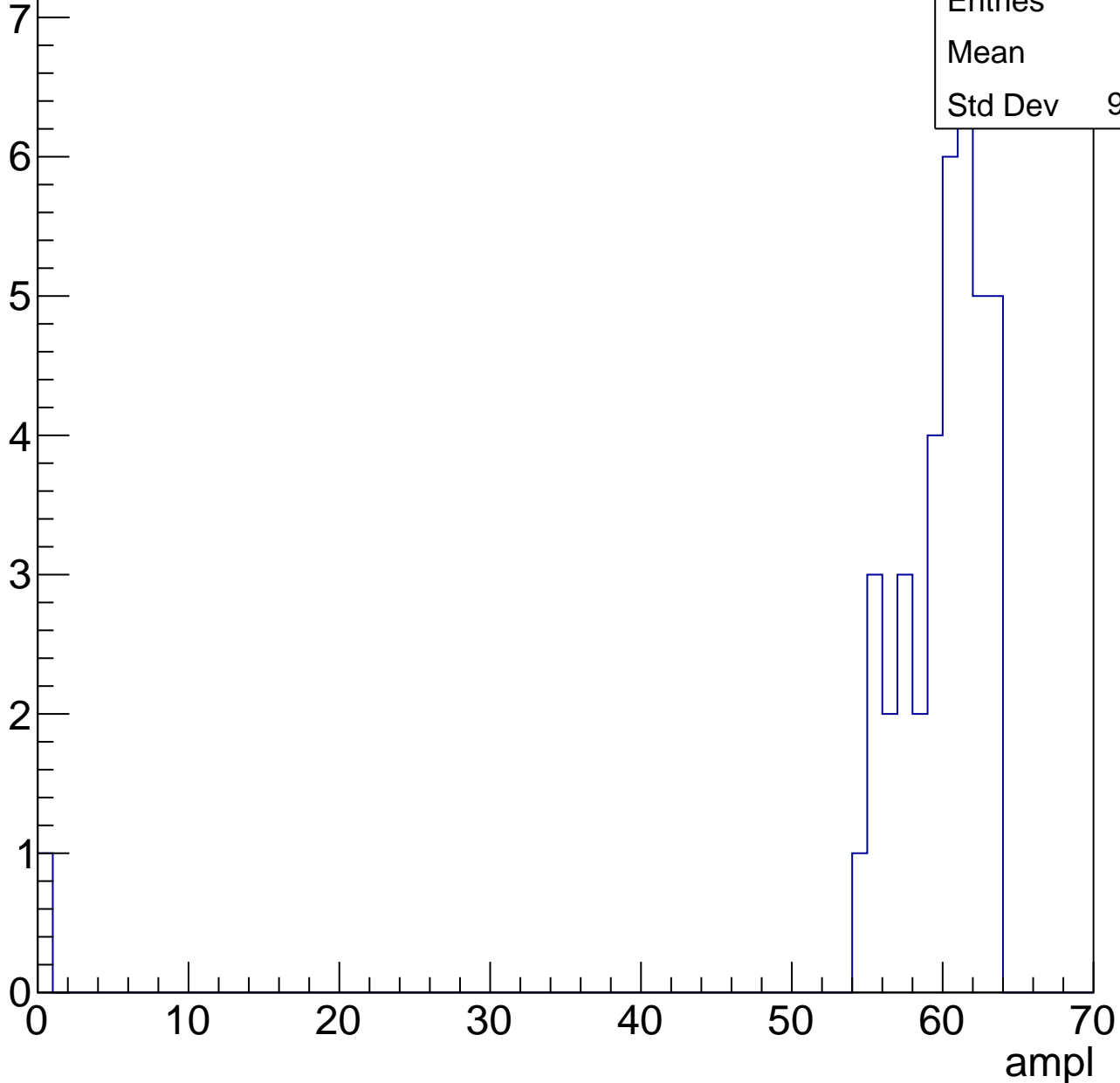


B0L001S, U11-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.1
Std Dev	9.758

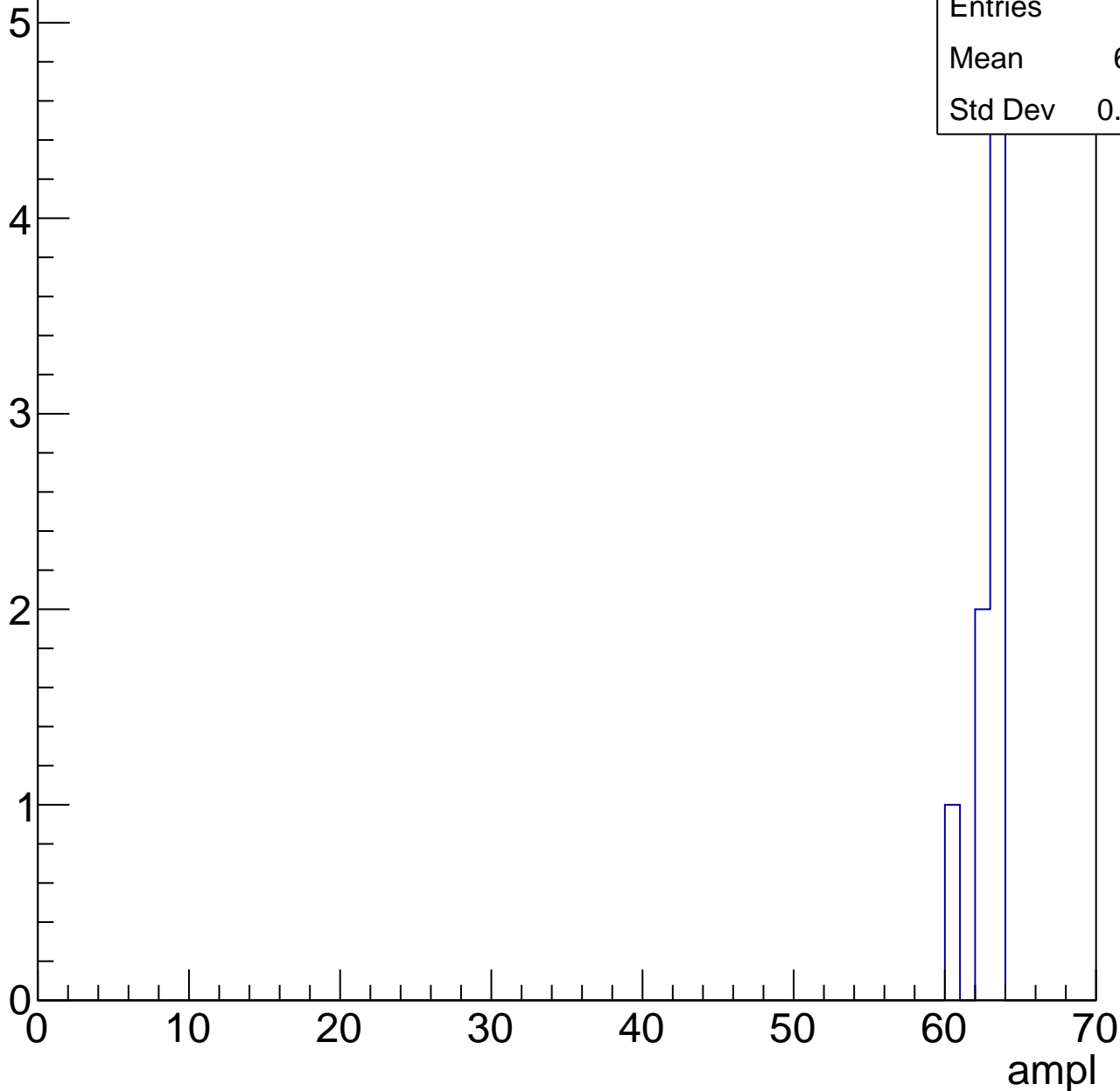


B0L001S, U11-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B0L001S, U11-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch61, adc0

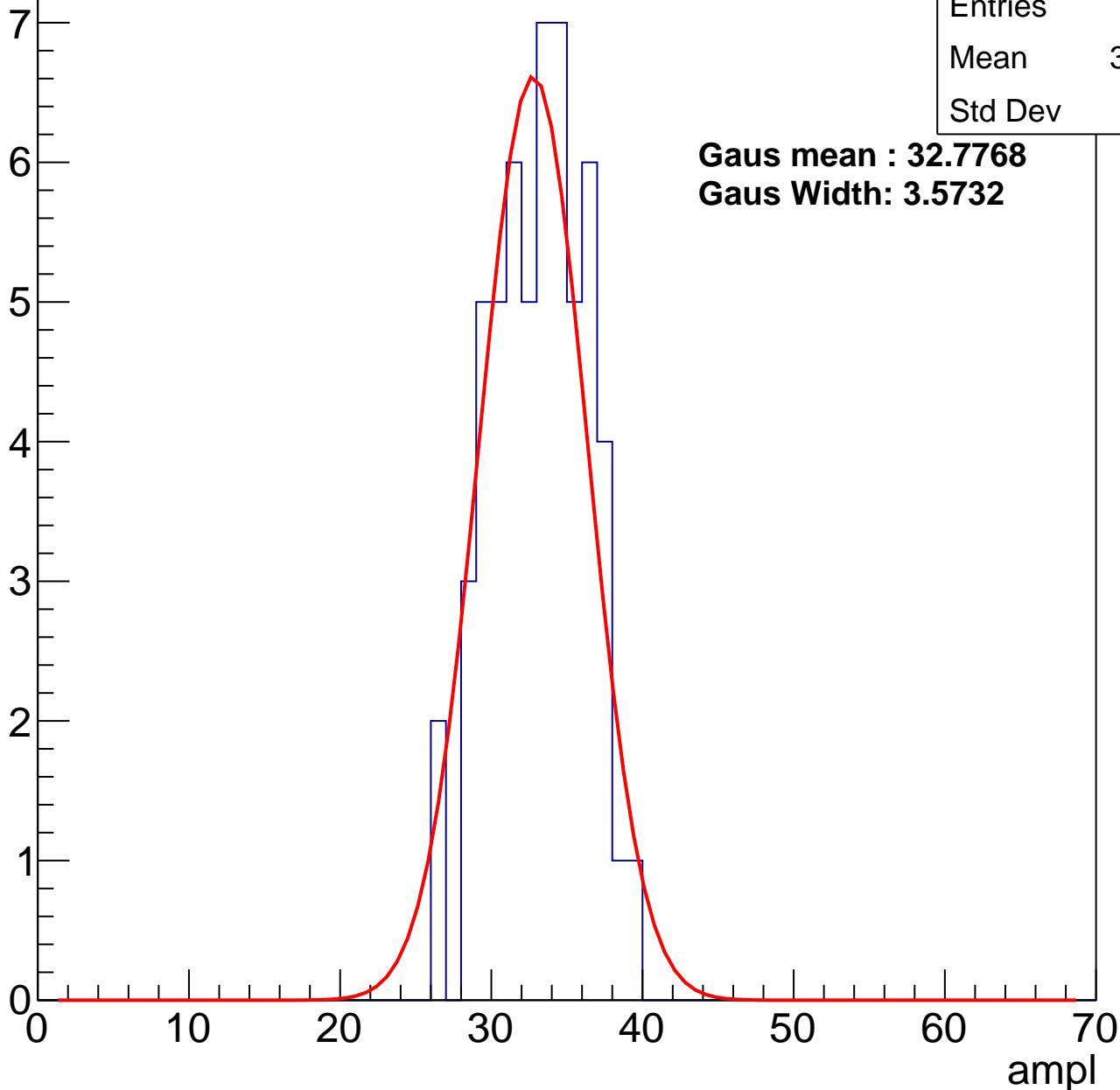
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	32.67
Std Dev	3.04

Gaus mean : 32.7768

Gaus Width: 3.5732



B0L001S, U11-ch61, adc1

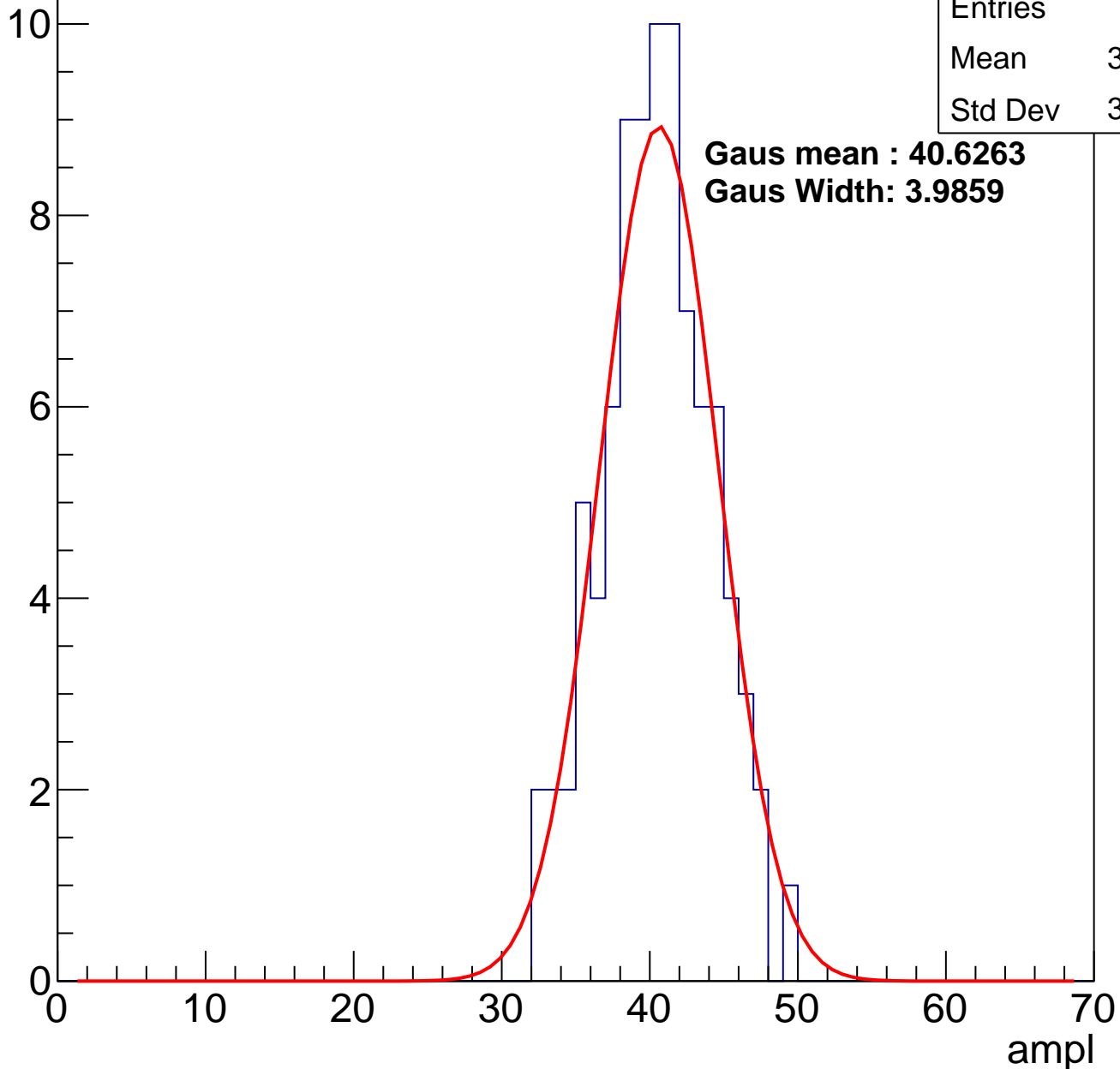
calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	39.99
Std Dev	3.632

Gaus mean : 40.6263

Gaus Width: 3.9859

Entry



B0L001S, U11-ch61, adc2

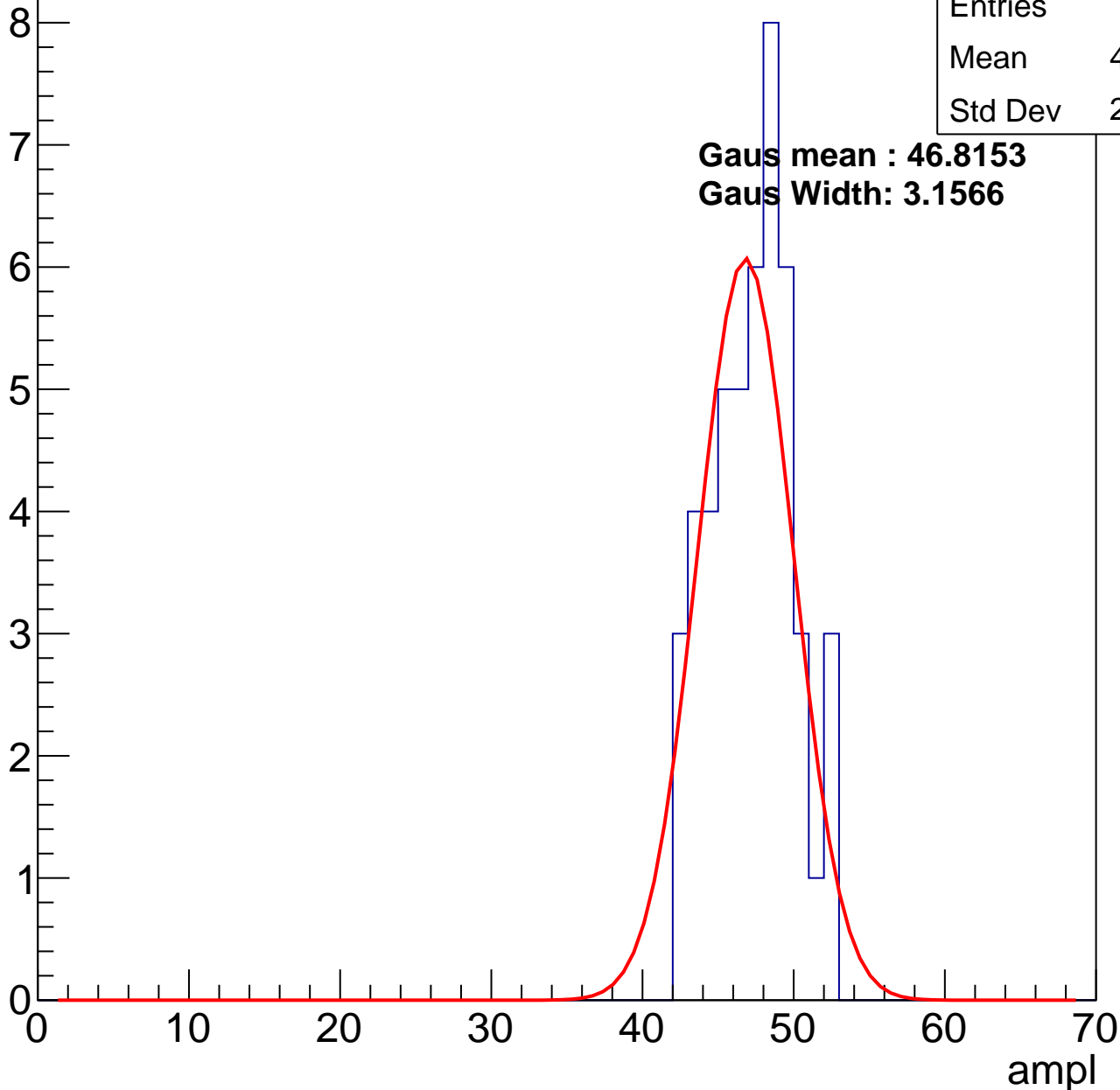
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	46.79
Std Dev	2.692

Gaus mean : 46.8153

Gaus Width: 3.1566

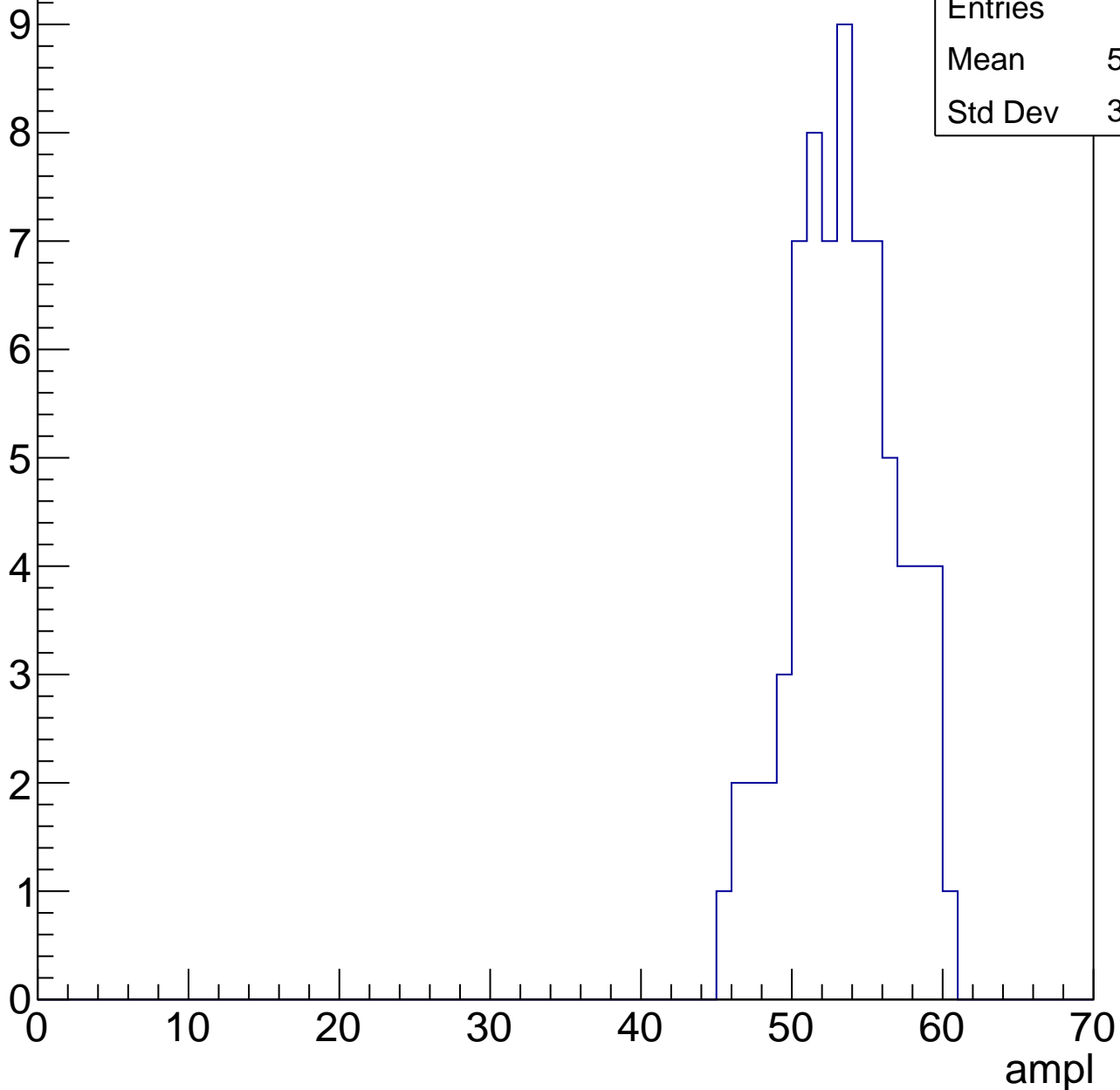


B0L001S, U11-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	53.04
Std Dev	3.454

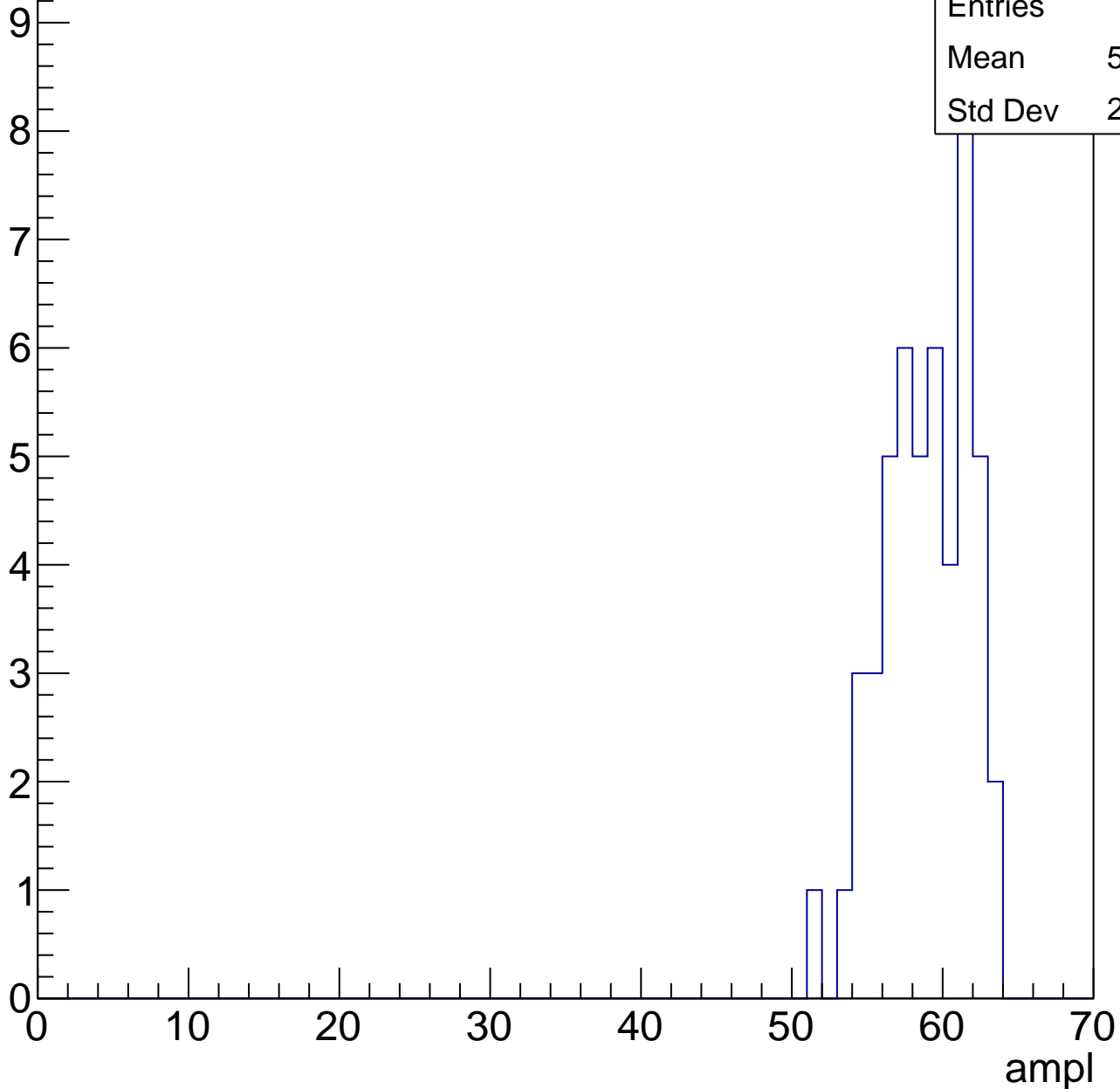


B0L001S, U11-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

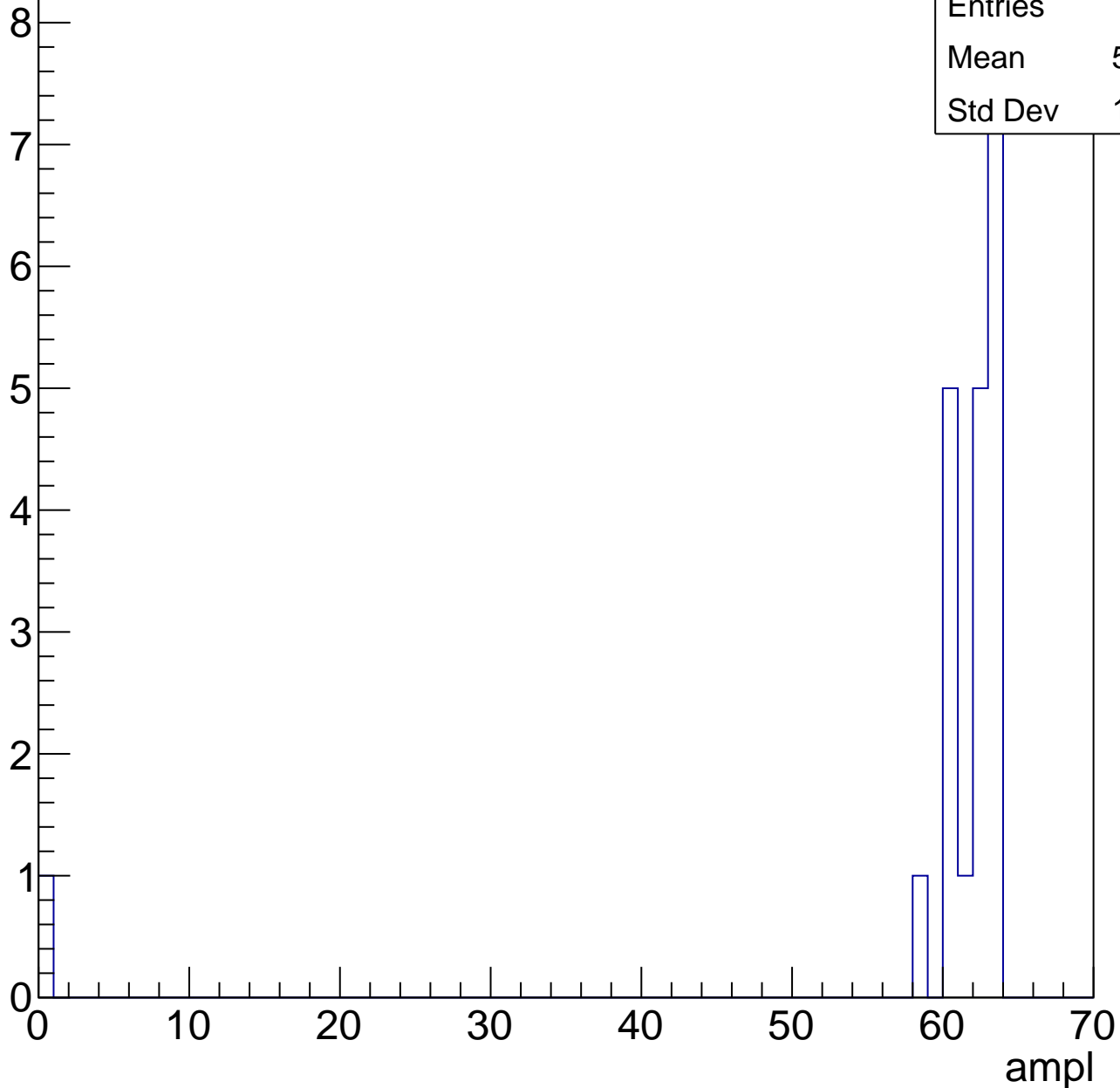
Entries	50
Mean	58.44
Std Dev	2.822



B0L001S, U11-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch62, adc0

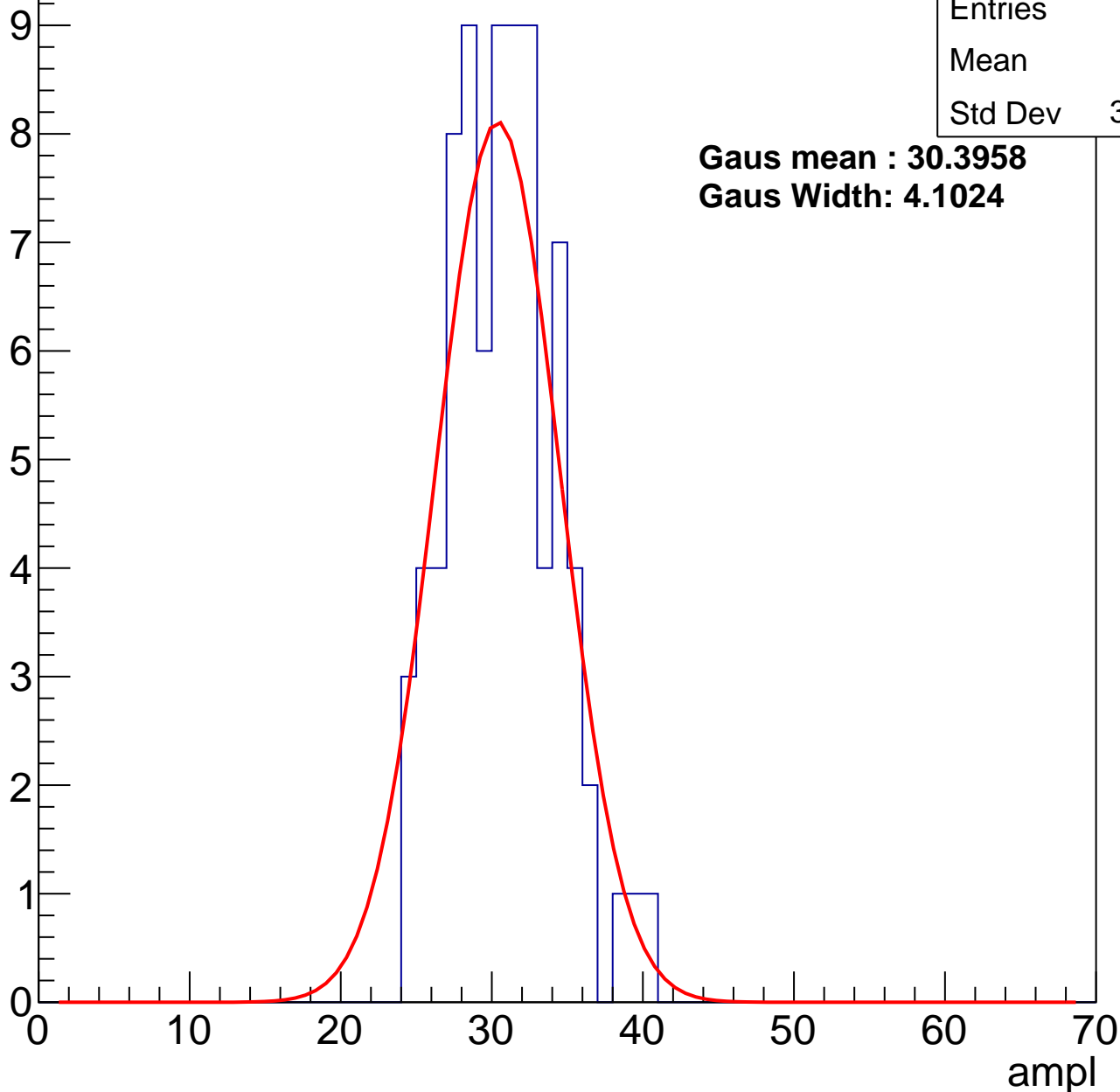
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.3
Std Dev	3.494

Gaus mean : 30.3958

Gaus Width: 4.1024



B0L001S, U11-ch62, adc1

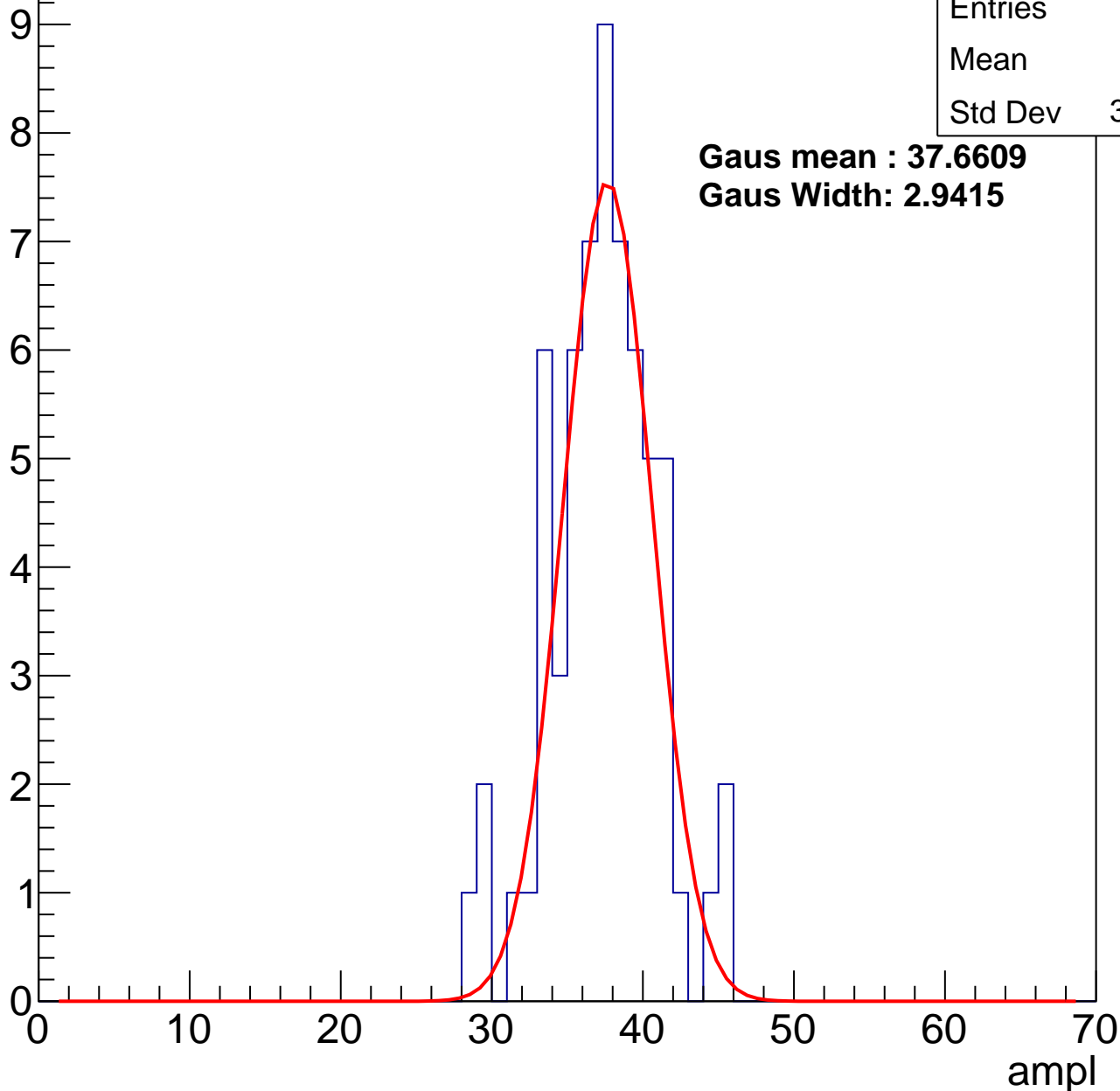
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	36.9
Std Dev	3.522

Gaus mean : 37.6609

Gaus Width: 2.9415



B0L001S, U11-ch62, adc2

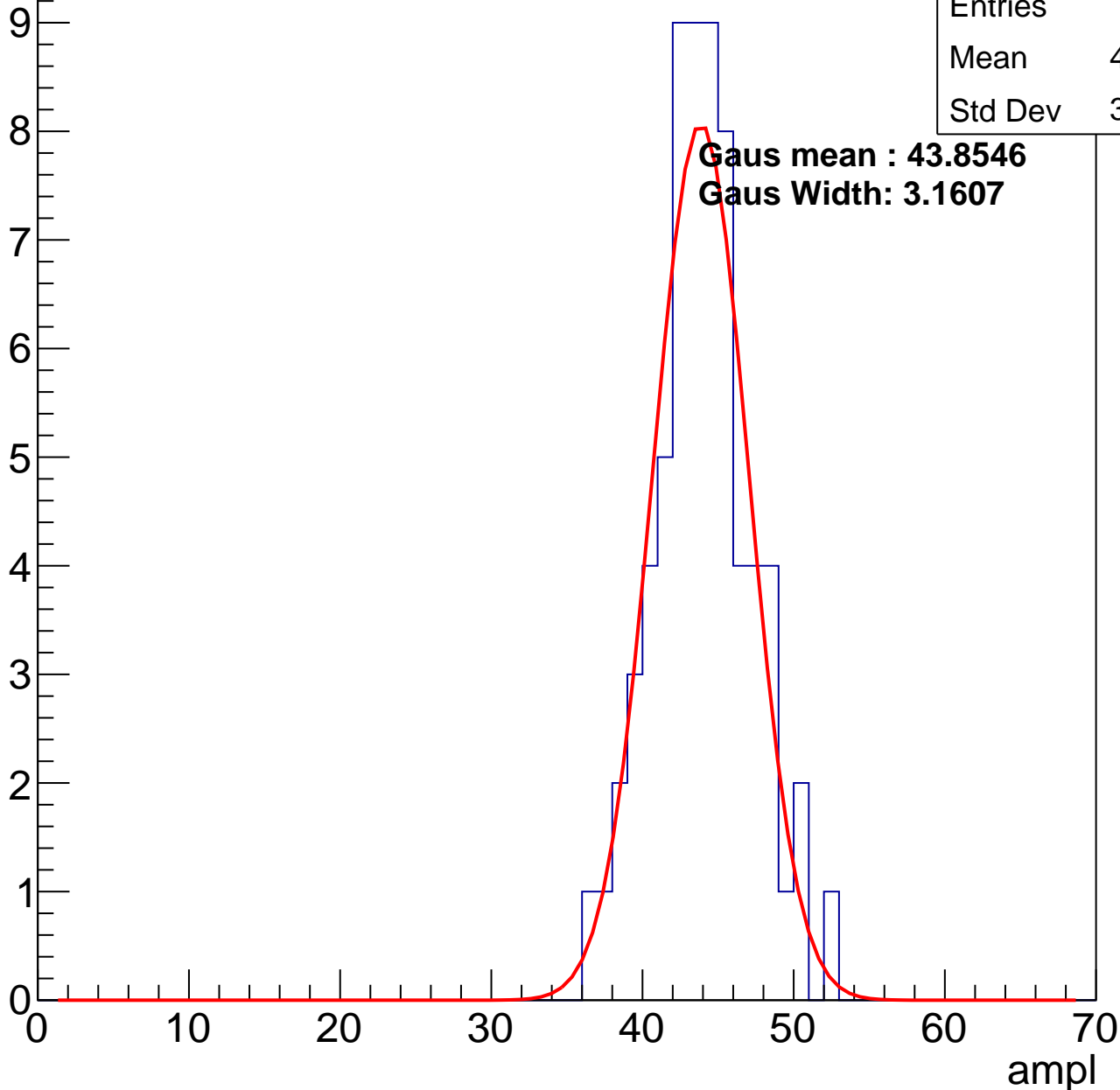
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.54
Std Dev	3.197

Gaus mean : 43.8546

Gaus Width: 3.1607

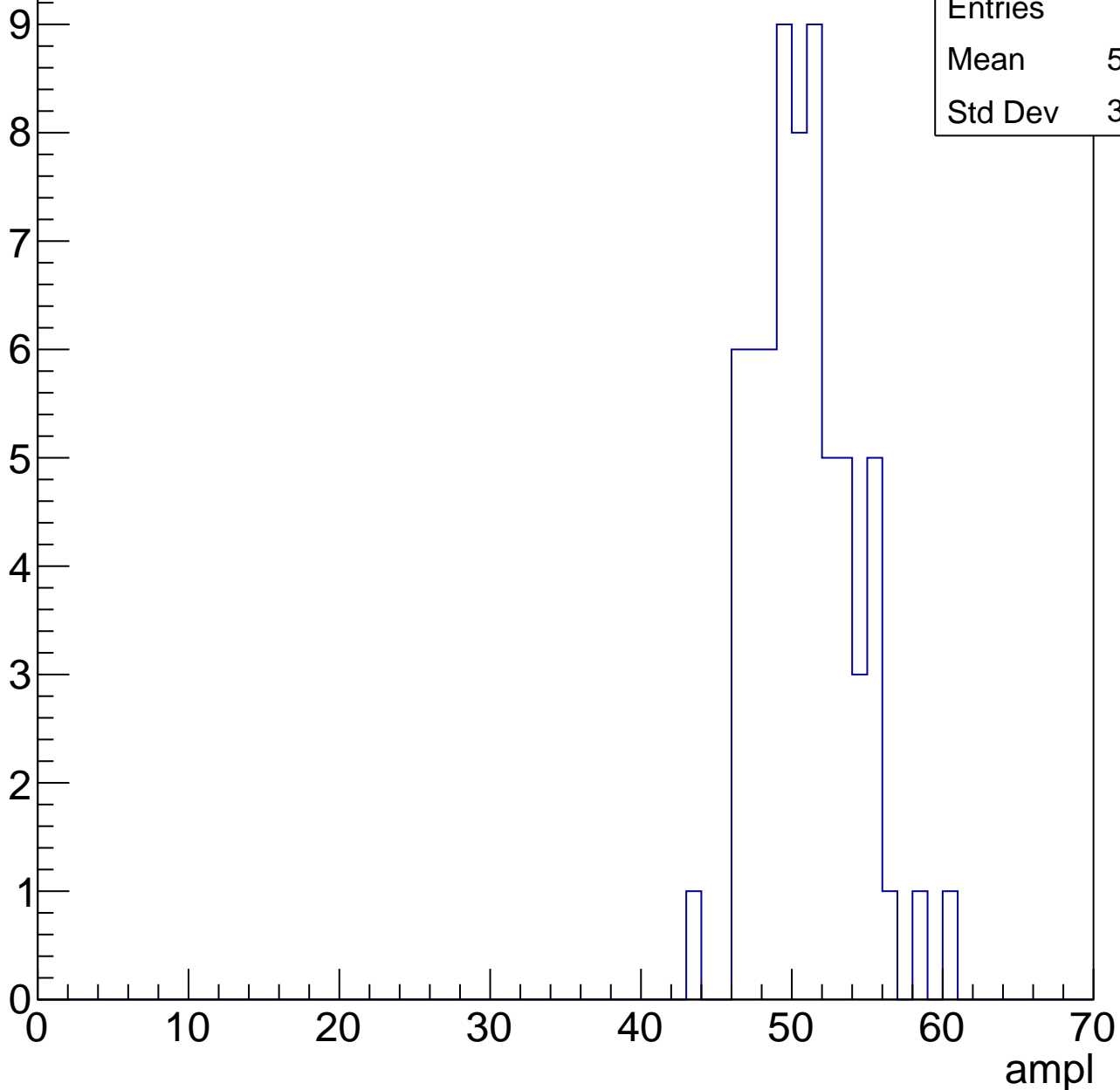


B0L001S, U11-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.38
Std Dev	3.185

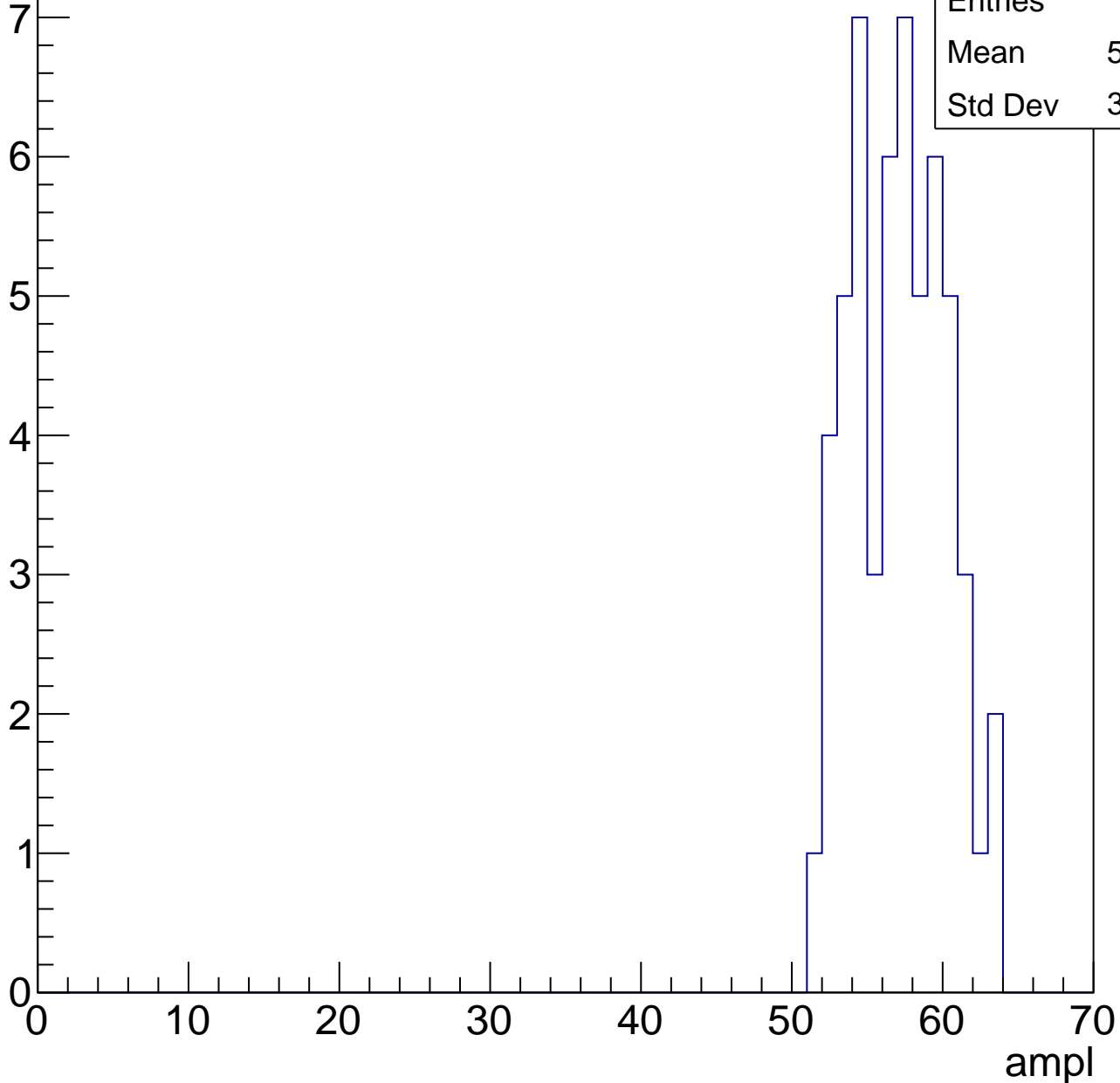


B0L001S, U11-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.67
Std Dev	3.045



B0L001S, U11-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.1
Std Dev	2.448

ampl

0

10

20

30

40

50

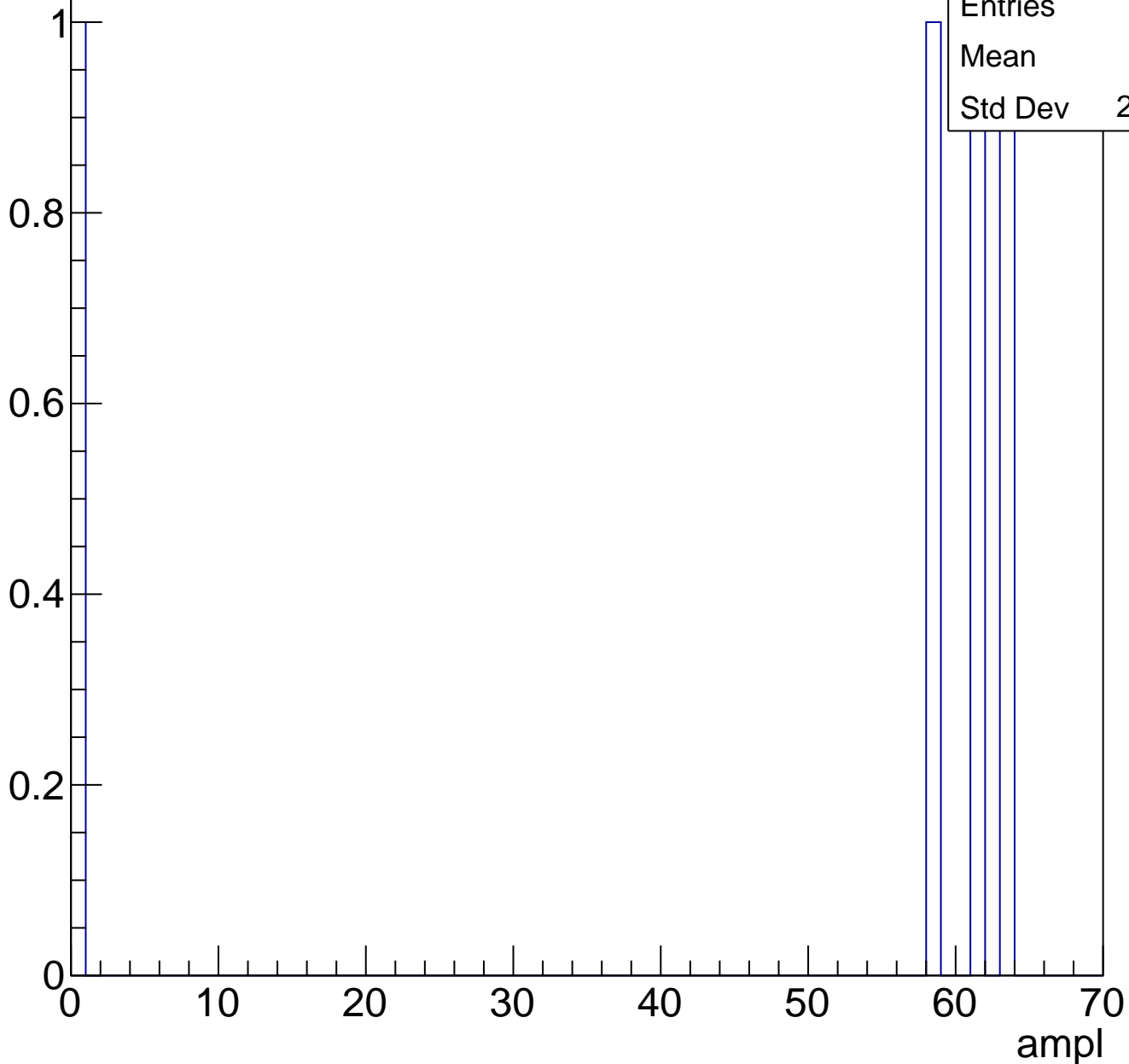
60

70

B0L001S, U11-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U11-ch63, adc0

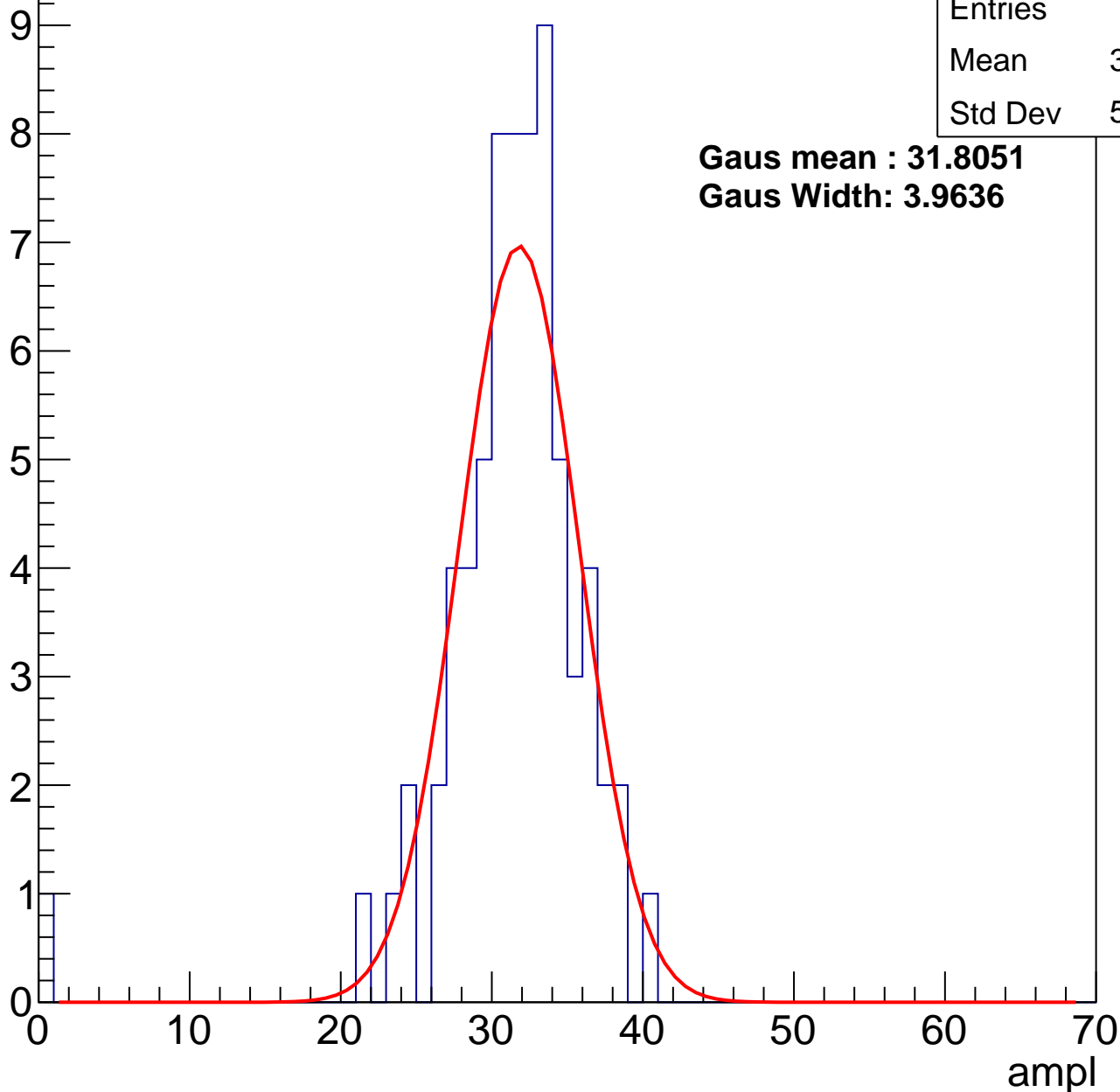
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.84
Std Dev	5.192

Gaus mean : 31.8051

Gaus Width: 3.9636



B0L001S, U11-ch63, adc1

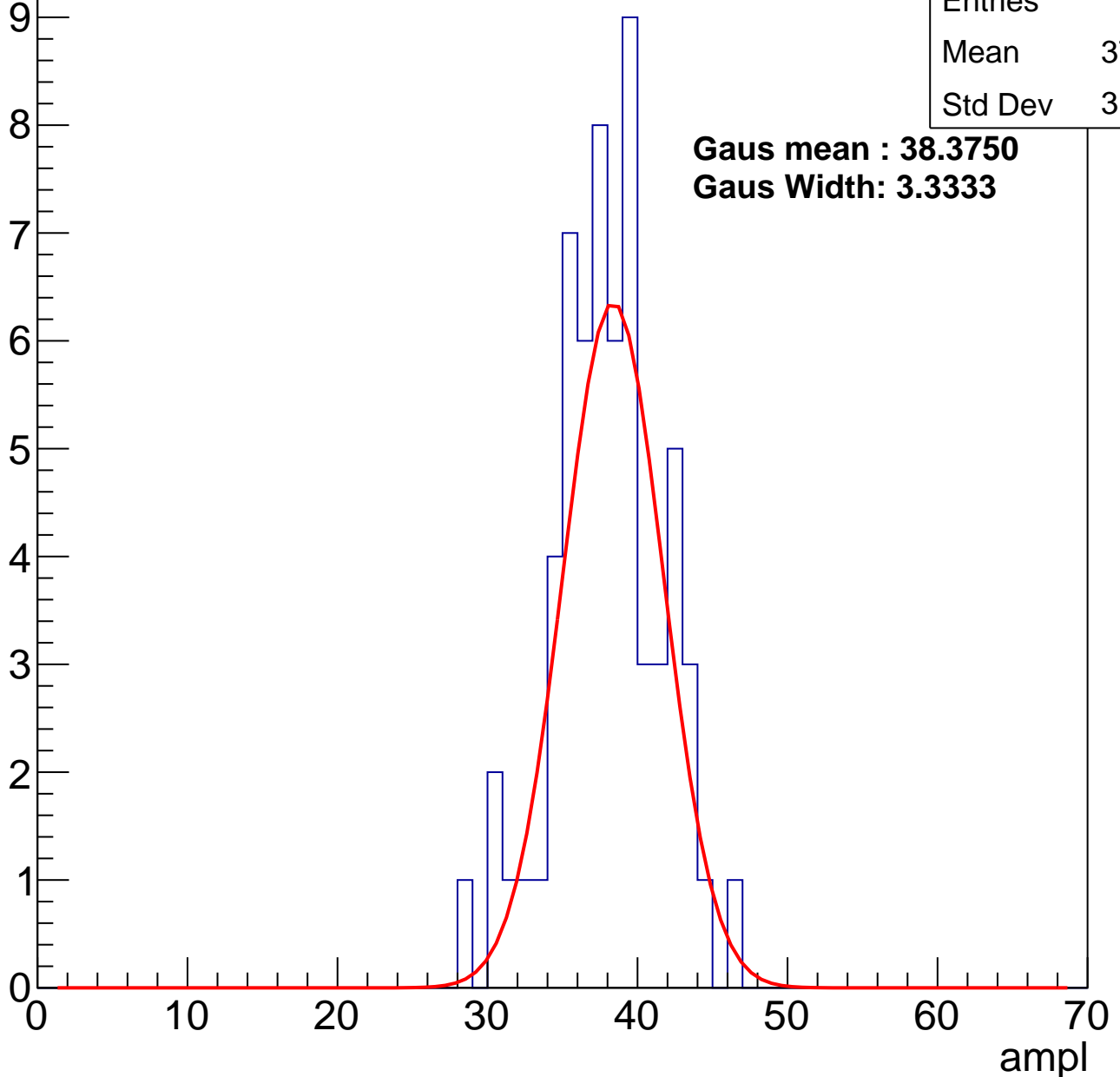
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.55
Std Dev	3.564

Gaus mean : 38.3750

Gaus Width: 3.3333



B0L001S, U11-ch63, adc2

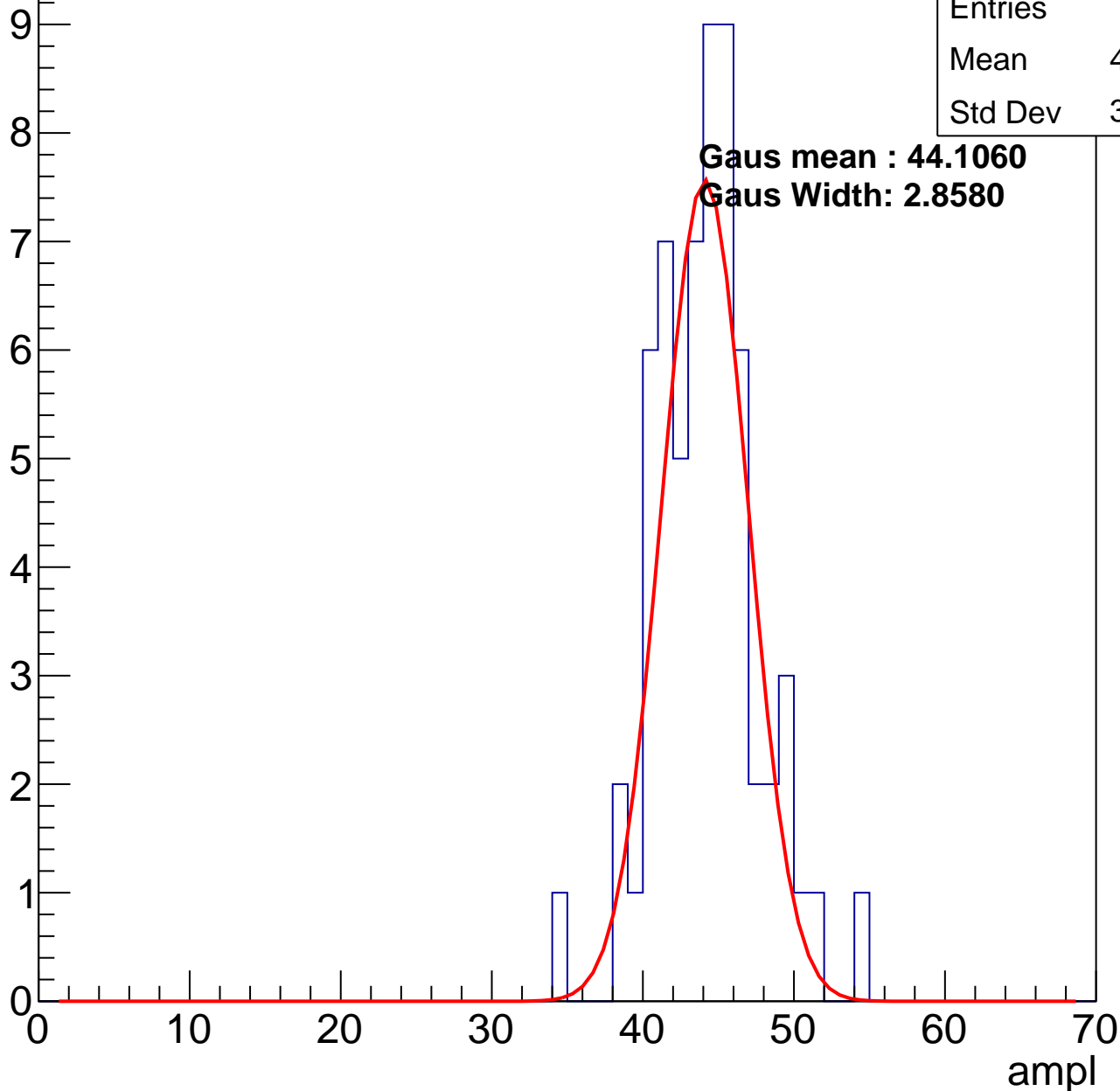
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.75
Std Dev	3.385

Gaus mean : 44.1060

Gaus Width: 2.8580



B0L001S, U11-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	51.34
Std Dev	3.37

Entry

10

8

6

4

2

0

0

10

20

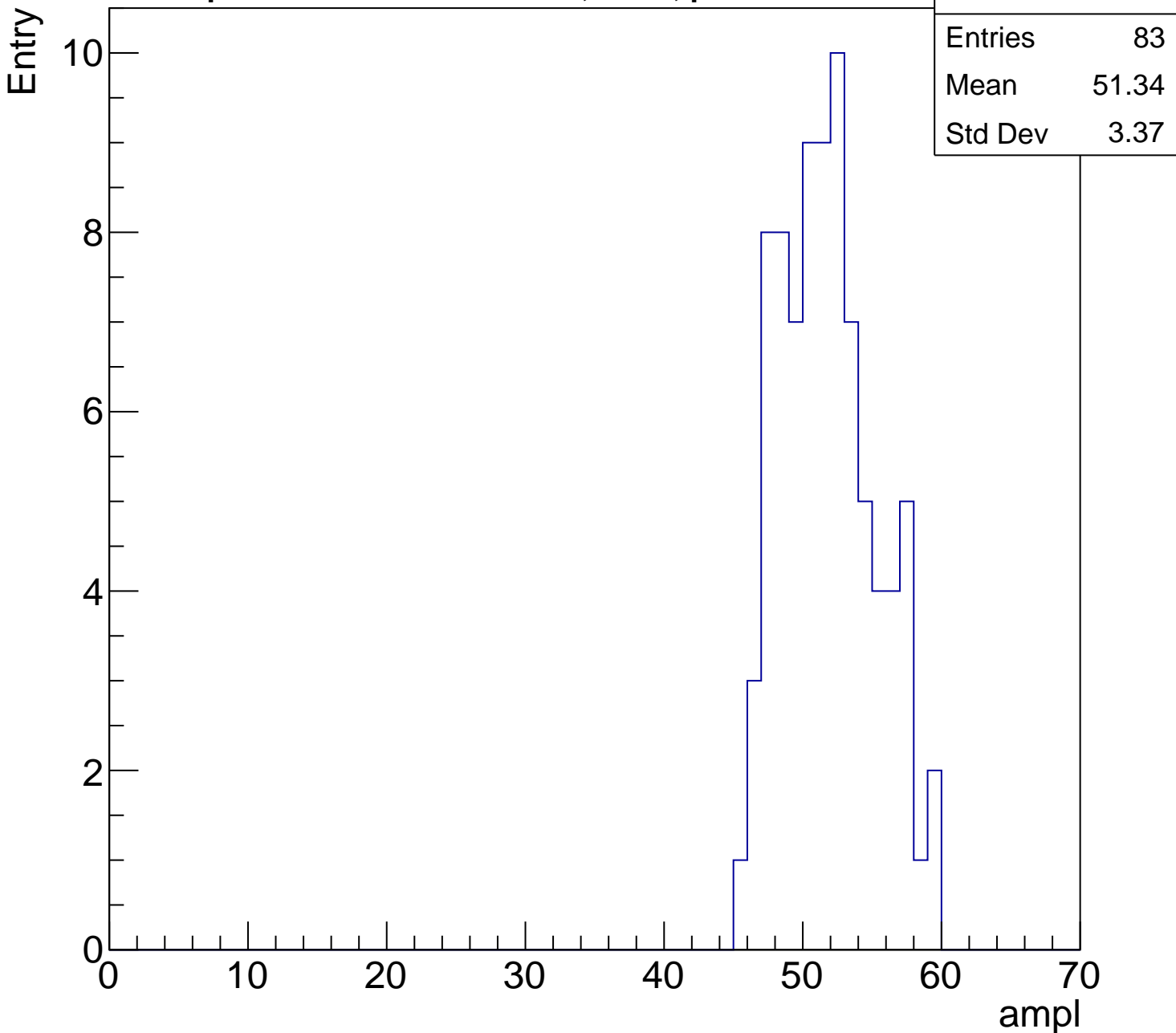
30

40

50

60

ampl

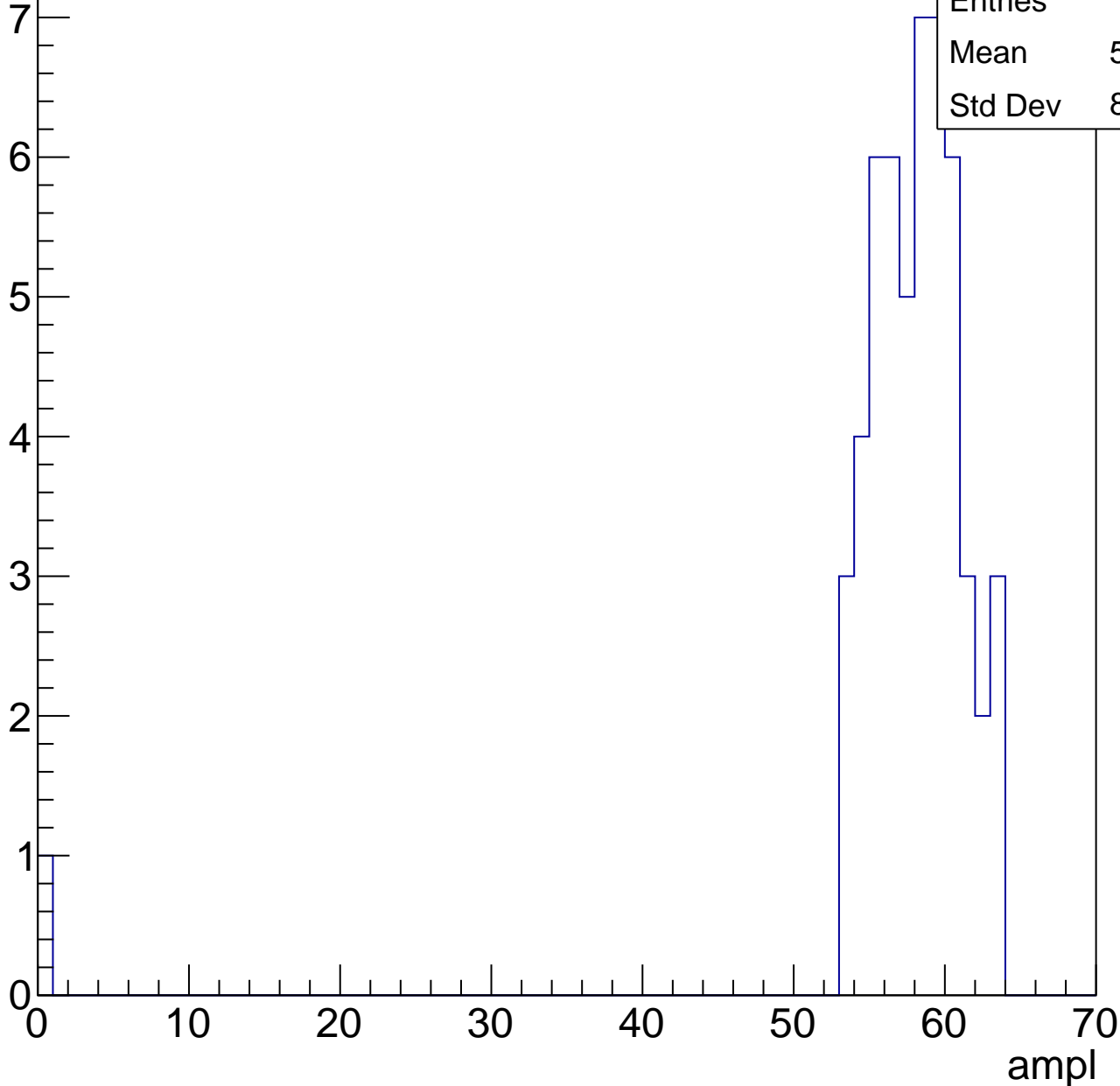


B0L001S, U11-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.62
Std Dev	8.299

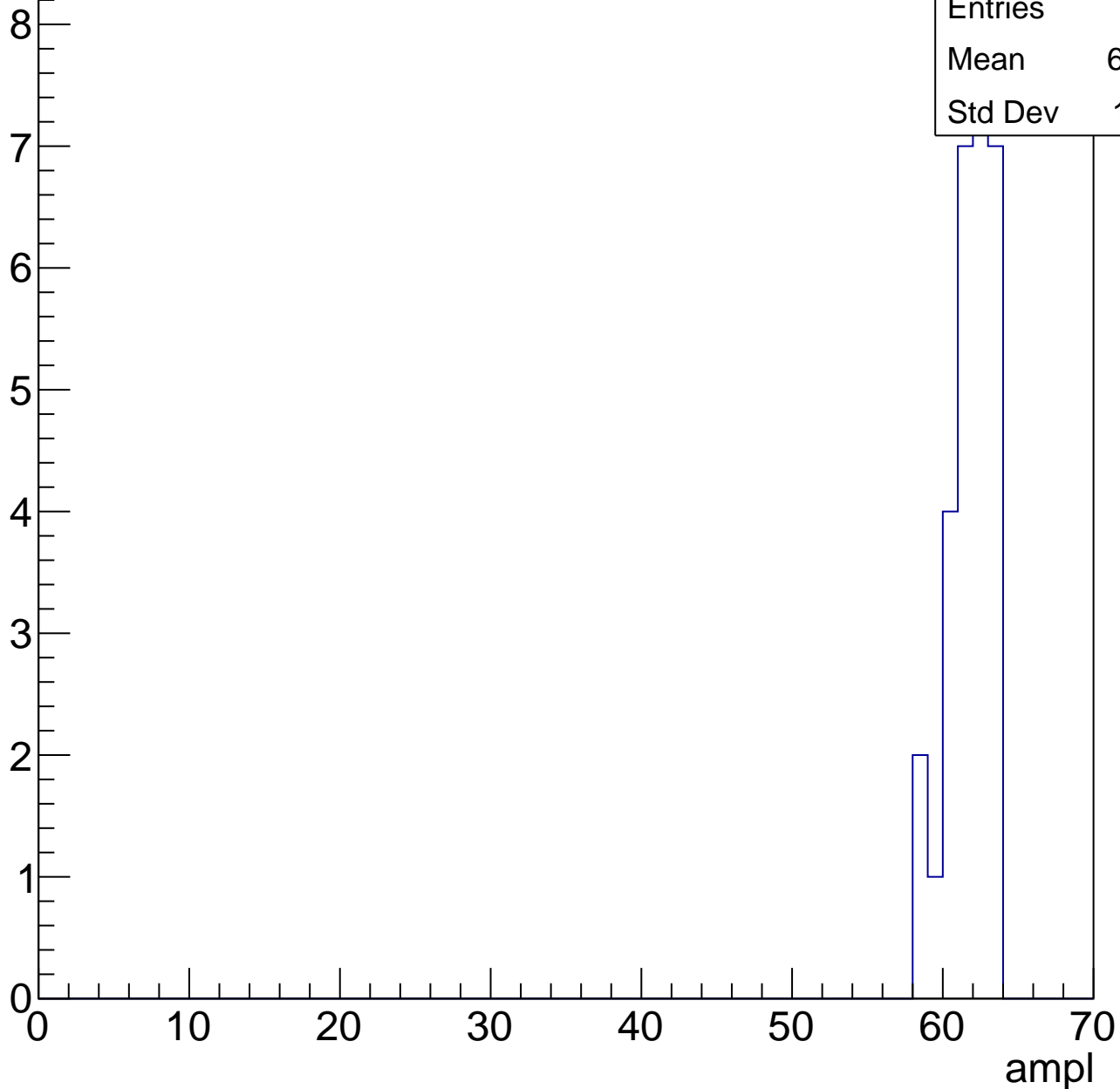


B0L001S, U11-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	61.34
Std Dev	1.421



B0L001S, U11-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch64, adc0

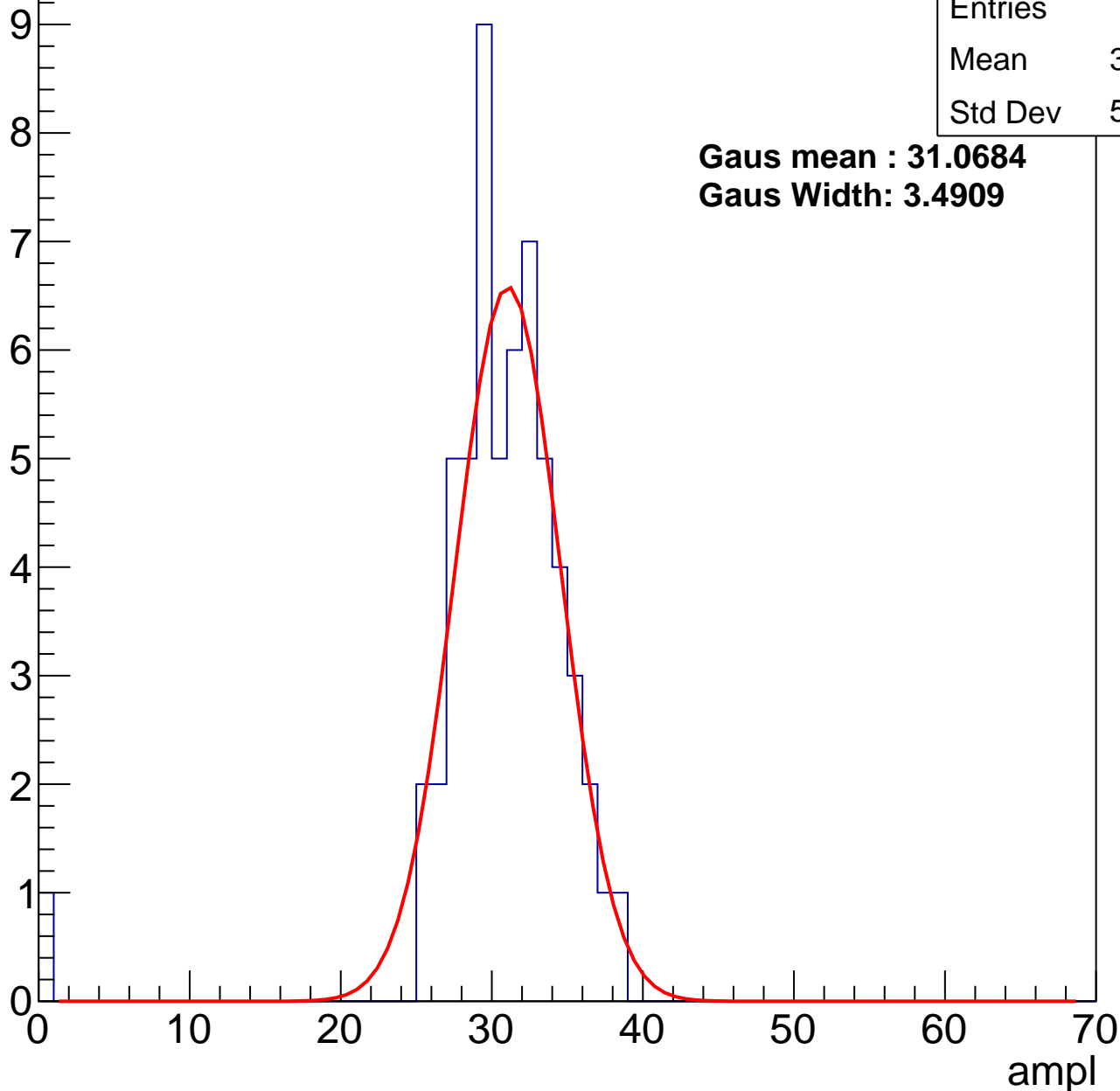
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	30.19
Std Dev	5.012

Gaus mean : 31.0684

Gaus Width: 3.4909



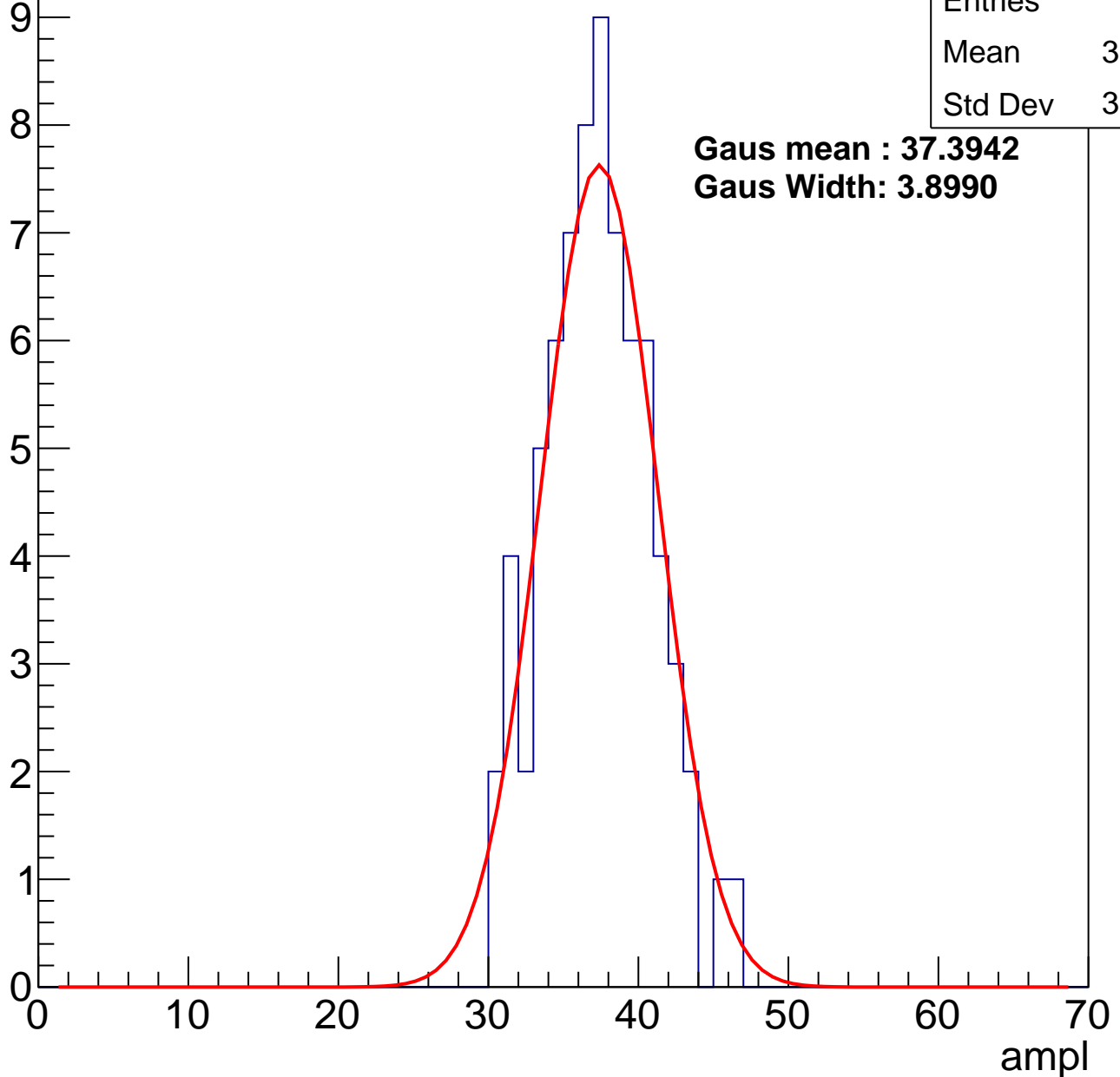
B0L001S, U11-ch64, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	36.85
Std Dev	3.514

Gaus mean : 37.3942
Gaus Width: 3.8990



B0L001S, U11-ch64, adc2

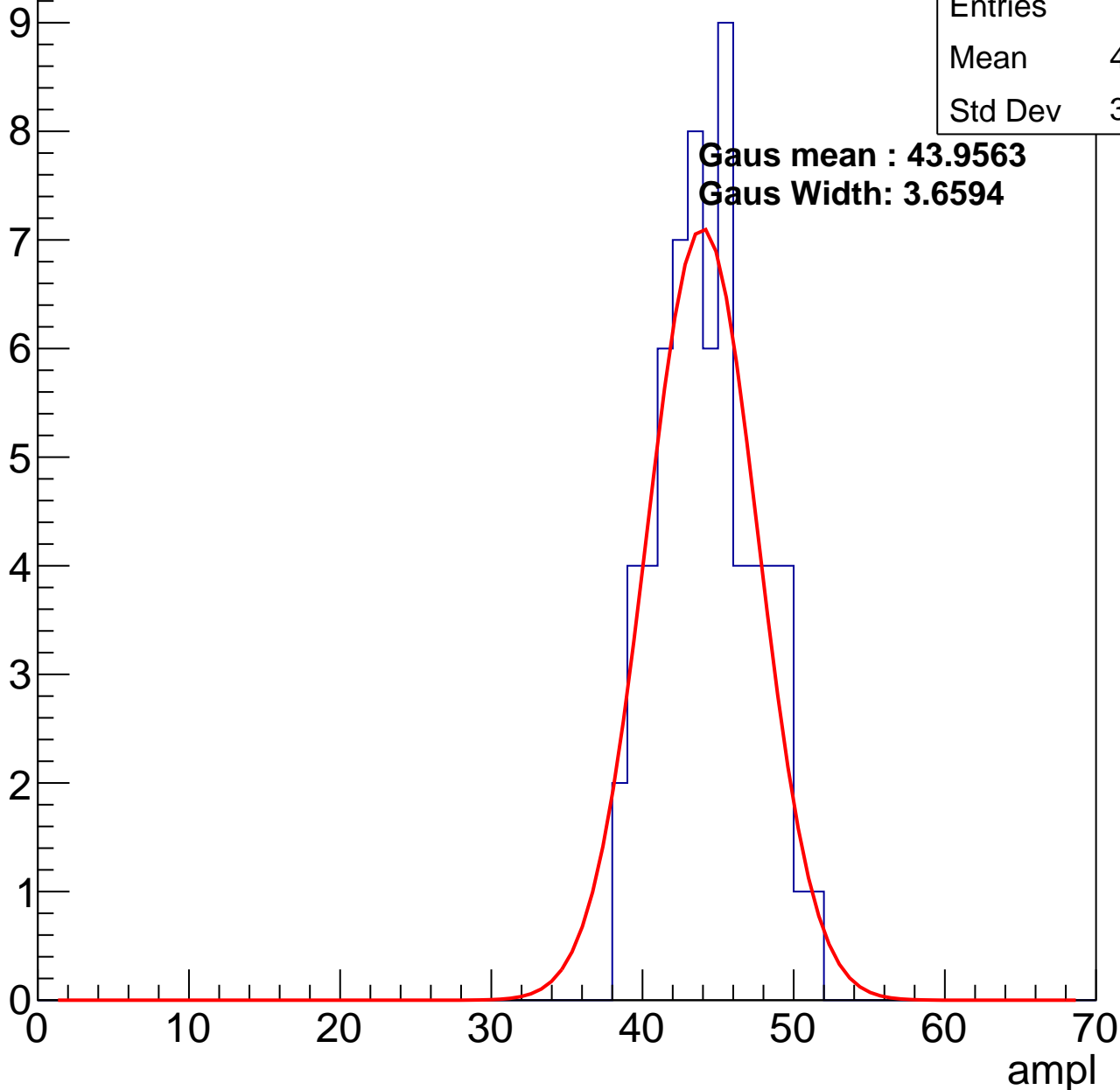
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.84
Std Dev	3.149

Gaus mean : 43.9563

Gaus Width: 3.6594

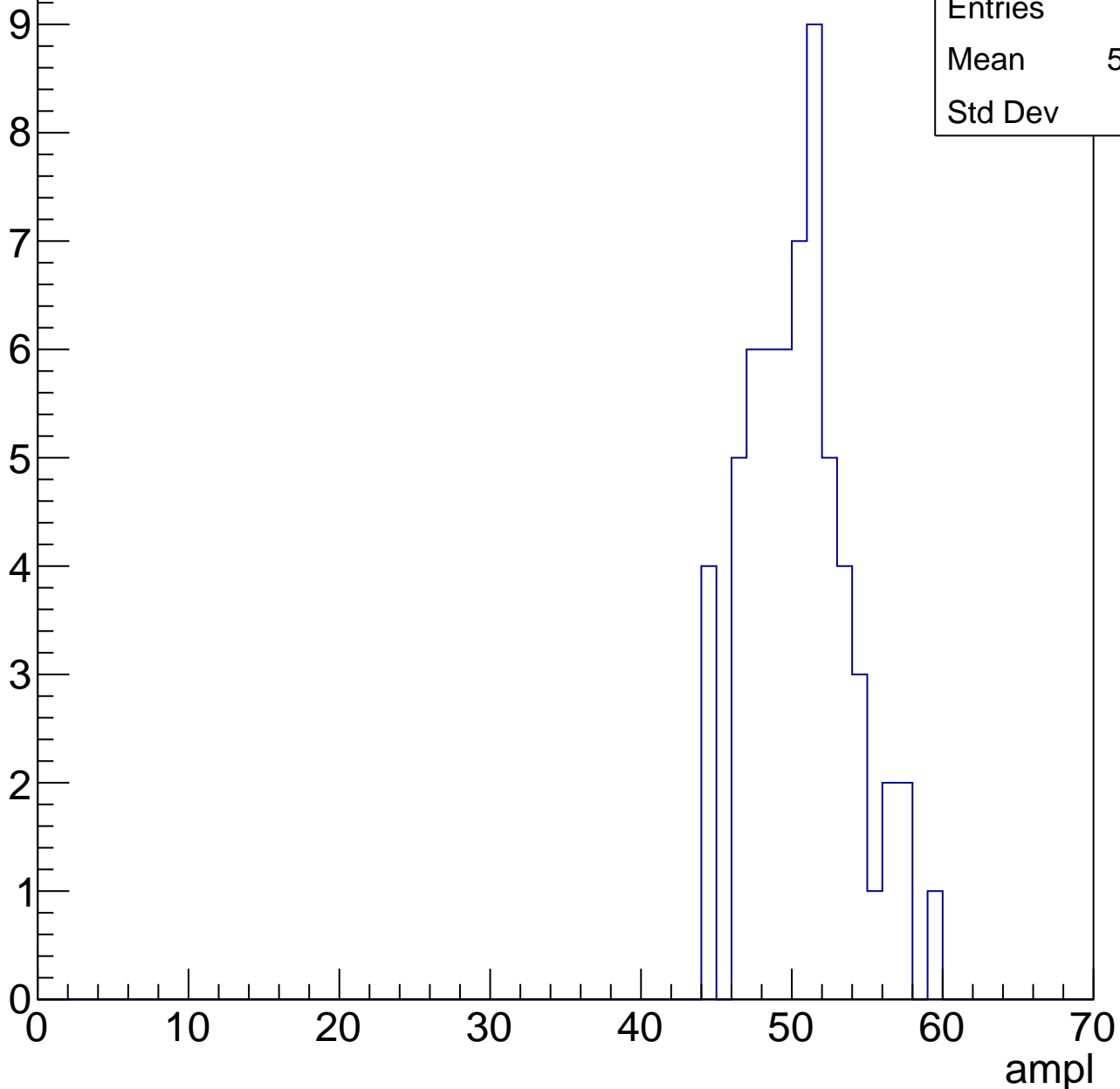


B0L001S, U11-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

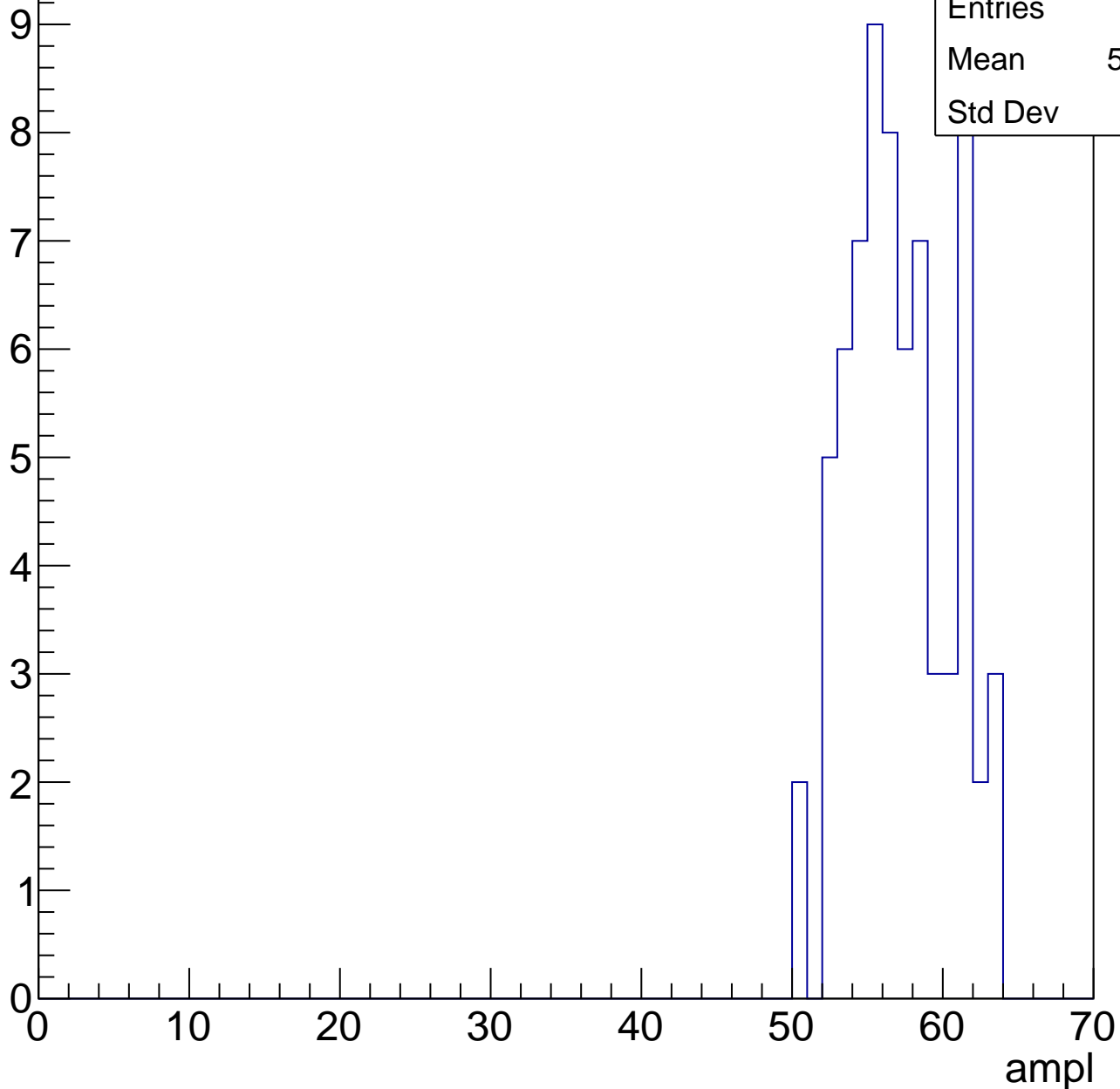
Entries	61
Mean	50.05
Std Dev	3.38



B0L001S, U11-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



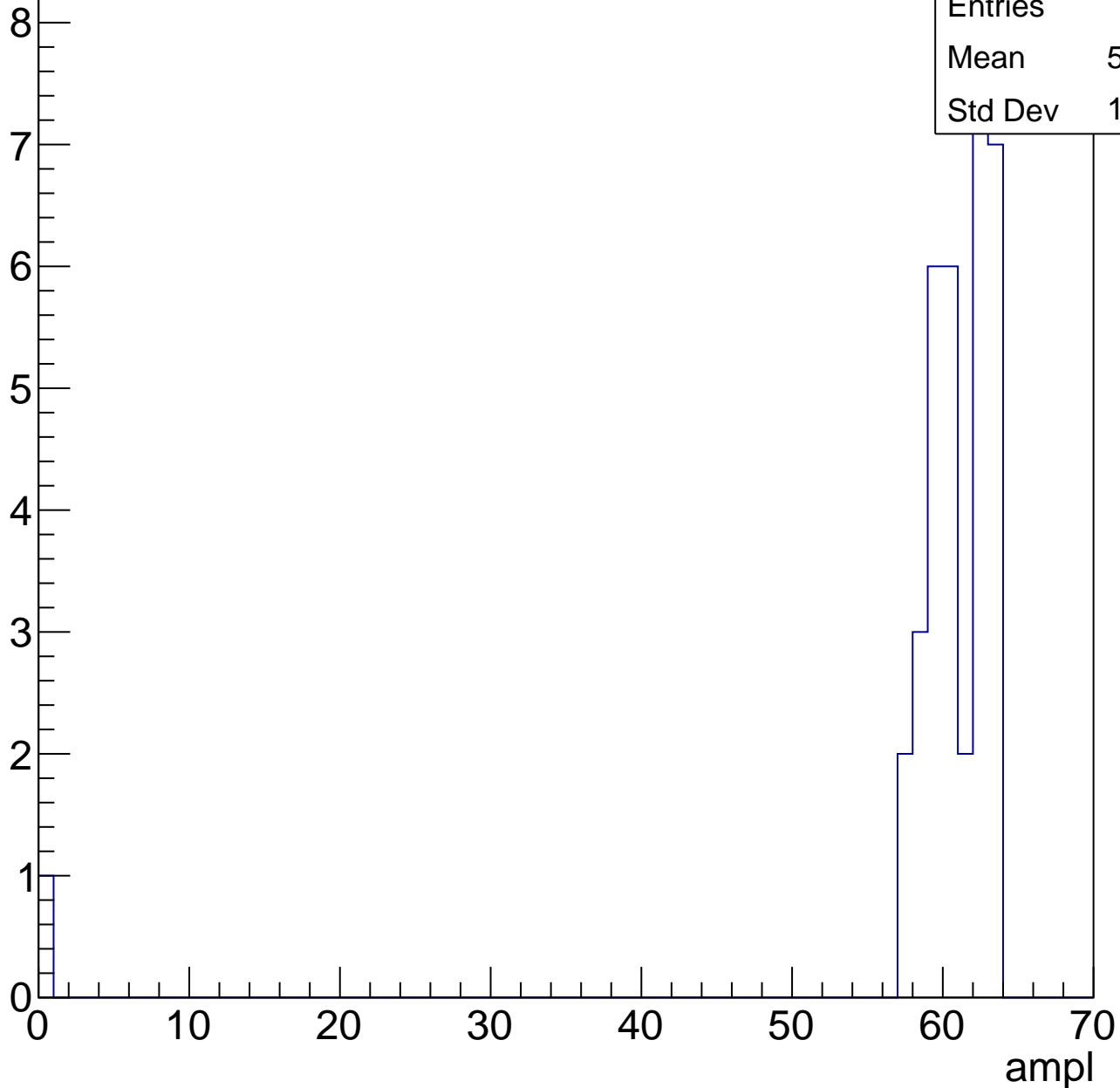
Entries	69
Mean	56.59
Std Dev	3.29

B0L001S, U11-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.89
Std Dev	10.27



B0L001S, U11-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch65, adc0

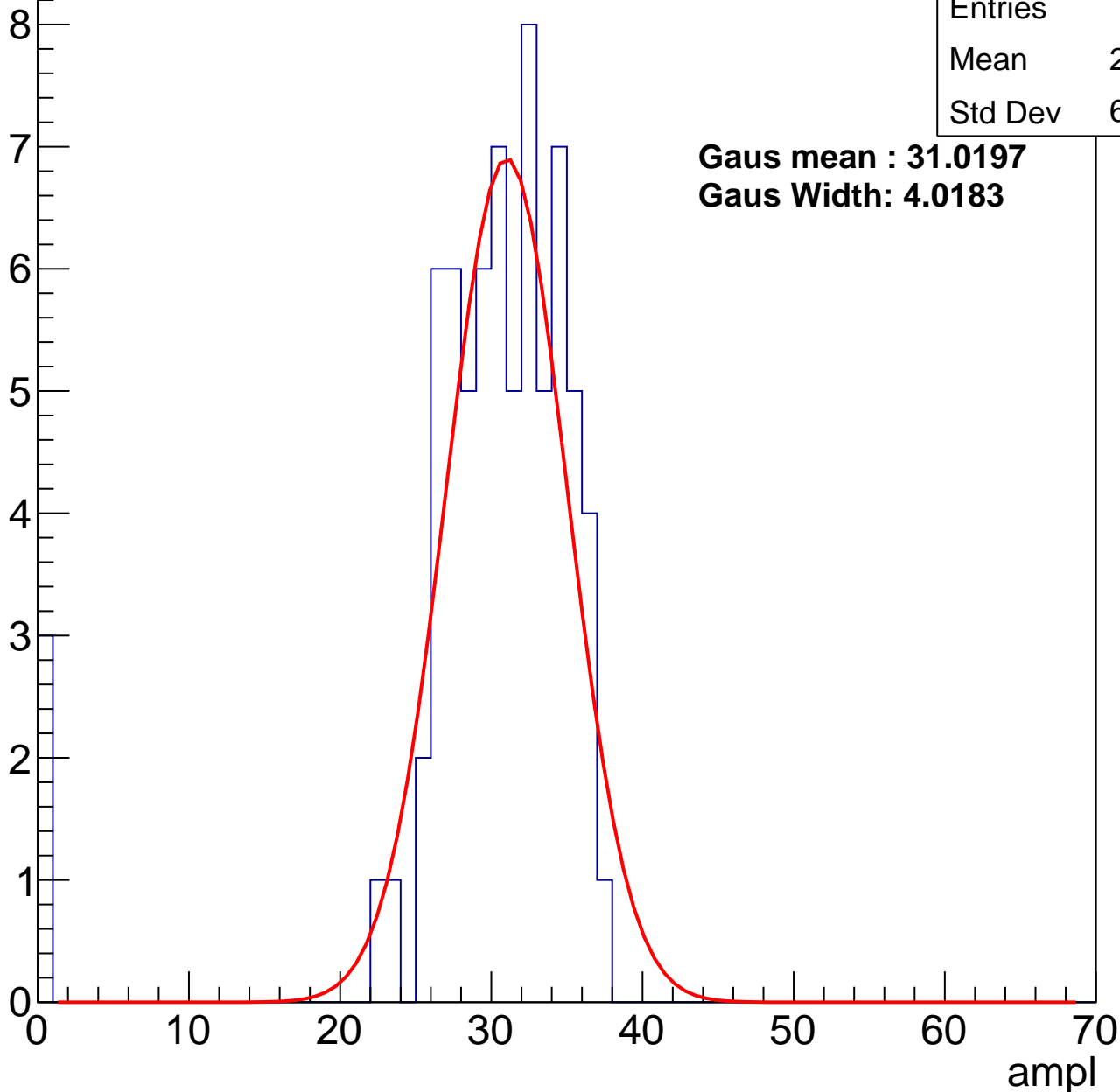
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.26
Std Dev	6.984

Gaus mean : 31.0197

Gaus Width: 4.0183



B0L001S, U11-ch65, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	38.42
Std Dev	3.847

Gaus mean : 39.3923

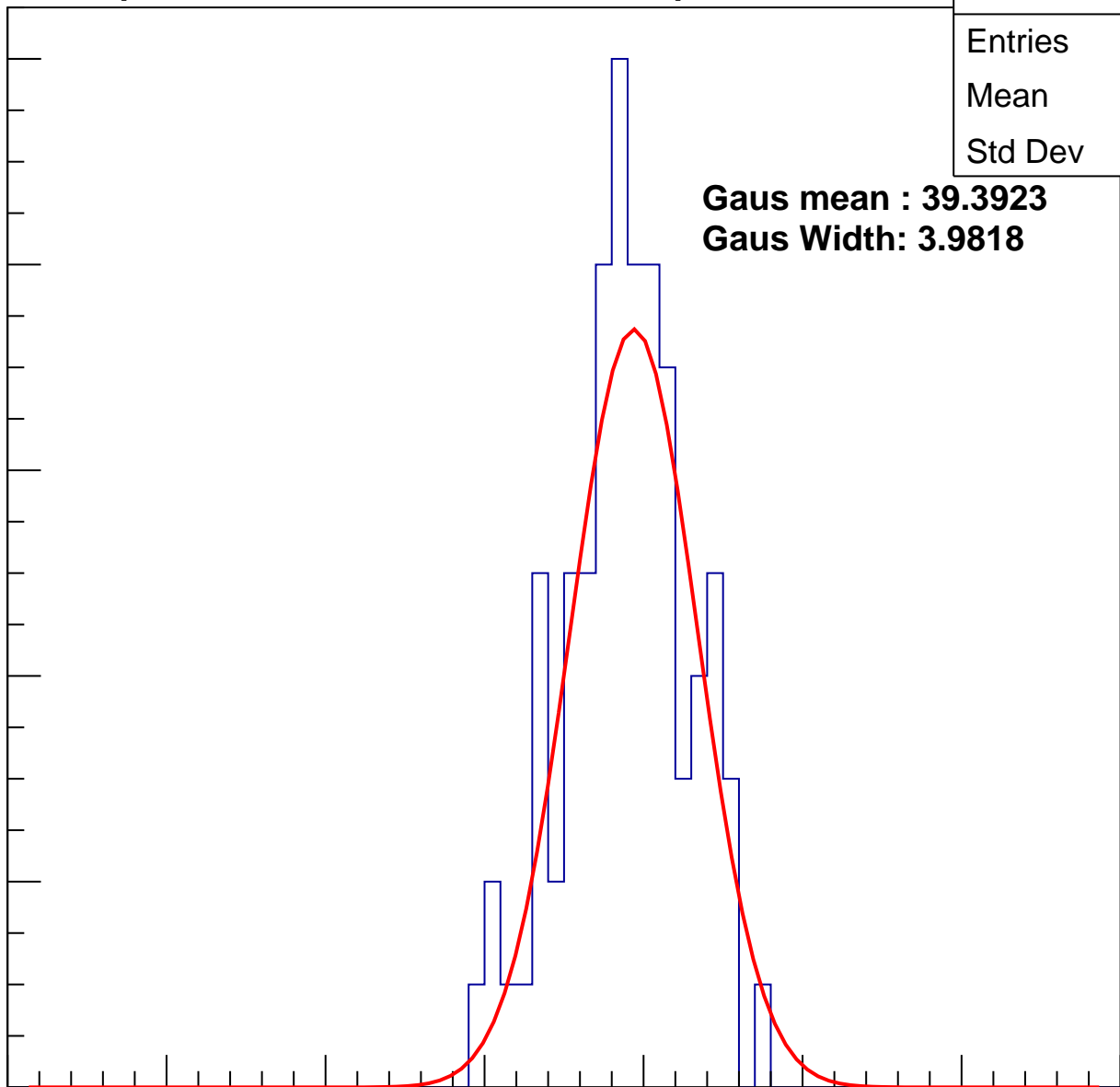
Gaus Width: 3.9818

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch65, adc2

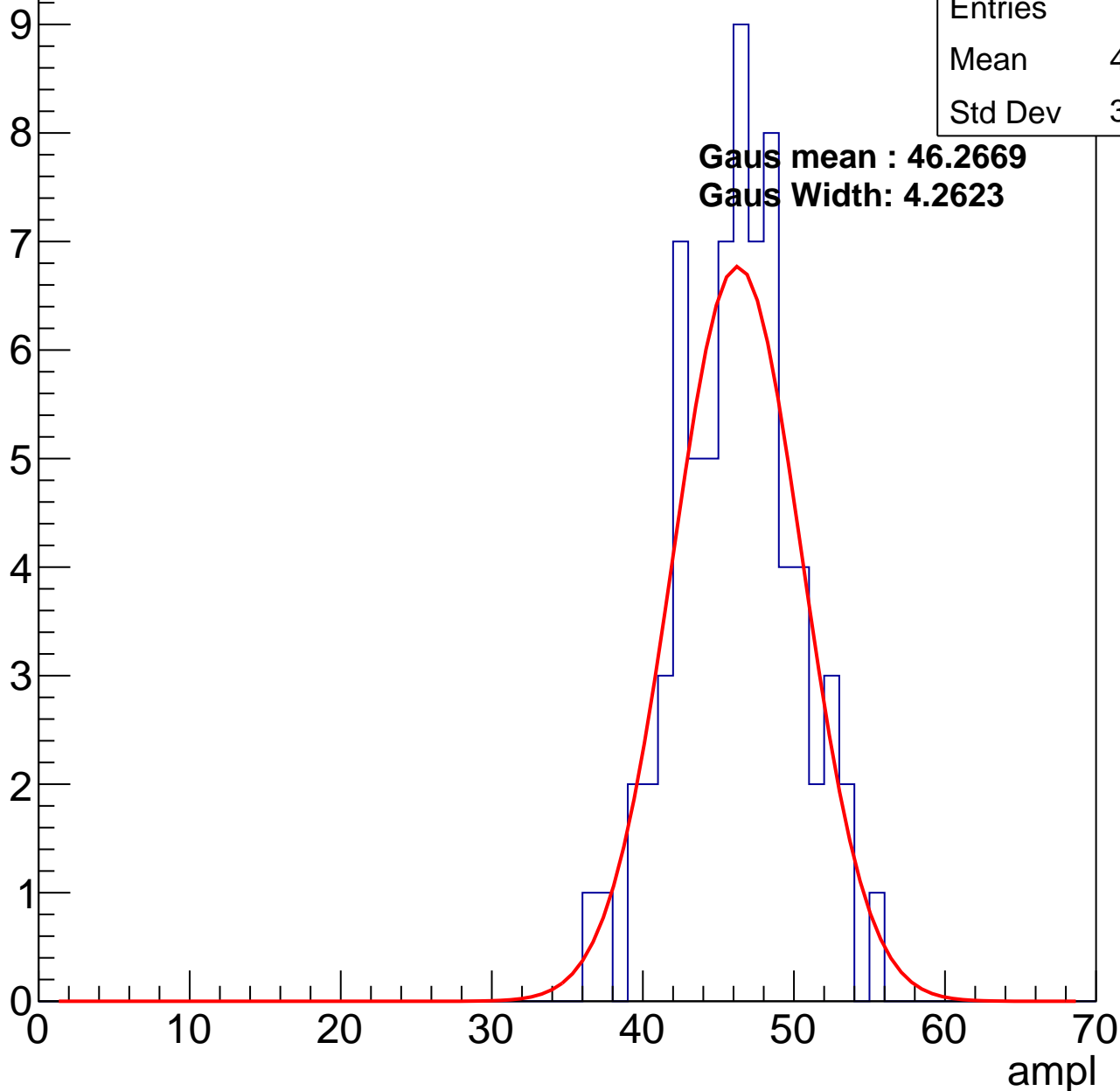
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.75
Std Dev	3.849

Gaus mean : 46.2669

Gaus Width: 4.2623

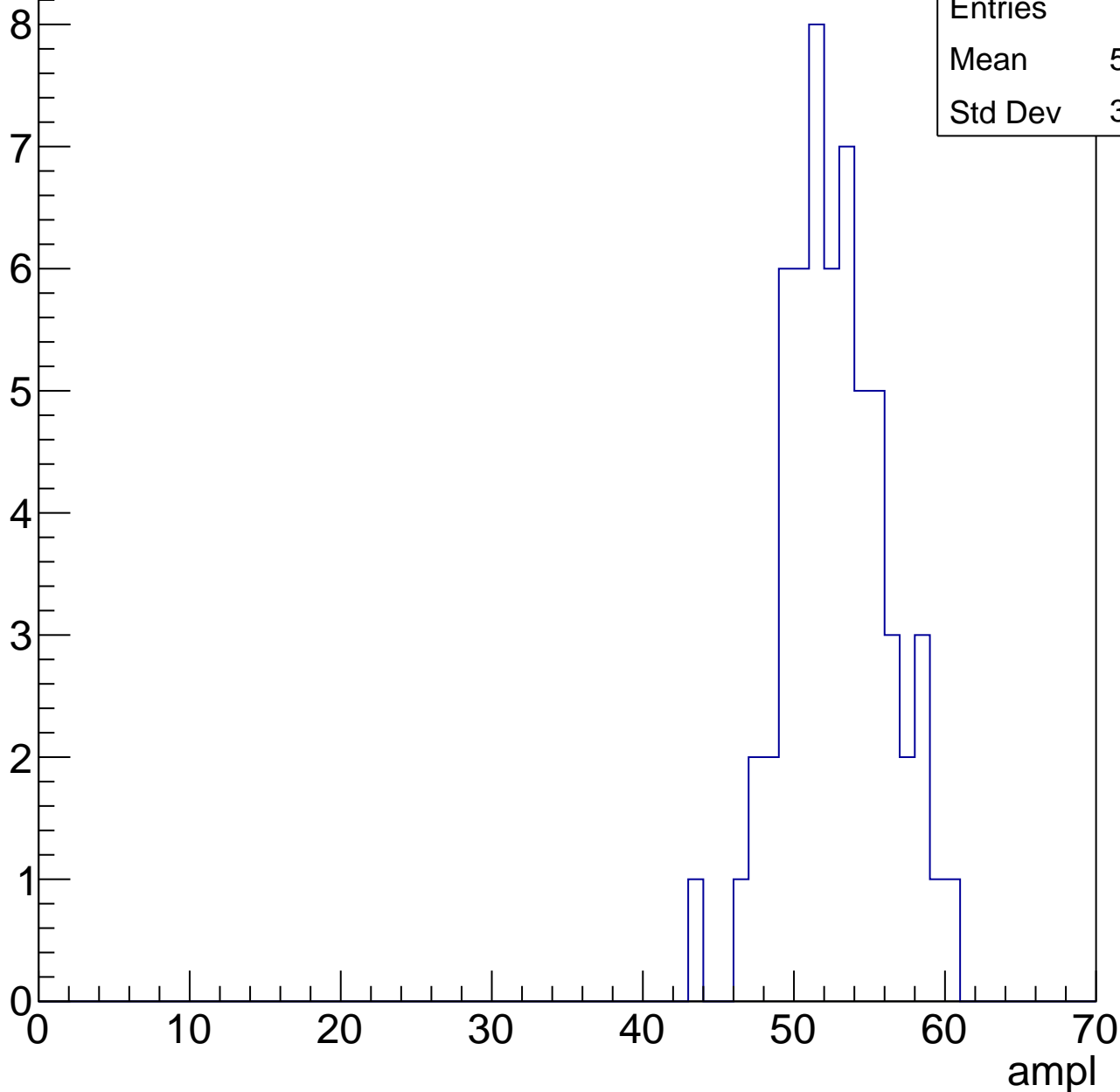


B0L001S, U11-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	52.27
Std Dev	3.374

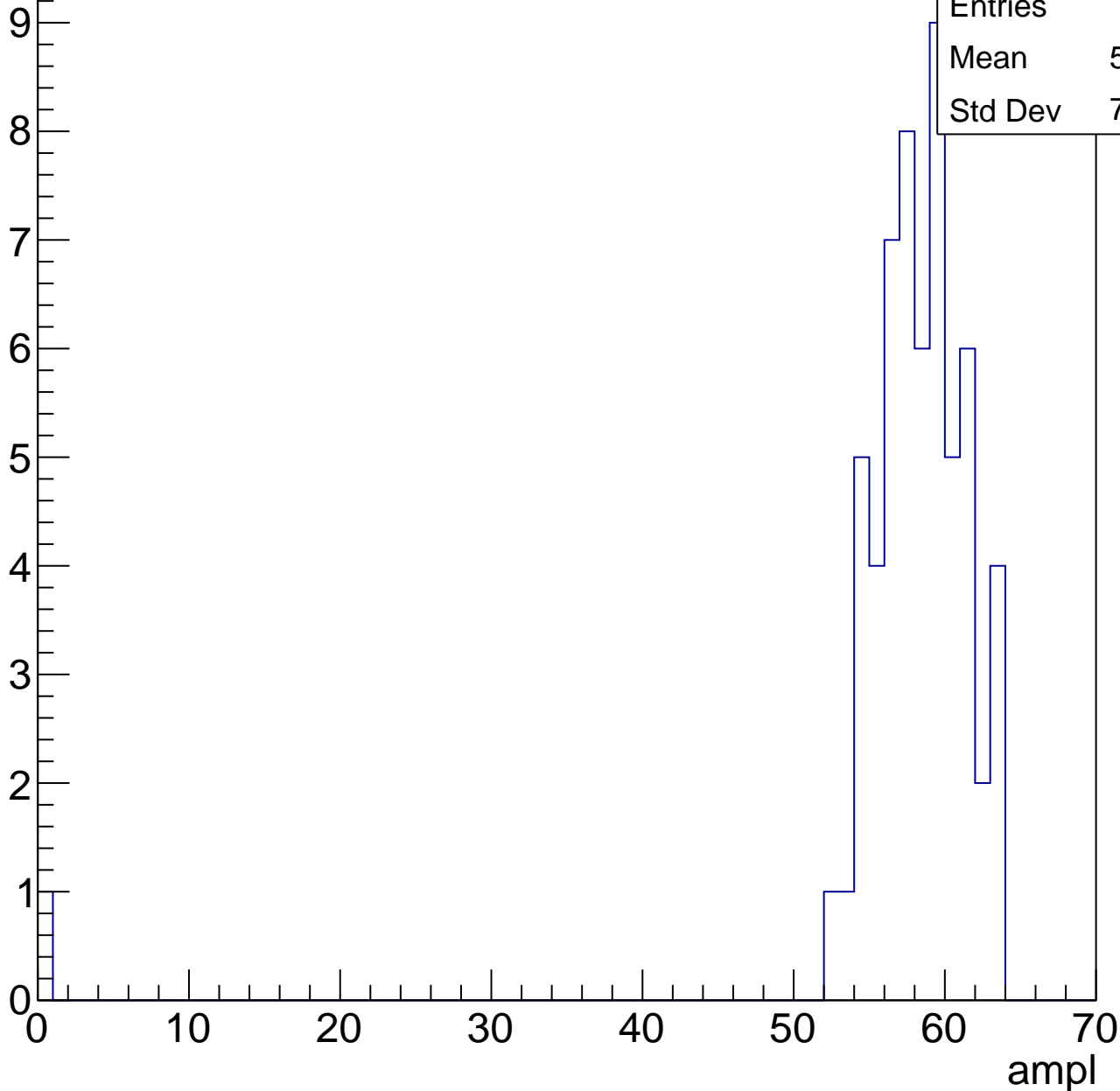


B0L001S, U11-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

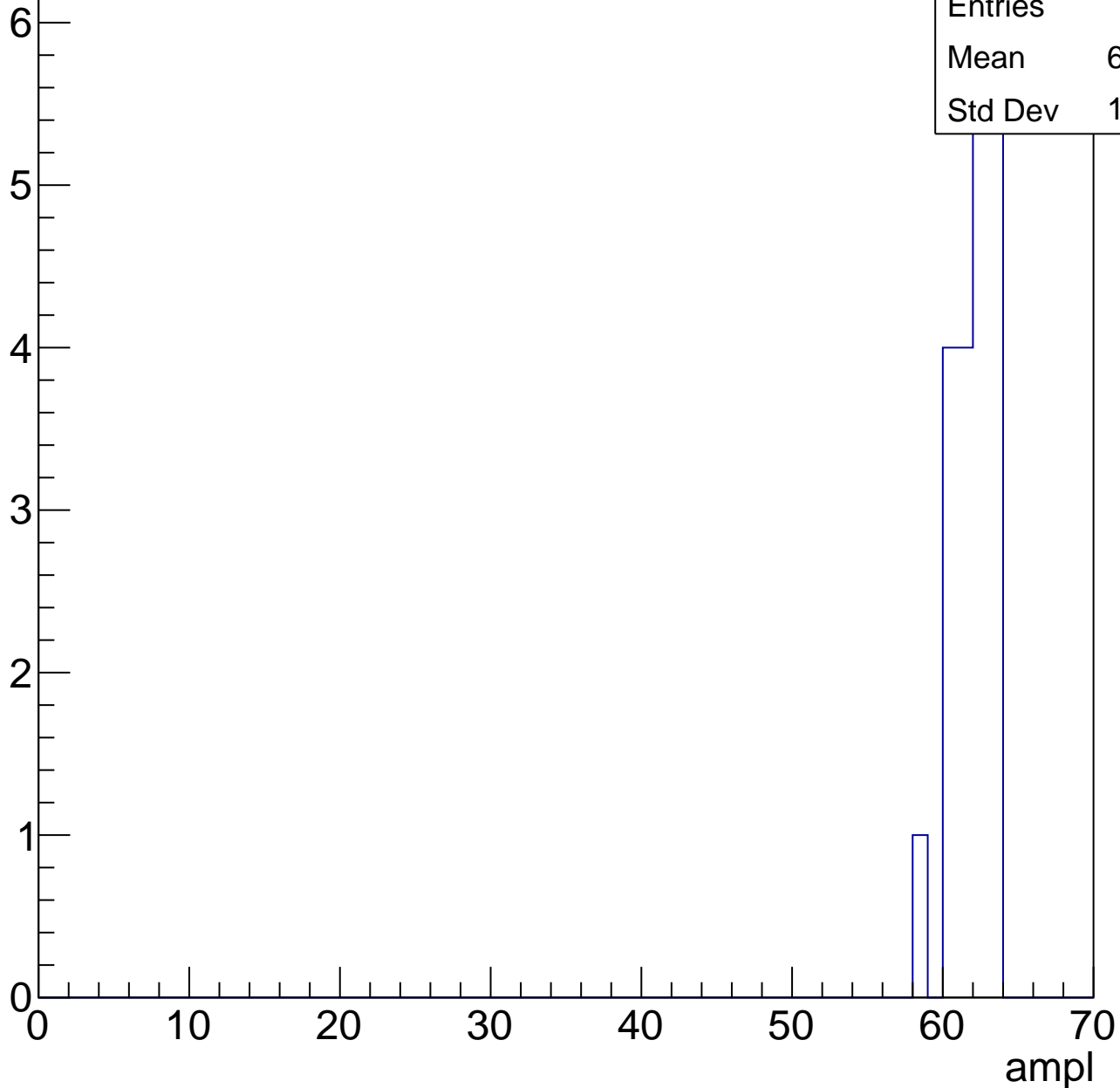
Entries	59
Mean	57.02
Std Dev	7.956



B0L001S, U11-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B0L001S, U11-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch66, adc0

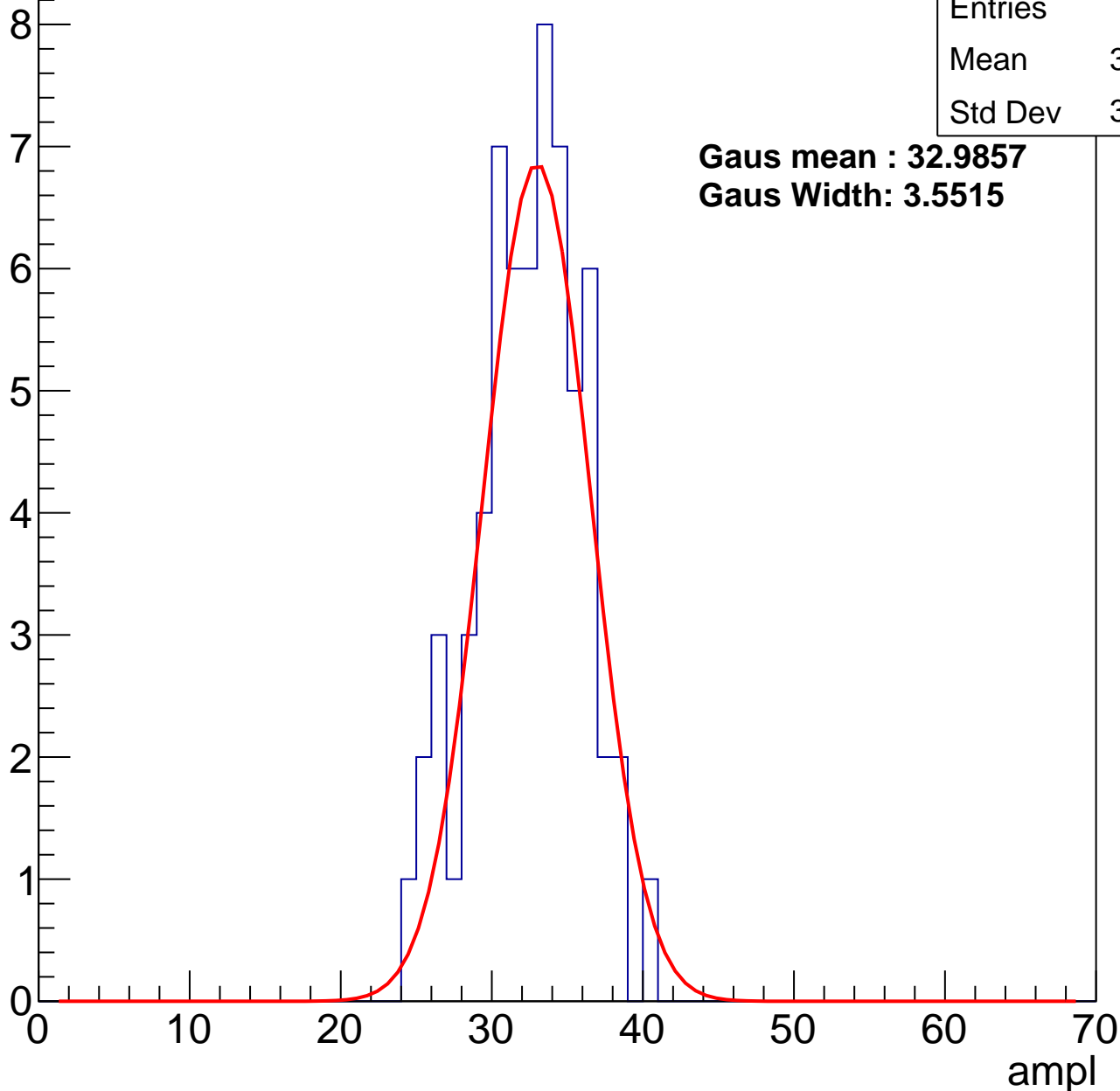
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	32.03
Std Dev	3.482

Gaus mean : 32.9857

Gaus Width: 3.5515



B0L001S, U11-ch66, adc1

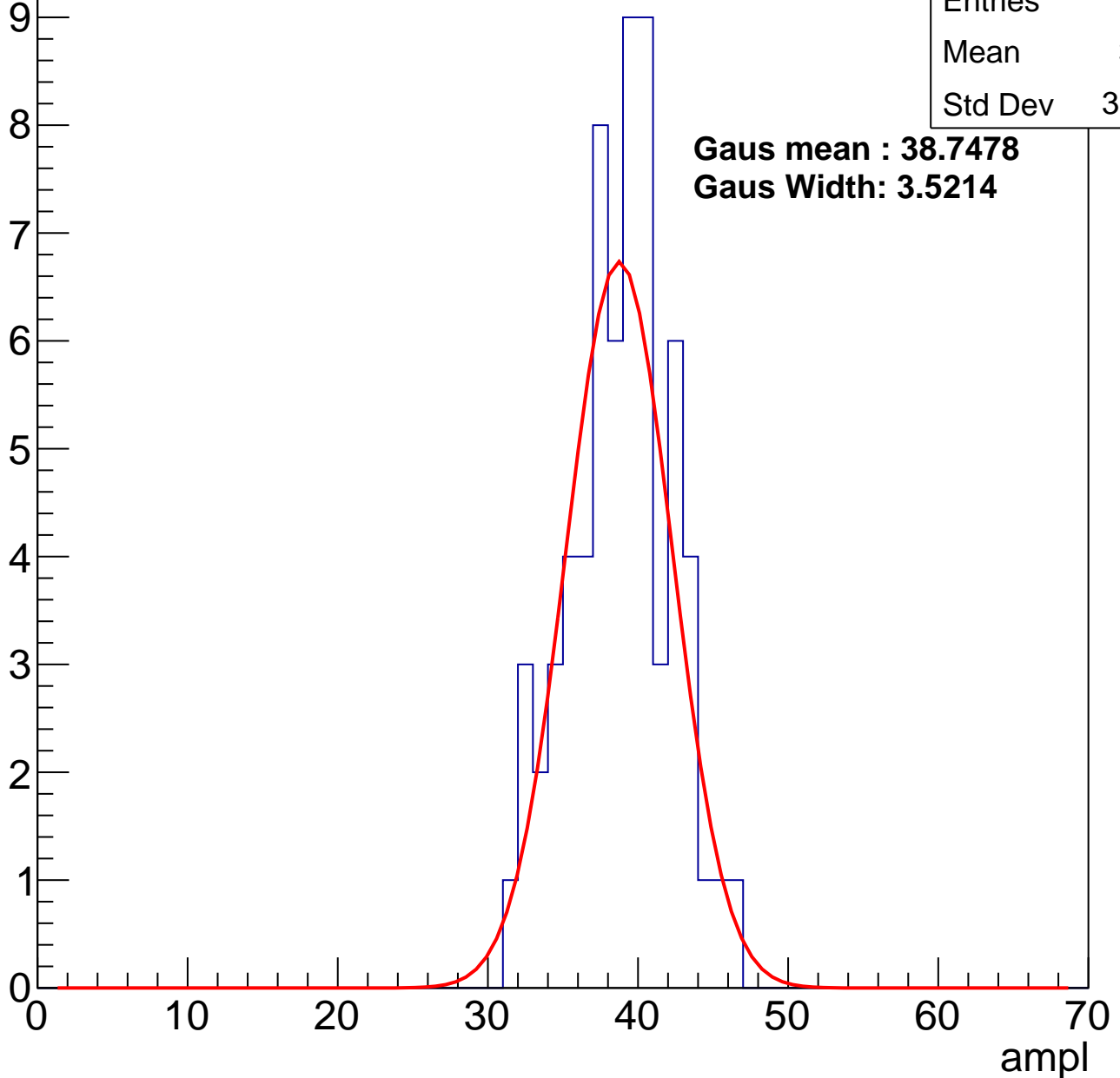
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.4
Std Dev	3.332

Gaus mean : 38.7478

Gaus Width: 3.5214



B0L001S, U11-ch66, adc2

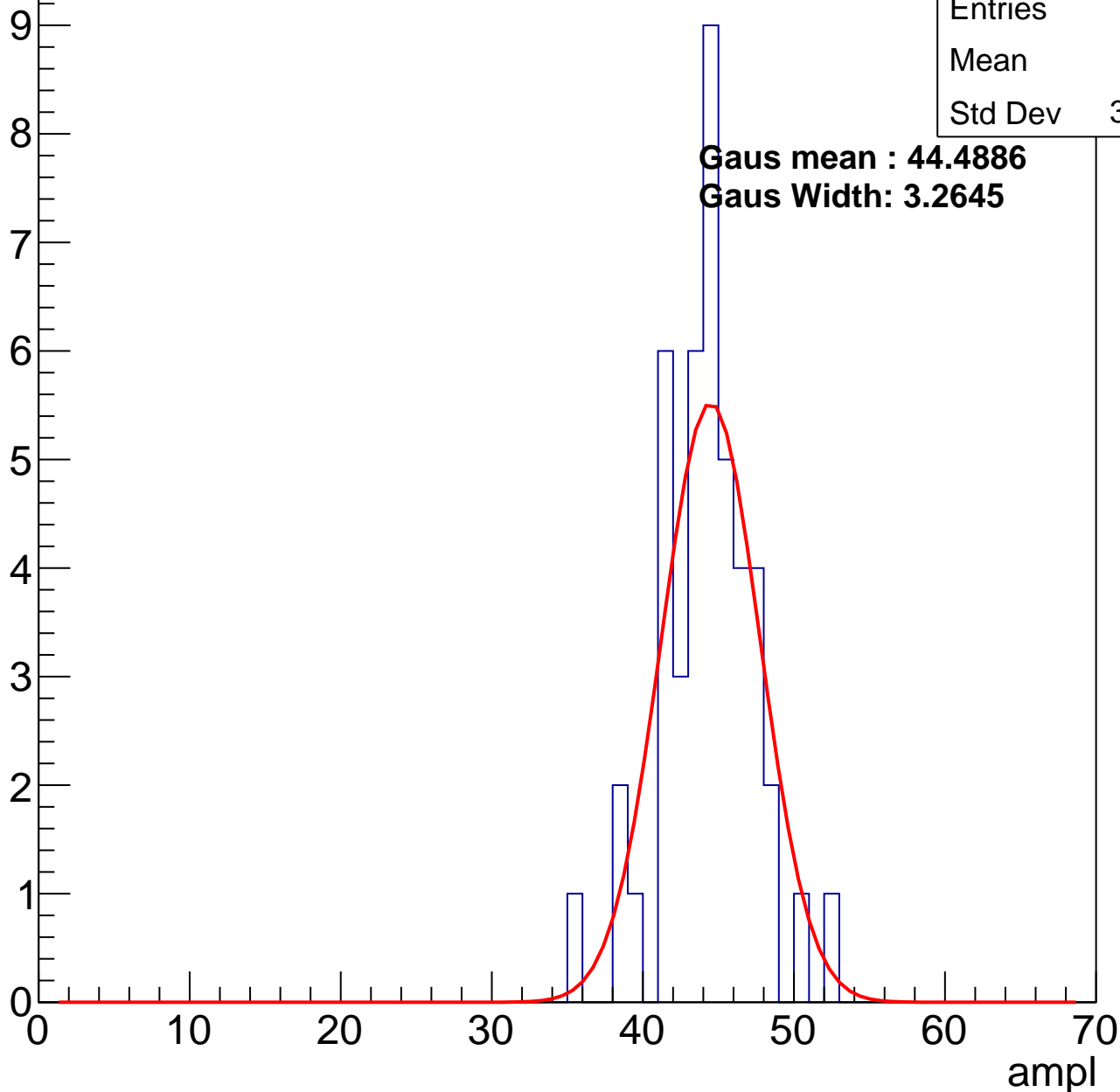
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	43.8
Std Dev	3.117

Gaus mean : 44.4886

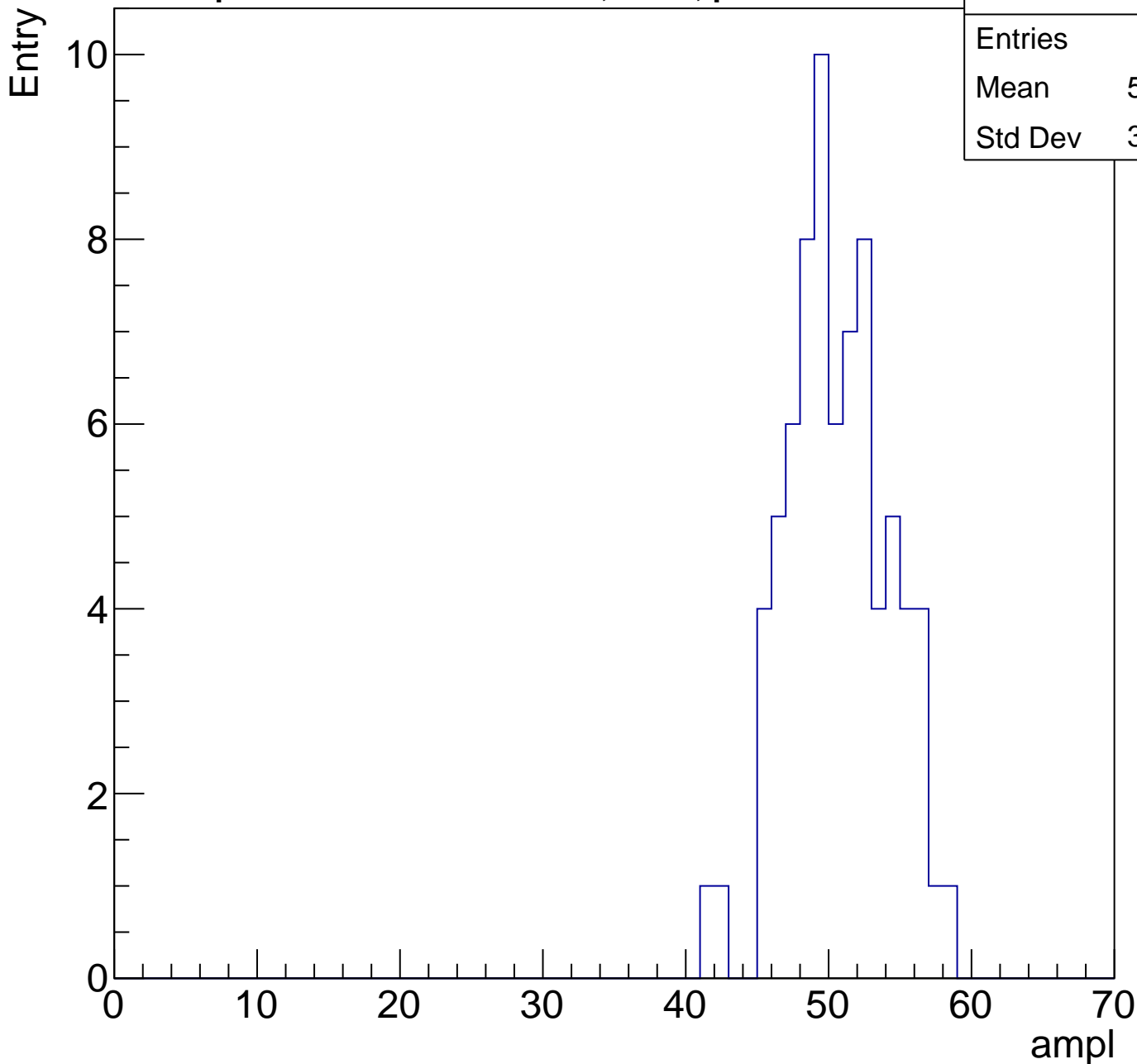
Gaus Width: 3.2645



B0L001S, U11-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

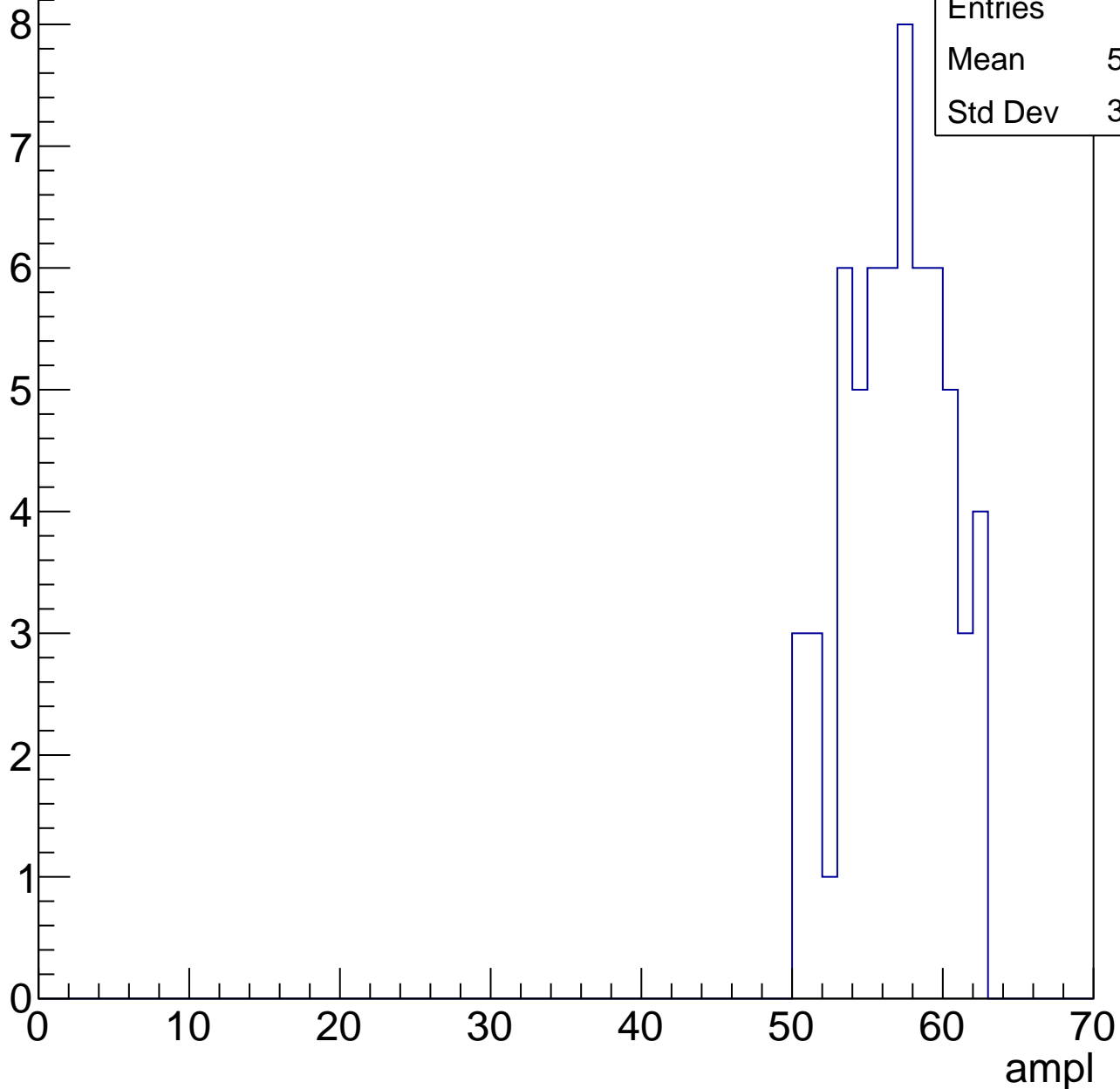
Entries	75
Mean	50.17
Std Dev	3.523



B0L001S, U11-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

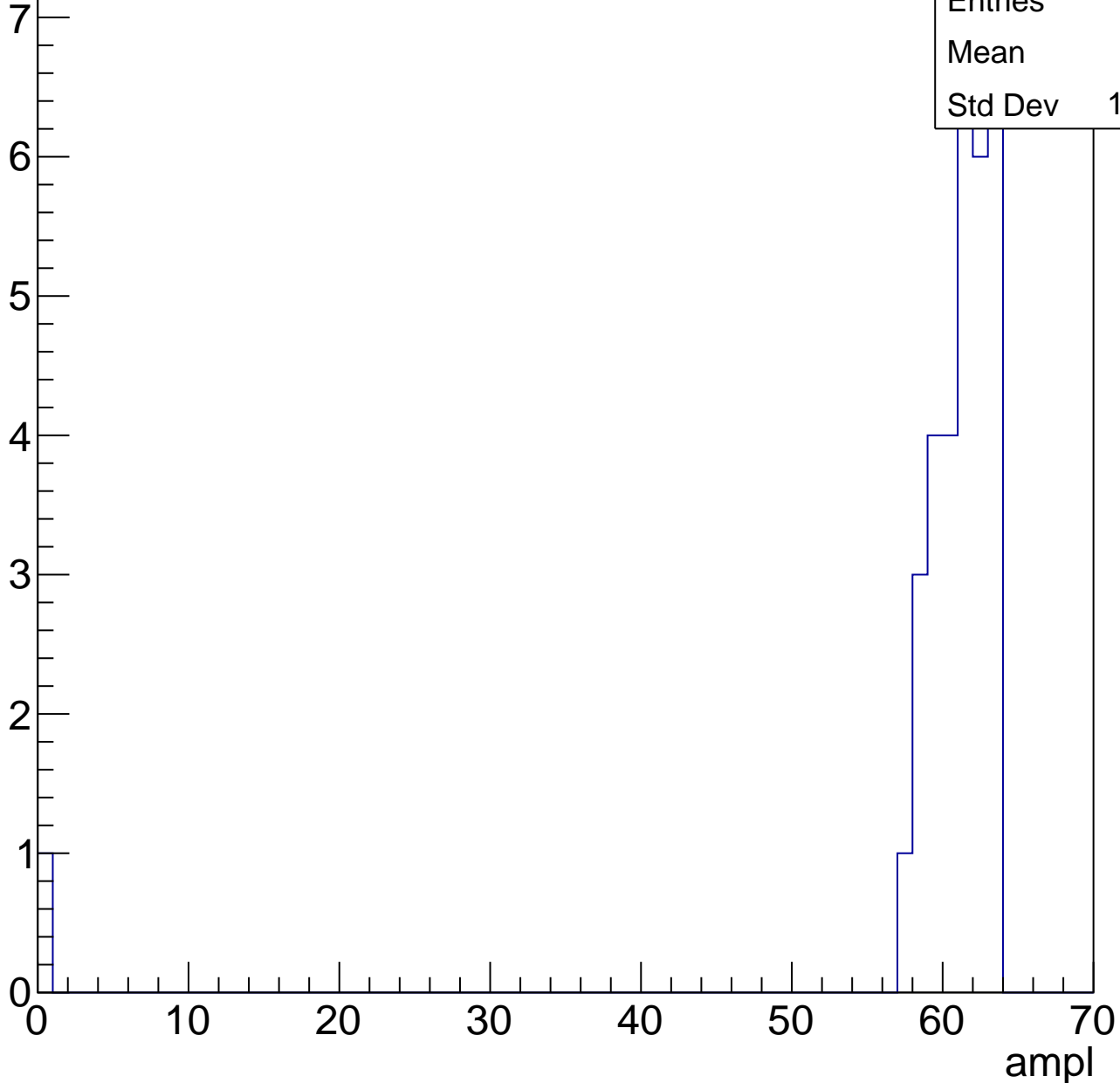


B0L001S, U11-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59
Std Dev	10.57



B0L001S, U11-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	62
Std Dev	1.414

B0L001S, U11-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch67, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	30.43
Std Dev	6.018

Gaus mean : 31.8477

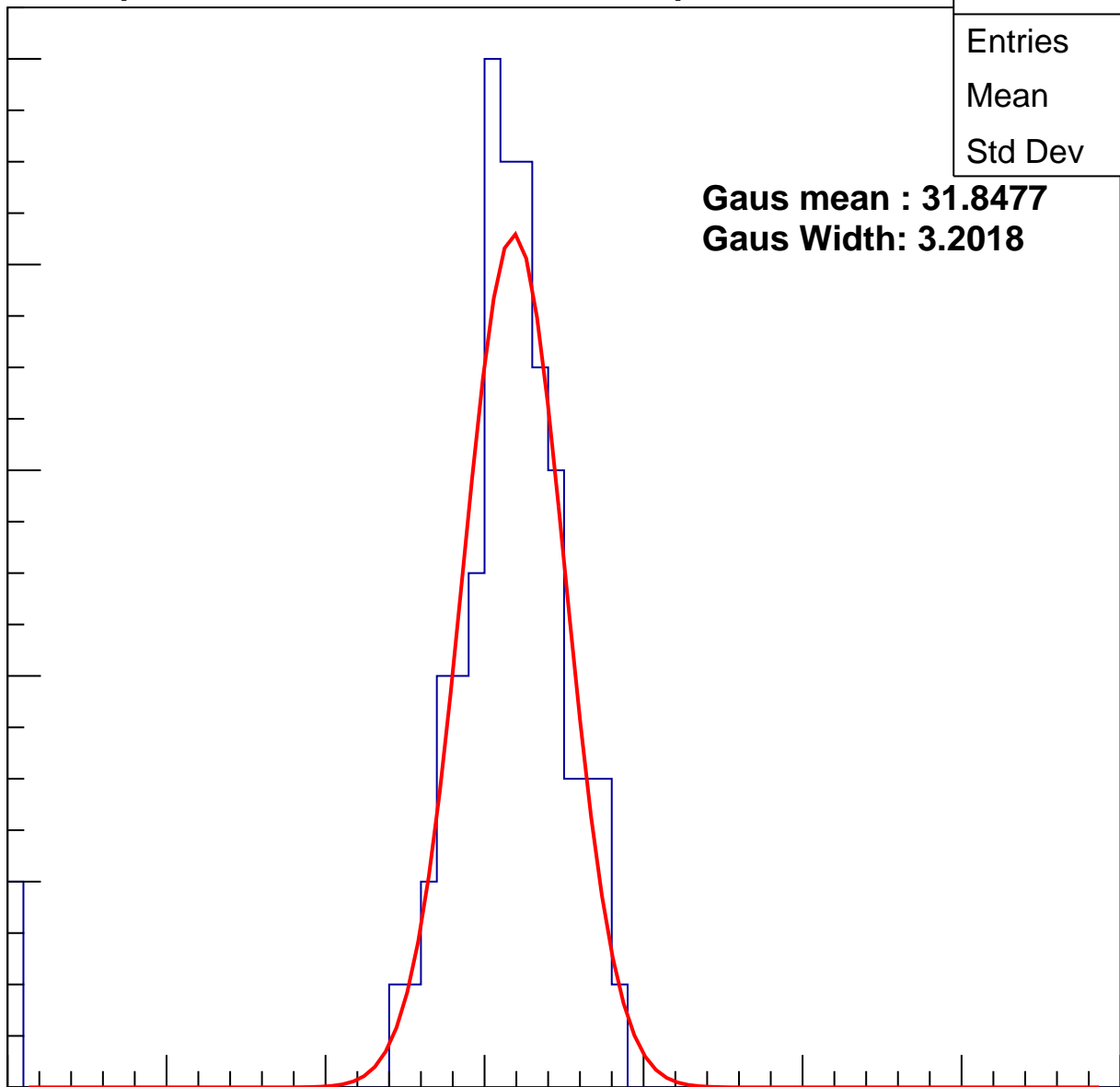
Gaus Width: 3.2018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch67, adc1

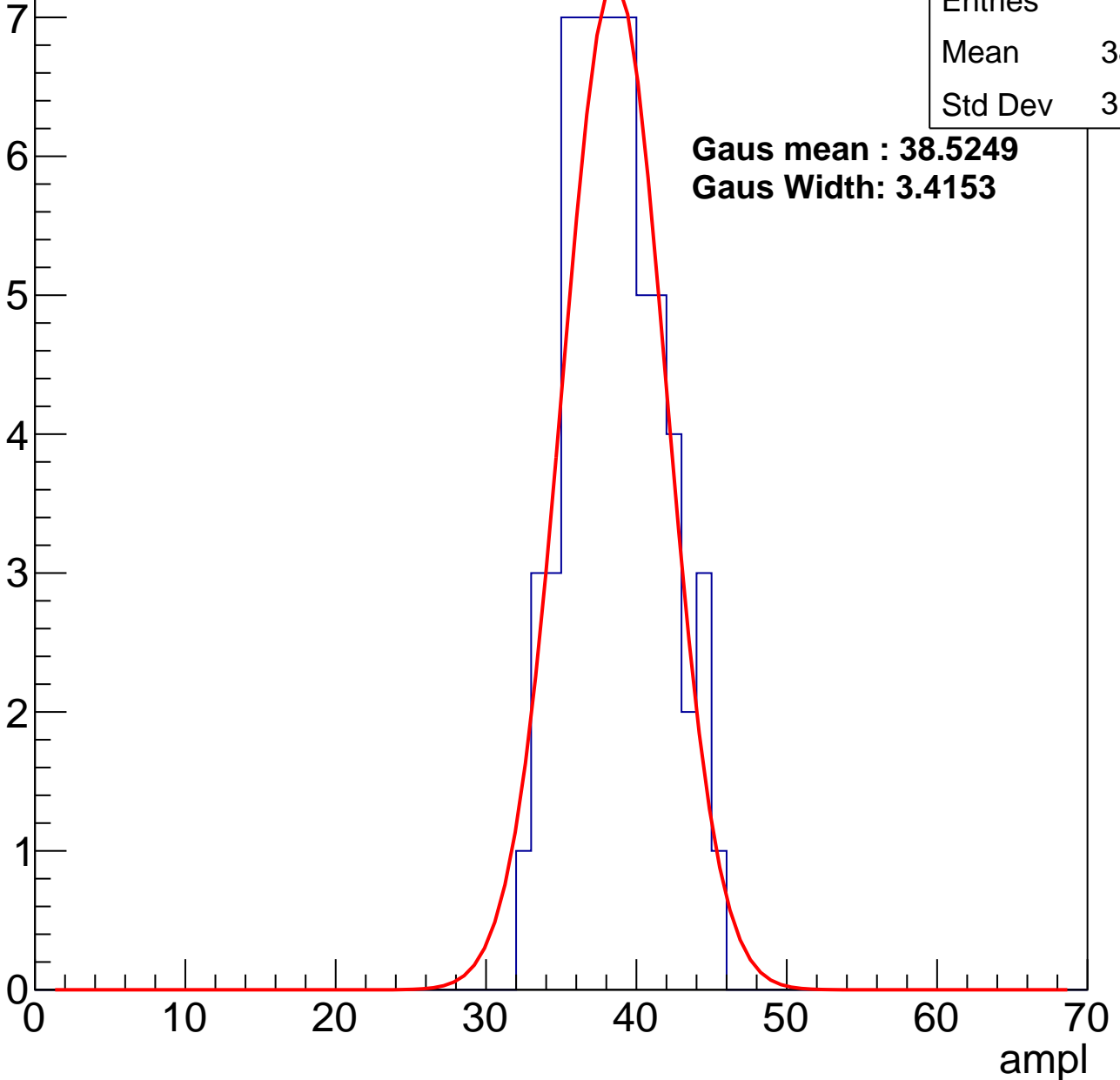
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	38.13
Std Dev	3.108

Gaus mean : 38.5249

Gaus Width: 3.4153



B0L001S, U11-ch67, adc2

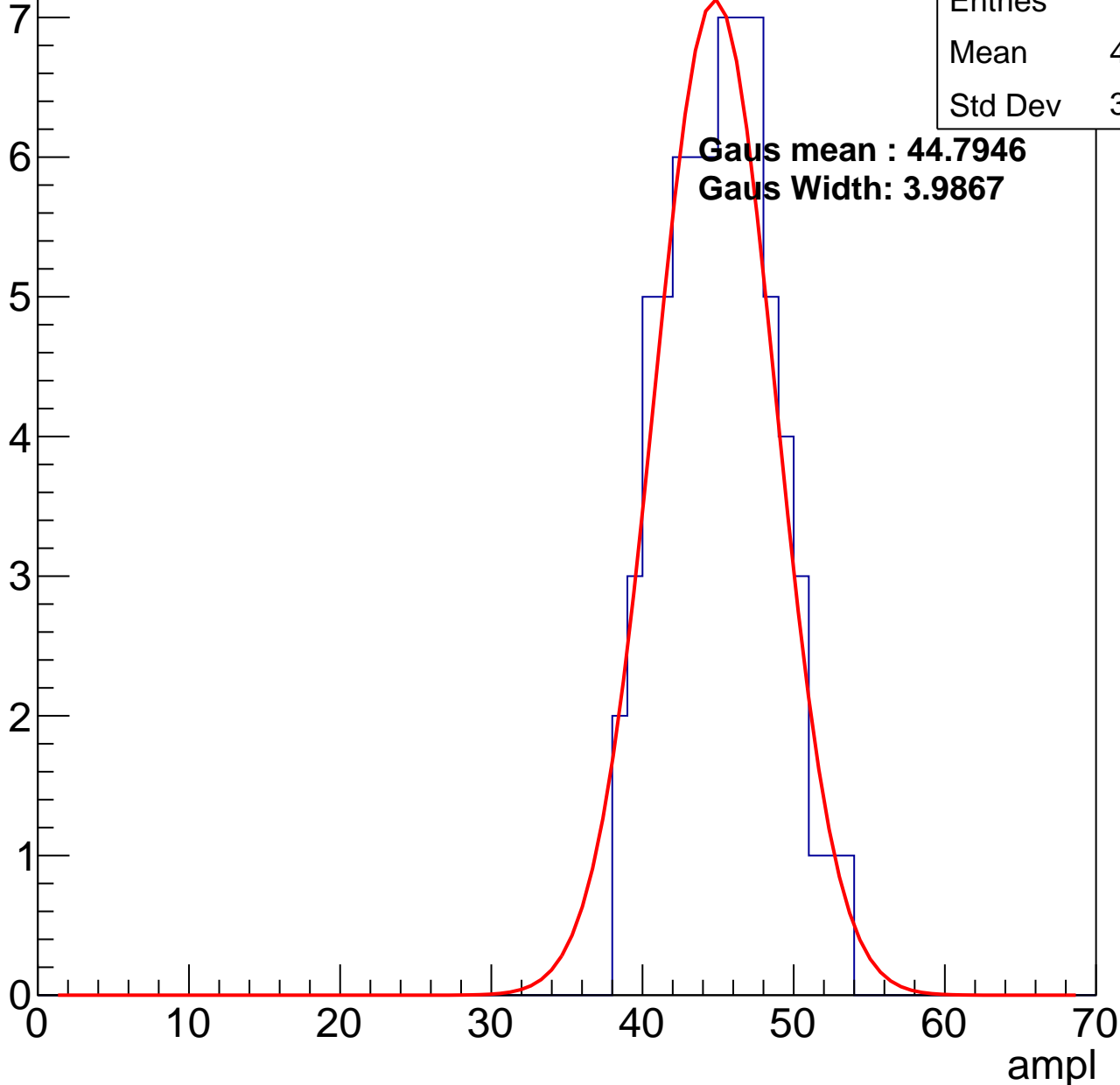
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.64
Std Dev	3.518

Gaus mean : 44.7946

Gaus Width: 3.9867

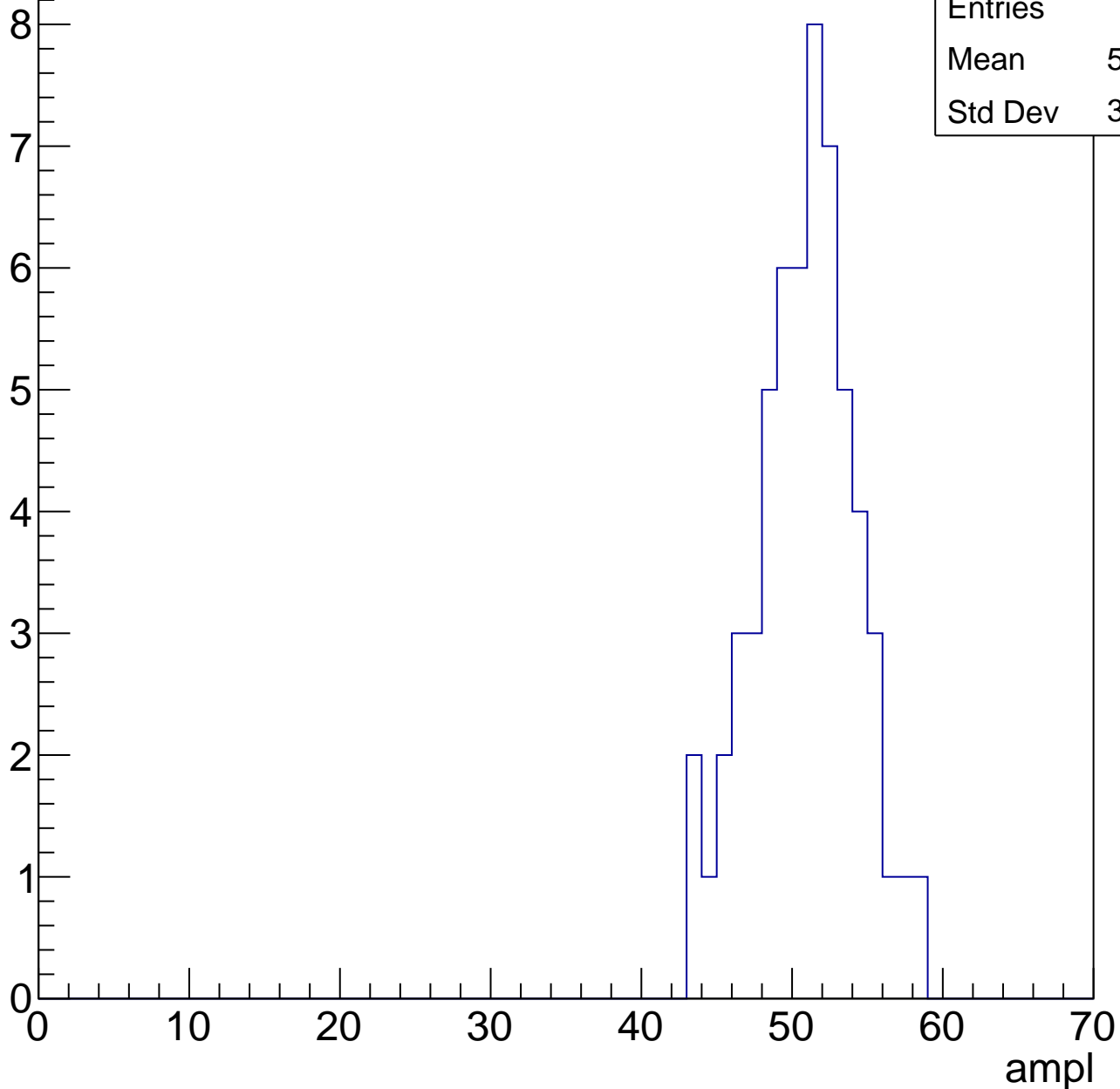


B0L001S, U11-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

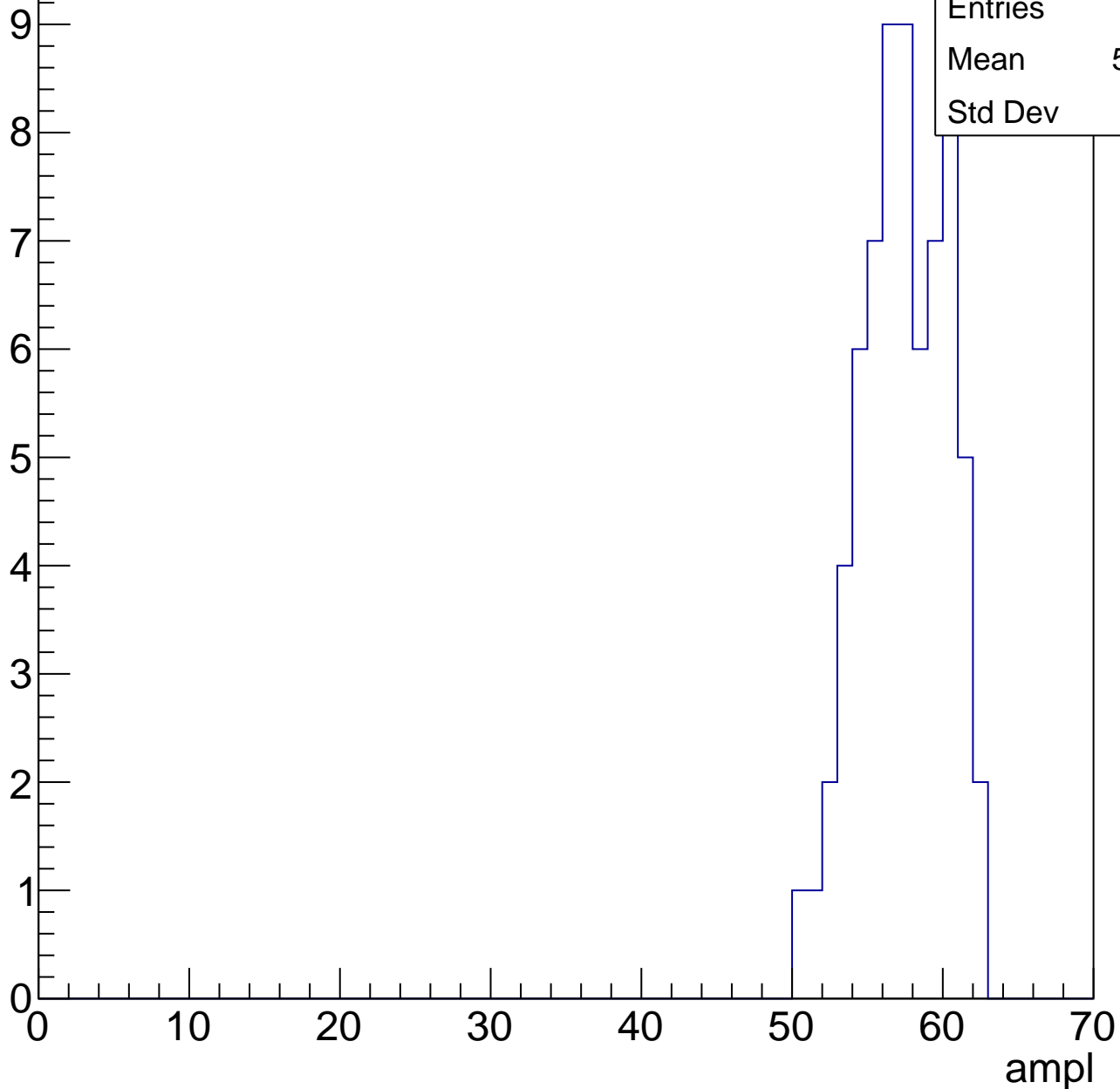
Entries	58
Mean	50.38
Std Dev	3.336



B0L001S, U11-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



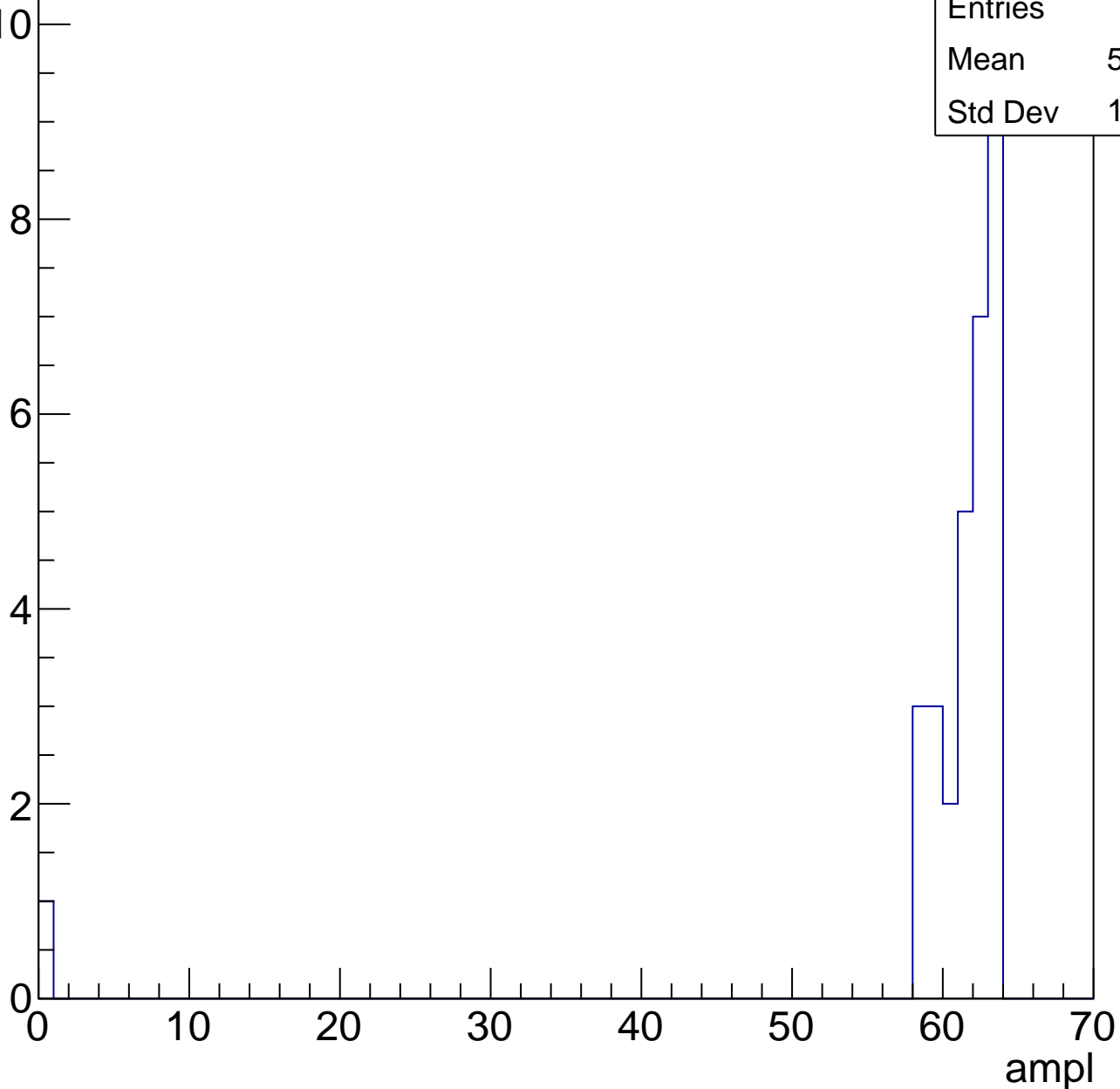
Entries	67
Mean	56.91
Std Dev	2.8

B0L001S, U11-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.35
Std Dev	10.96



B0L001S, U11-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch68, adc0

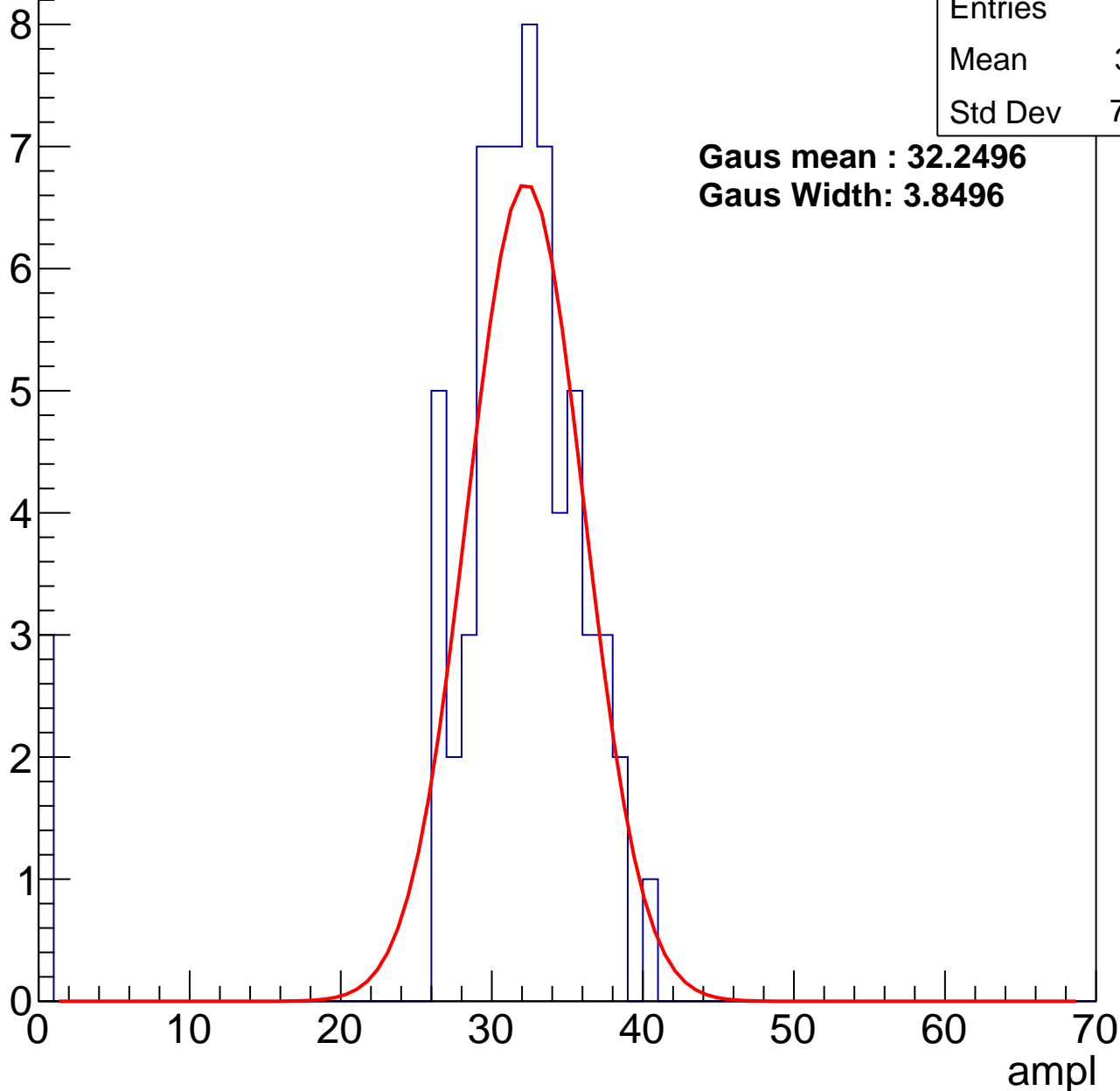
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.31
Std Dev	7.314

Gaus mean : 32.2496

Gaus Width: 3.8496



B0L001S, U11-ch68, adc1

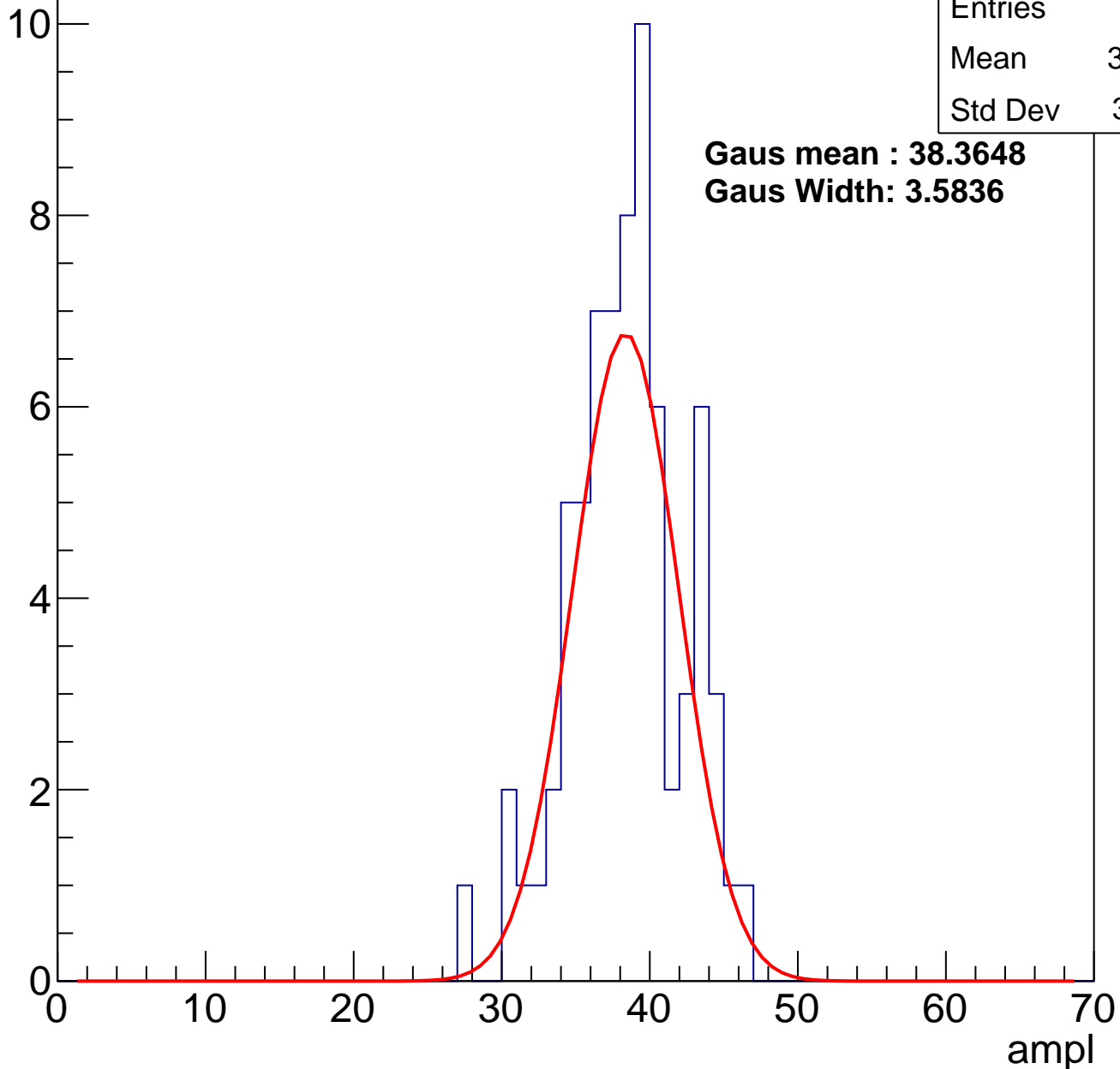
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.96
Std Dev	3.781

Gaus mean : 38.3648

Gaus Width: 3.5836

Entry



B0L001S, U11-ch68, adc2

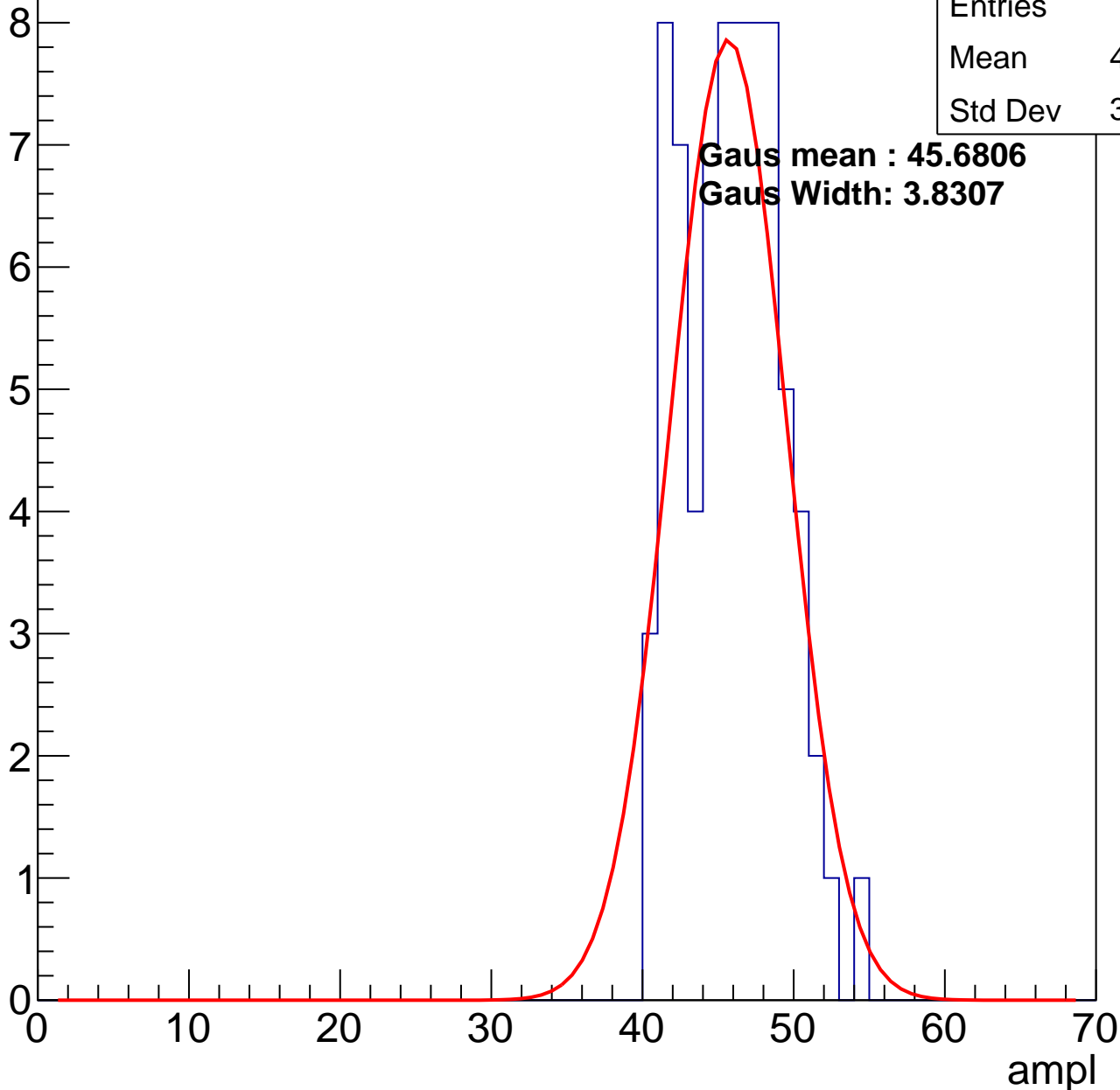
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	45.45
Std Dev	3.222

Gaus mean : 45.6806

Gaus Width: 3.8307

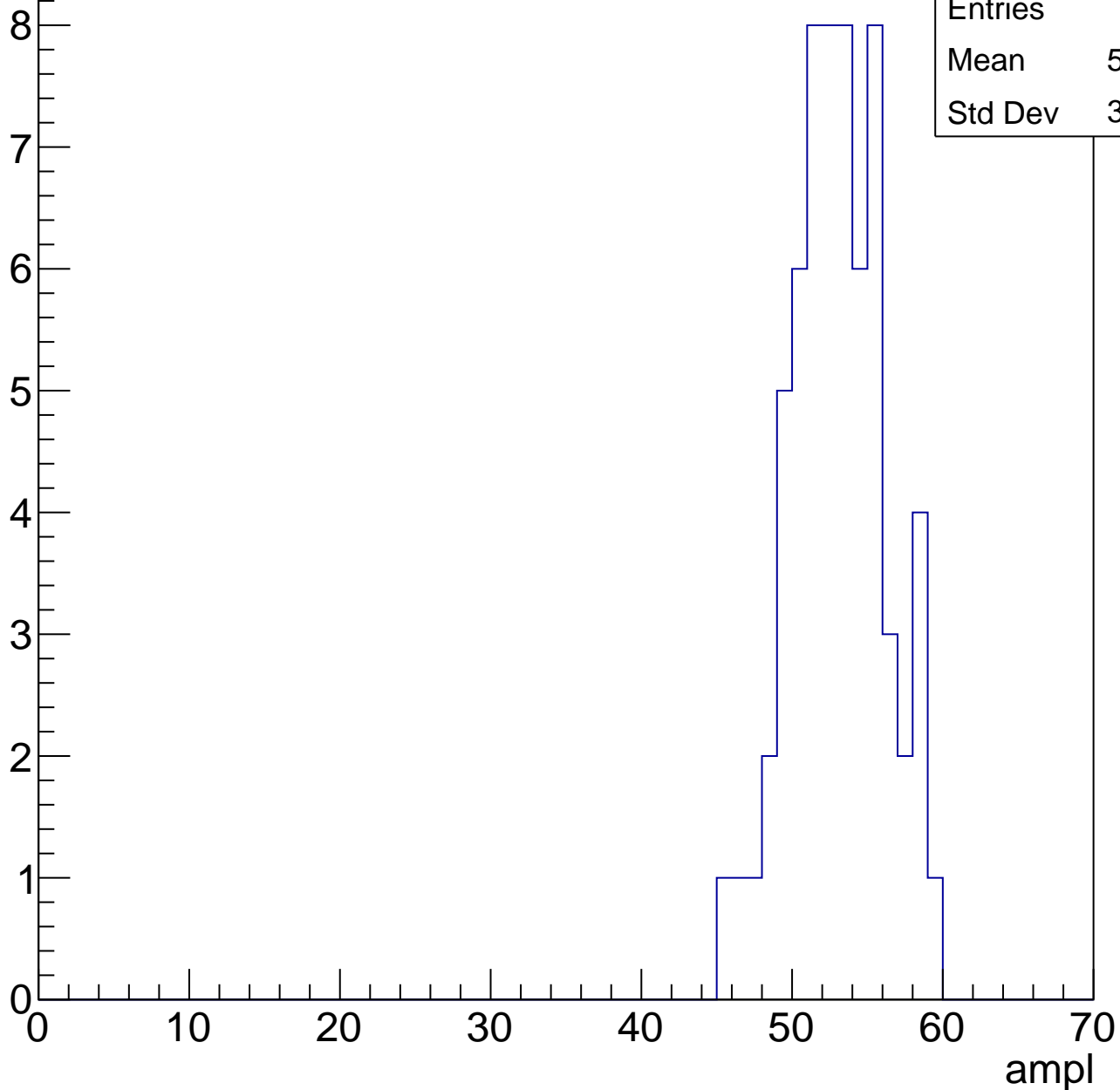


B0L001S, U11-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

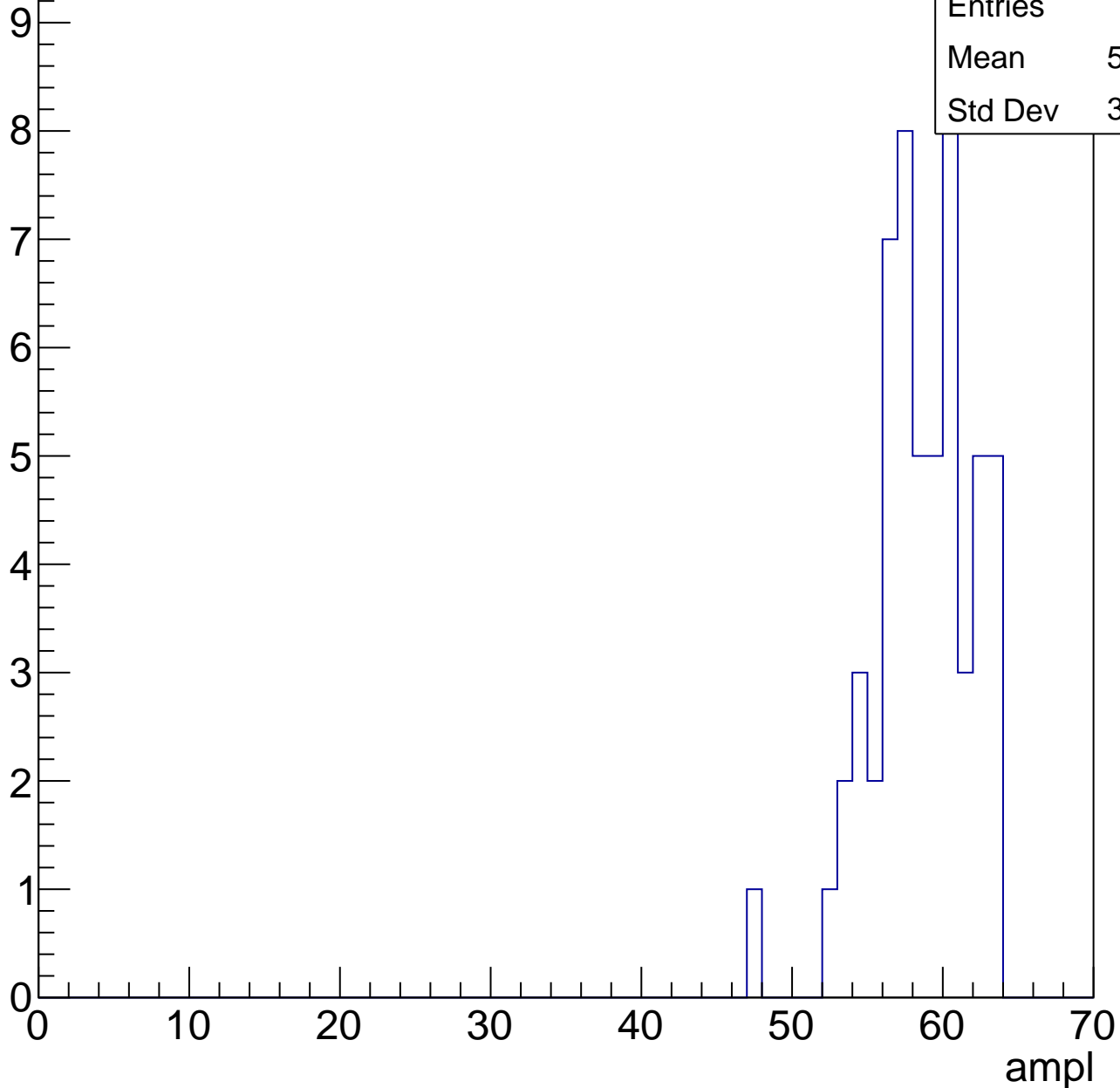
Entries	64
Mean	52.56
Std Dev	3.046



B0L001S, U11-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



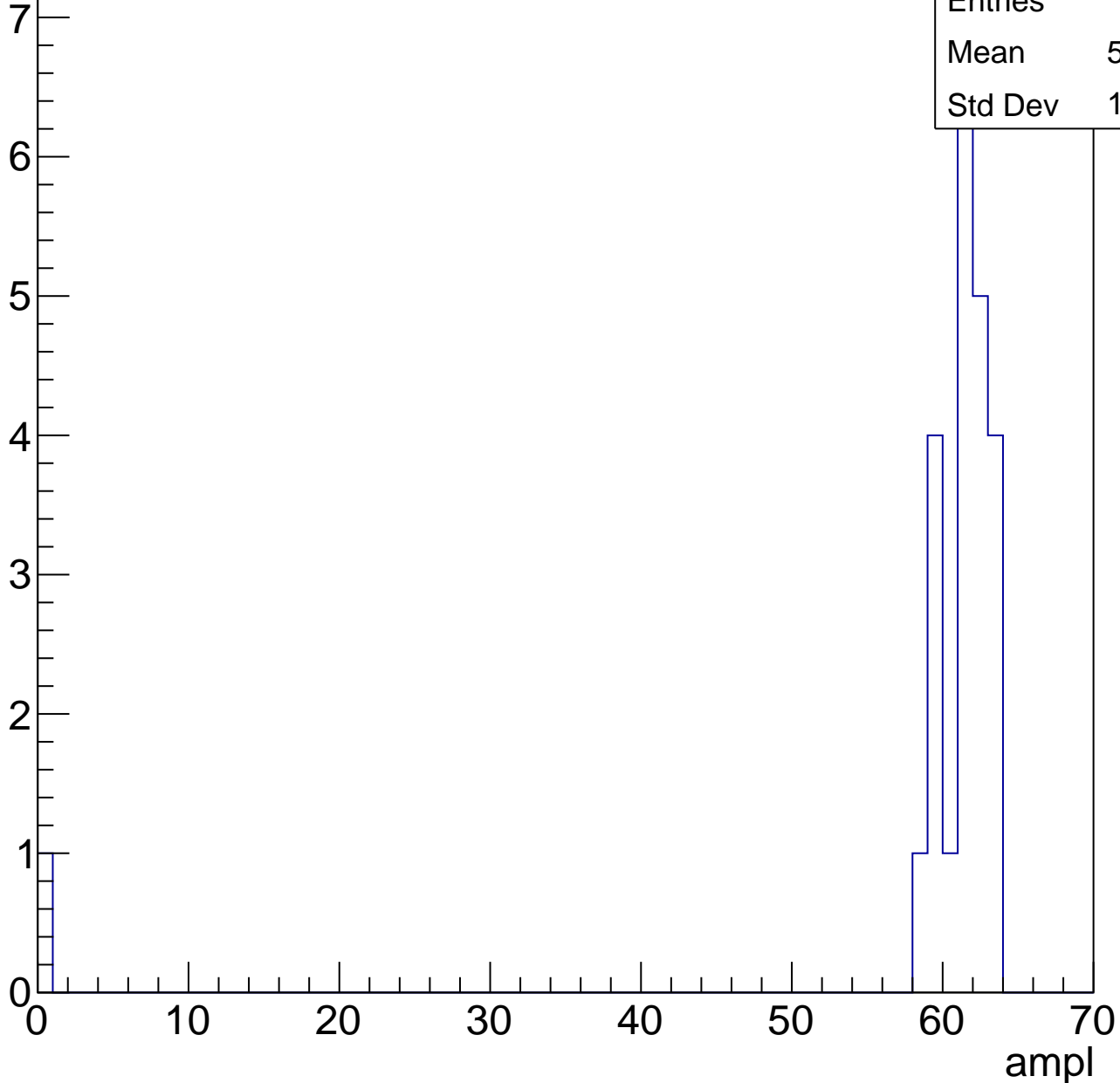
Entries	56
Mean	58.18
Std Dev	3.219

B0L001S, U11-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.39
Std Dev	12.53



B0L001S, U11-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch69, adc0

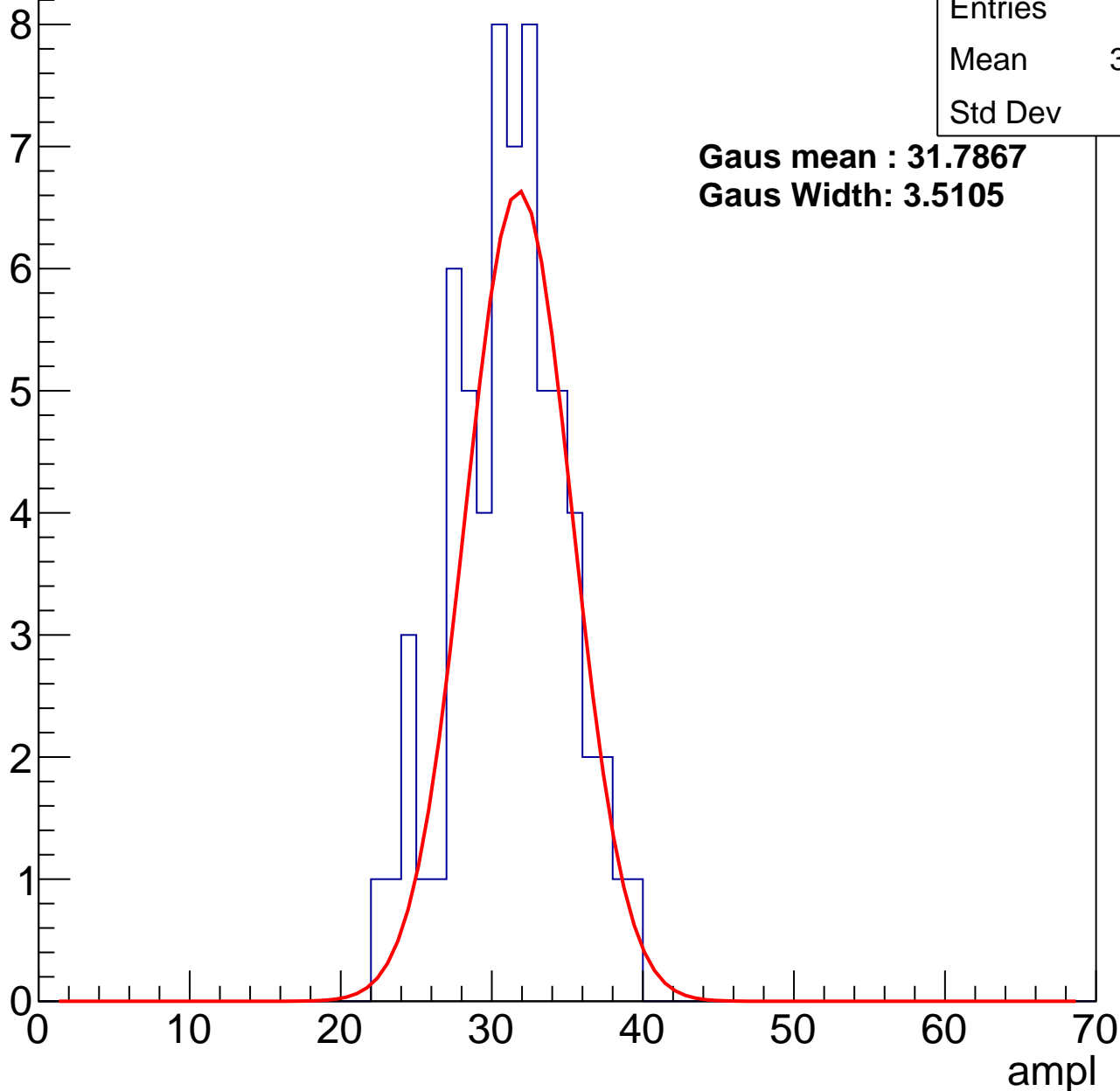
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.72
Std Dev	3.69

Gaus mean : 31.7867

Gaus Width: 3.5105



B0L001S, U11-ch69, adc1

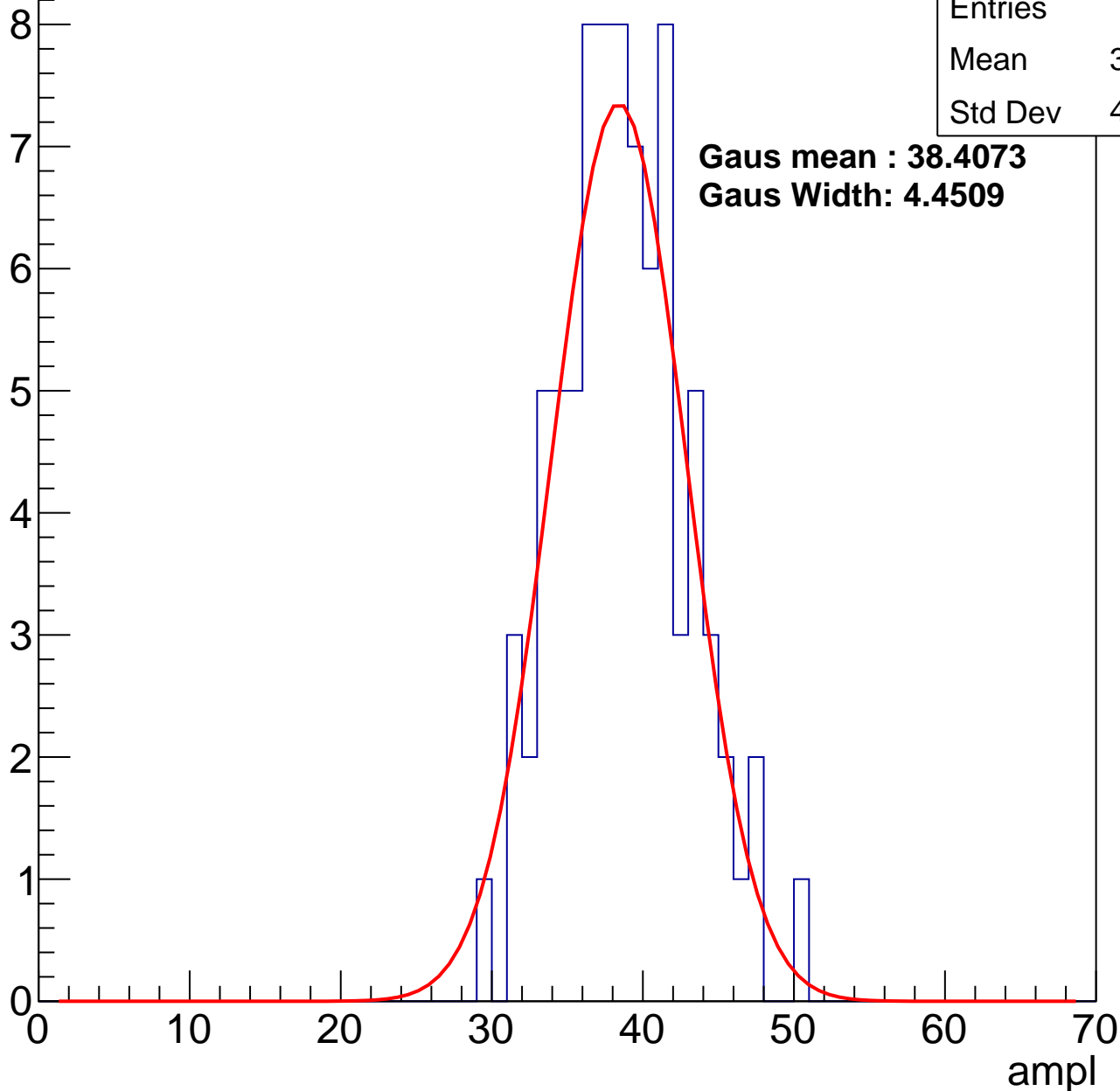
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	38.29
Std Dev	4.158

Gaus mean : 38.4073

Gaus Width: 4.4509

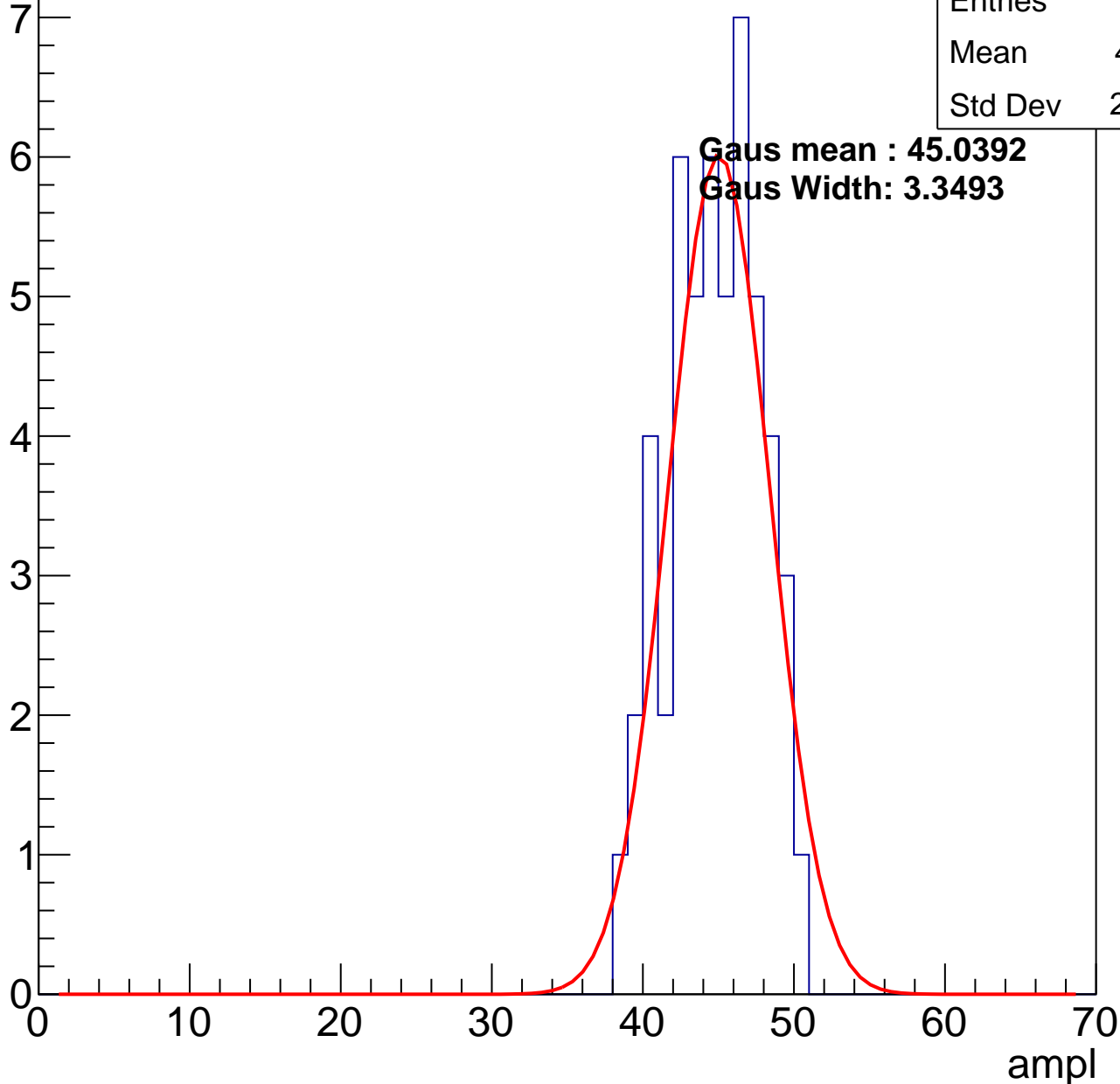


B0L001S, U11-ch69, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.31
Std Dev	2.954

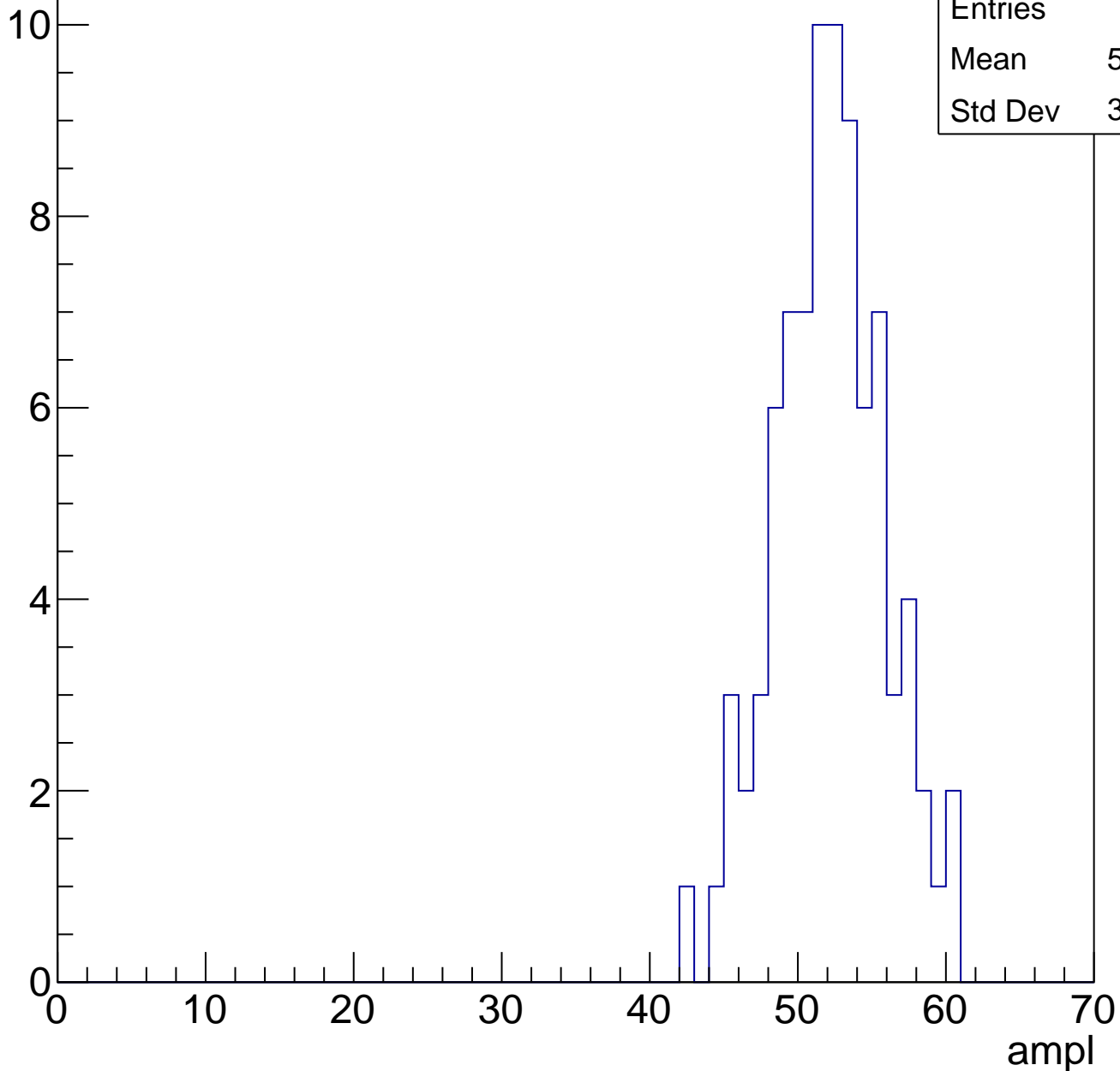


B0L001S, U11-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	51.69
Std Dev	3.684

Entry

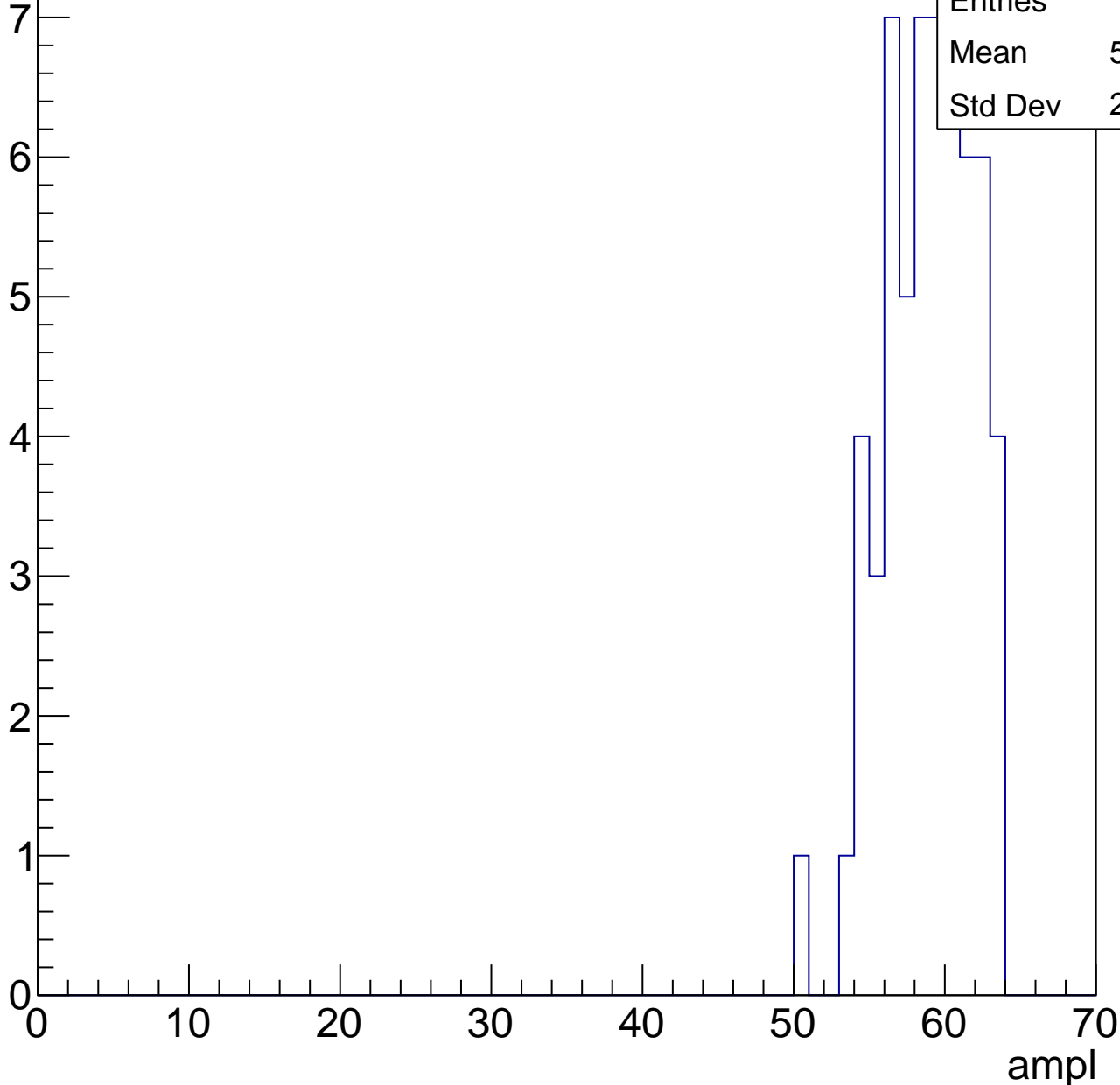


B0L001S, U11-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	58.45
Std Dev	2.896

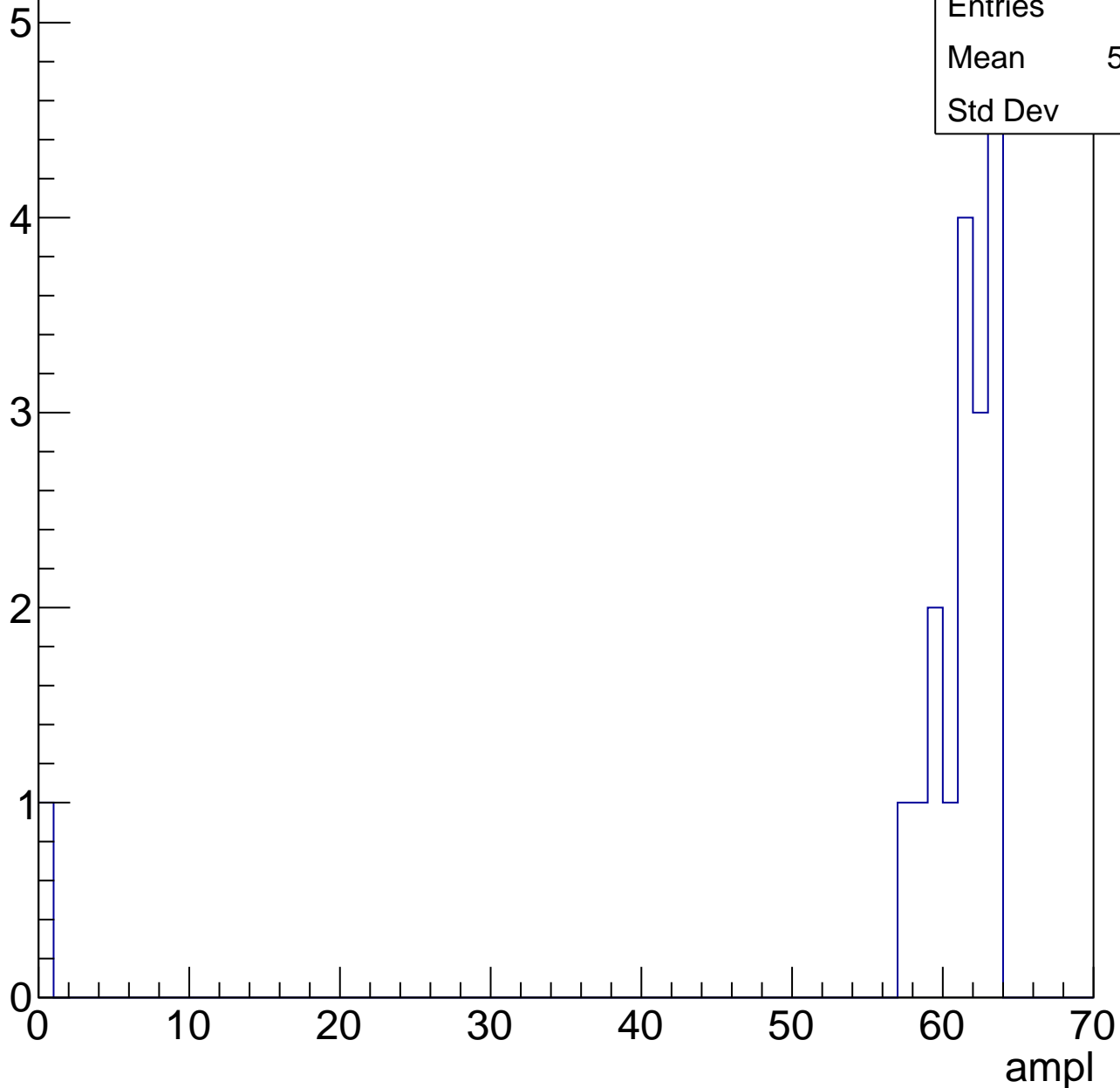


B0L001S, U11-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	57.67
Std Dev	14.1



B0L001S, U11-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch70, adc0

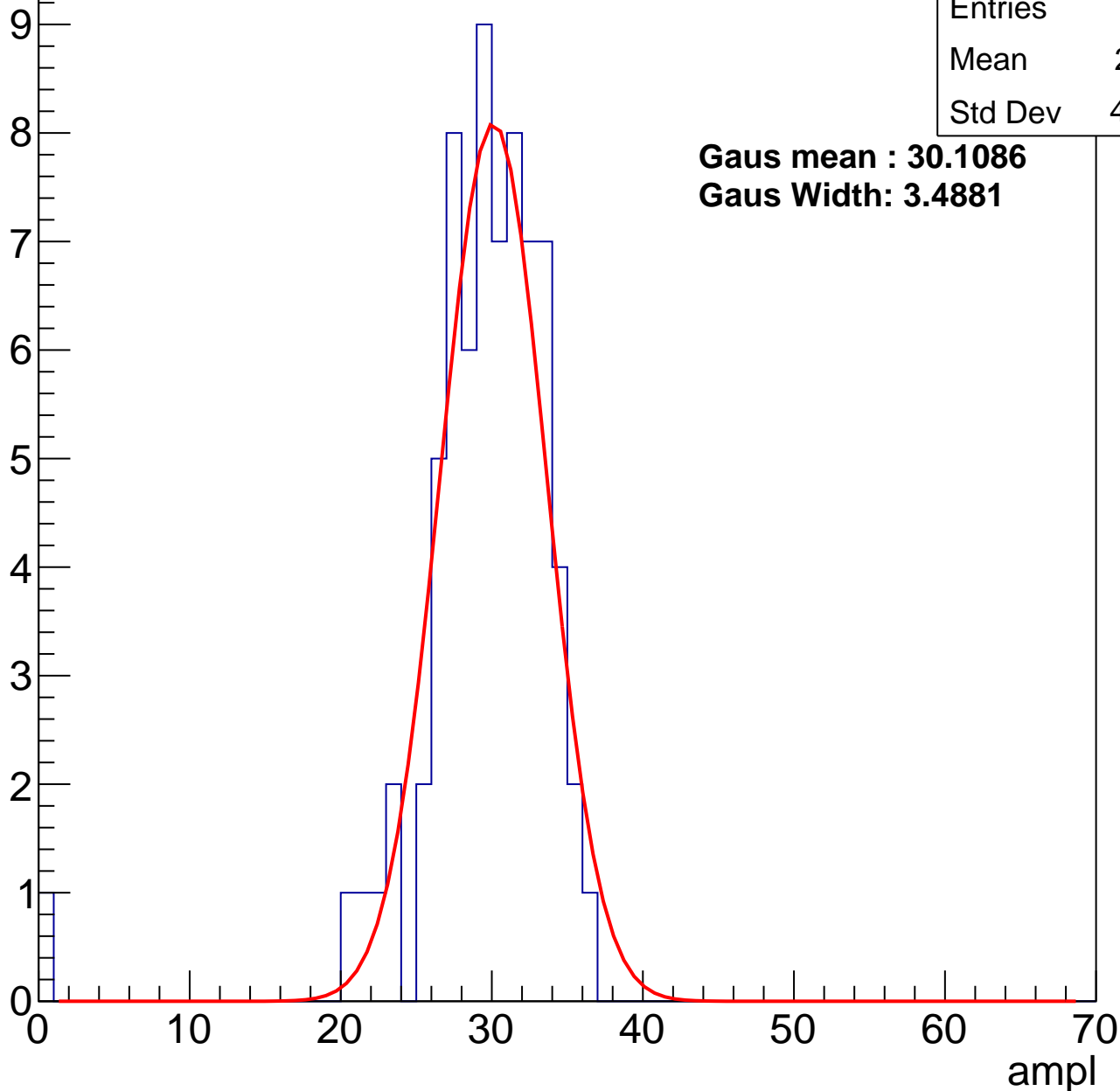
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.01
Std Dev	4.797

Gaus mean : 30.1086

Gaus Width: 3.4881



B0L001S, U11-ch70, adc1

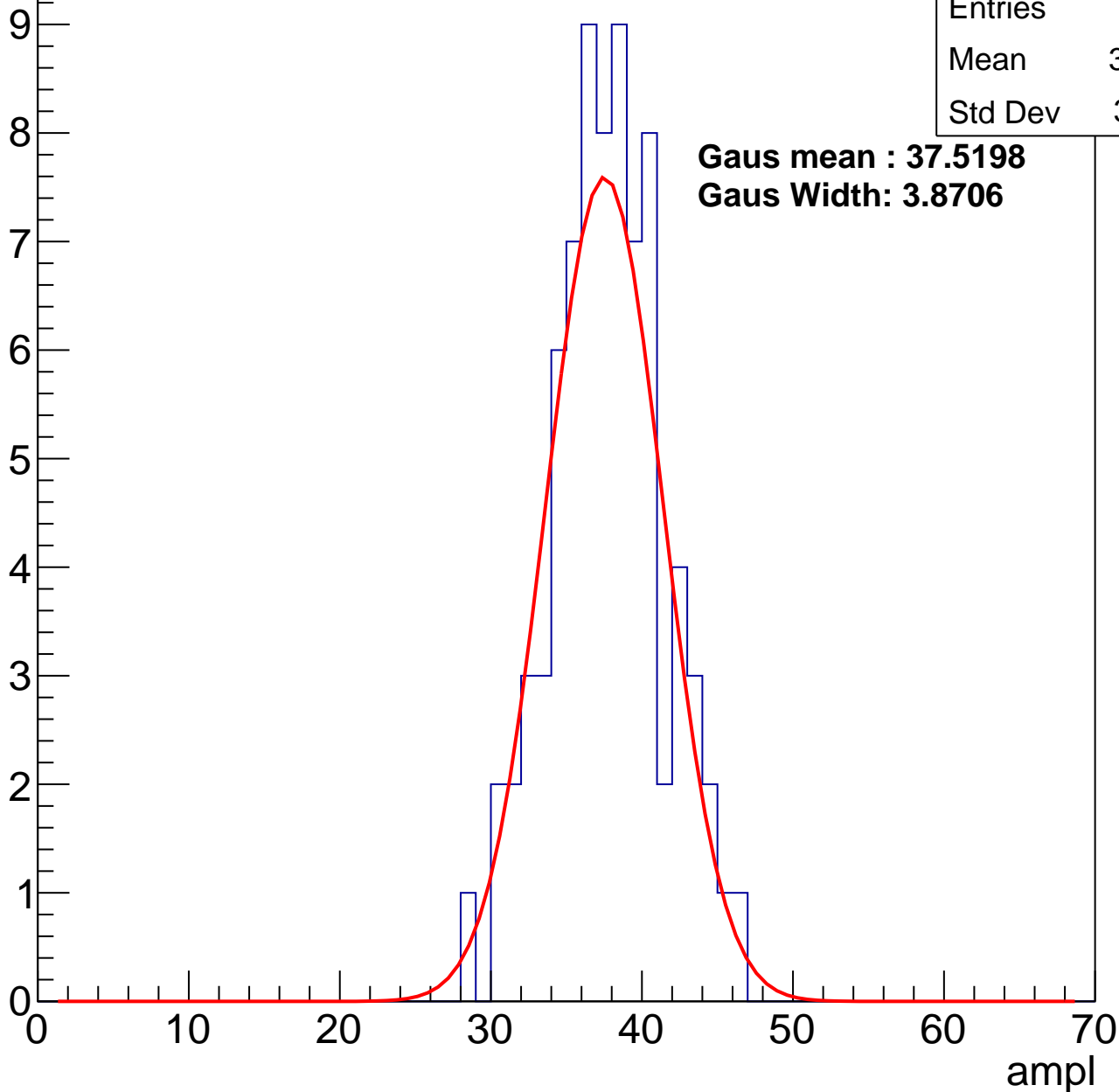
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.27
Std Dev	3.671

Gaus mean : 37.5198

Gaus Width: 3.8706



B0L001S, U11-ch70, adc2

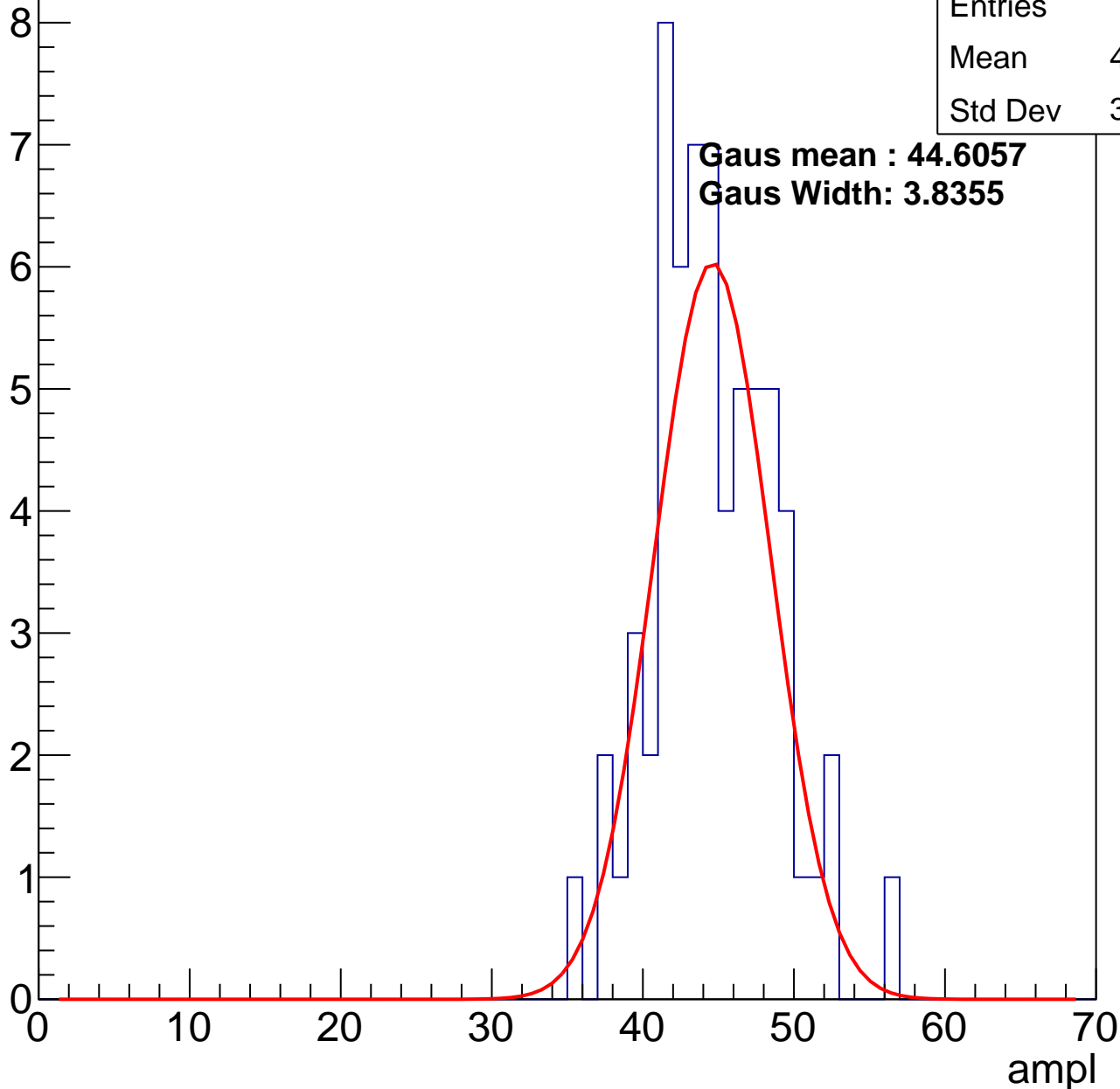
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.23
Std Dev	3.976

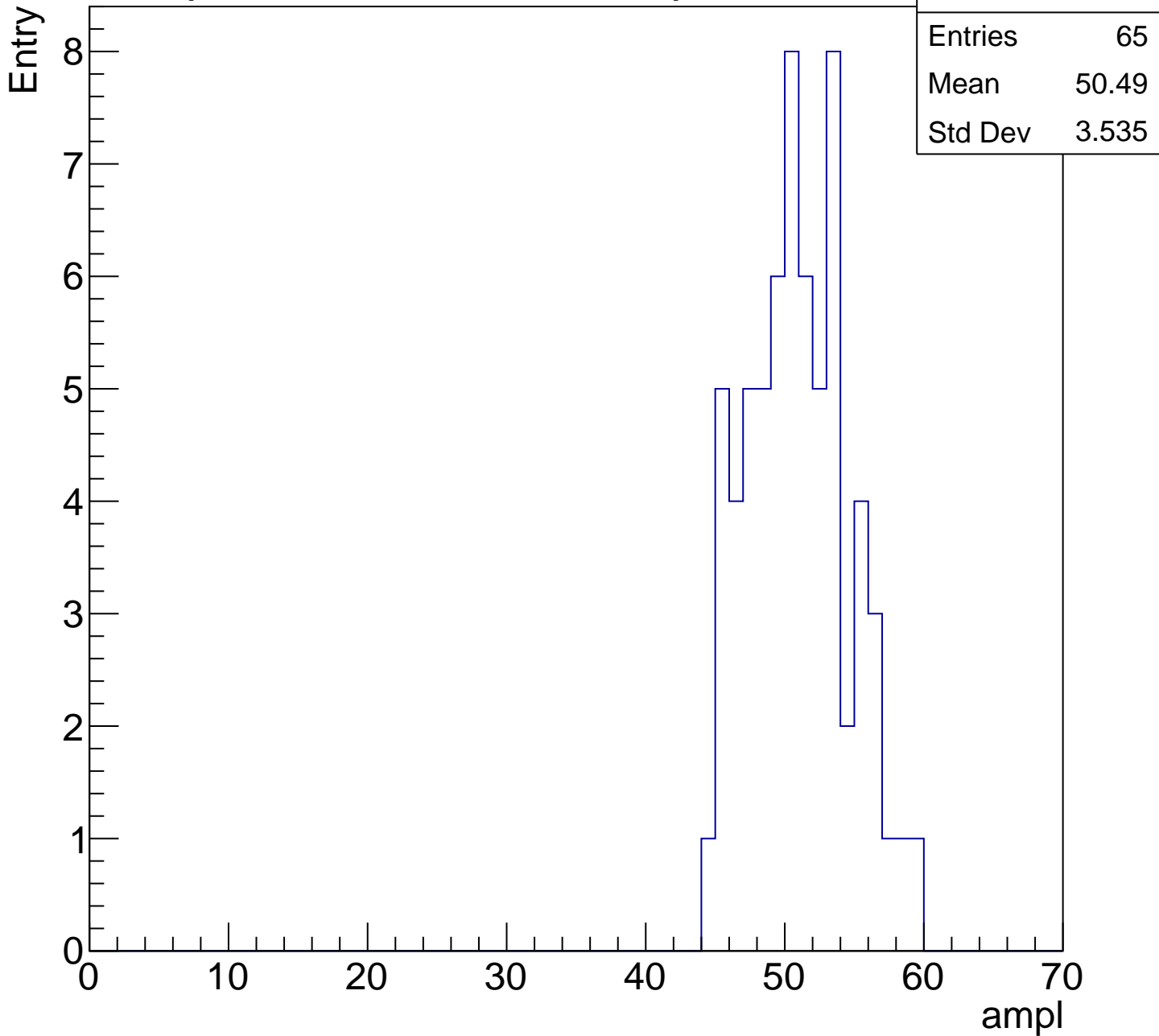
Gaus mean : 44.6057

Gaus Width: 3.8355



B0L001S, U11-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

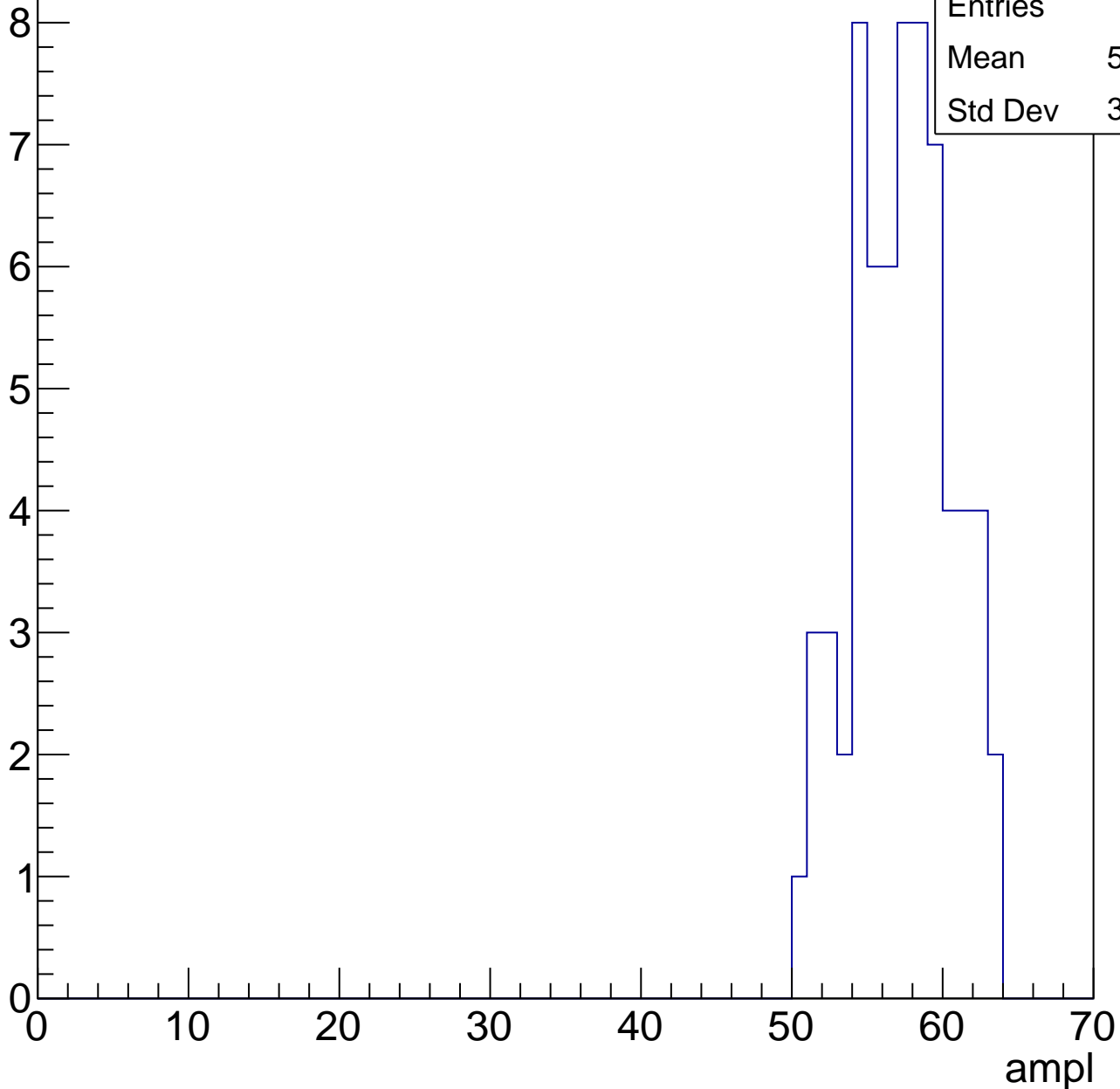


B0L001S, U11-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.88
Std Dev	3.193

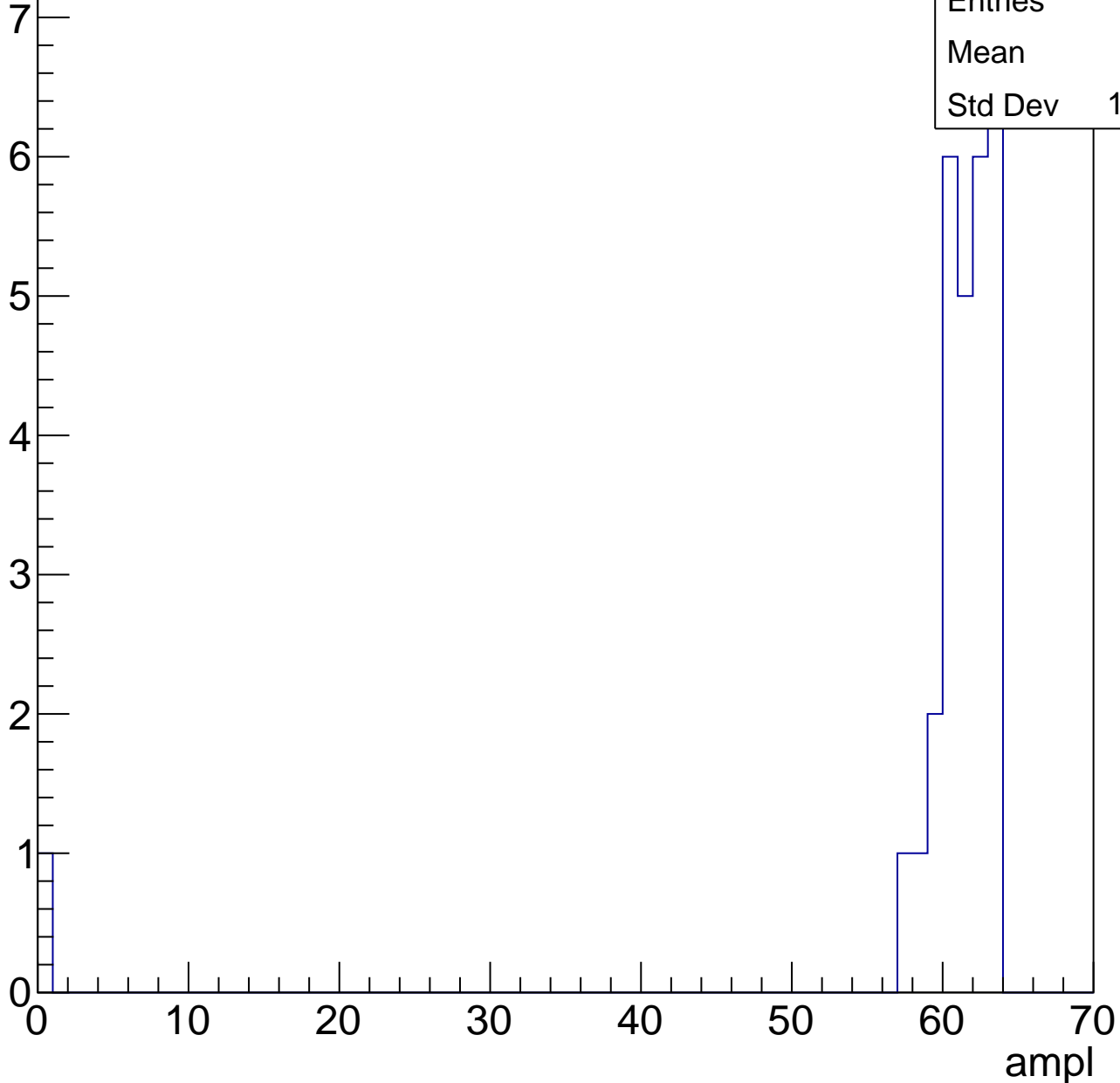


B0L001S, U11-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59
Std Dev	11.26



B0L001S, U11-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch71, adc0

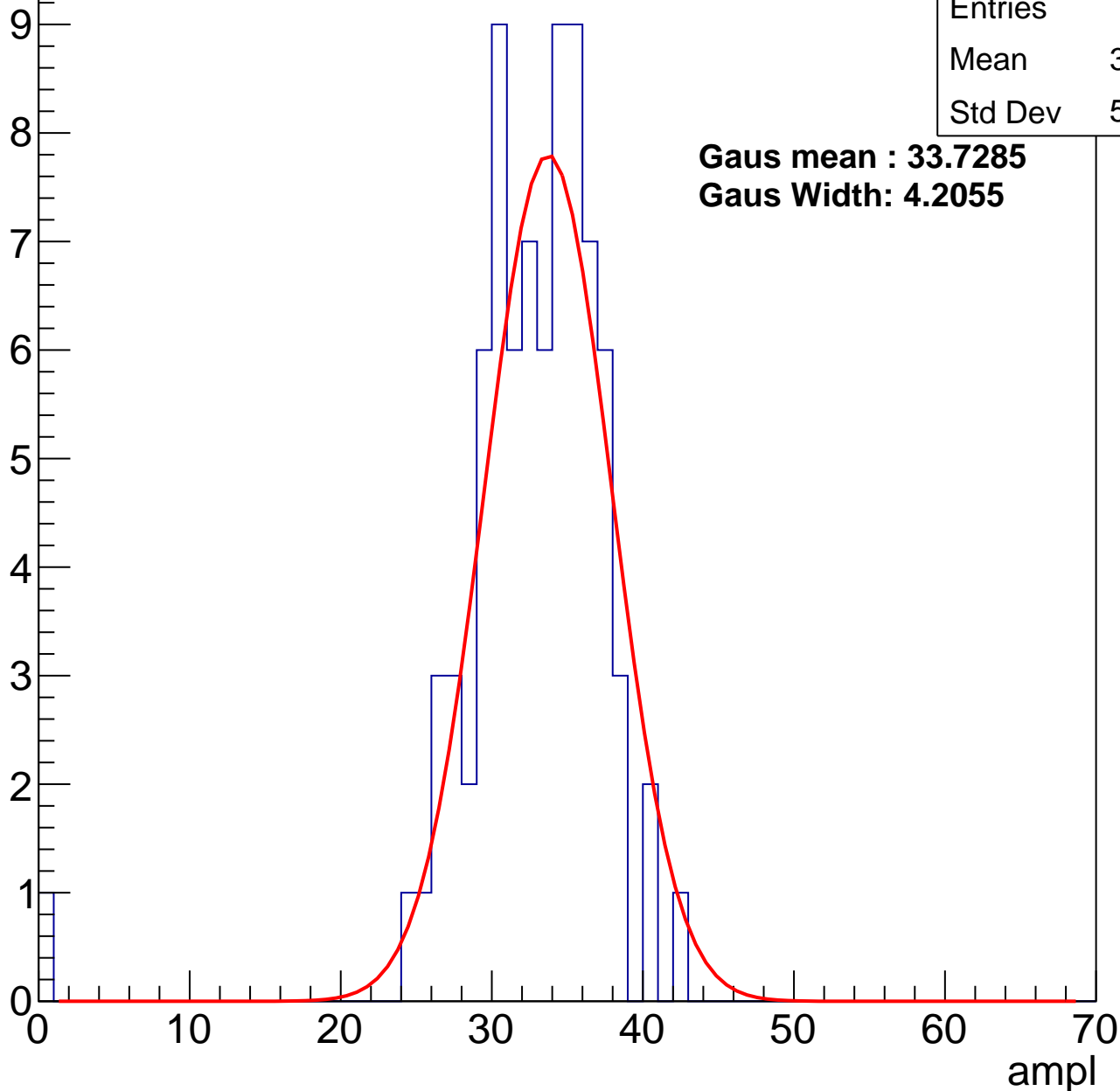
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	32.28
Std Dev	5.135

Gaus mean : 33.7285

Gaus Width: 4.2055



B0L001S, U11-ch71, adc1

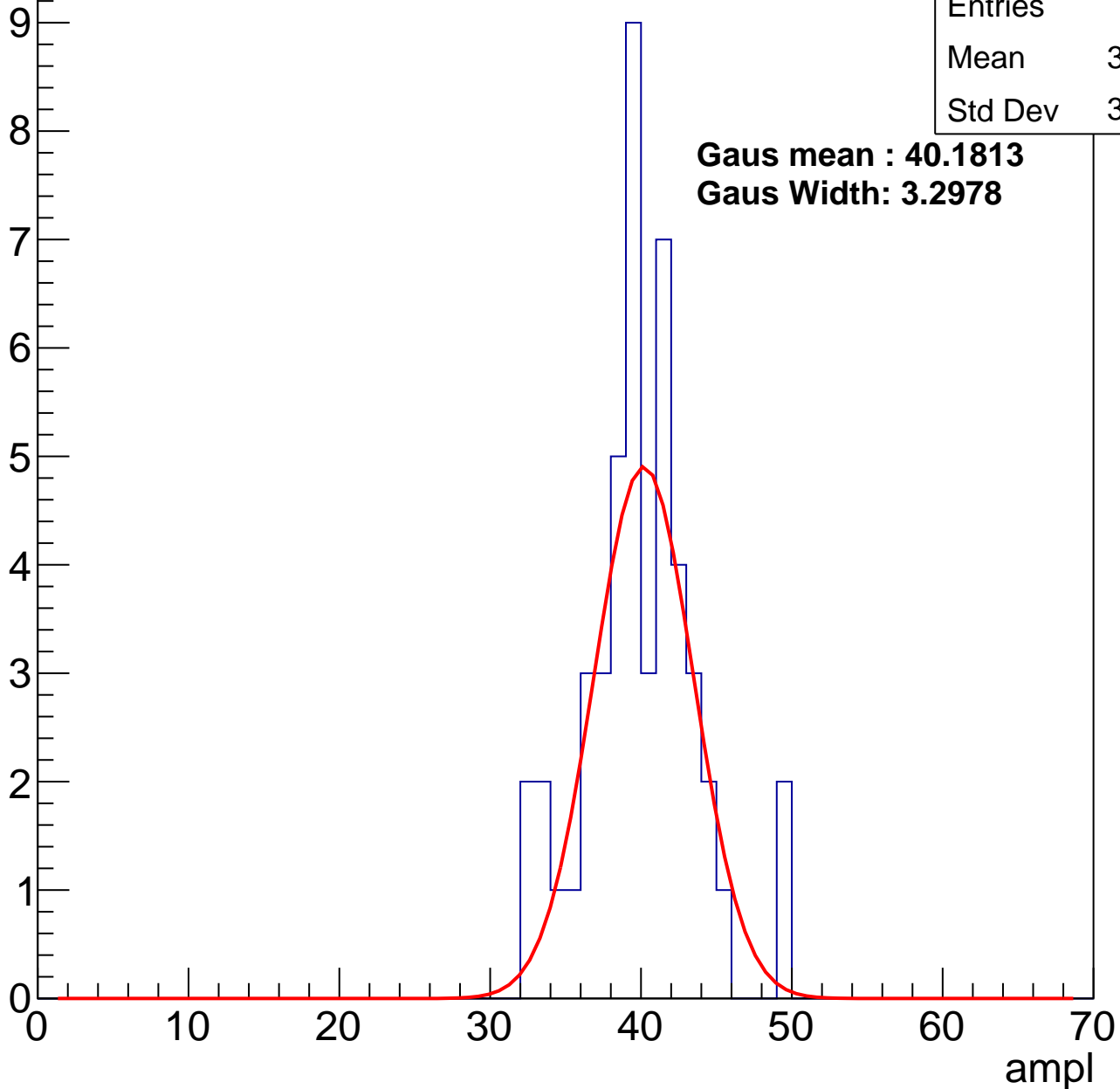
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	39.46
Std Dev	3.668

Gaus mean : 40.1813

Gaus Width: 3.2978



B0L001S, U11-ch71, adc2

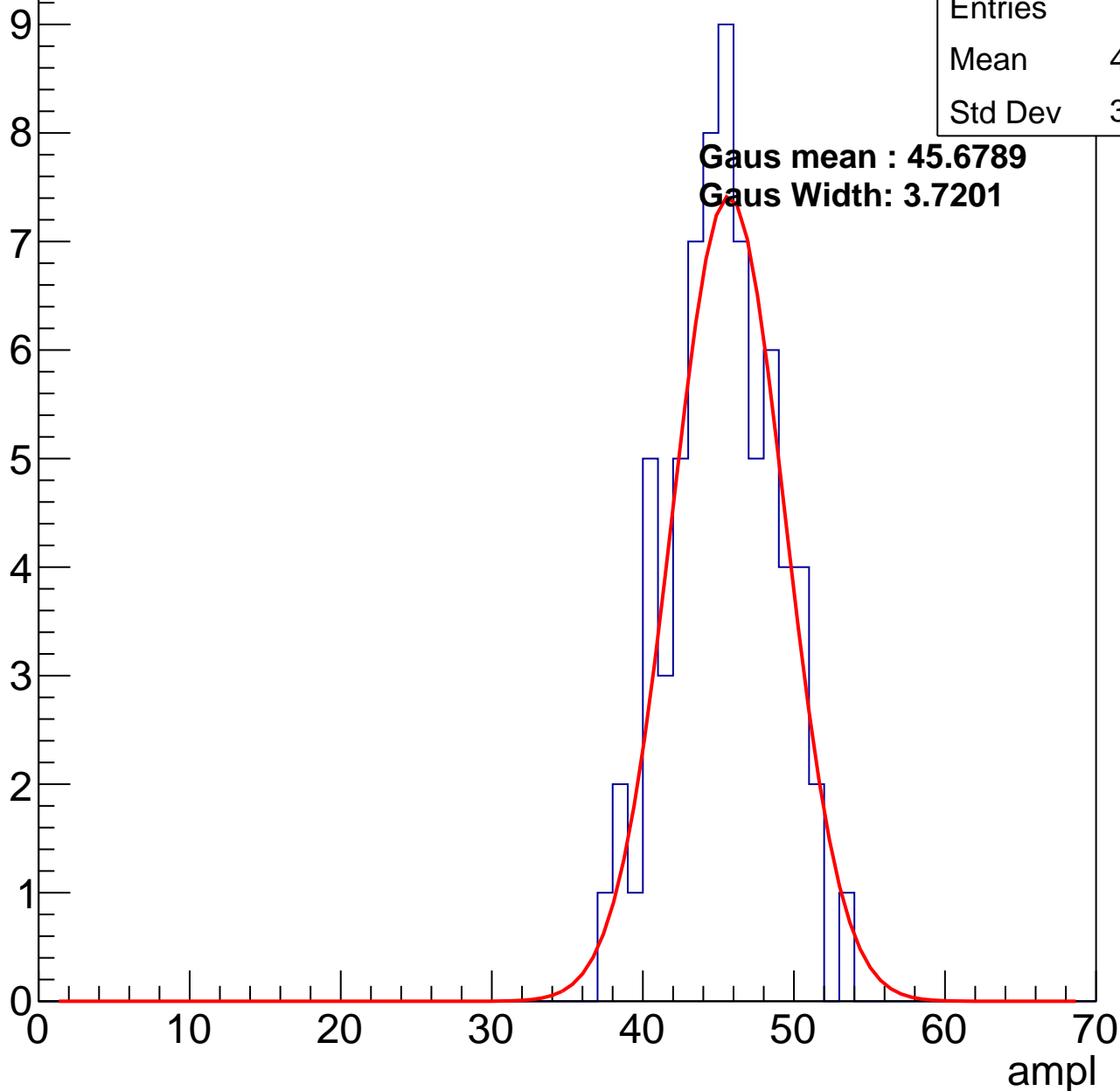
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.84
Std Dev	3.446

Gaus mean : 45.6789

Gaus Width: 3.7201



B0L001S, U11-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

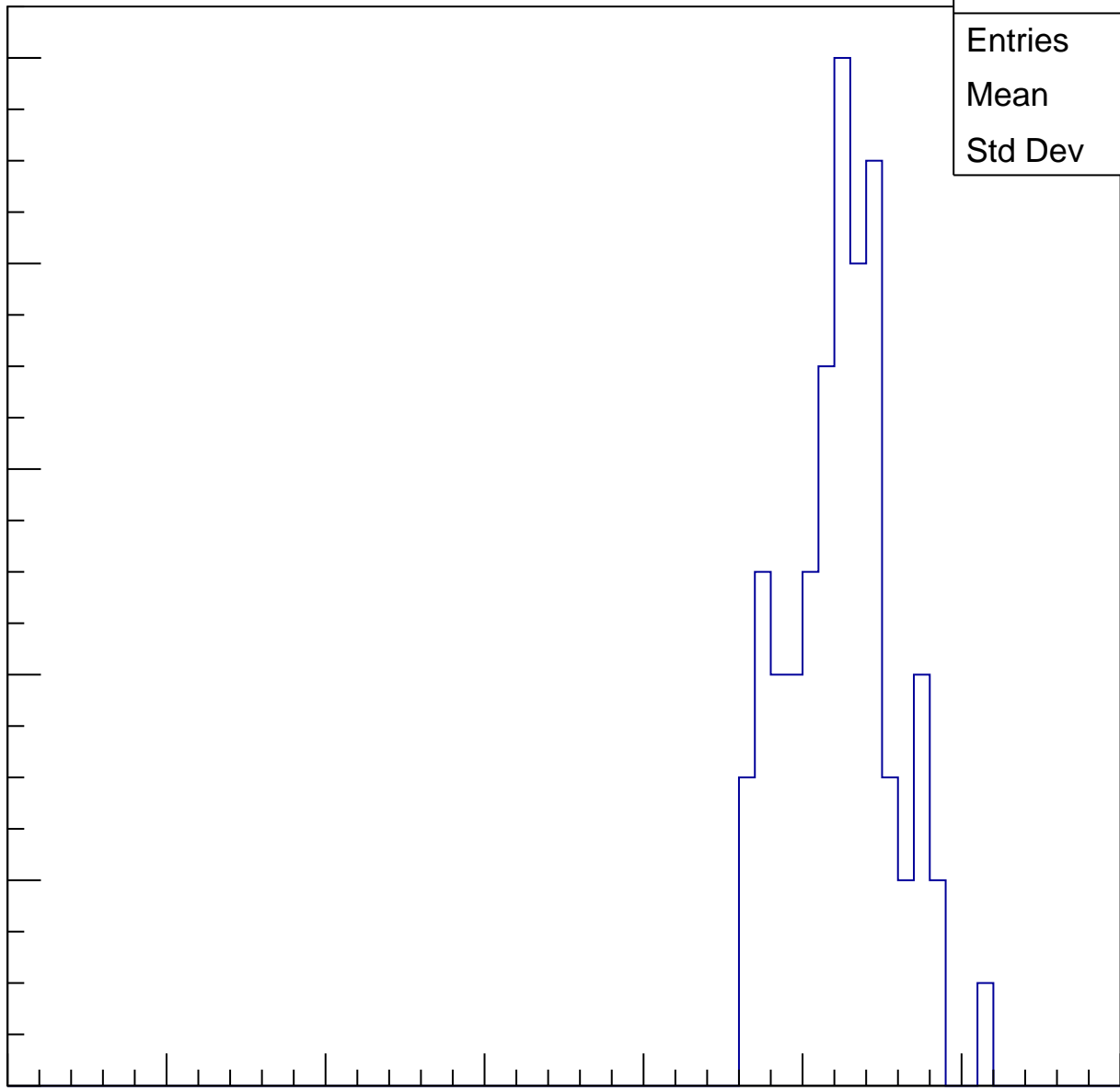
Entries	67
Mean	51.94
Std Dev	3.268

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

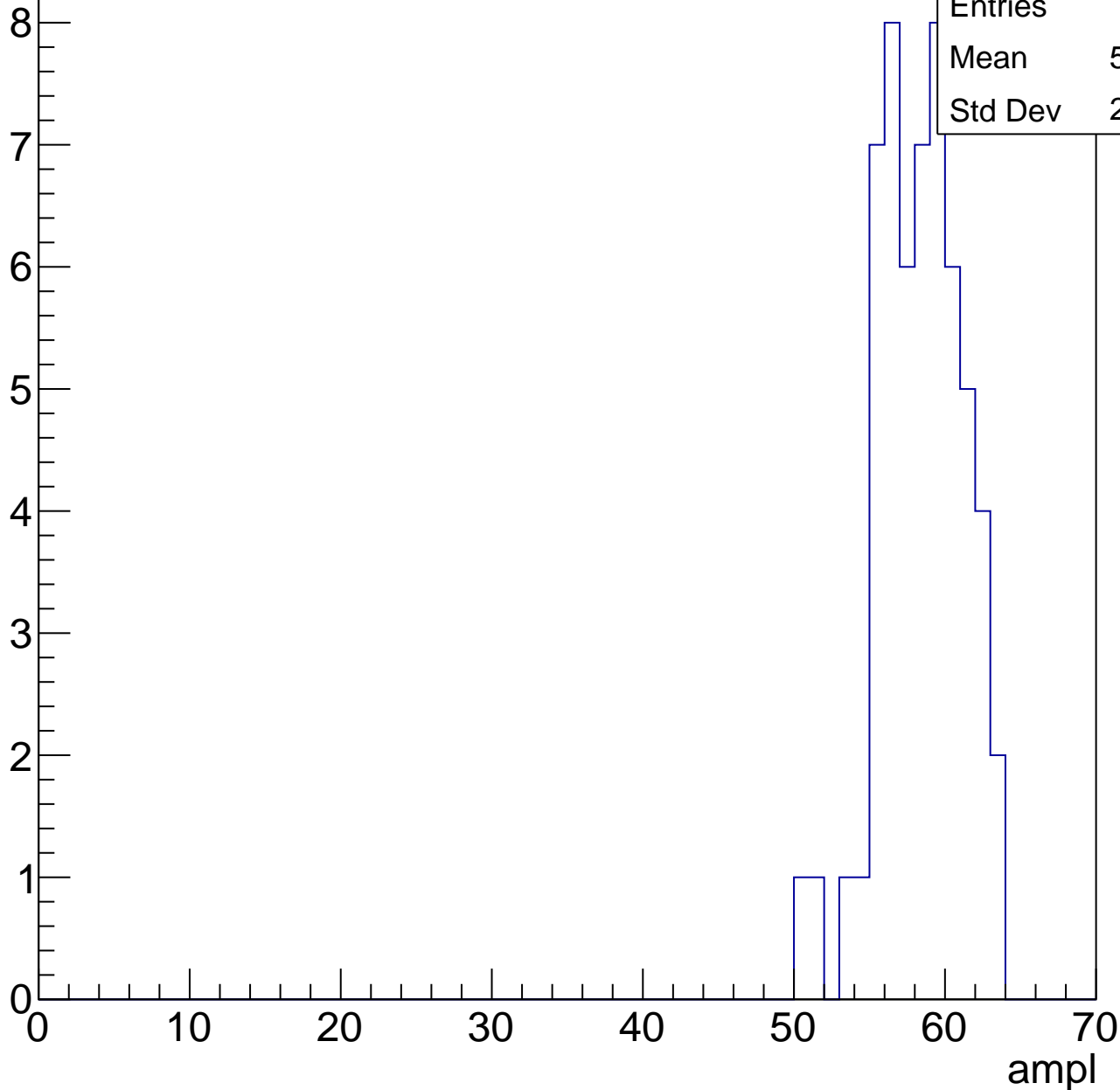


B0L001S, U11-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.89
Std Dev	2.795

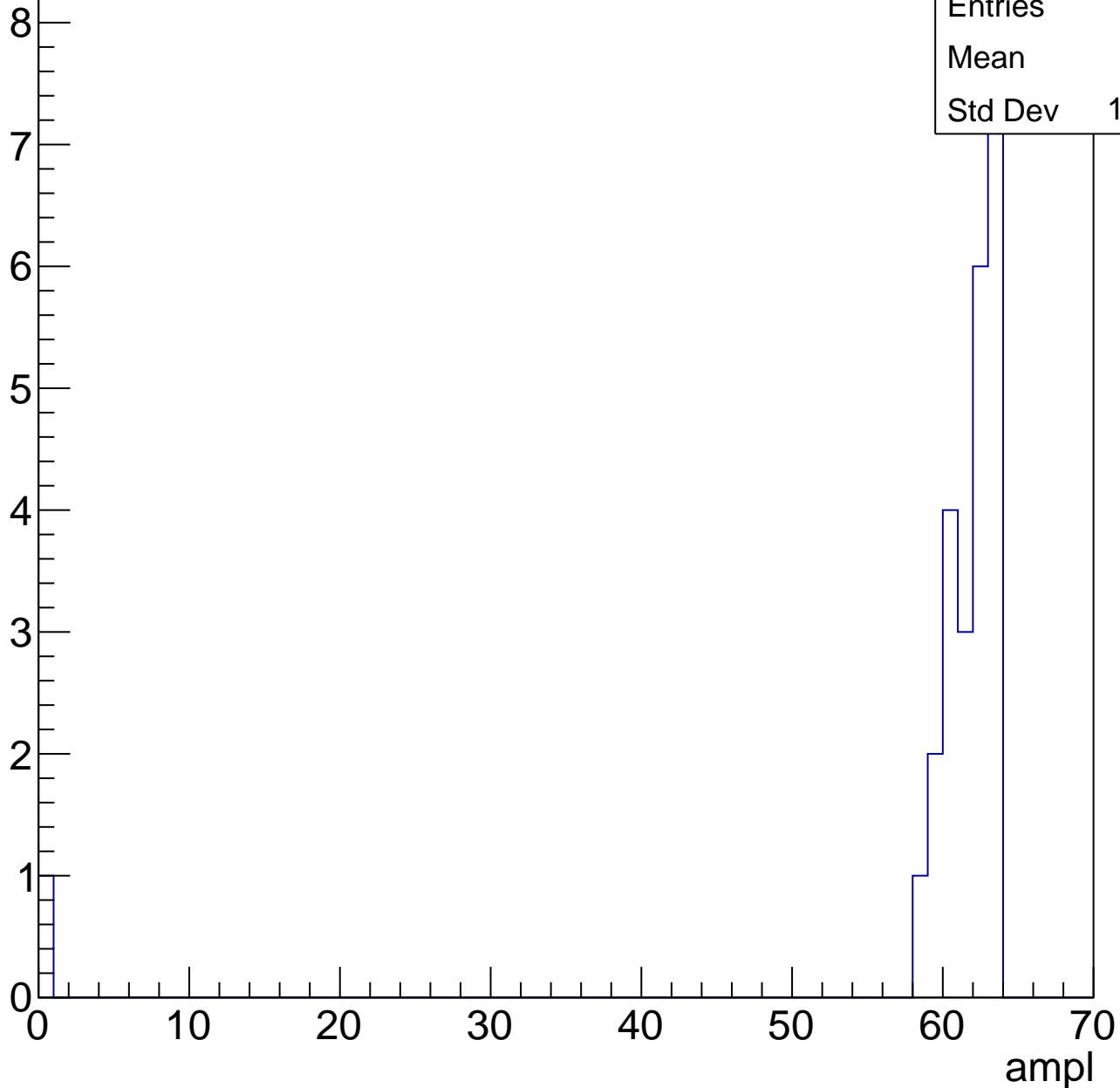


B0L001S, U11-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	59
Std Dev	12.13



B0L001S, U11-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch72, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	29.75
Std Dev	6.125

Gaus mean : 30.8638

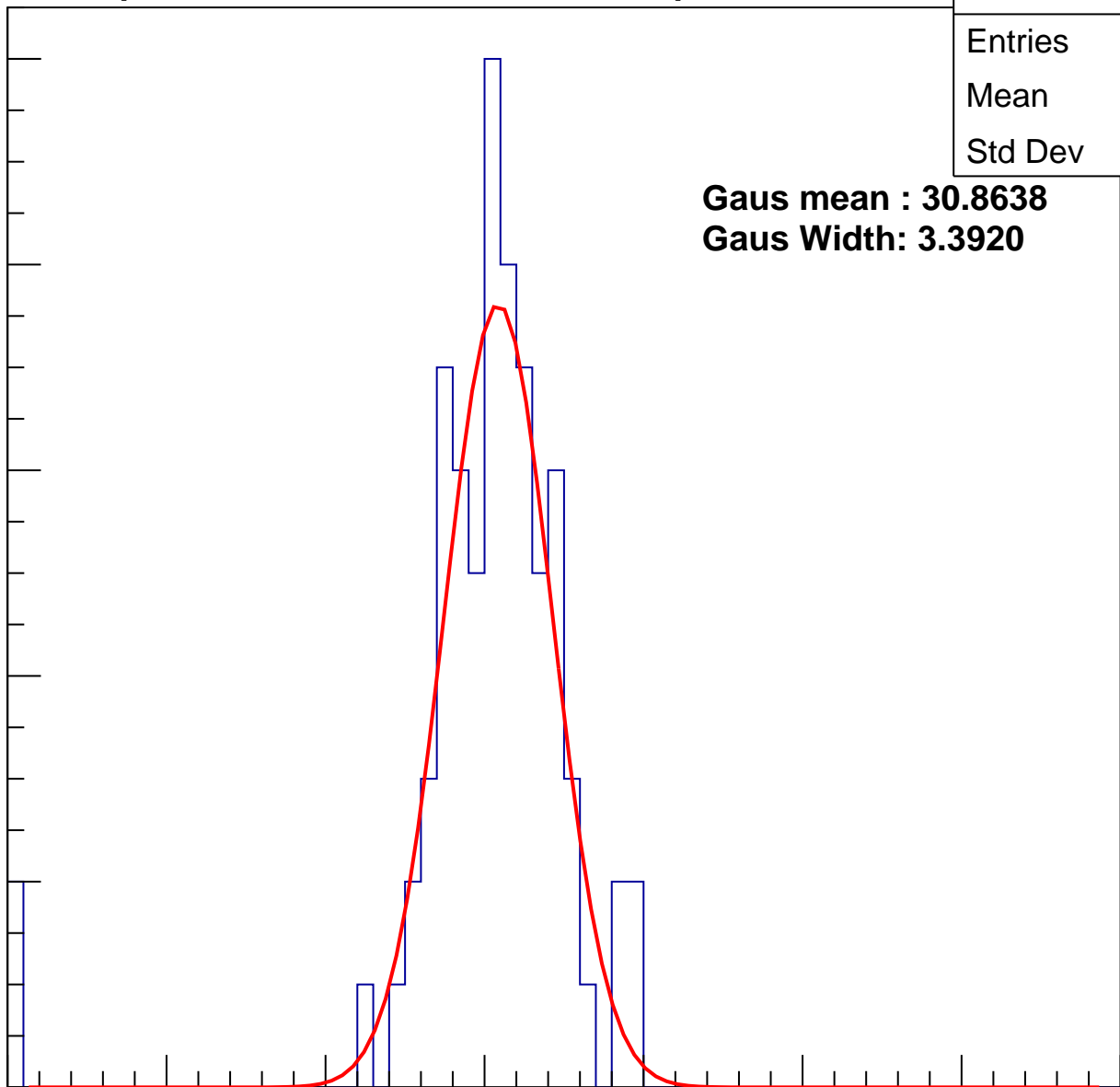
Gaus Width: 3.3920

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch72, adc1

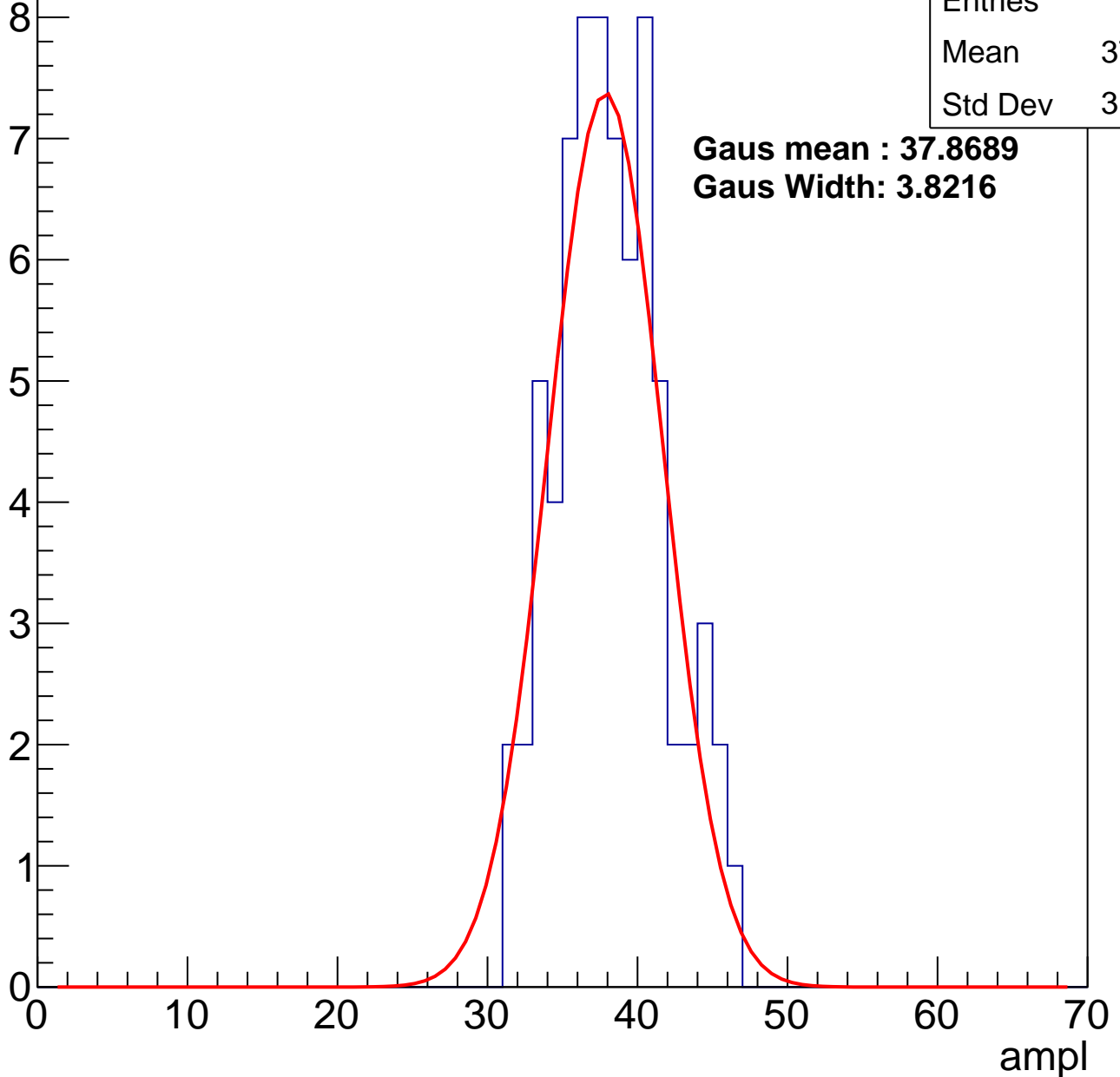
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.76
Std Dev	3.522

Gaus mean : 37.8689

Gaus Width: 3.8216



B0L001S, U11-ch72, adc2

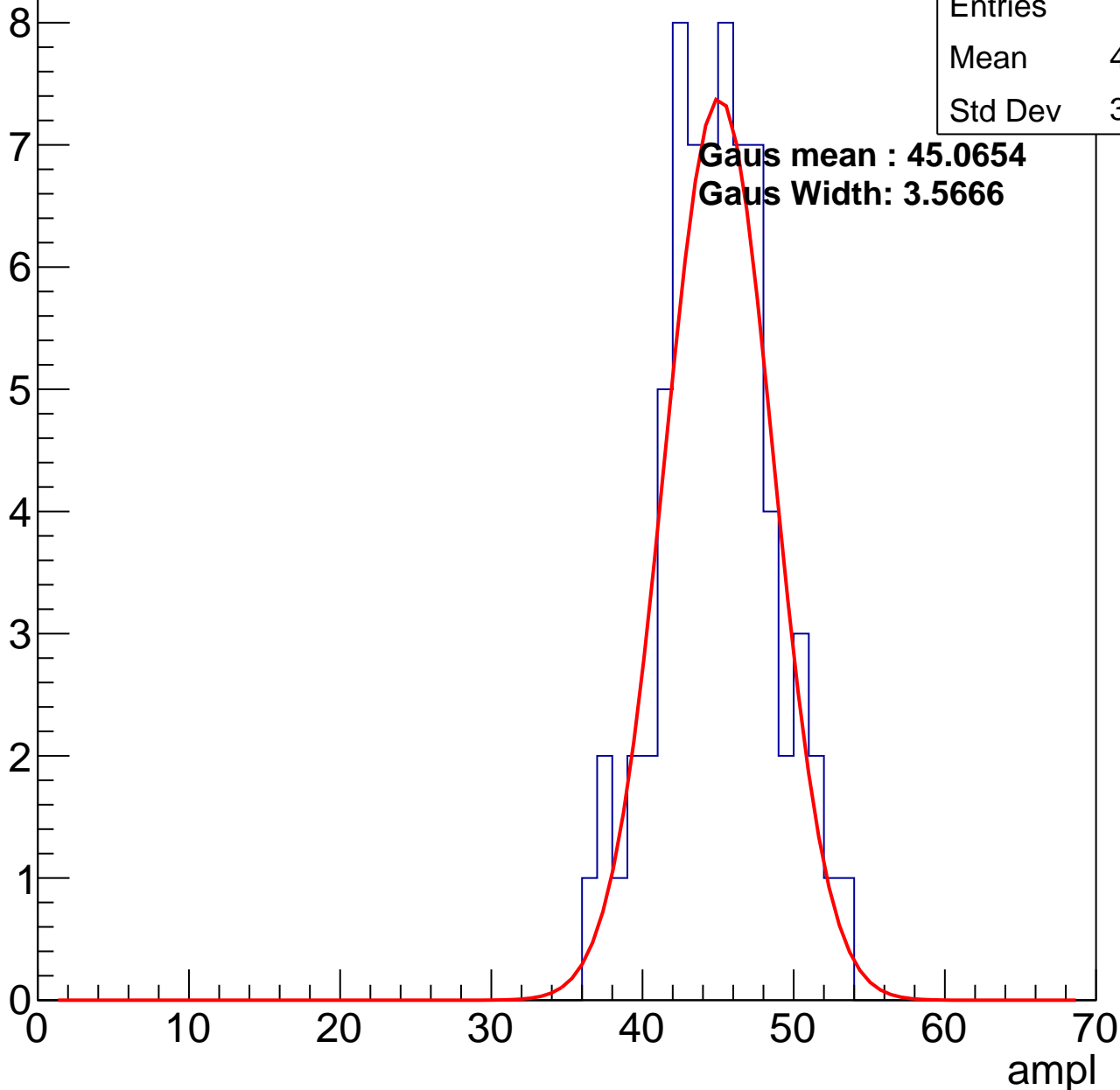
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.49
Std Dev	3.608

Gaus mean : 45.0654

Gaus Width: 3.5666

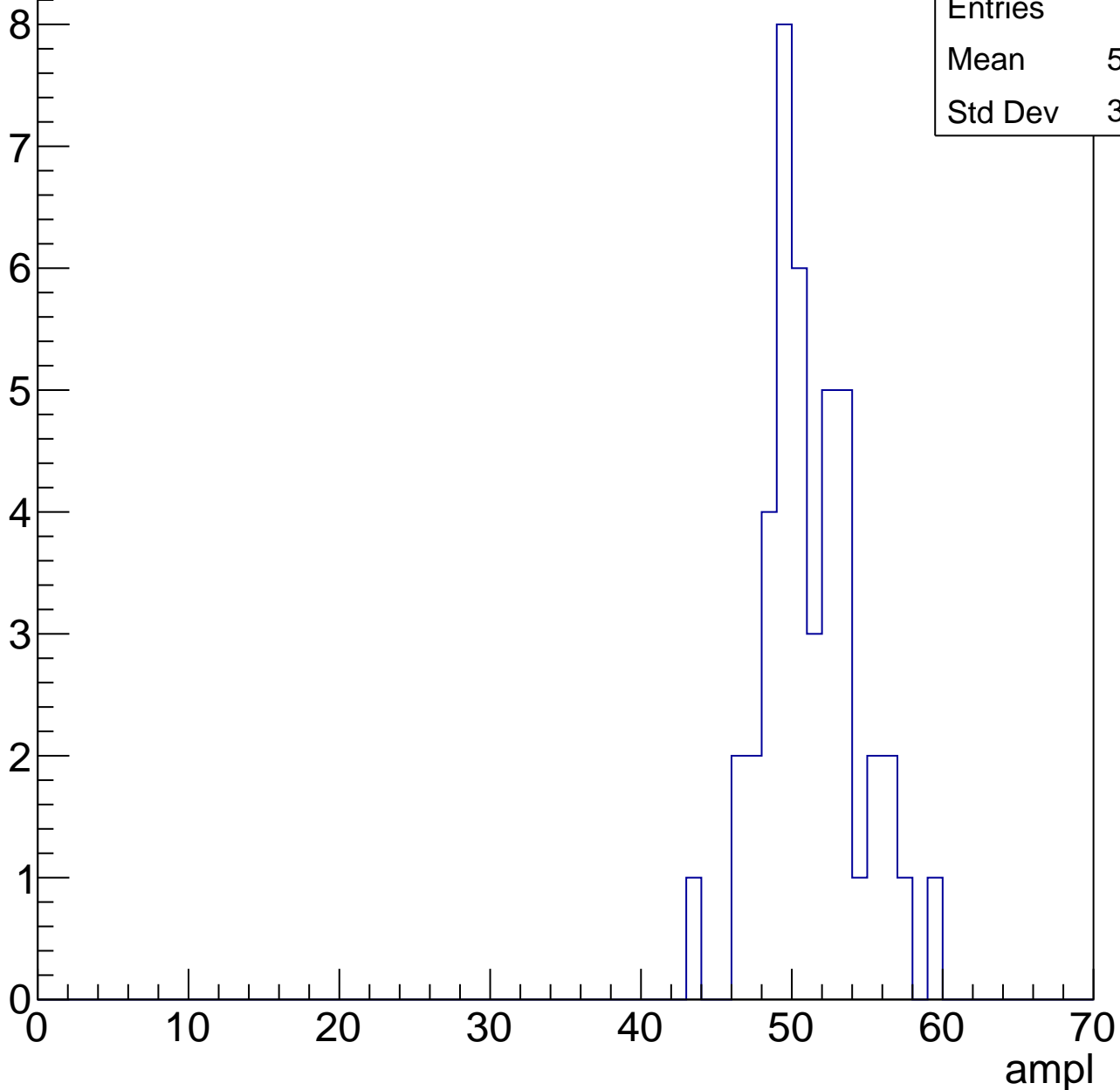


B0L001S, U11-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	50.77
Std Dev	3.176

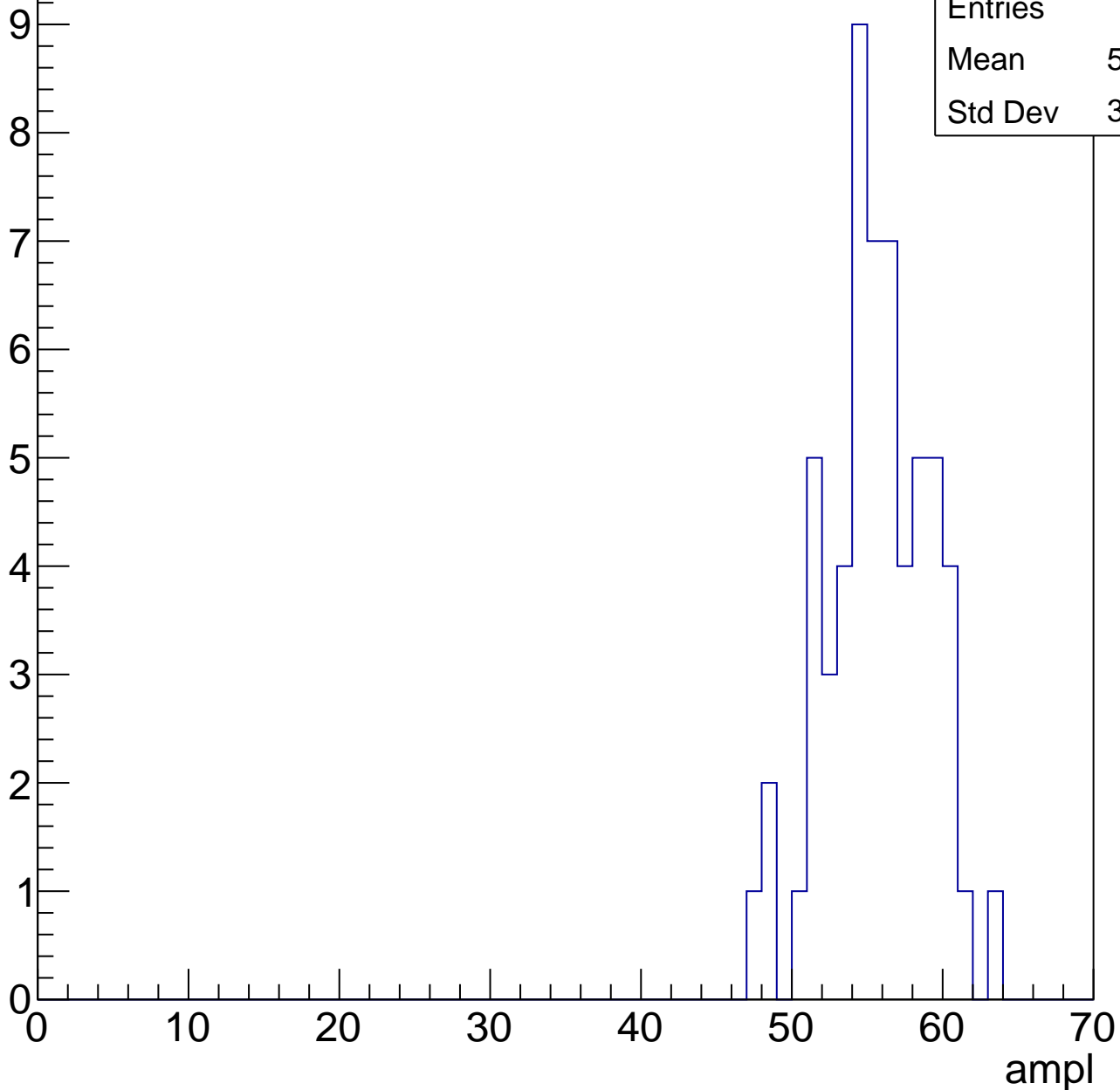


B0L001S, U11-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.19
Std Dev	3.357

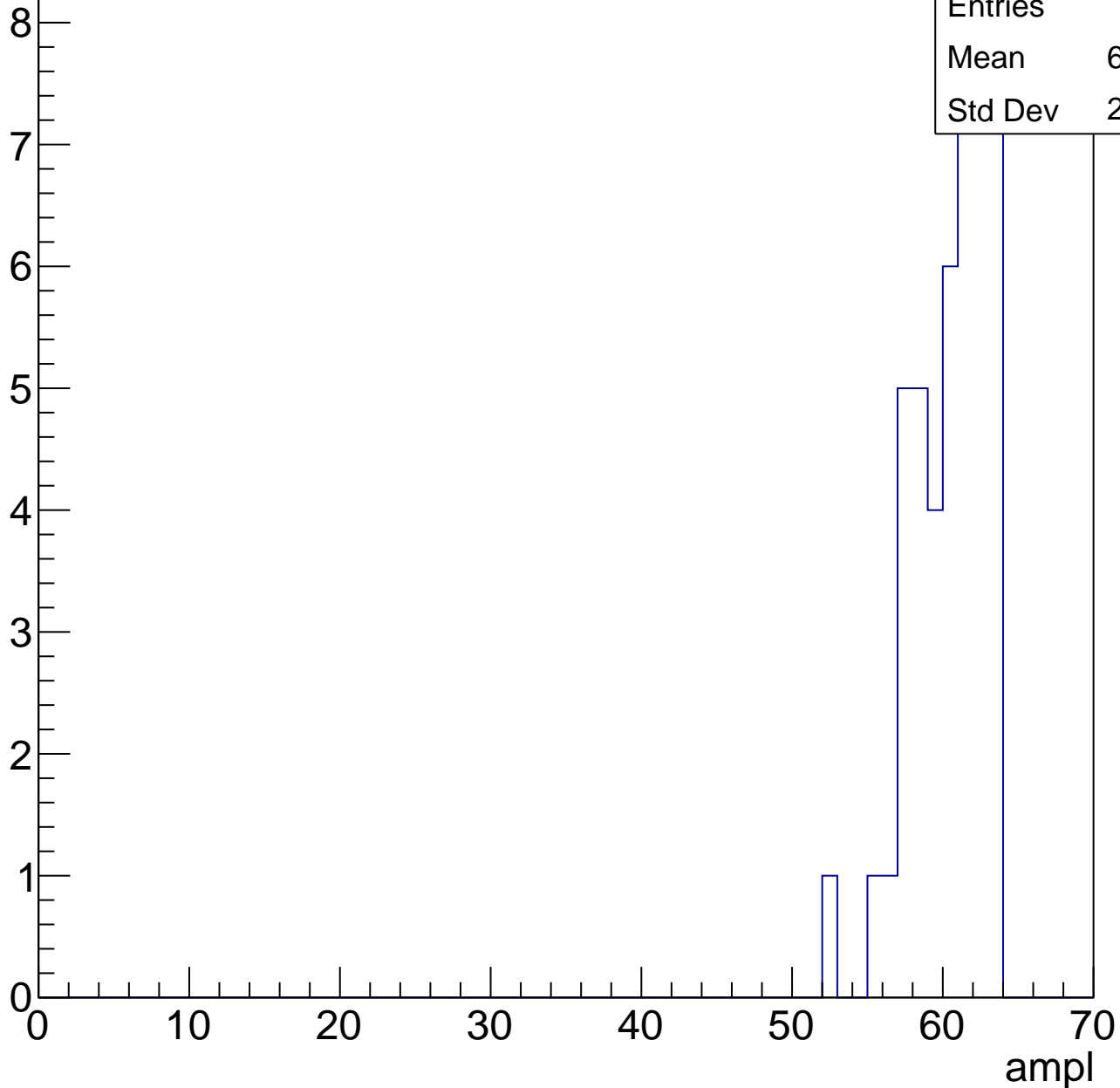


B0L001S, U11-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	60.04
Std Dev	2.466



B0L001S, U11-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch73, adc0

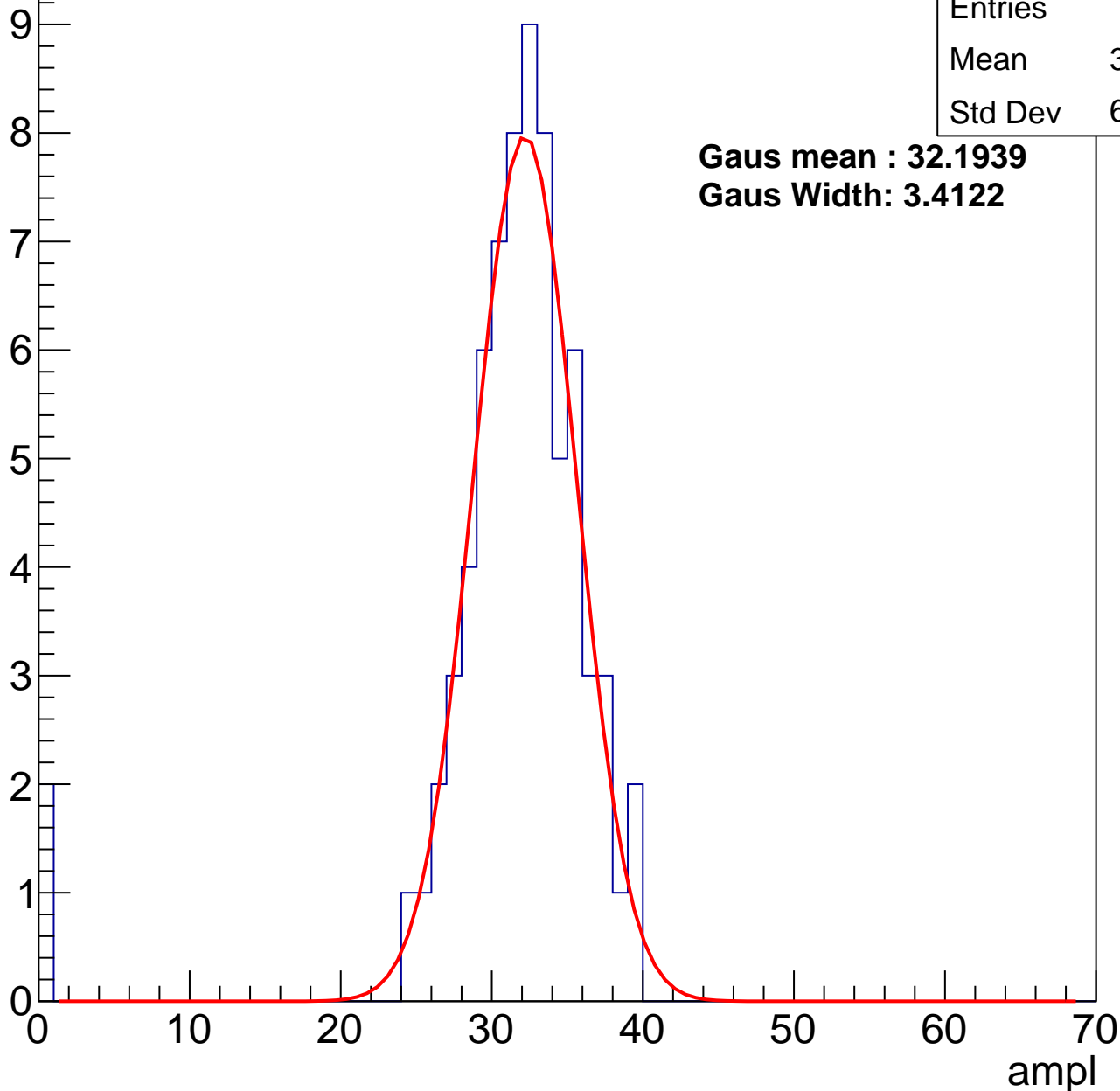
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	30.89
Std Dev	6.182

Gaus mean : 32.1939

Gaus Width: 3.4122



B0L001S, U11-ch73, adc1

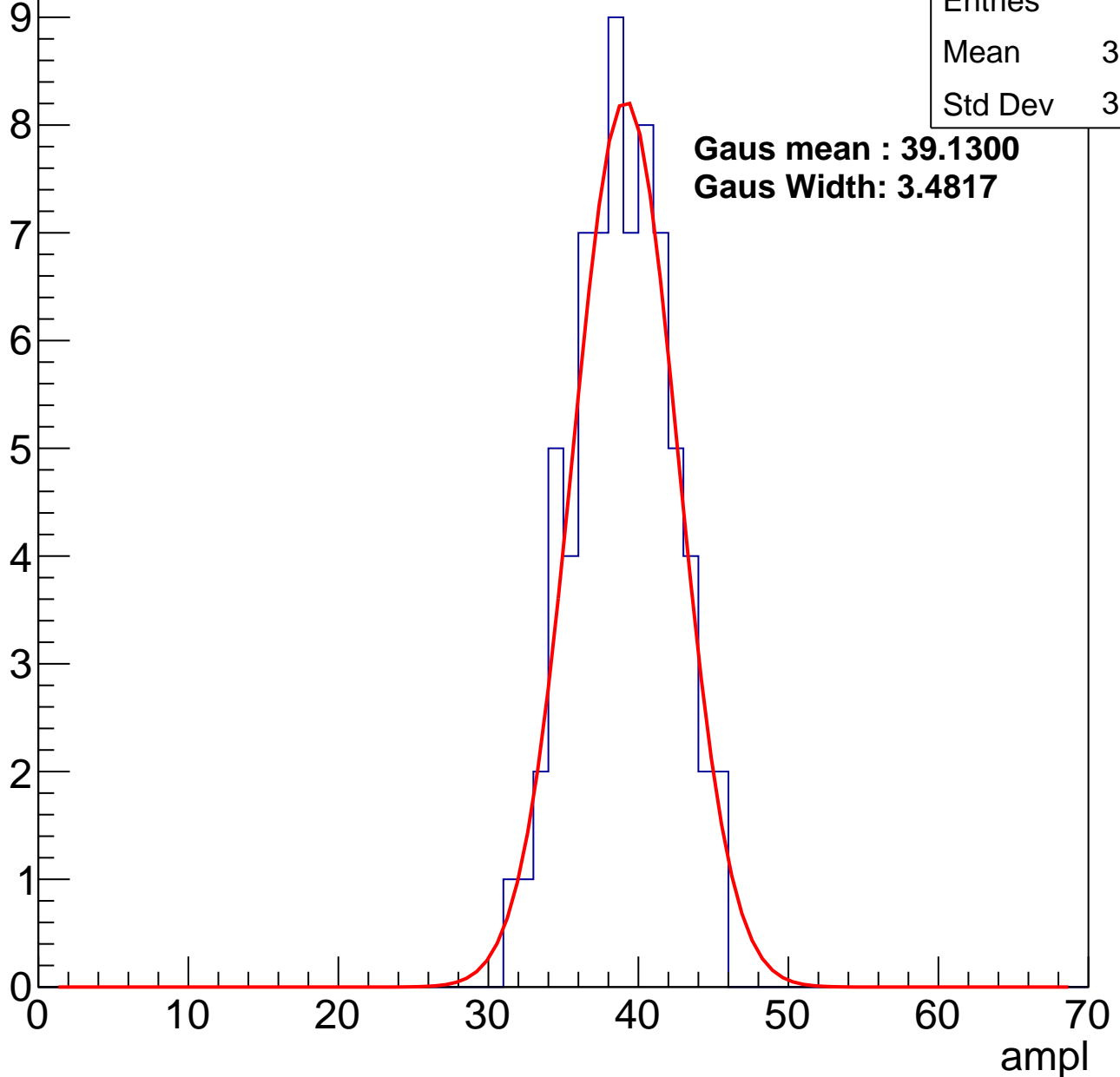
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.48
Std Dev	3.188

Gaus mean : 39.1300

Gaus Width: 3.4817



B0L001S, U11-ch73, adc2

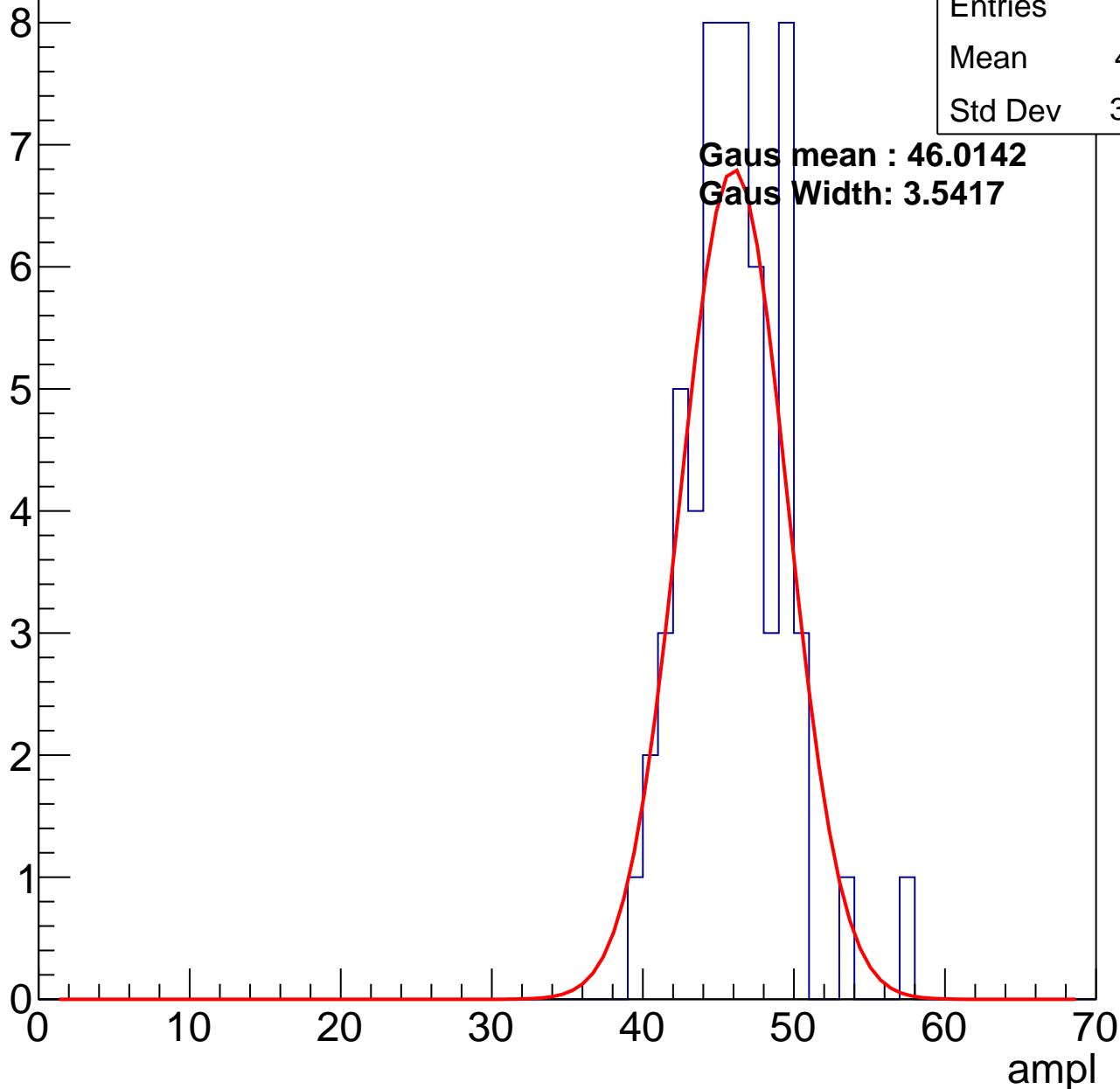
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	45.61
Std Dev	3.266

Gaus mean : 46.0142

Gaus Width: 3.5417



B0L001S, U11-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	52.09
Std Dev	3.746

Entry

10

8

6

4

2

0

0

10

20

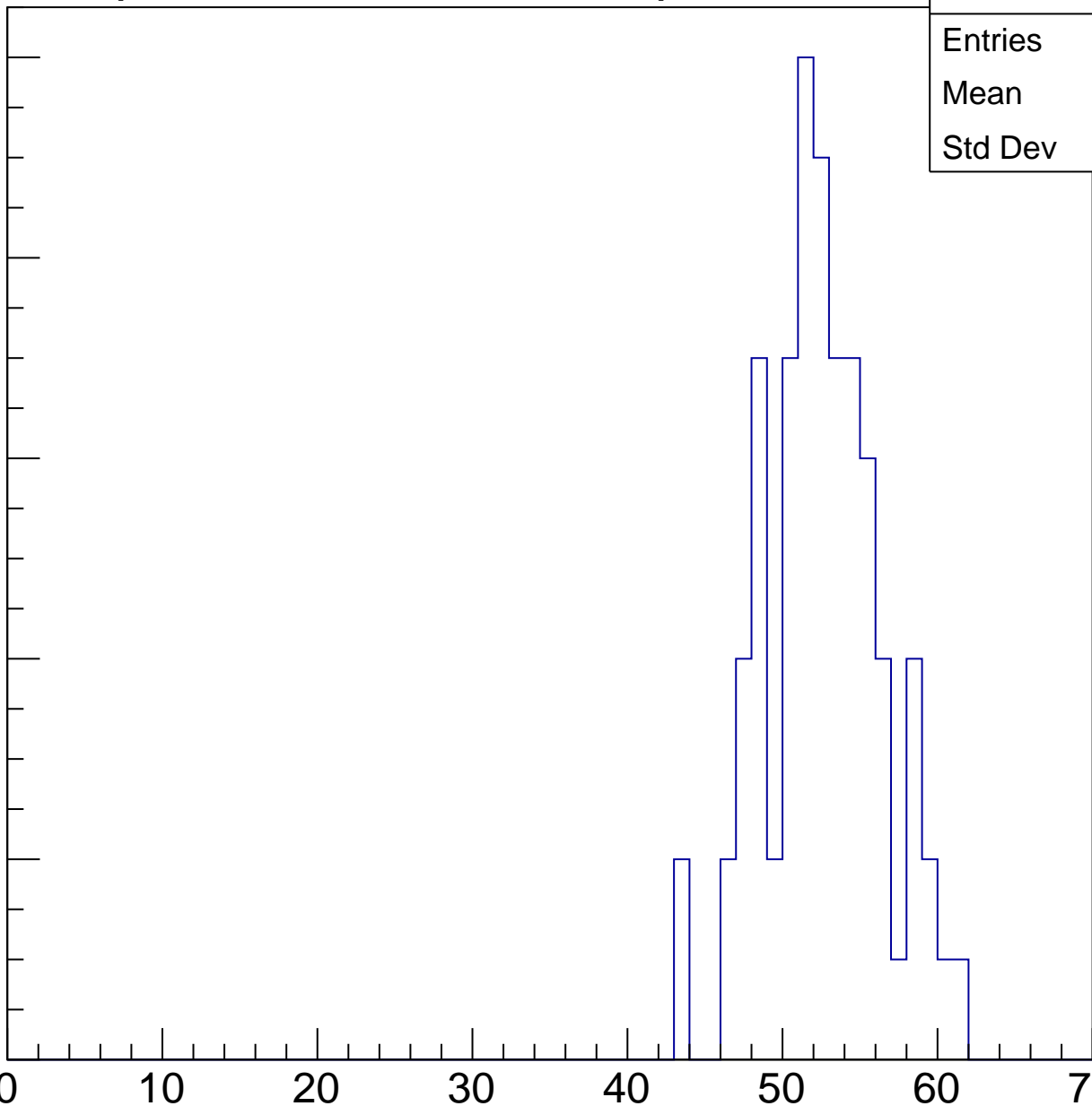
30

40

50

60

ampl

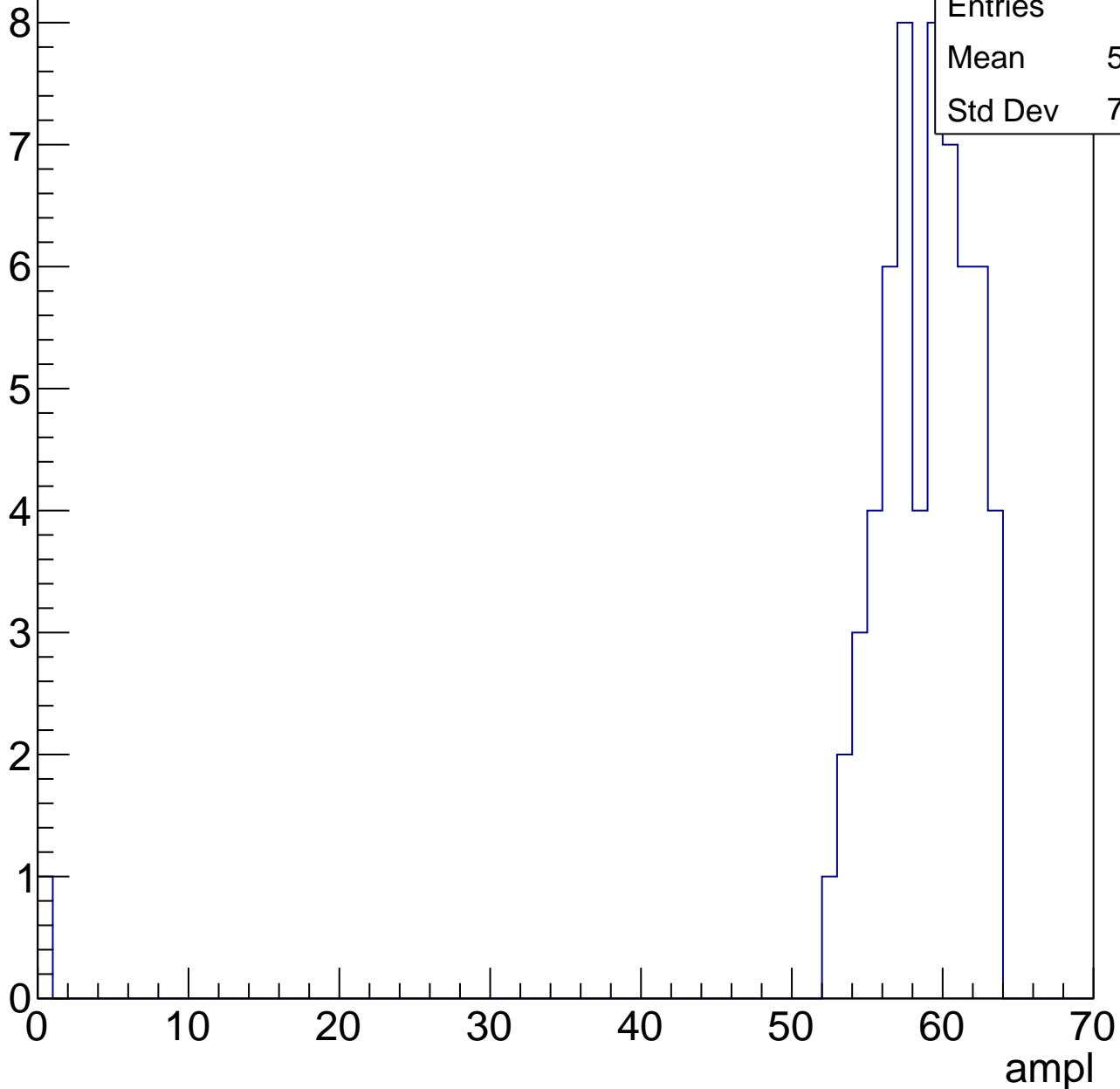


B0L001S, U11-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.43
Std Dev	7.992

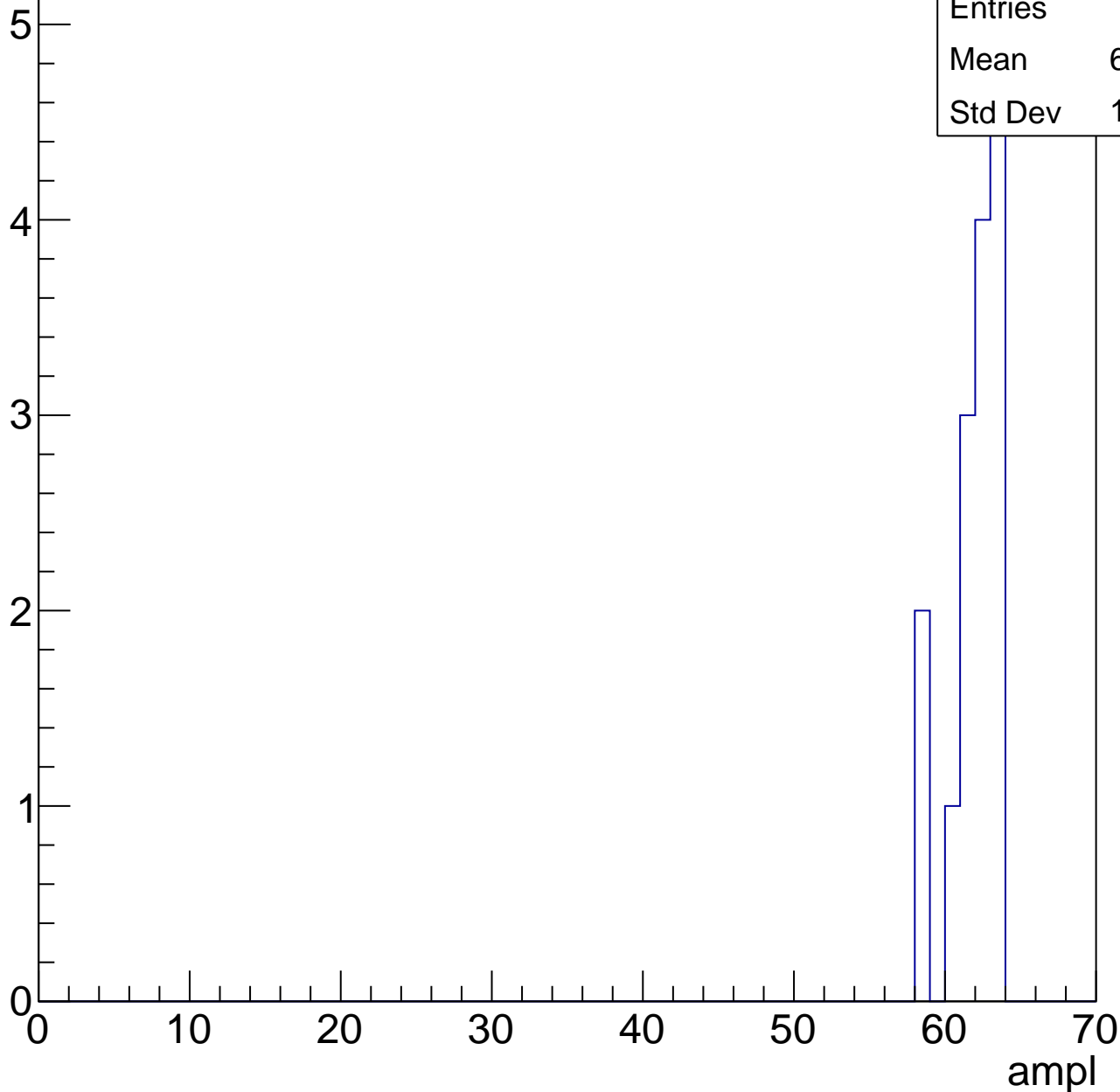


B0L001S, U11-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.47
Std Dev	1.628



B0L001S, U11-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch74, adc0

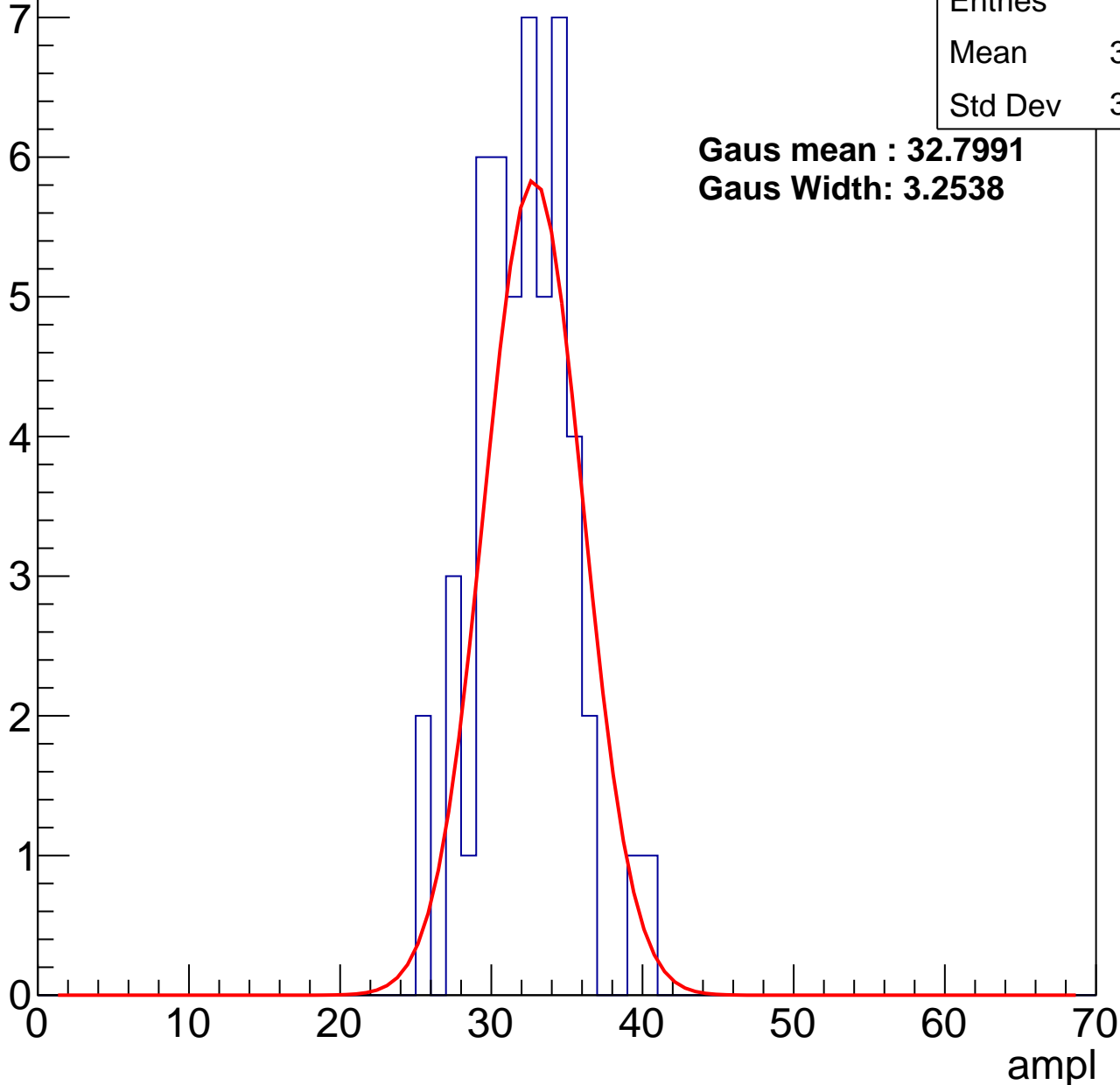
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	31.72
Std Dev	3.105

Gaus mean : 32.7991

Gaus Width: 3.2538



B0L001S, U11-ch74, adc1

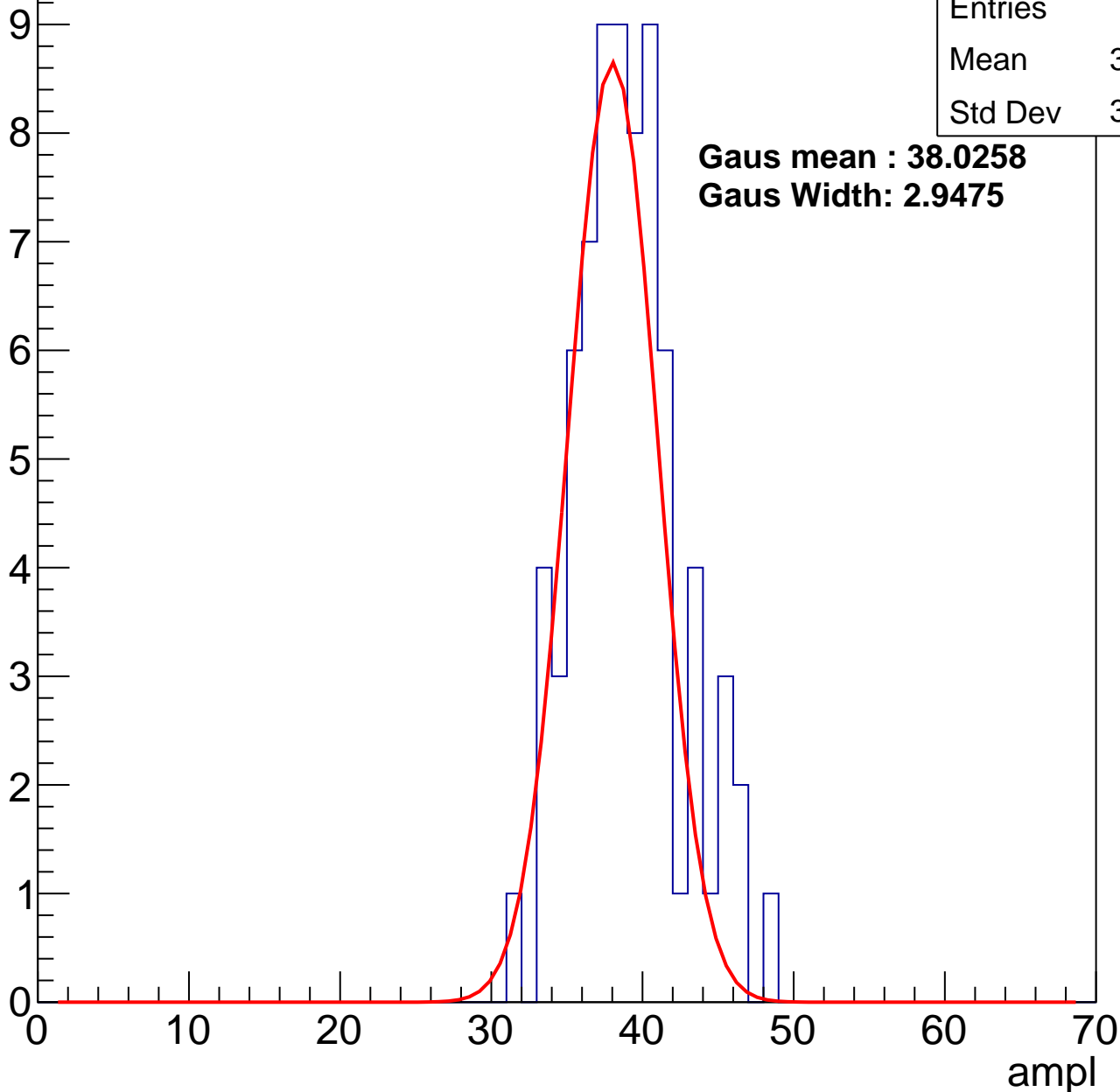
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.55
Std Dev	3.469

Gaus mean : 38.0258

Gaus Width: 2.9475



B0L001S, U11-ch74, adc2

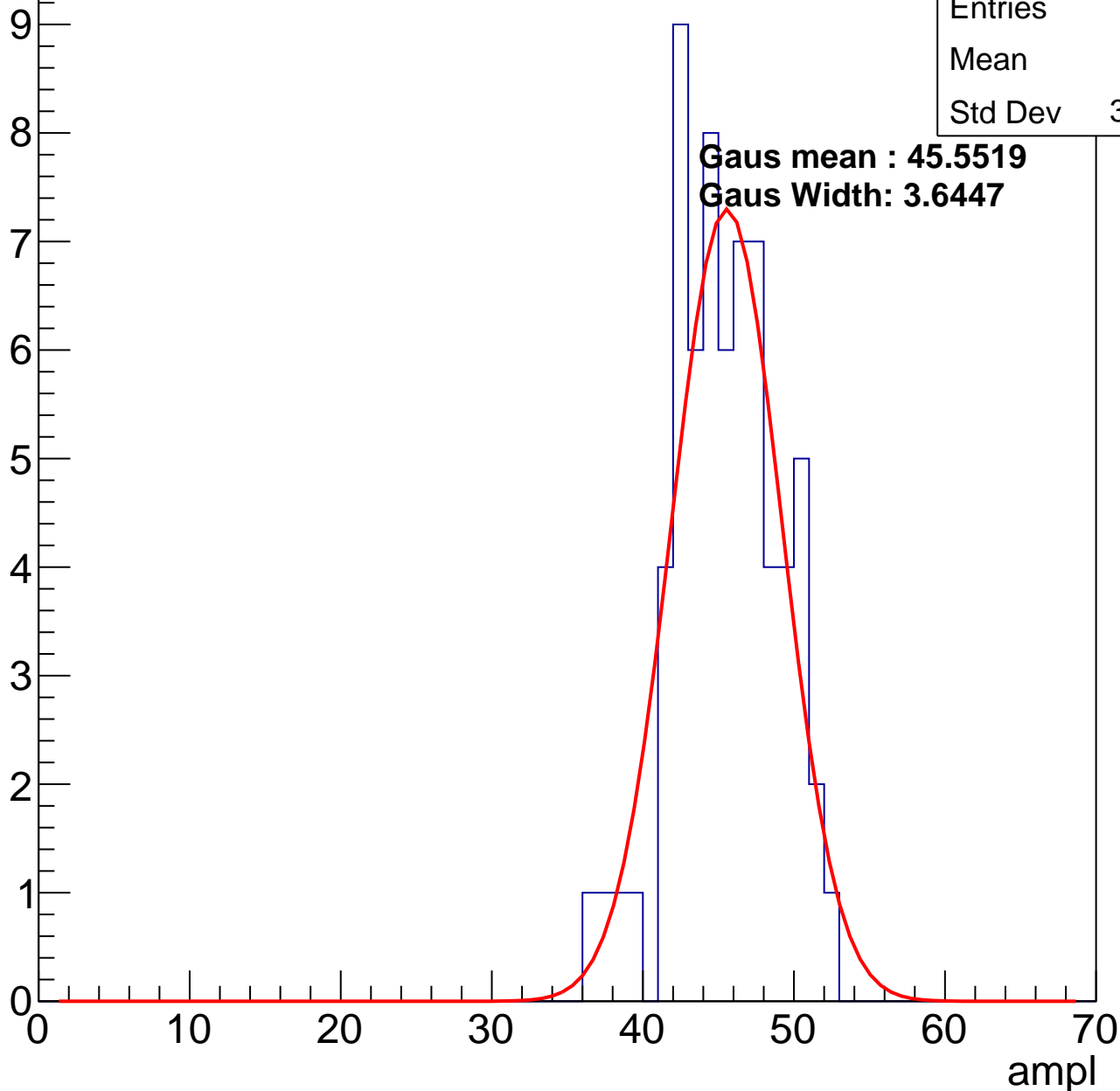
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	45
Std Dev	3.429

Gaus mean : 45.5519

Gaus Width: 3.6447

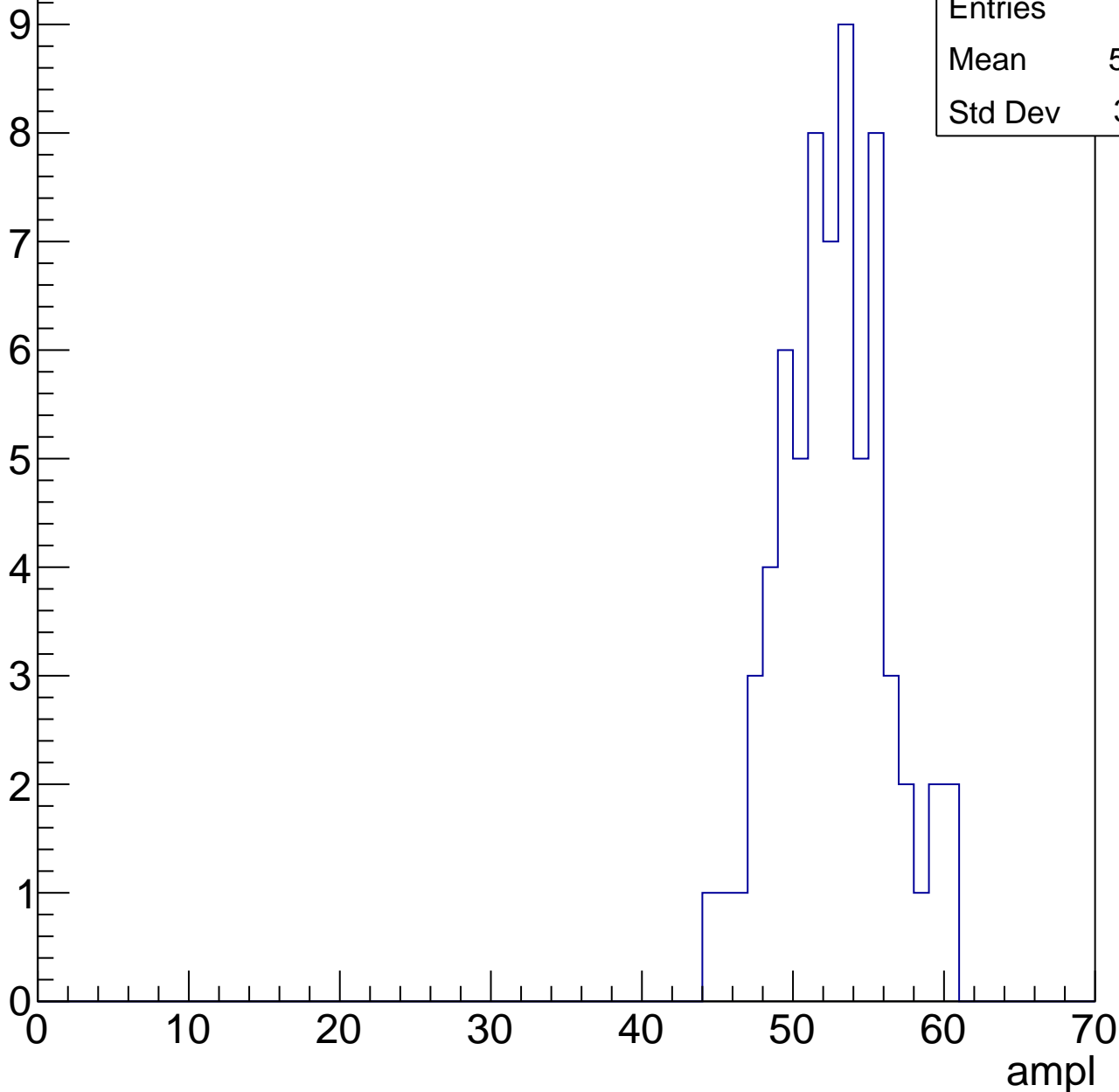


B0L001S, U11-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	52.19
Std Dev	3.491

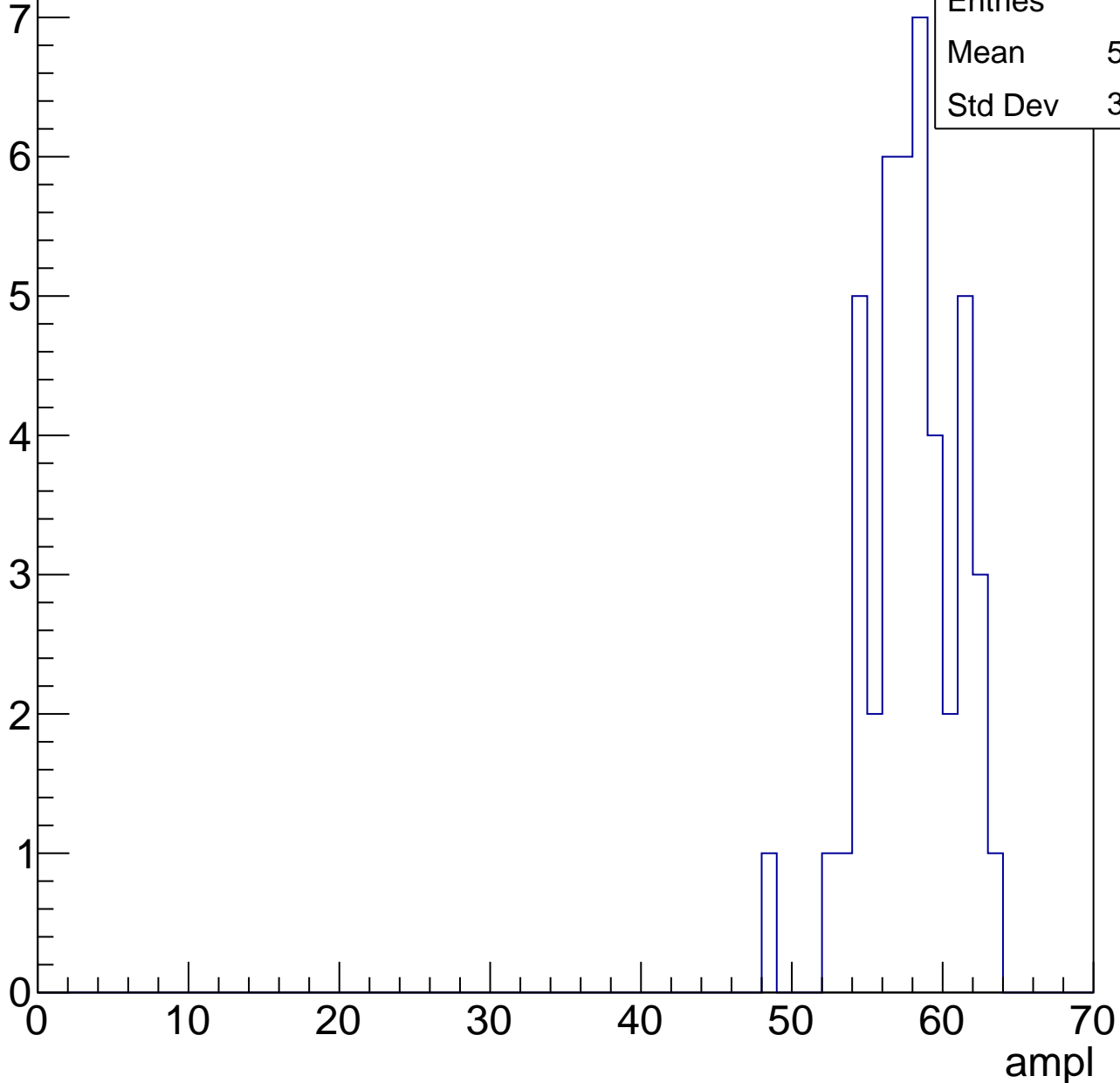


B0L001S, U11-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	57.43
Std Dev	3.033

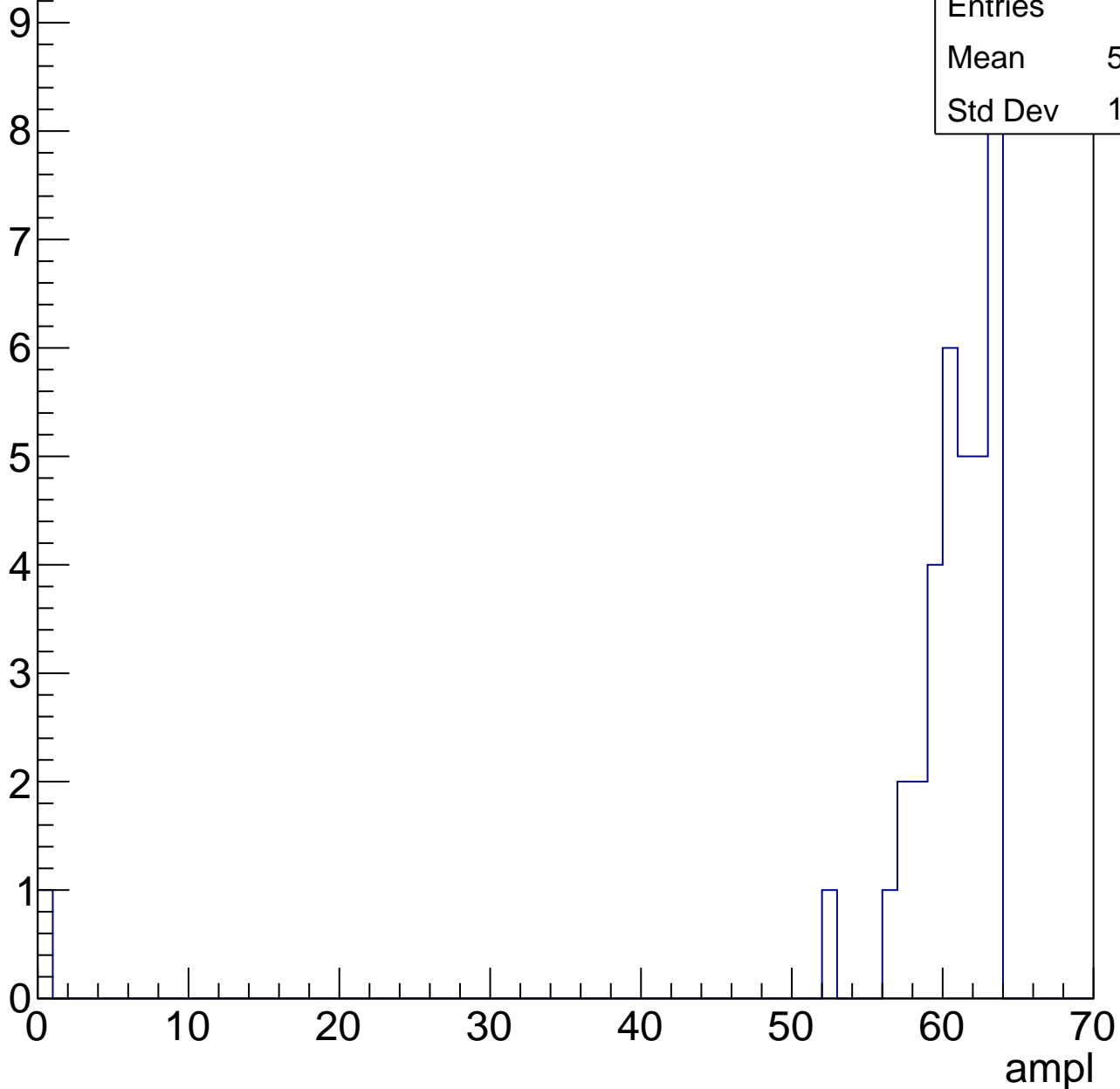


B0L001S, U11-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.78
Std Dev	10.22



B0L001S, U11-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B0L001S, U11-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch75, adc0

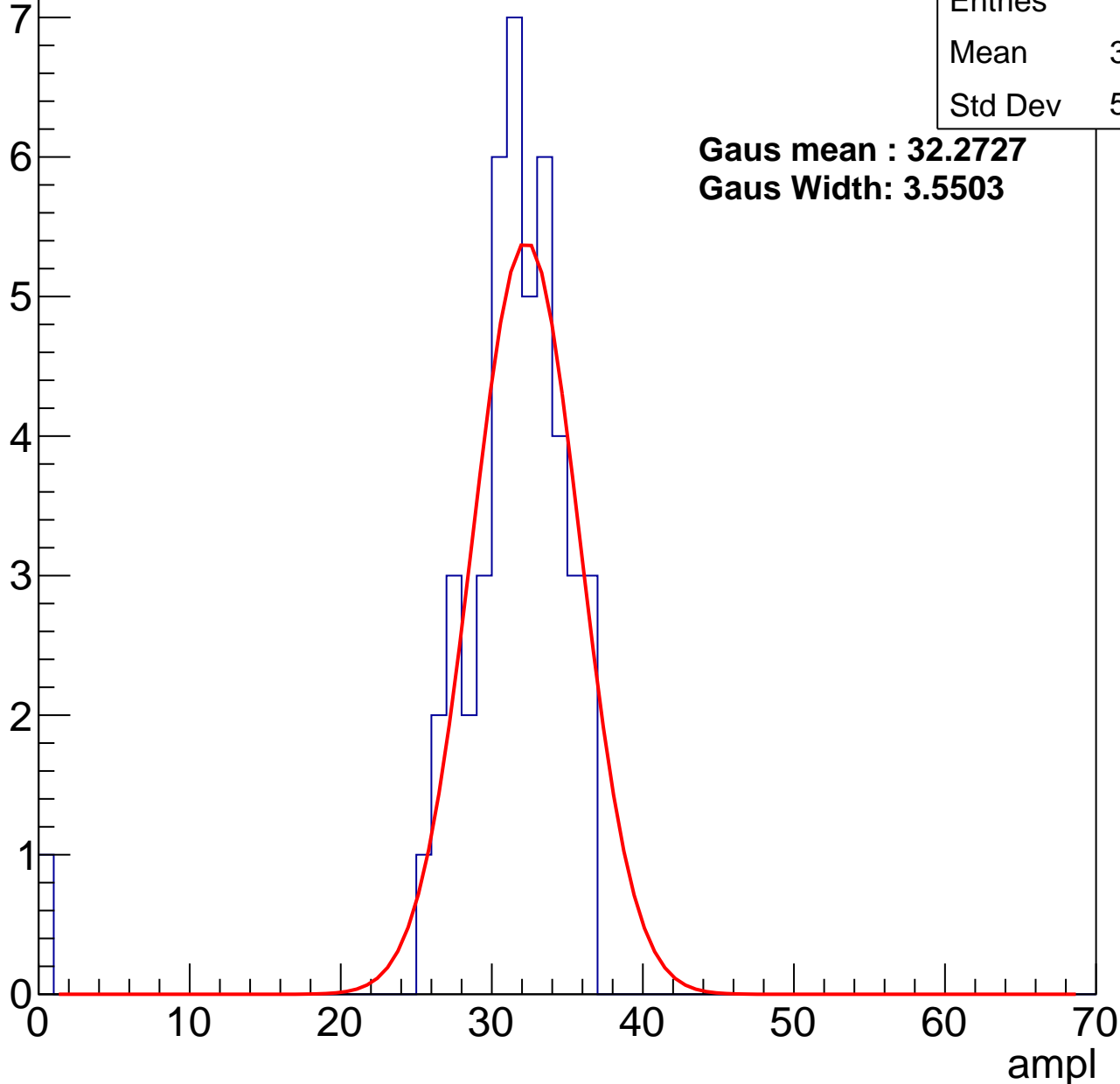
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	30.54
Std Dev	5.335

Gaus mean : 32.2727

Gaus Width: 3.5503



B0L001S, U11-ch75, adc1

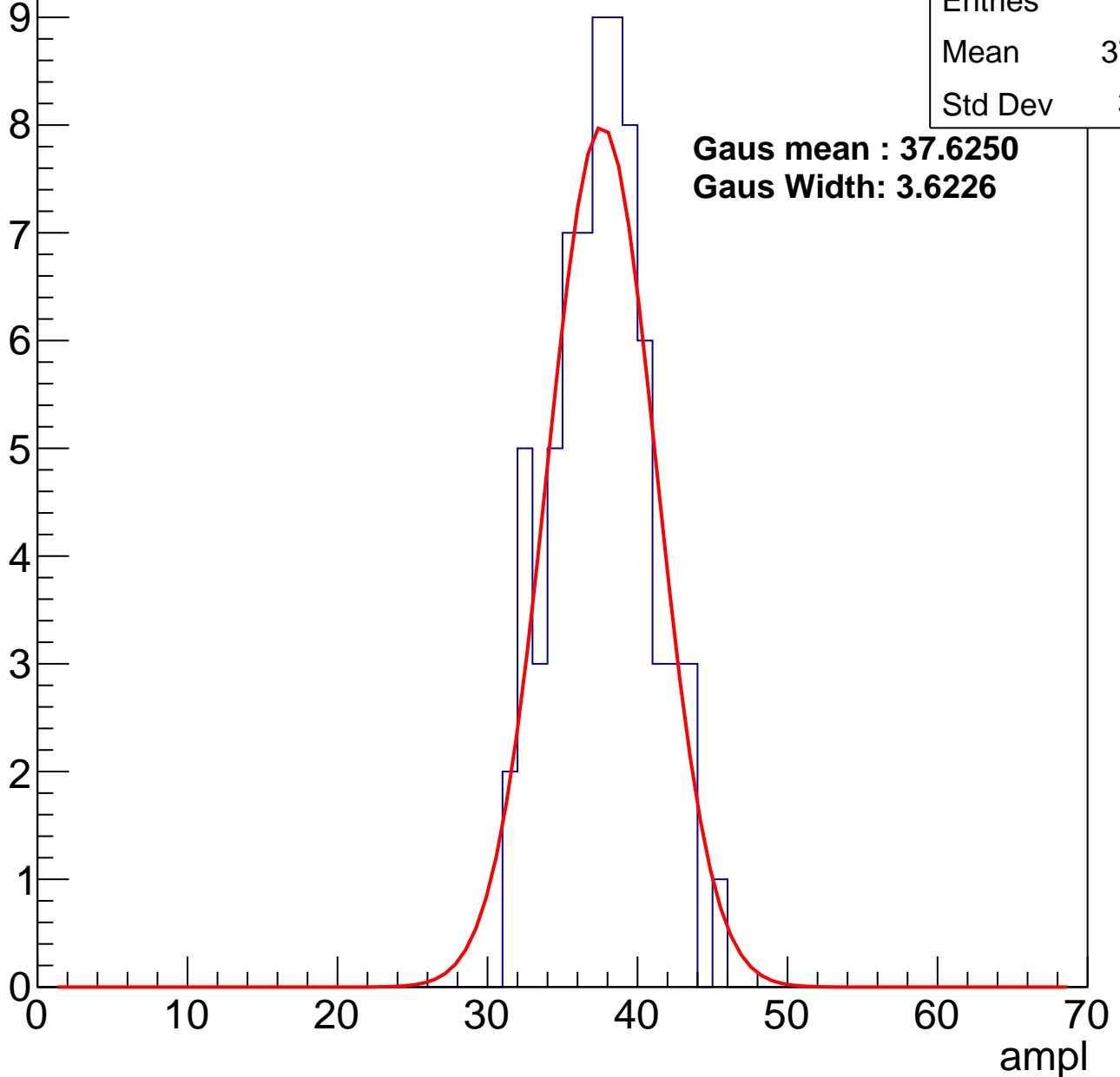
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.15
Std Dev	3.17

Gaus mean : 37.6250

Gaus Width: 3.6226



B0L001S, U11-ch75, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	44.35
Std Dev	3.39

Gaus mean : 44.5651

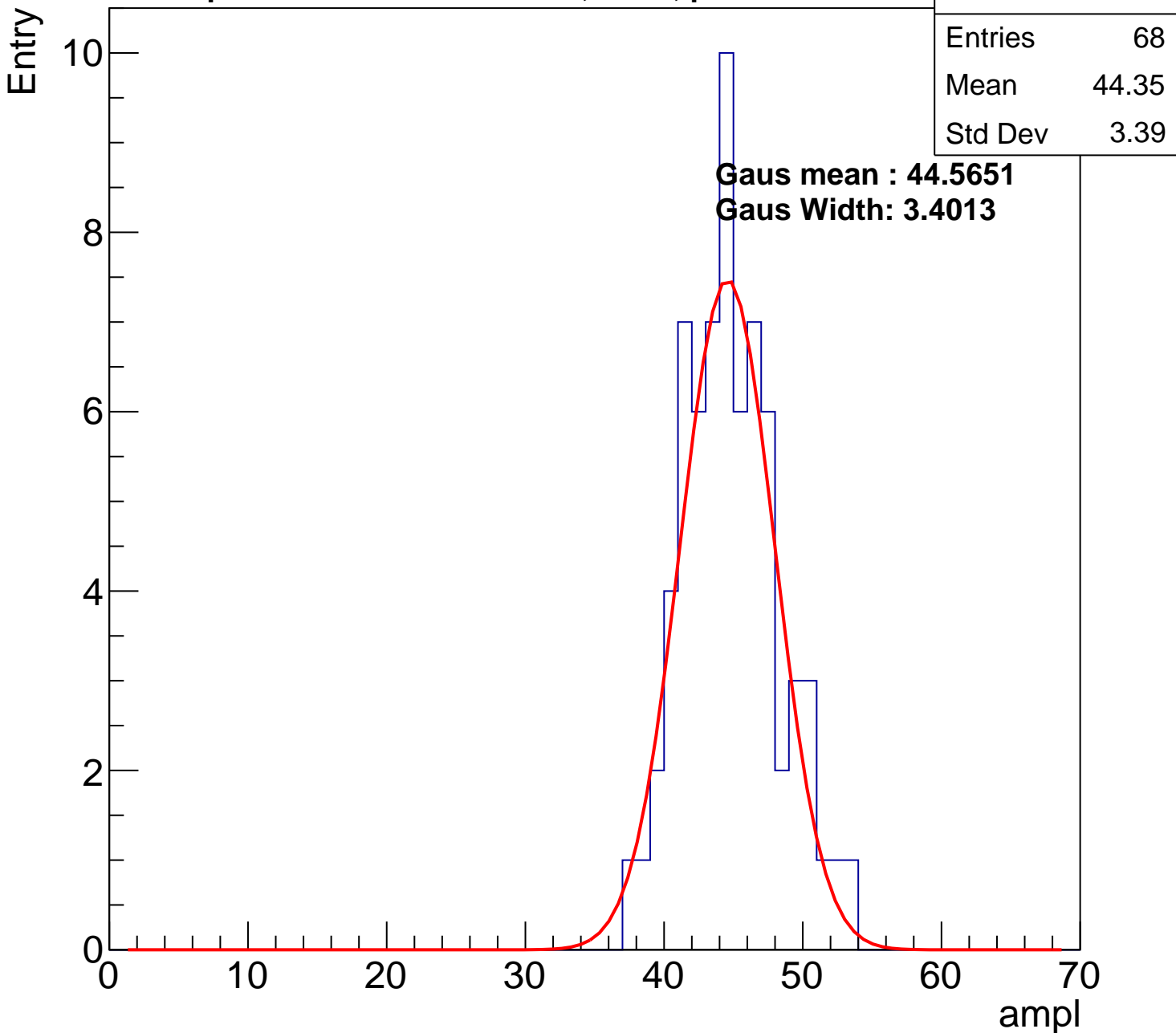
Gaus Width: 3.4013

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

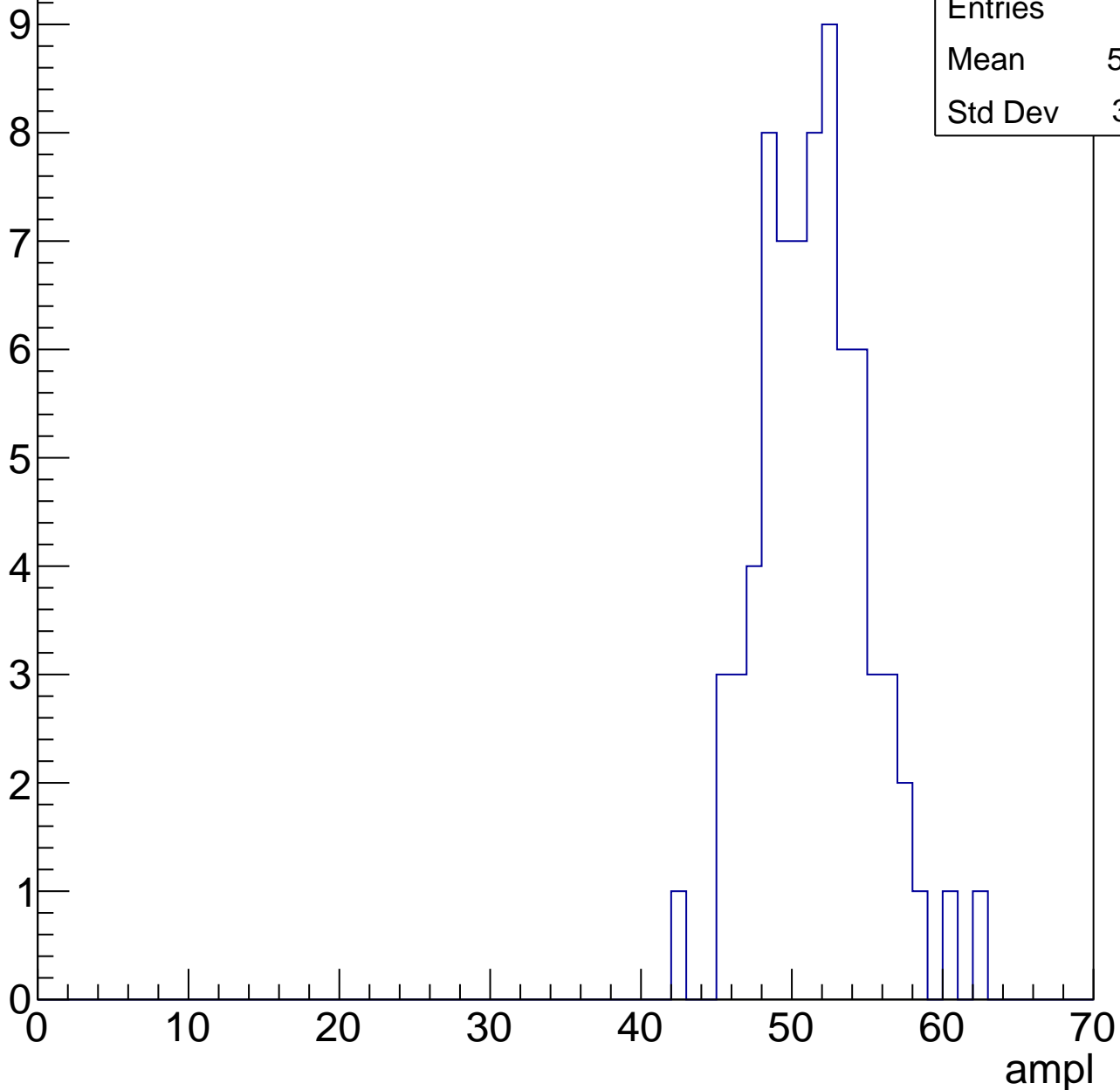


B0L001S, U11-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

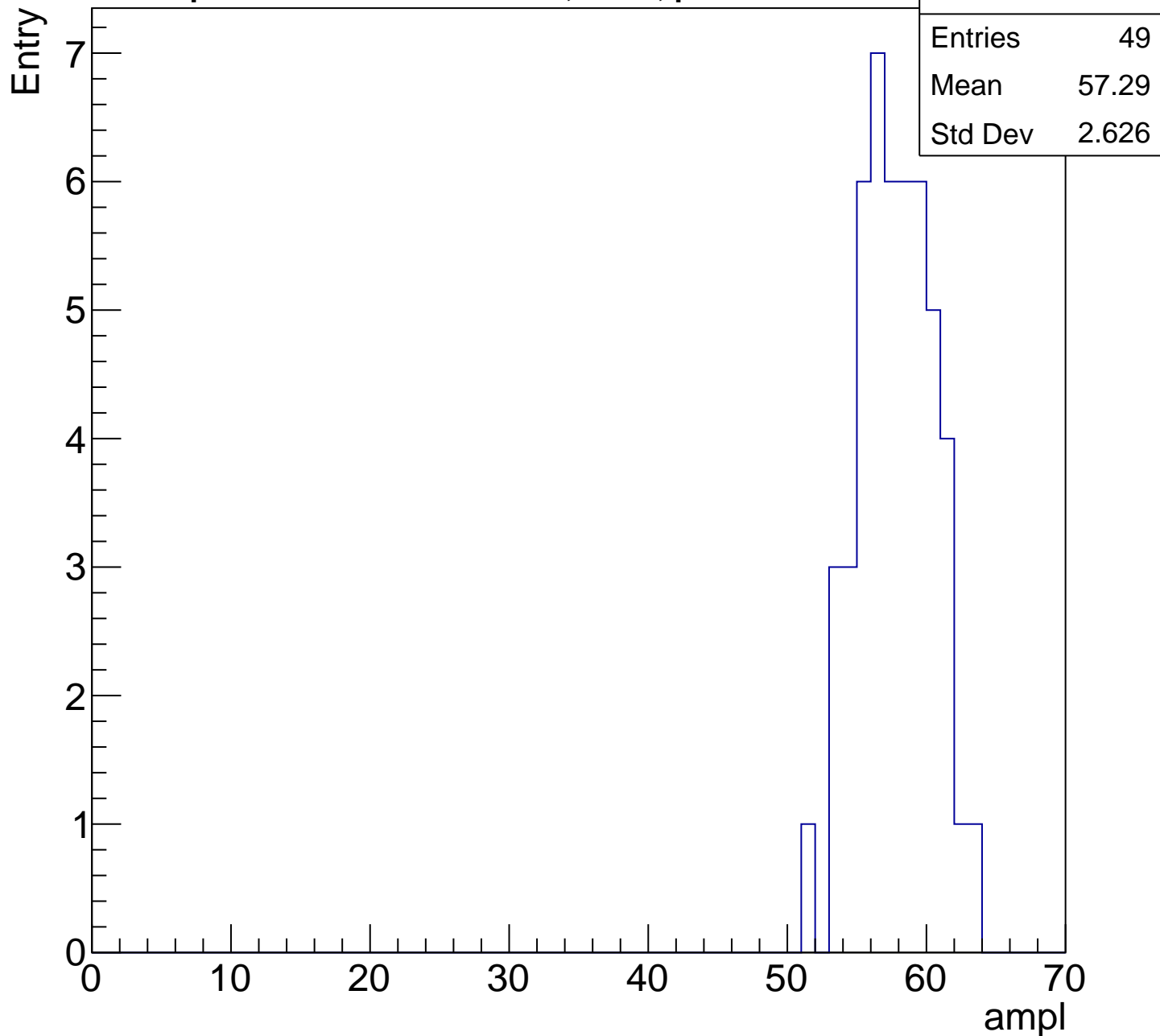
Entry

Entries	73
Mean	51.03
Std Dev	3.641



B0L001S, U11-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

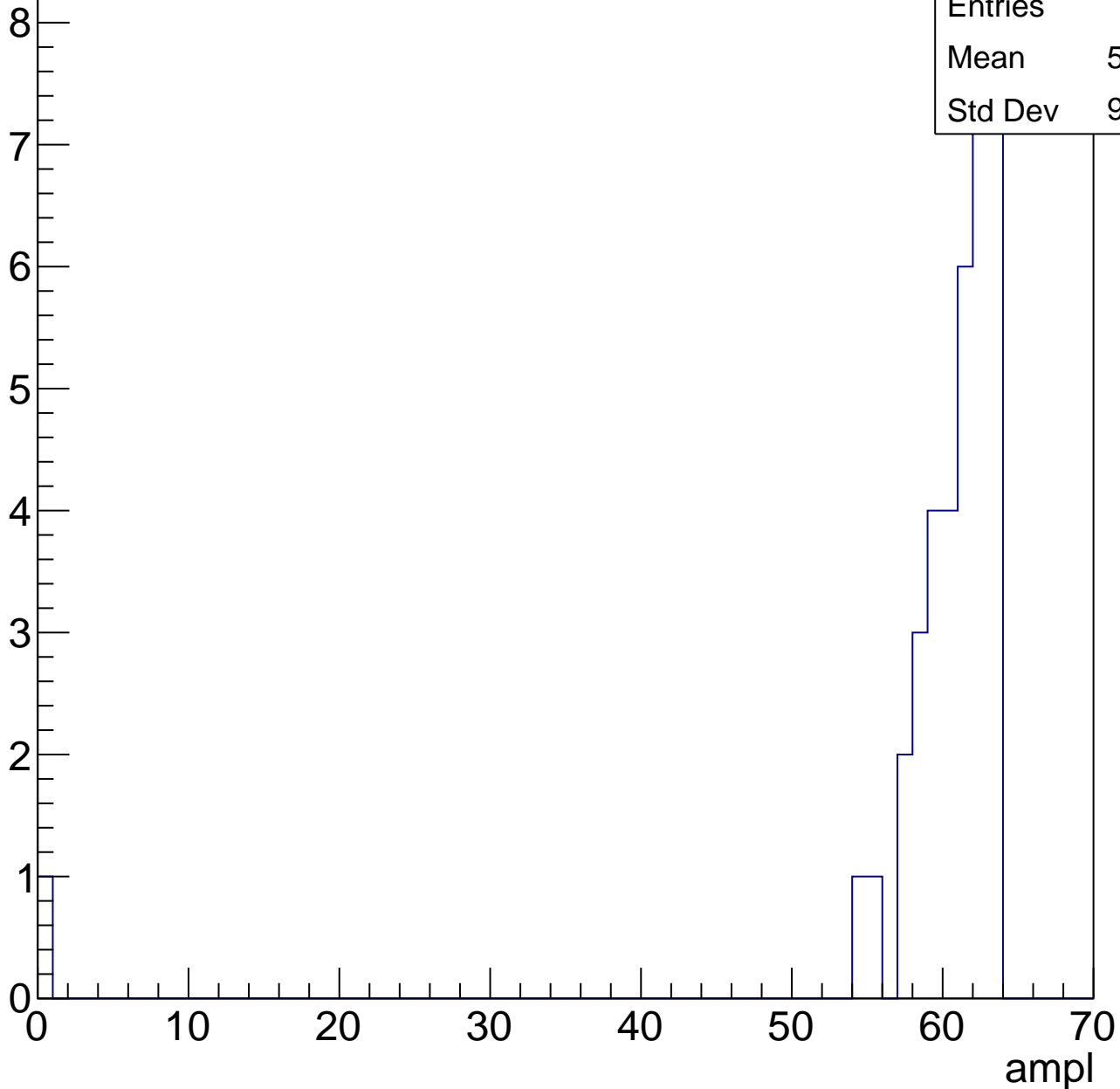


B0L001S, U11-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.92
Std Dev	9.948



B0L001S, U11-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch76, adc0

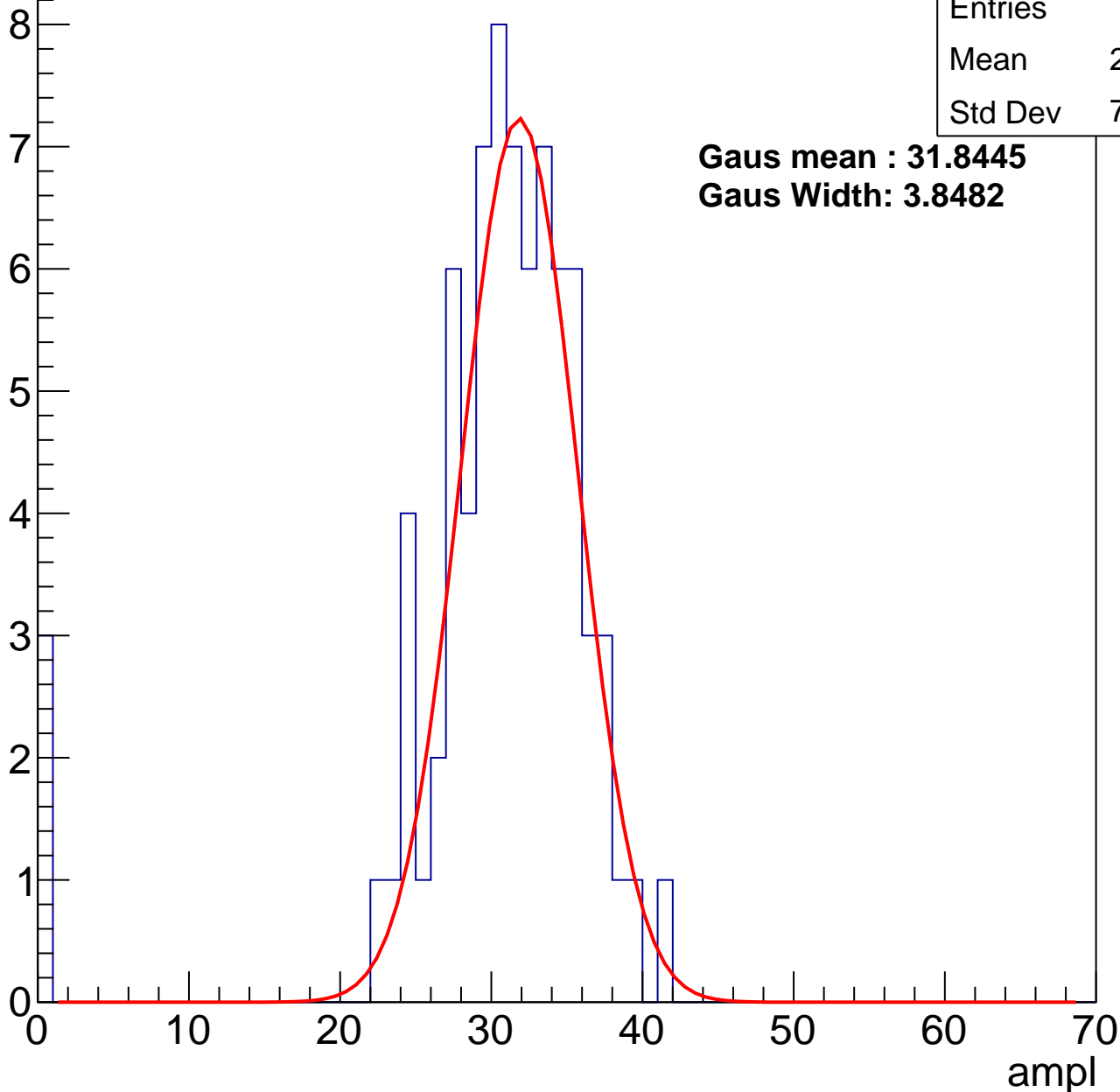
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	29.82
Std Dev	7.116

Gaus mean : 31.8445

Gaus Width: 3.8482



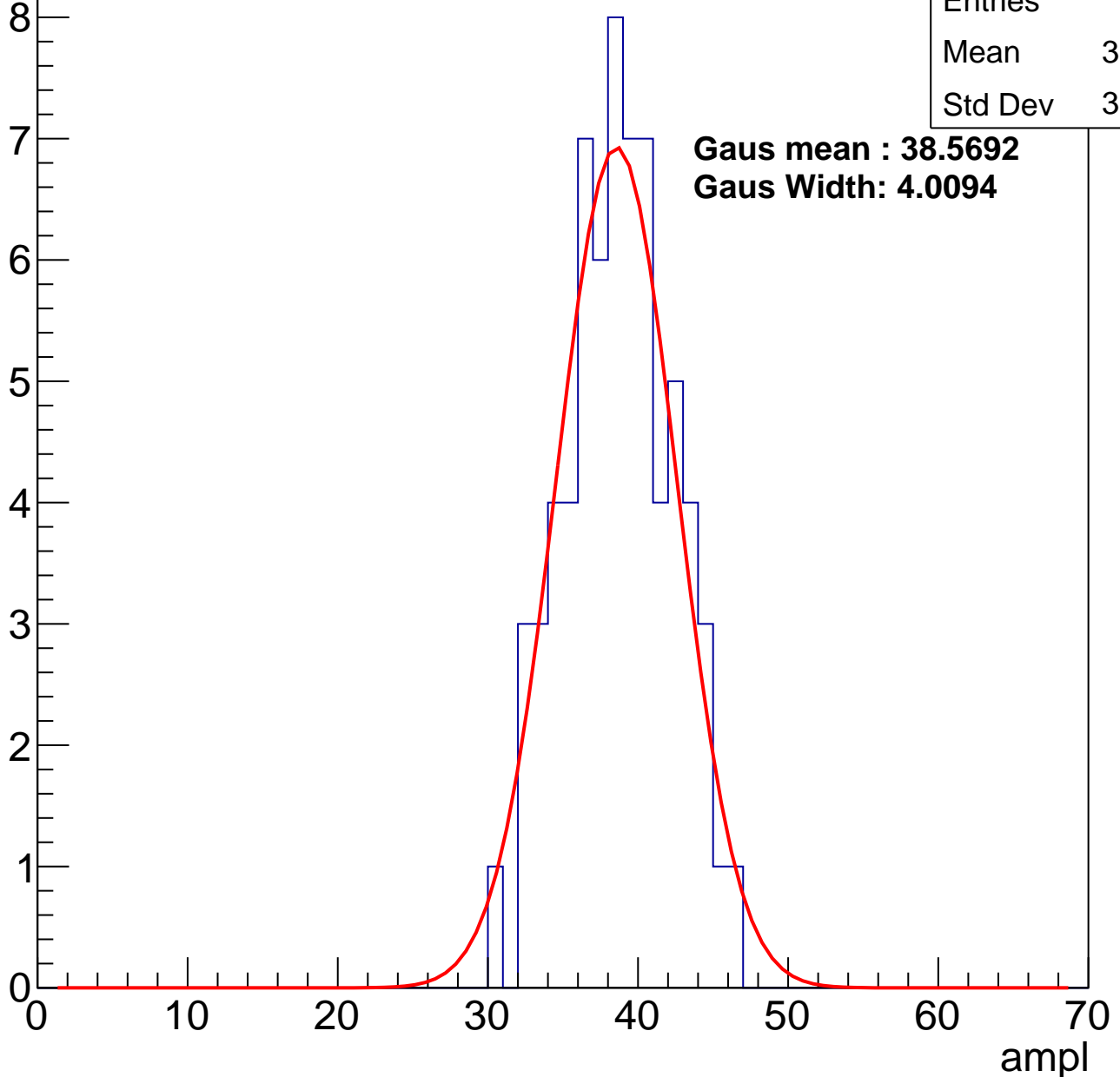
B0L001S, U11-ch76, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.25
Std Dev	3.533

Gaus mean : 38.5692
Gaus Width: 4.0094



B0L001S, U11-ch76, adc2

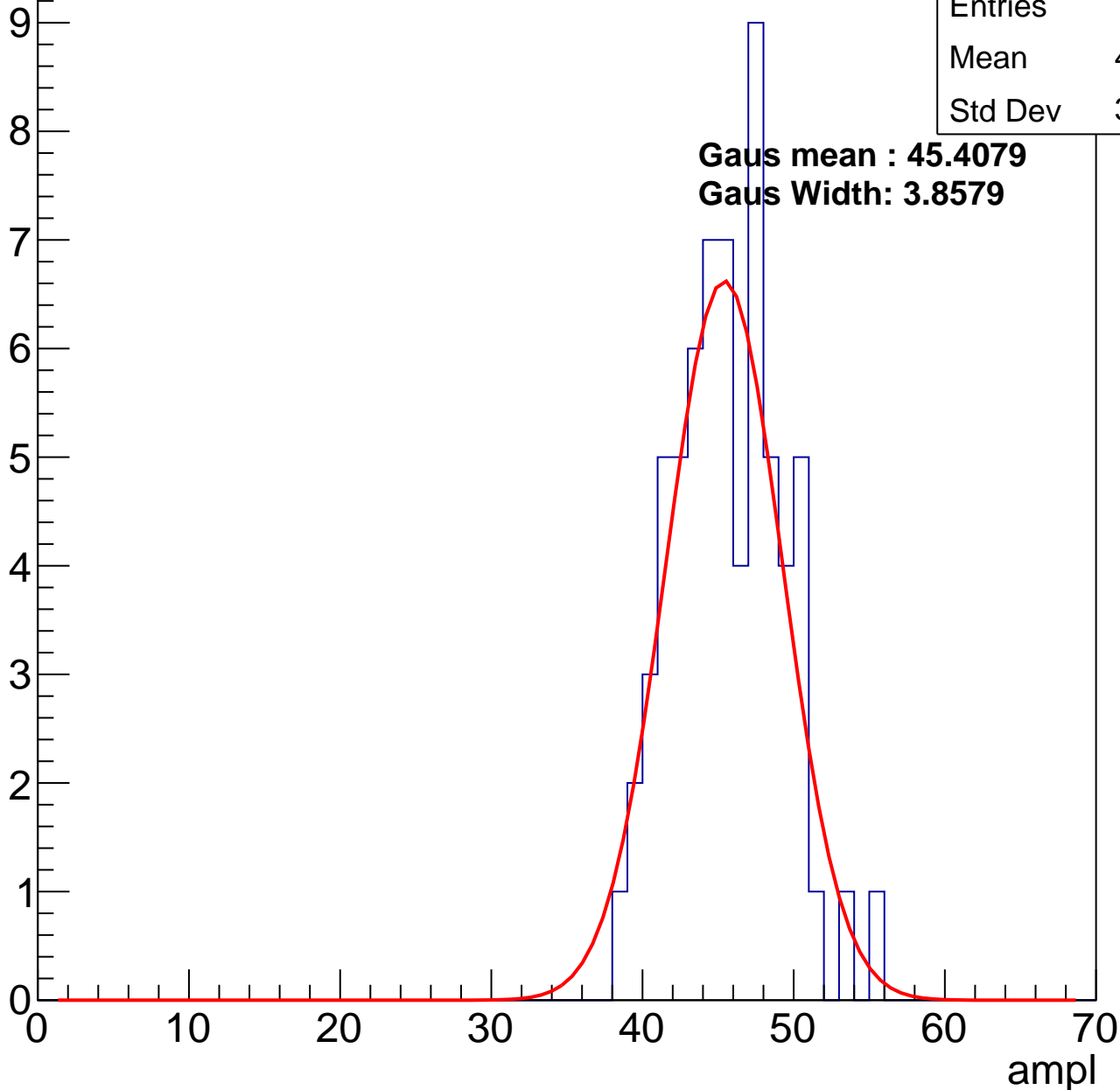
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.21
Std Dev	3.531

Gaus mean : 45.4079

Gaus Width: 3.8579

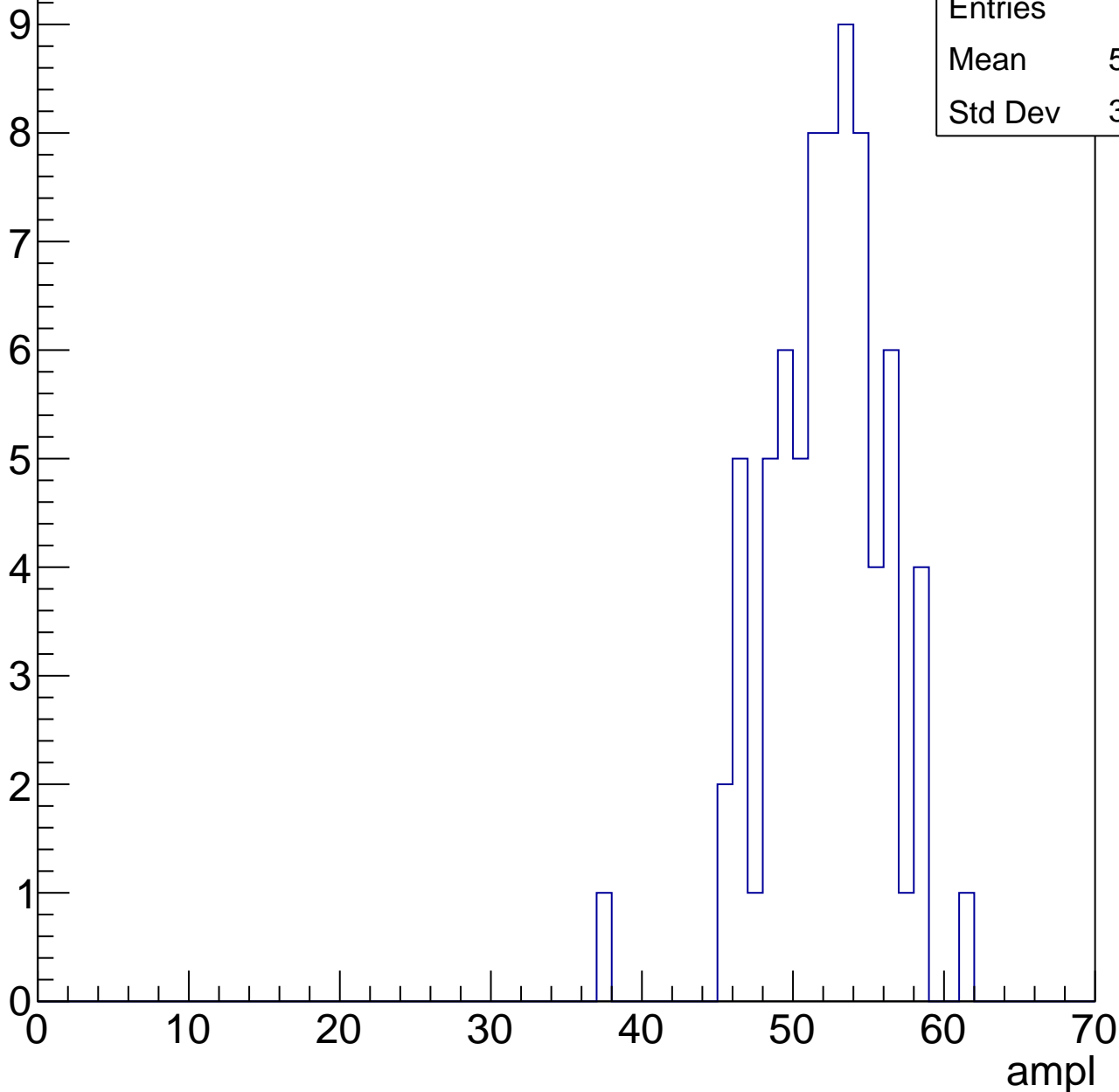


B0L001S, U11-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	51.72
Std Dev	3.868

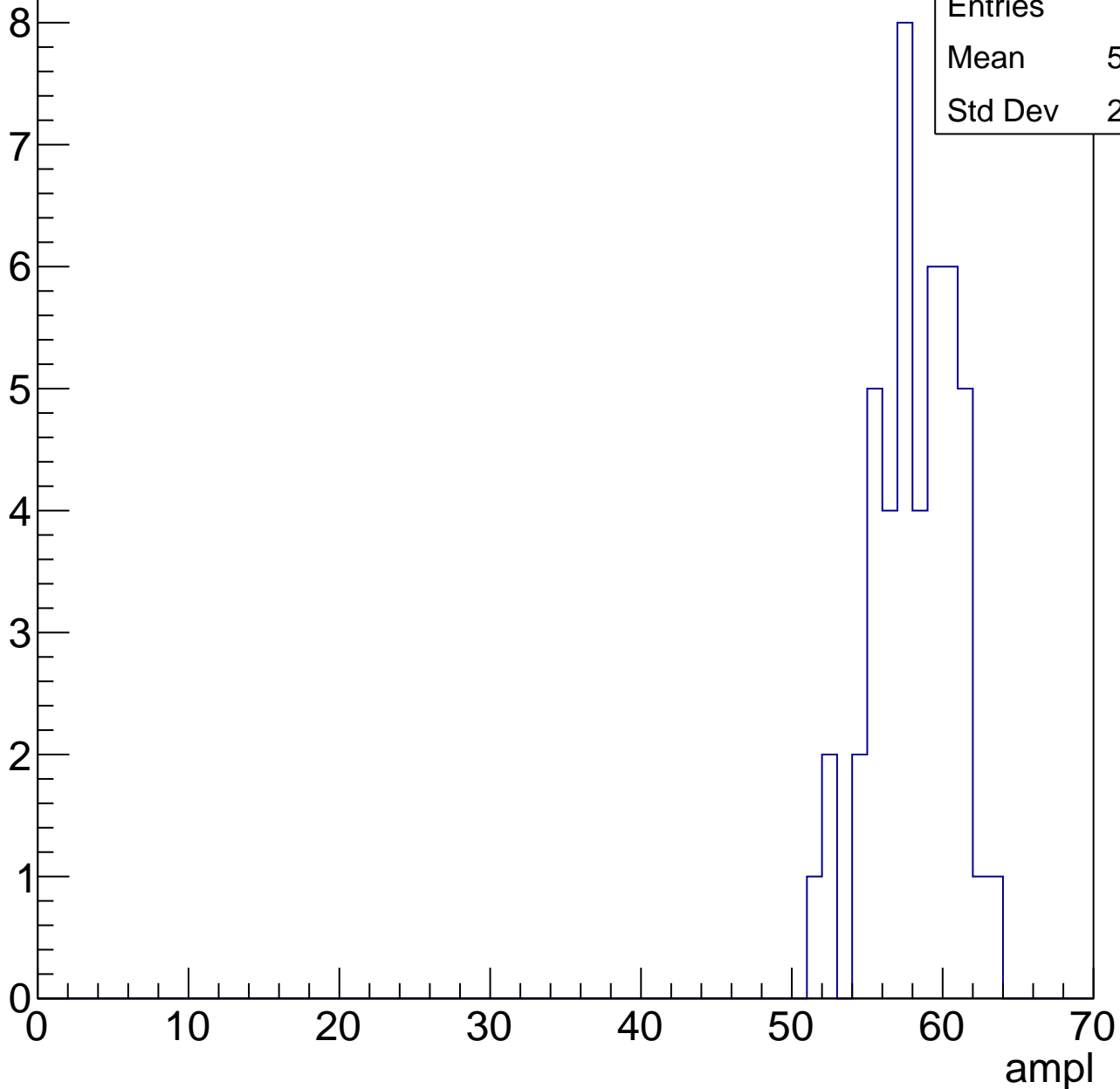


B0L001S, U11-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.64
Std Dev	2.717

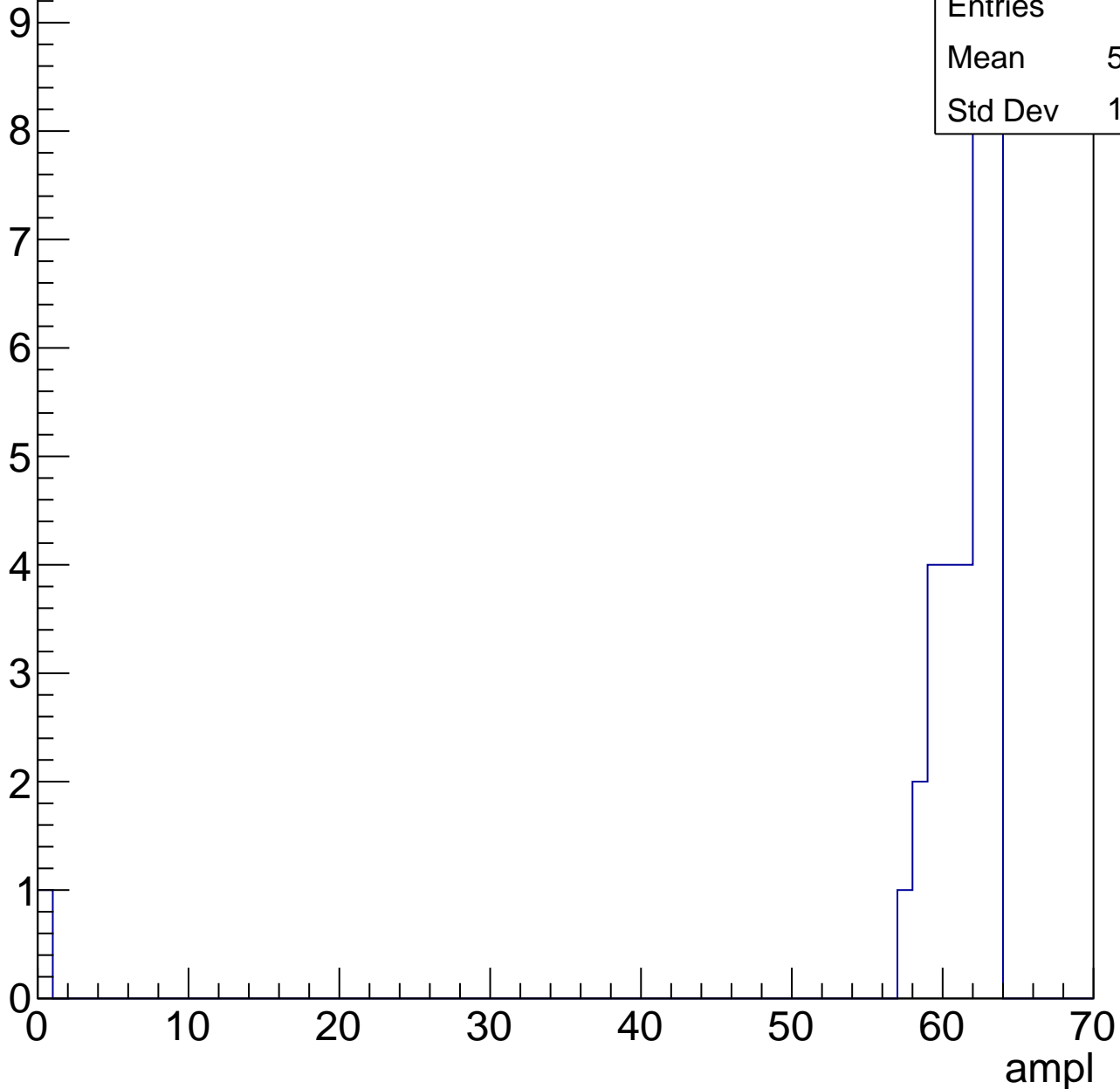


B0L001S, U11-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.27
Std Dev	10.62



B0L001S, U11-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U11-ch77, adc0

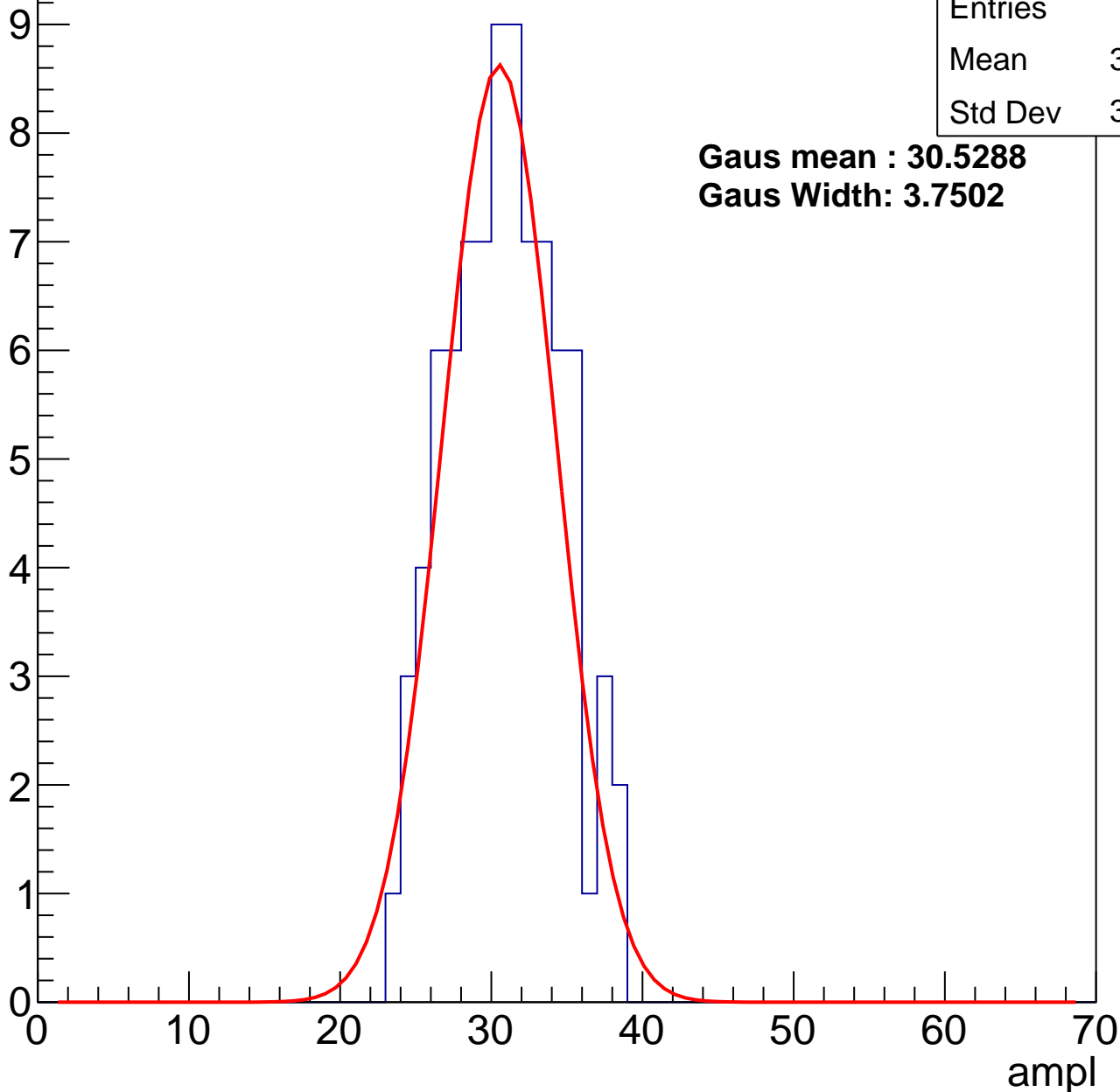
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	30.39
Std Dev	3.596

Gaus mean : 30.5288

Gaus Width: 3.7502



B0L001S, U11-ch77, adc1

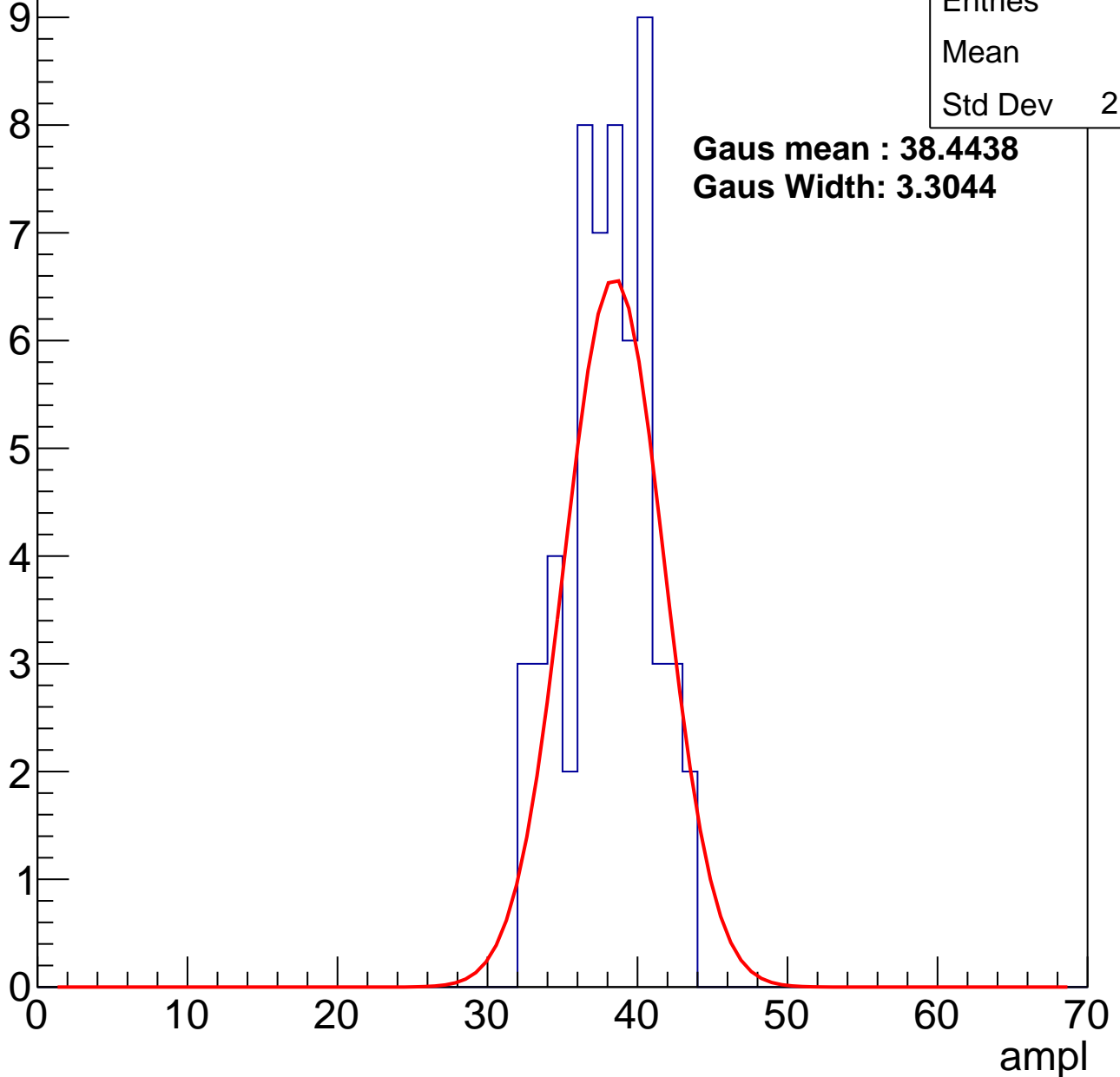
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.6
Std Dev	2.822

Gaus mean : 38.4438

Gaus Width: 3.3044



B0L001S, U11-ch77, adc2

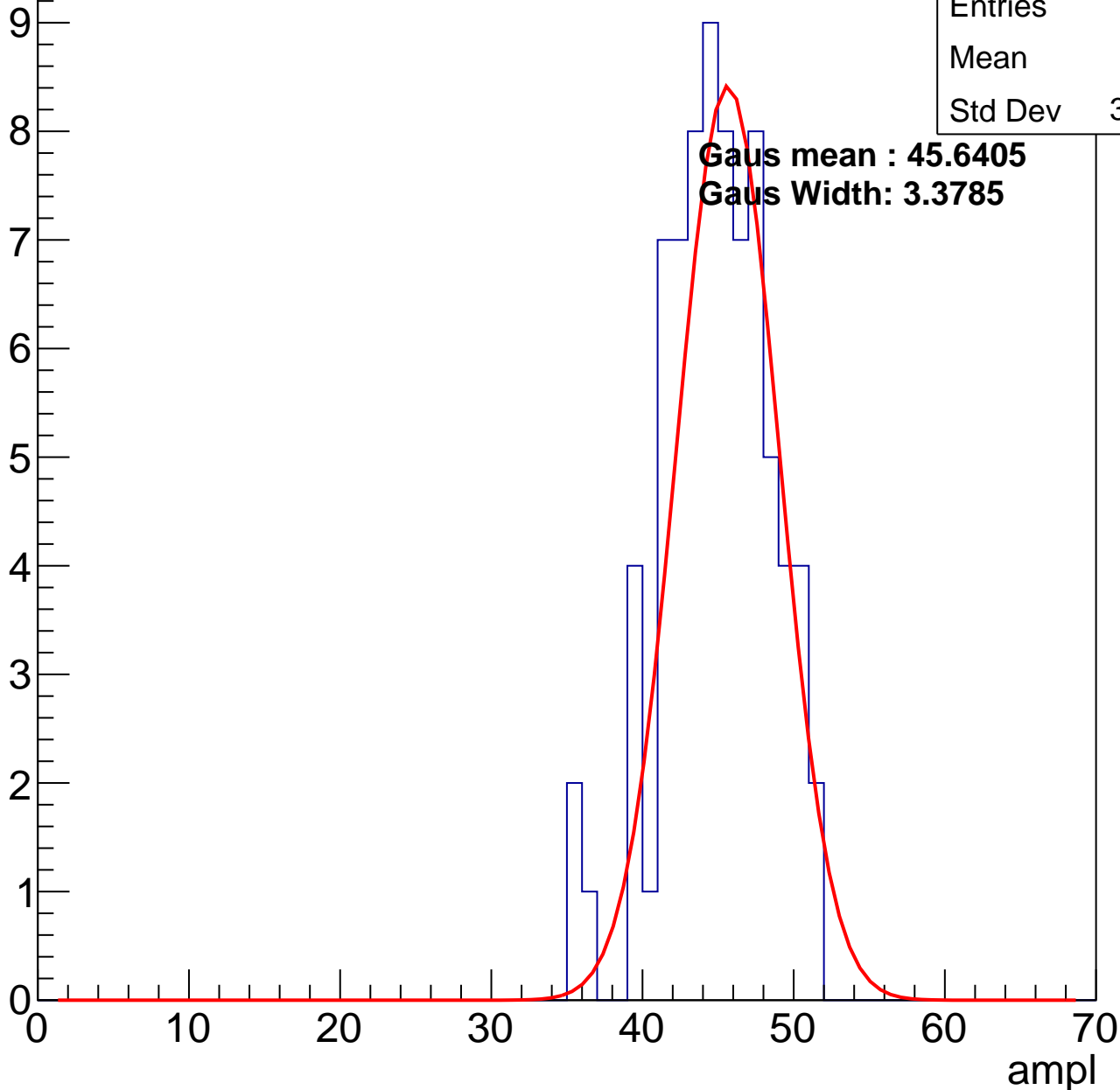
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.4
Std Dev	3.528

Gaus mean : 45.6405

Gaus Width: 3.3785

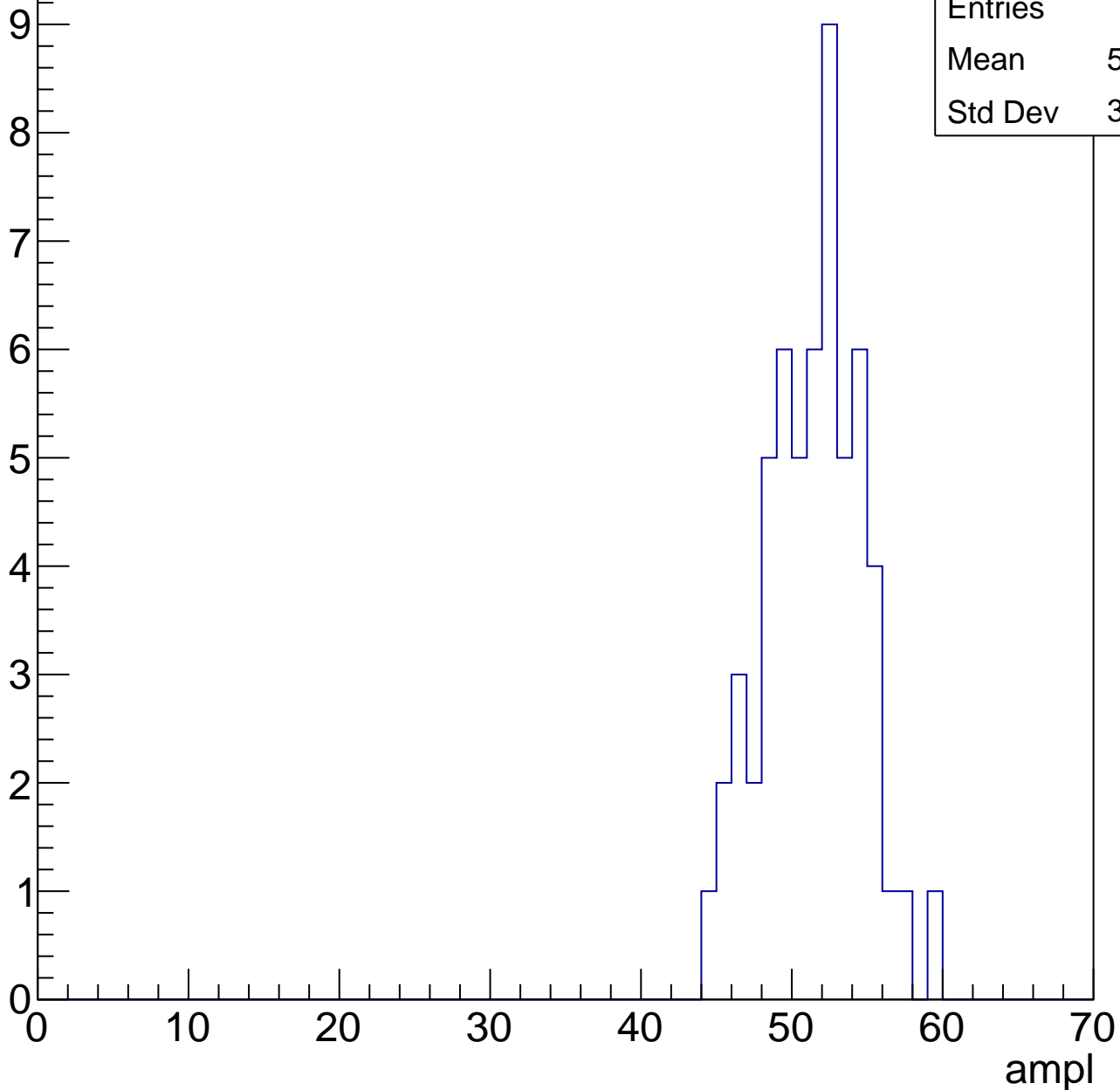


B0L001S, U11-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

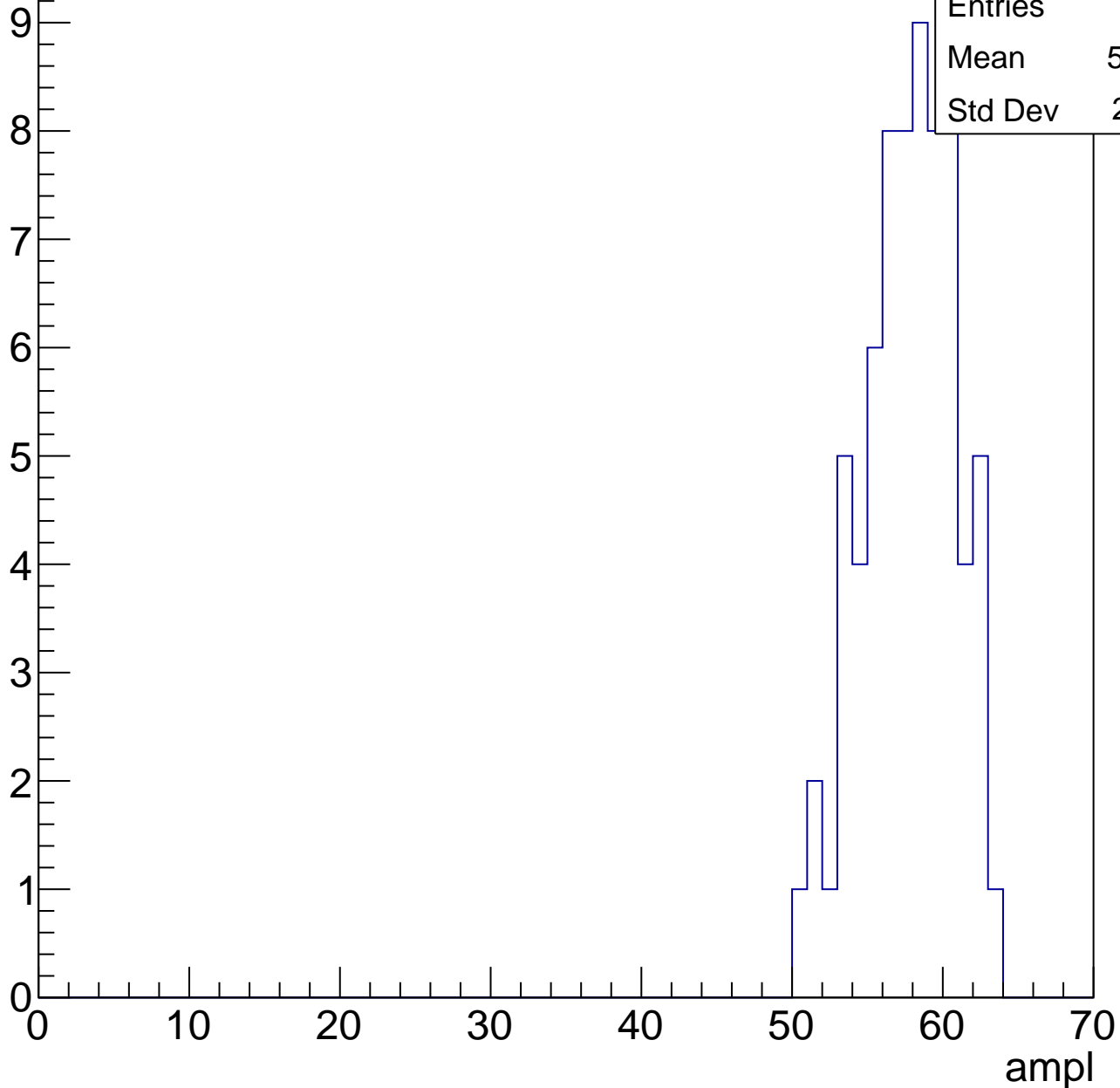
Entries	57
Mean	50.96
Std Dev	3.173



B0L001S, U11-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

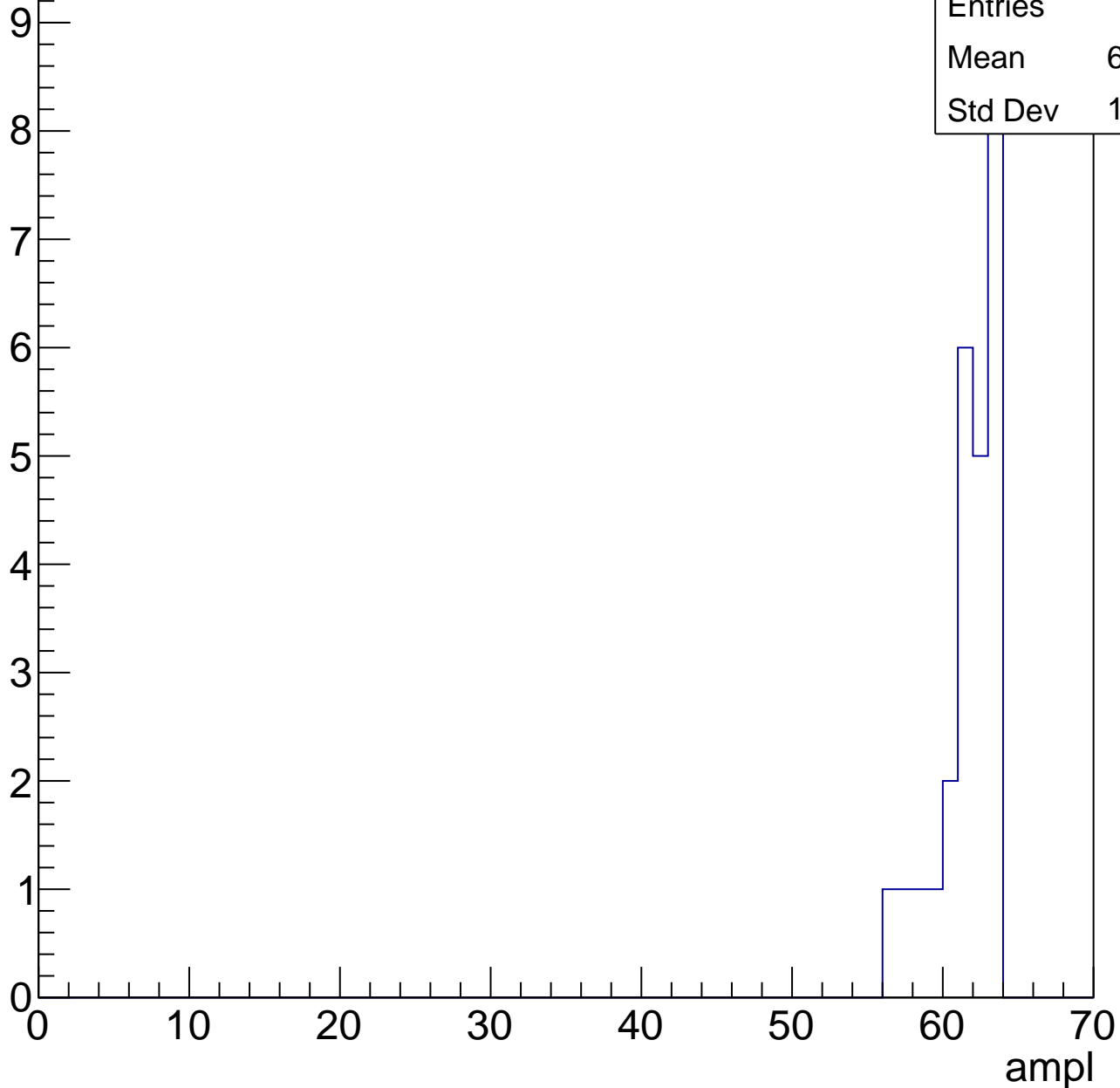
Entry



B0L001S, U11-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U11-ch78, adc0

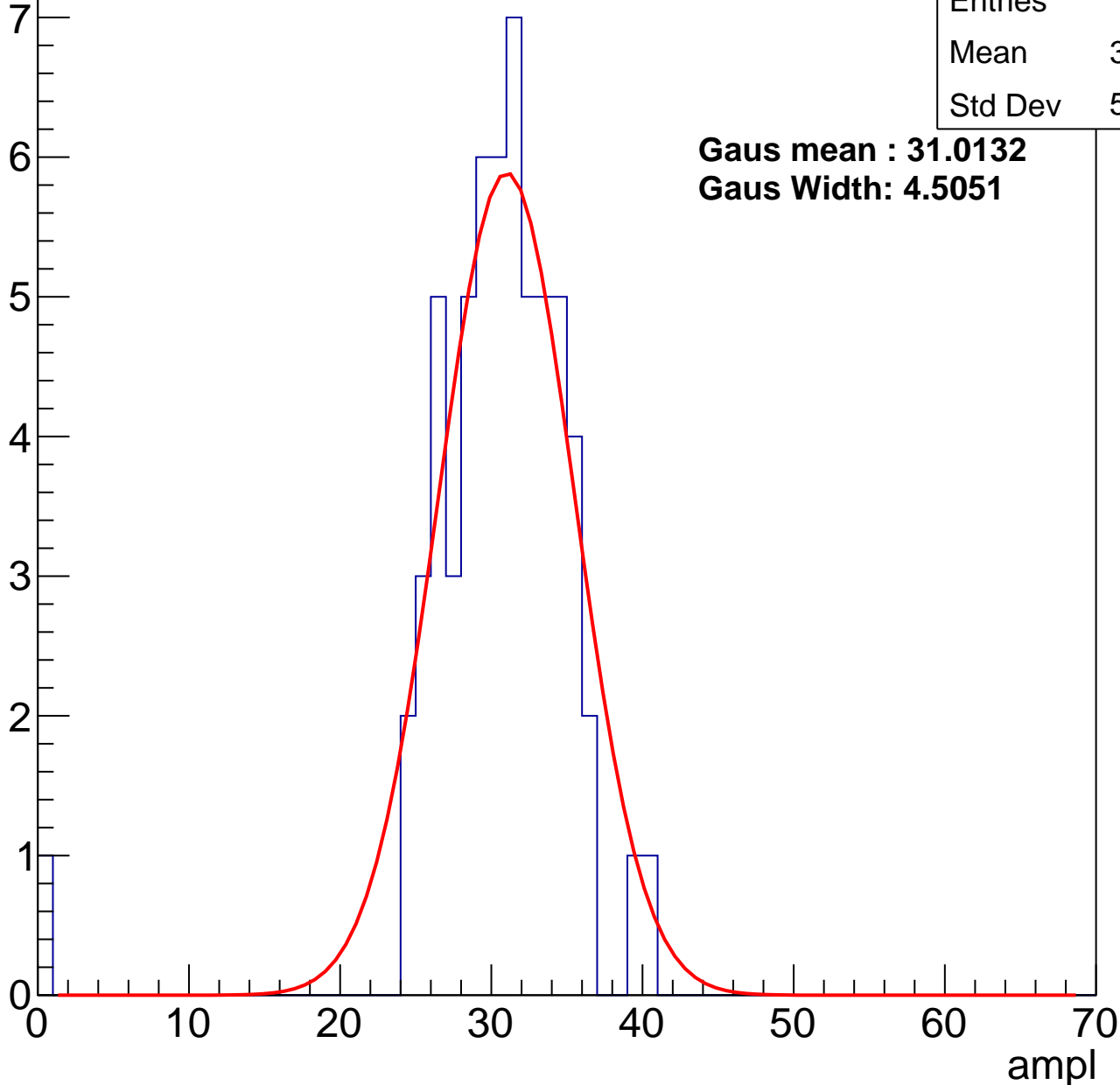
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.02
Std Dev	5.256

Gaus mean : 31.0132

Gaus Width: 4.5051



B0L001S, U11-ch78, adc1

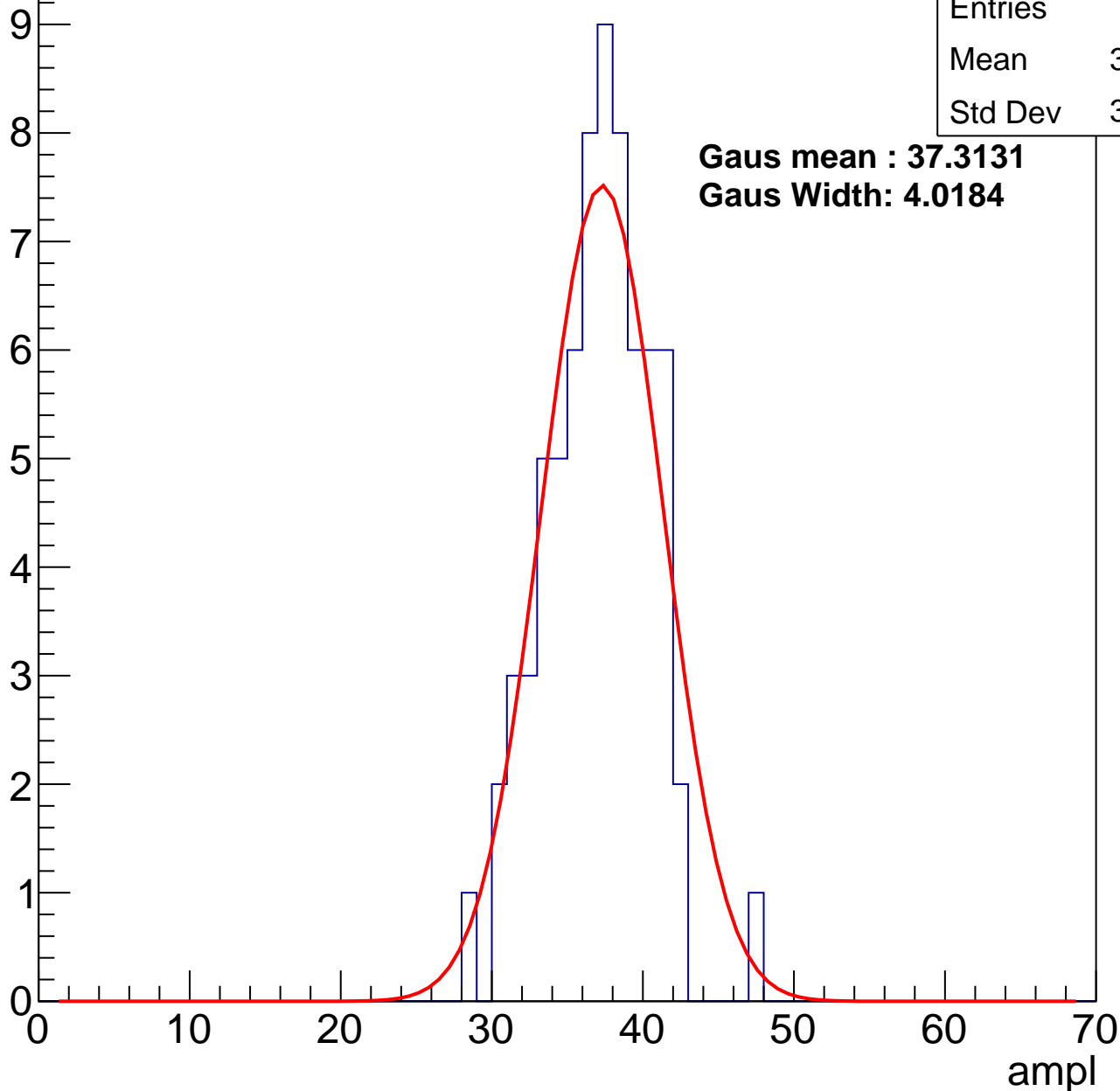
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.59
Std Dev	3.438

Gaus mean : 37.3131

Gaus Width: 4.0184



B0L001S, U11-ch78, adc2

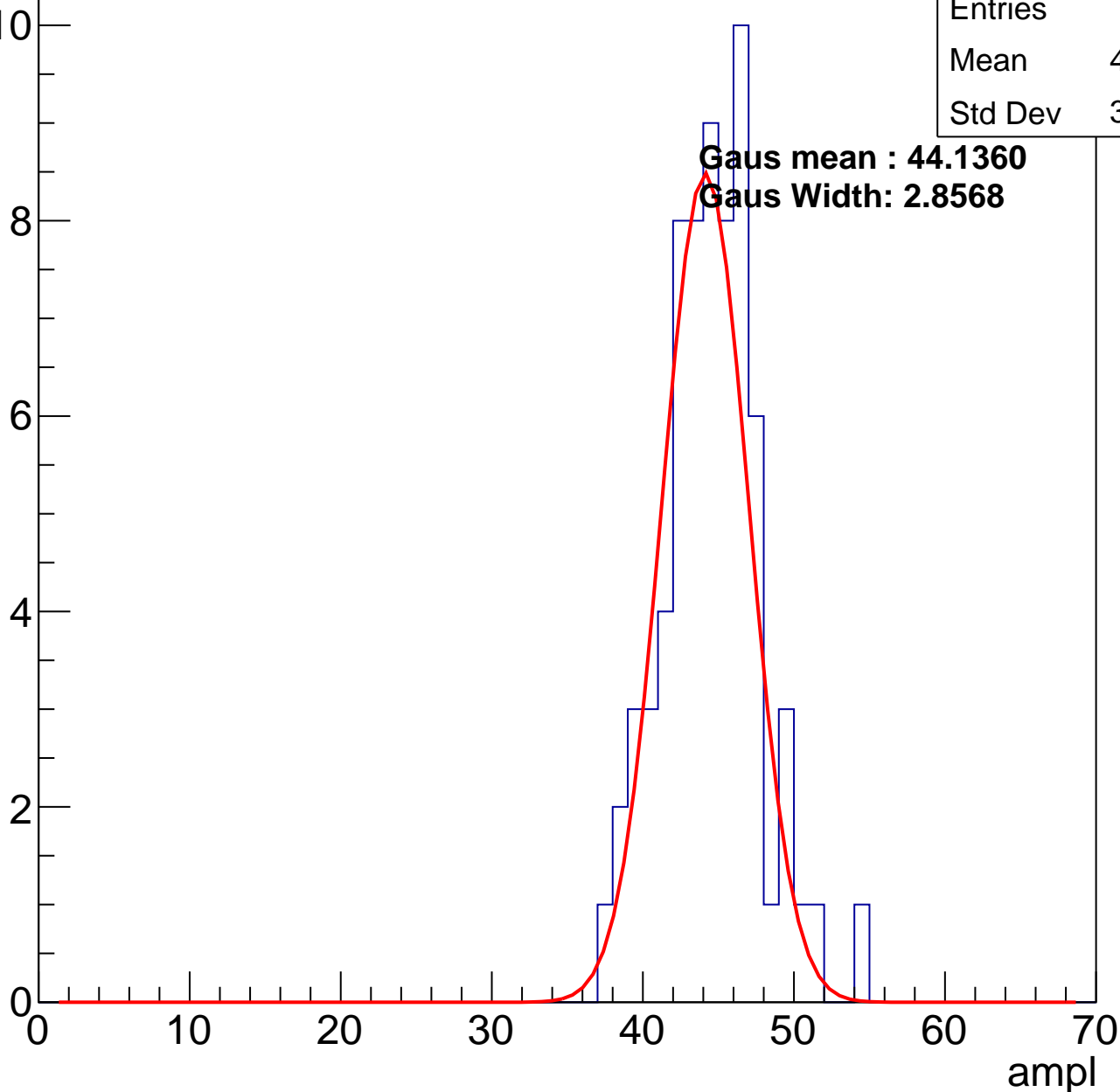
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.09
Std Dev	3.179

Gaus mean : 44.1360

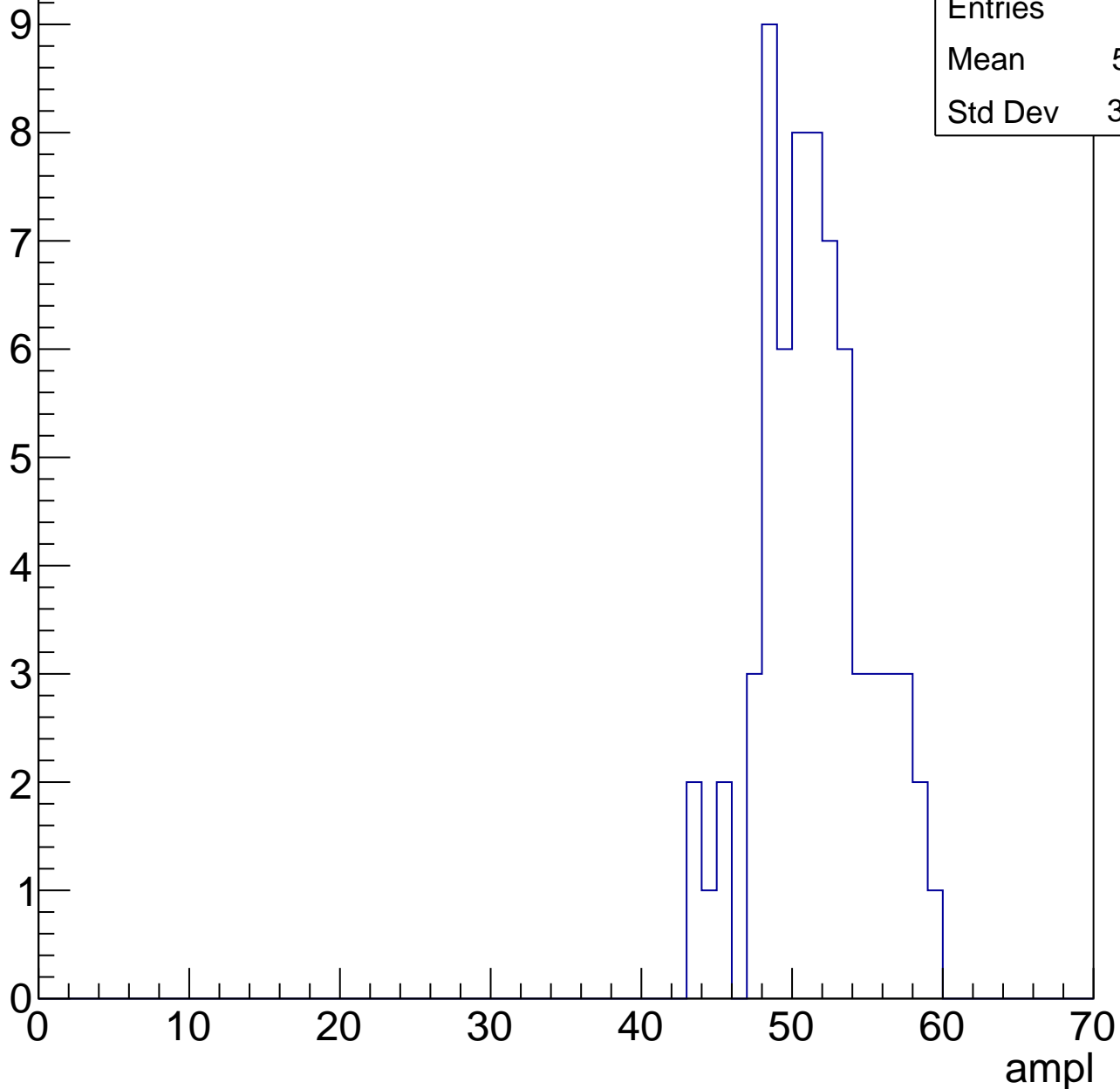
Gaus Width: 2.8568



B0L001S, U11-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

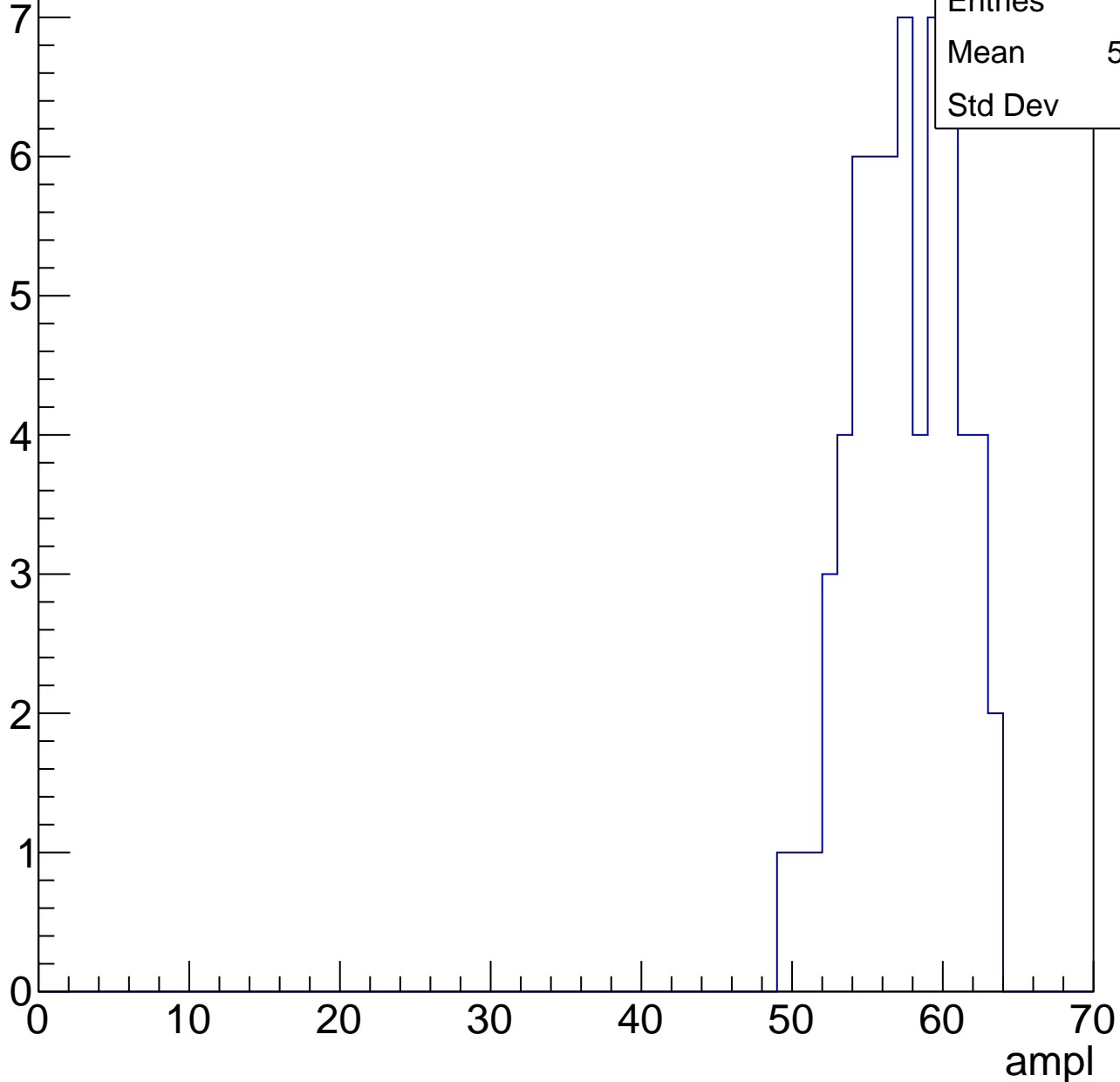


Entries	67
Mean	51.01
Std Dev	3.585

B0L001S, U11-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

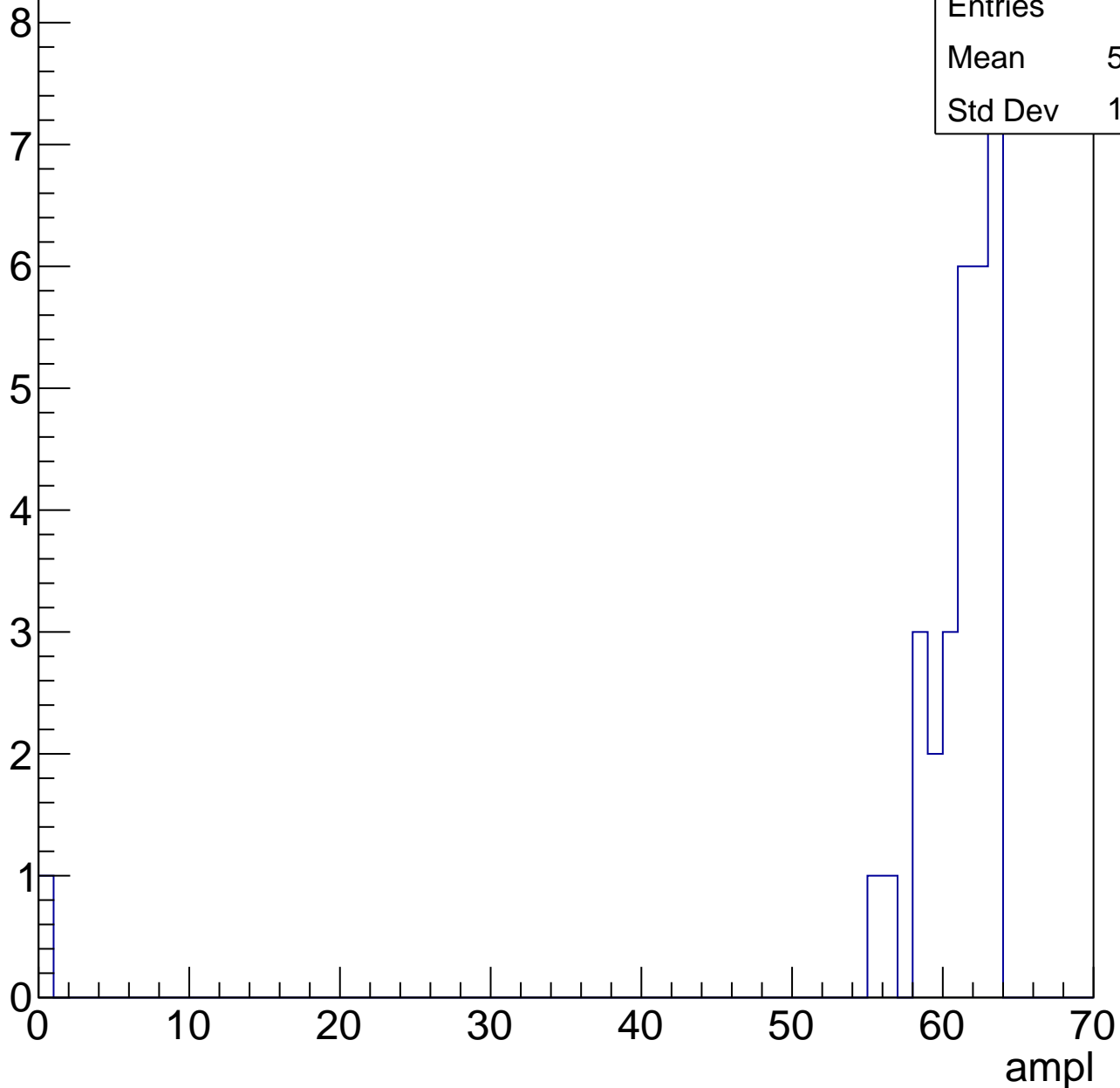


B0L001S, U11-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

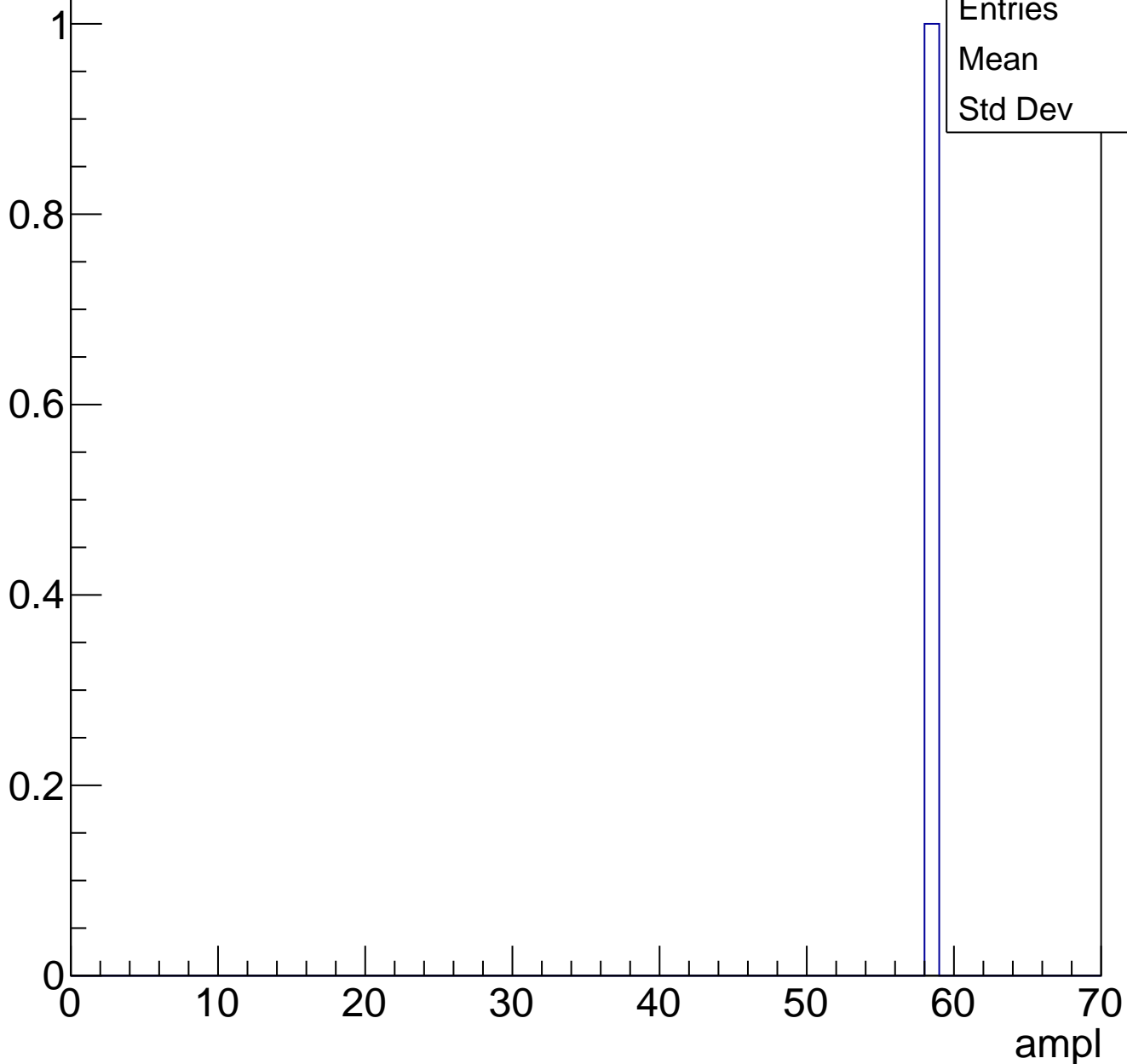
Entries	31
Mean	58.87
Std Dev	10.95



B0L001S, U11-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U11-ch79, adc0

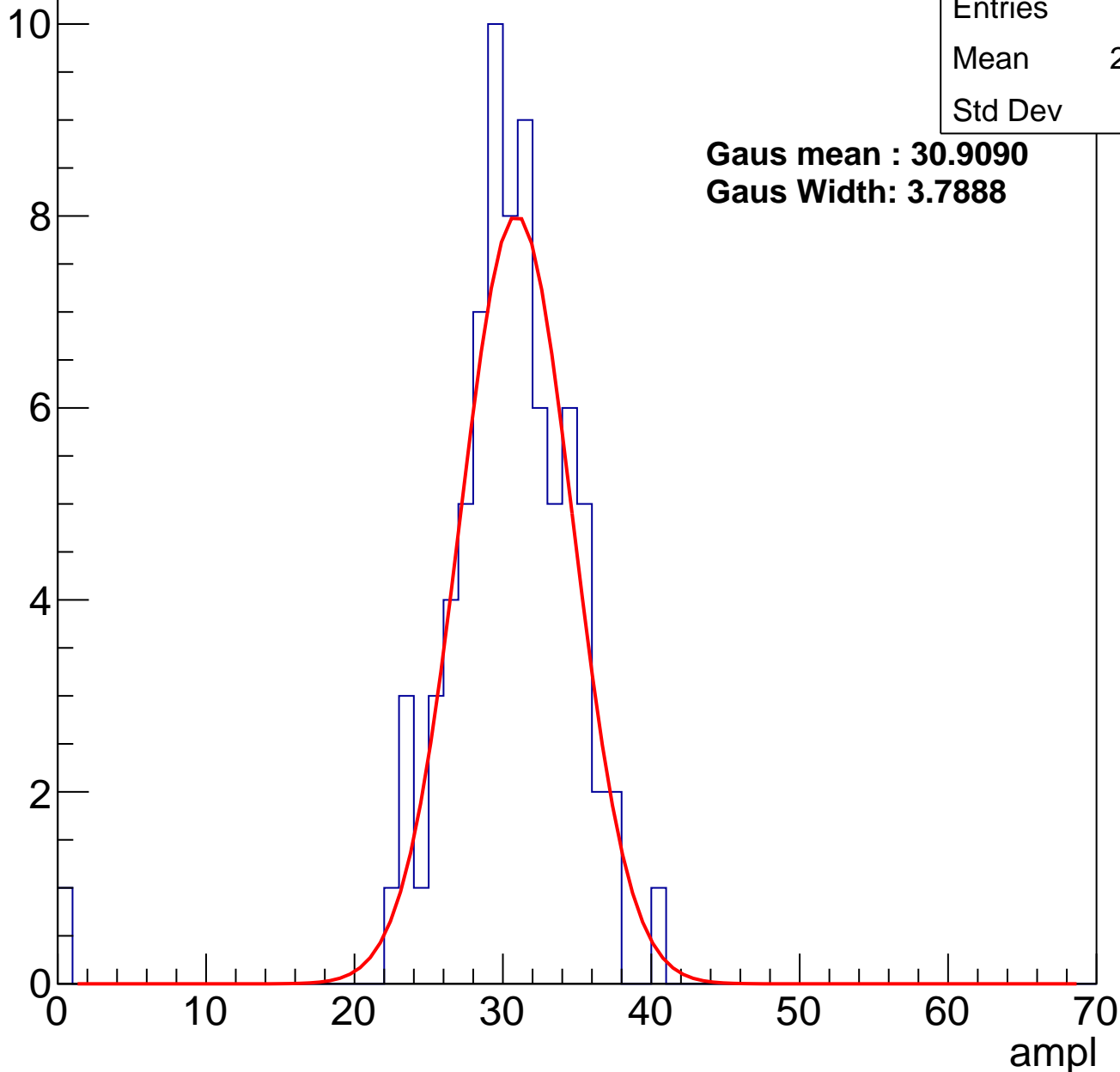
calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	29.82
Std Dev	4.95

Gaus mean : 30.9090

Gaus Width: 3.7888

Entry



B0L001S, U11-ch79, adc1

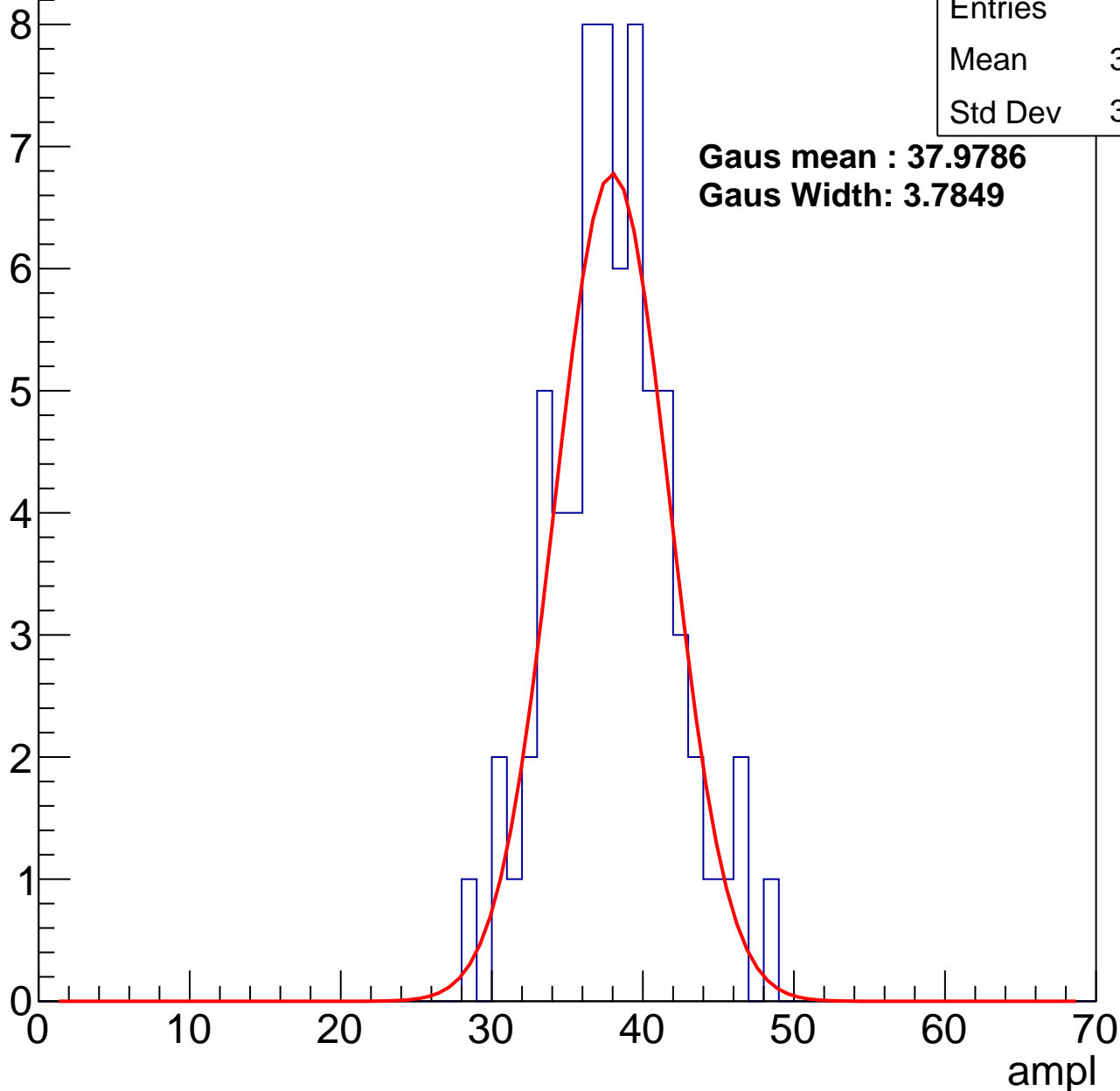
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.59
Std Dev	3.968

Gaus mean : 37.9786

Gaus Width: 3.7849



B0L001S, U11-ch79, adc2

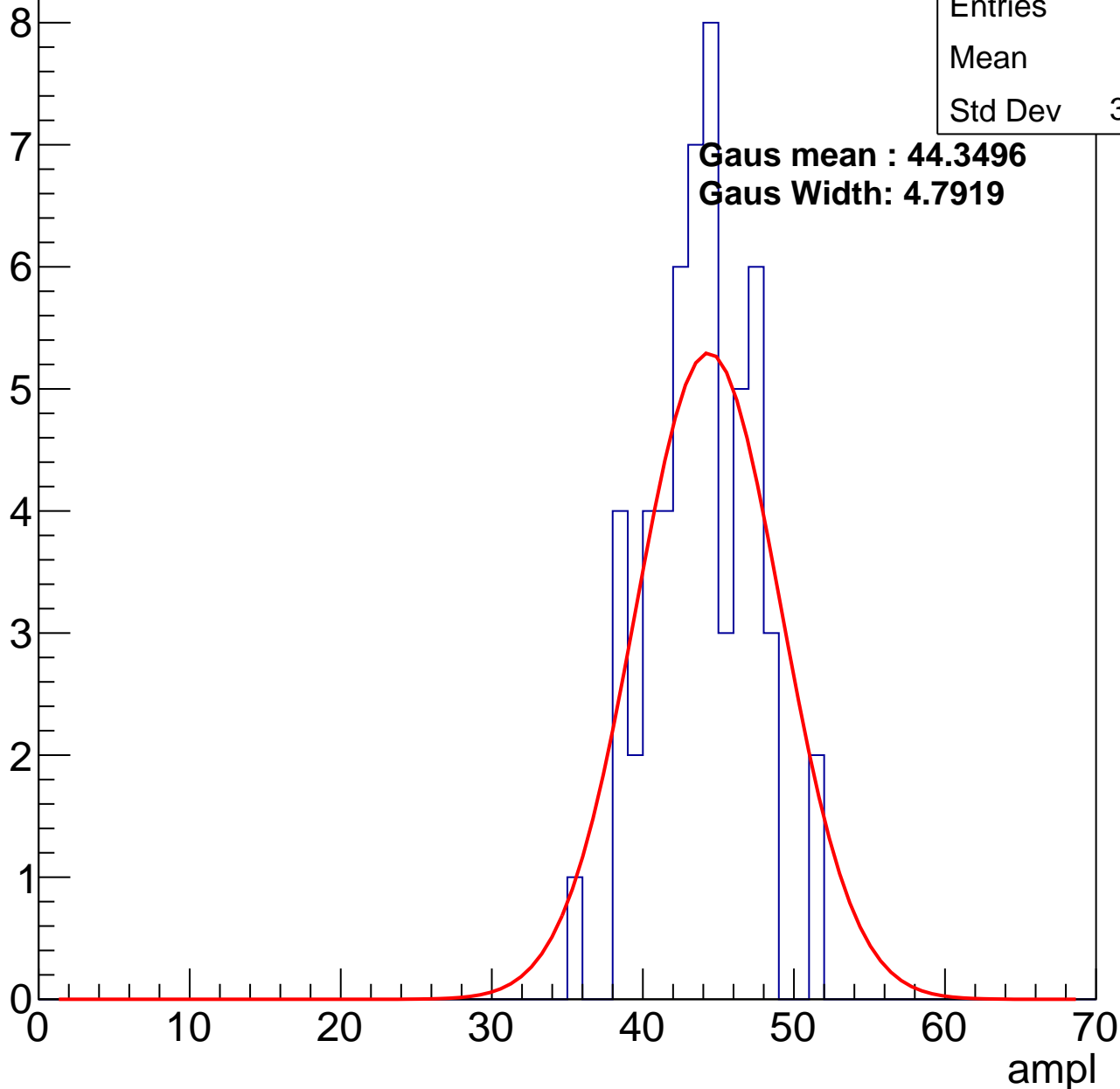
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.4
Std Dev	3.328

Gaus mean : 44.3496

Gaus Width: 4.7919



B0L001S, U11-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

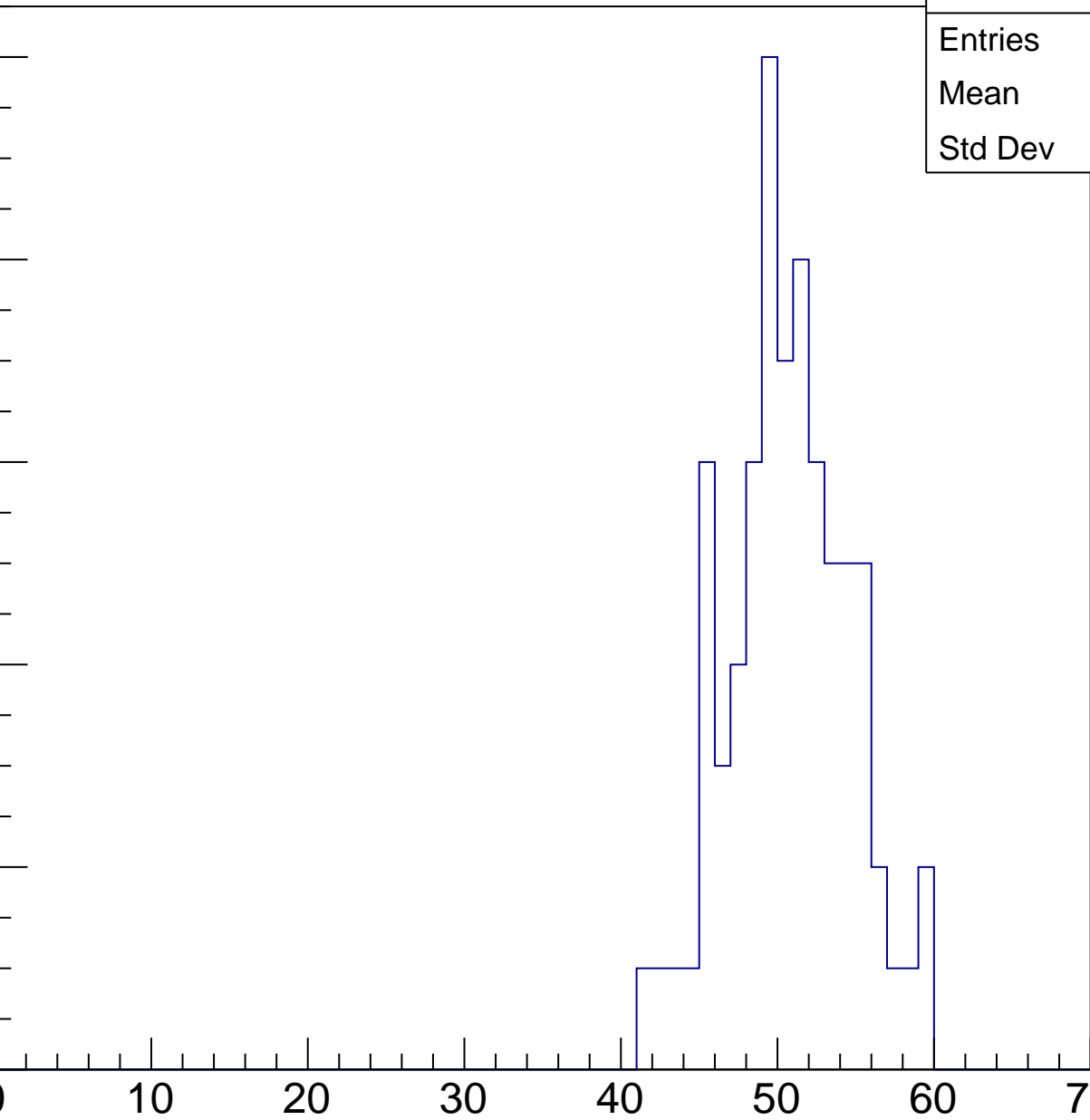
Entries	75
Mean	50.25
Std Dev	3.868

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

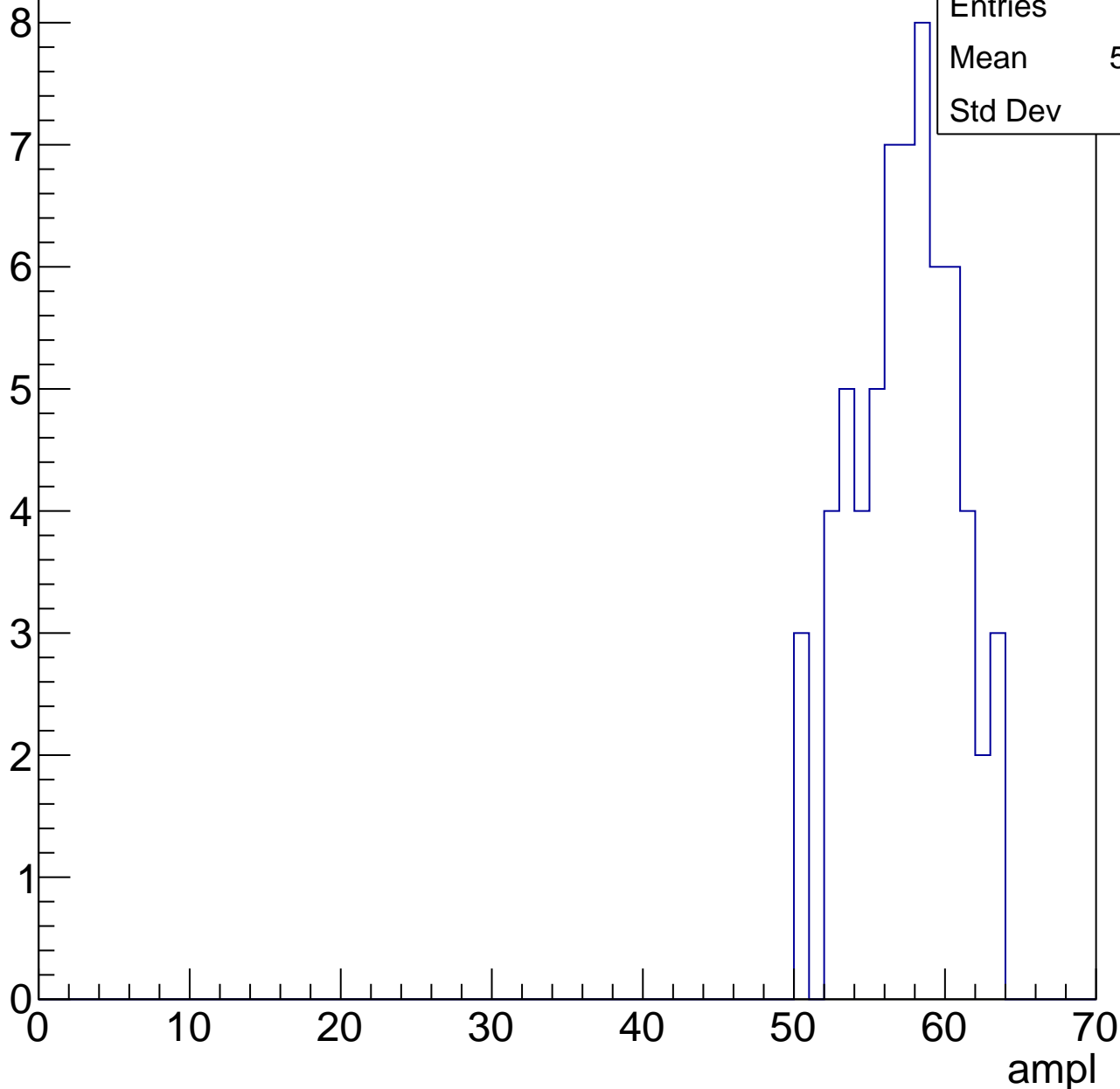


B0L001S, U11-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.88
Std Dev	3.3

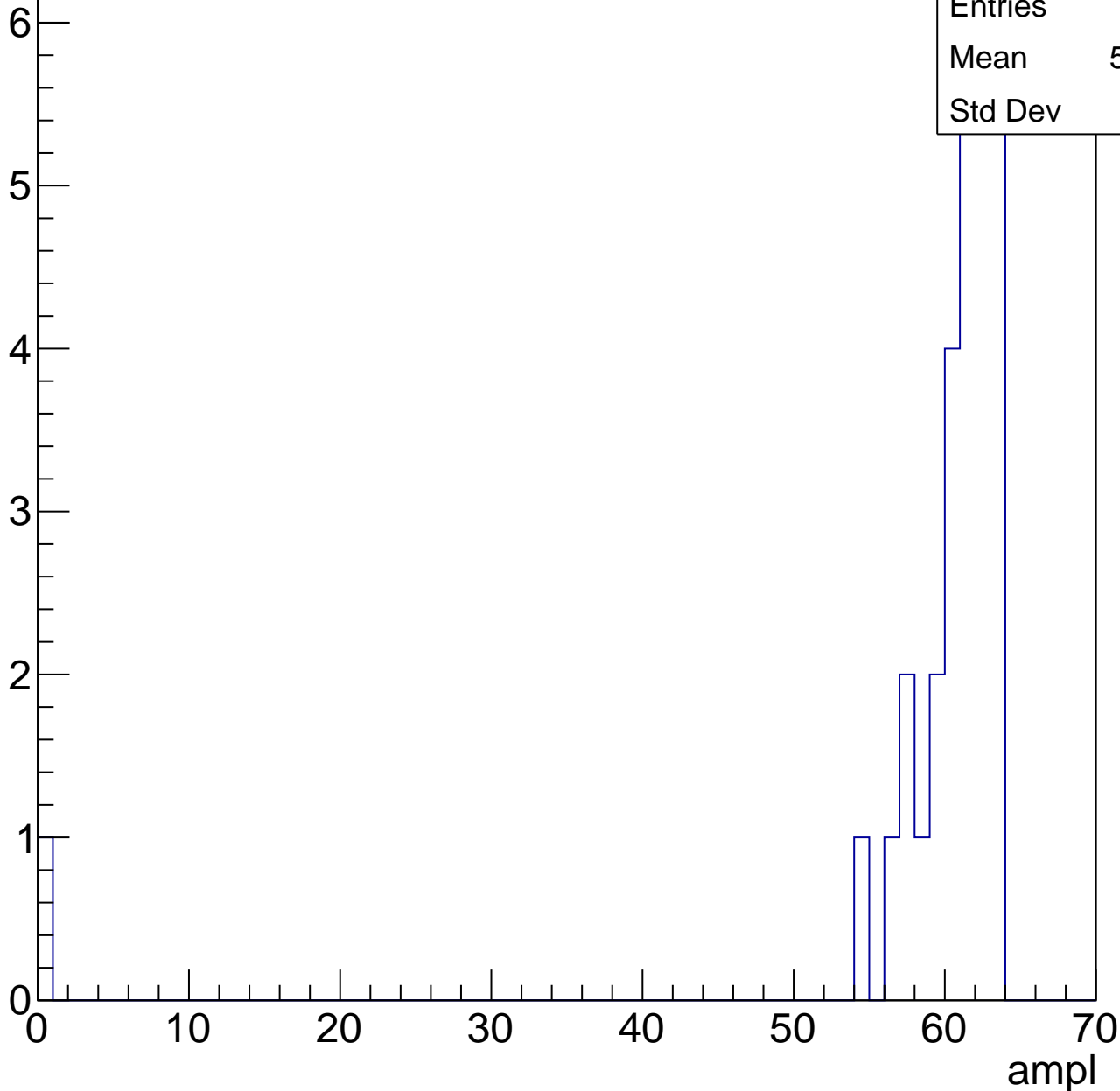


B0L001S, U11-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.53
Std Dev	11.1



B0L001S, U11-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B0L001S, U11-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch80, adc0

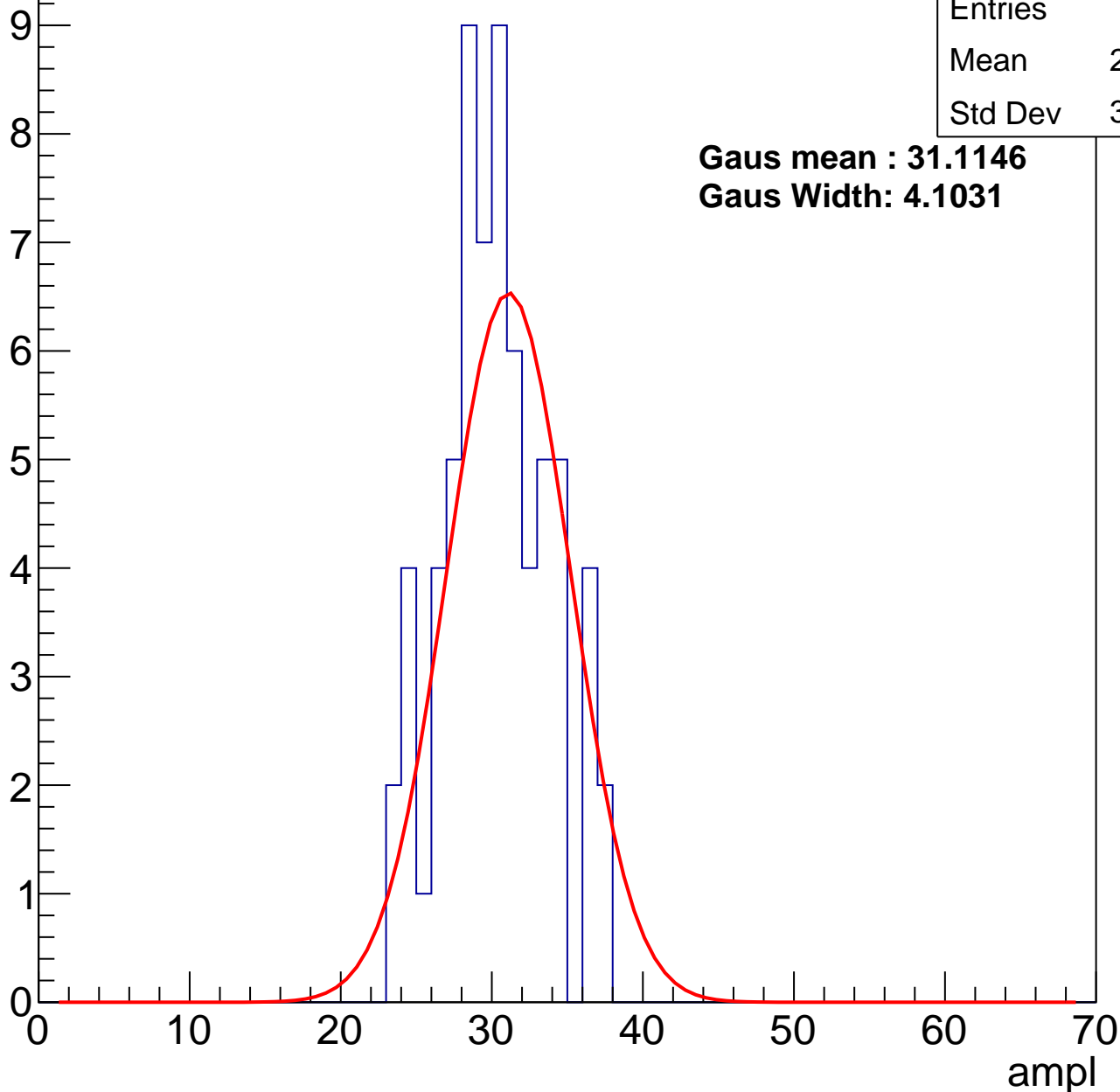
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.82
Std Dev	3.468

Gaus mean : 31.1146

Gaus Width: 4.1031



B0L001S, U11-ch80, adc1

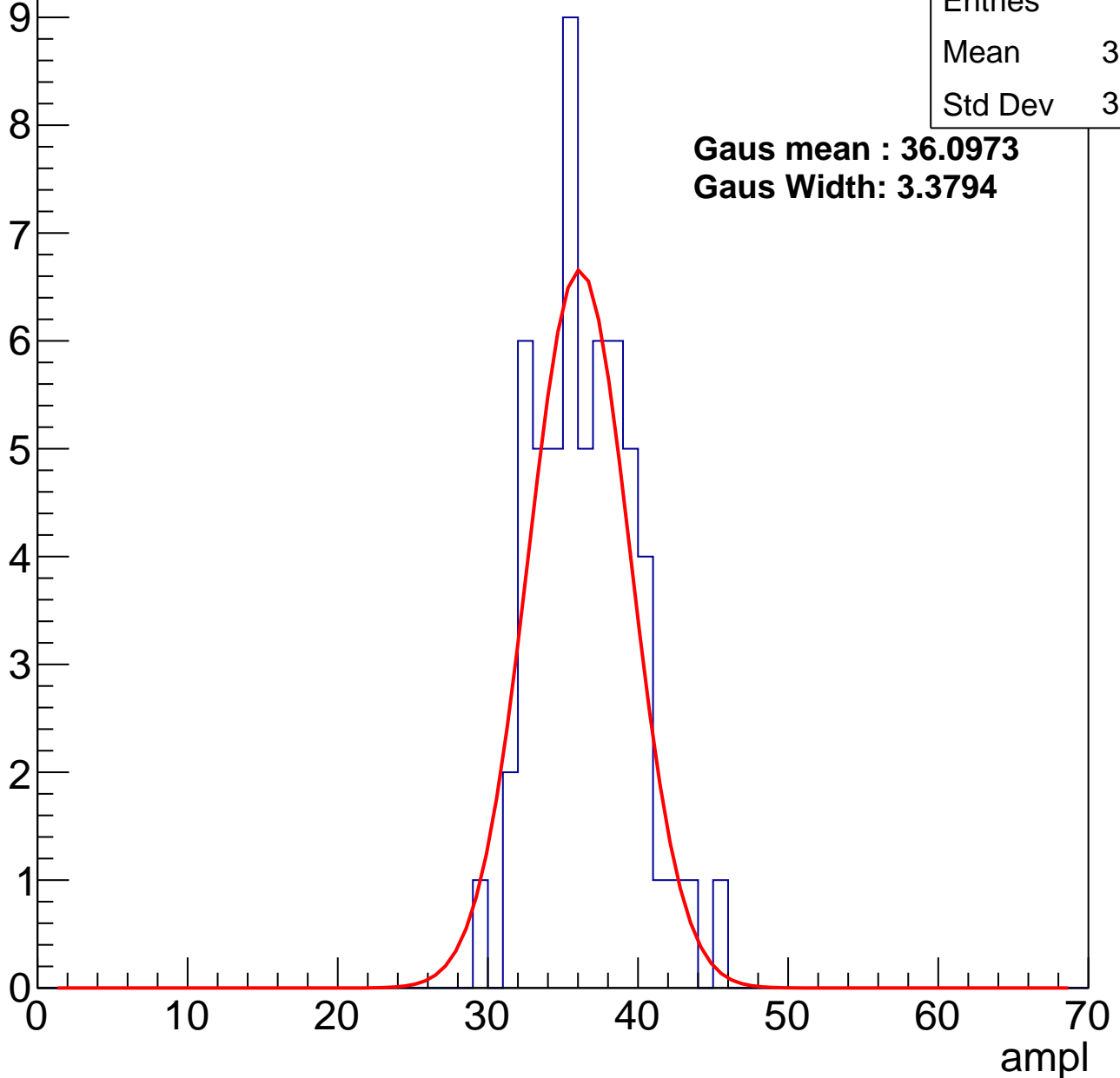
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	36.02
Std Dev	3.214

Gaus mean : 36.0973

Gaus Width: 3.3794



B0L001S, U11-ch80, adc2

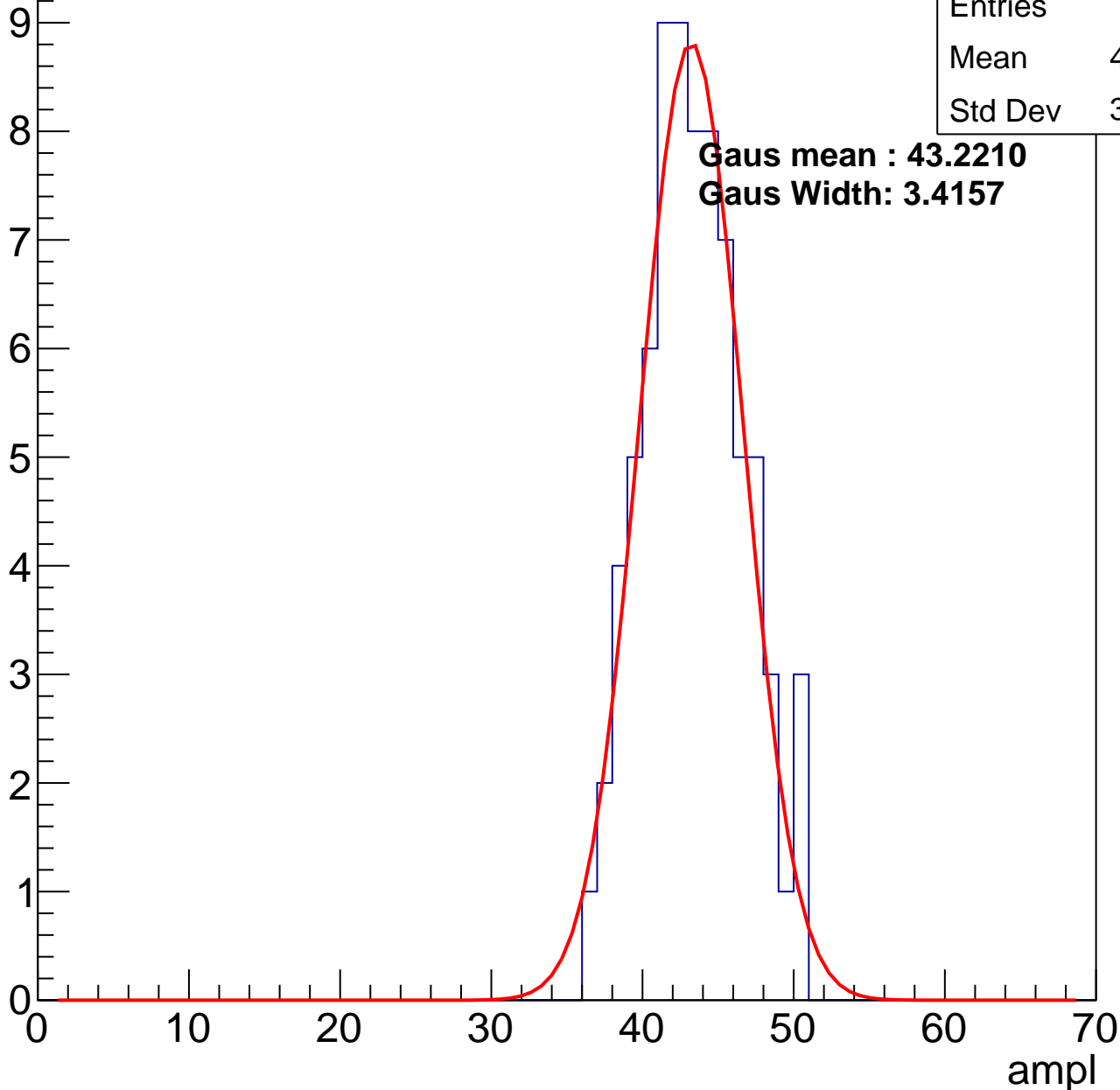
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	42.93
Std Dev	3.282

Gaus mean : 43.2210

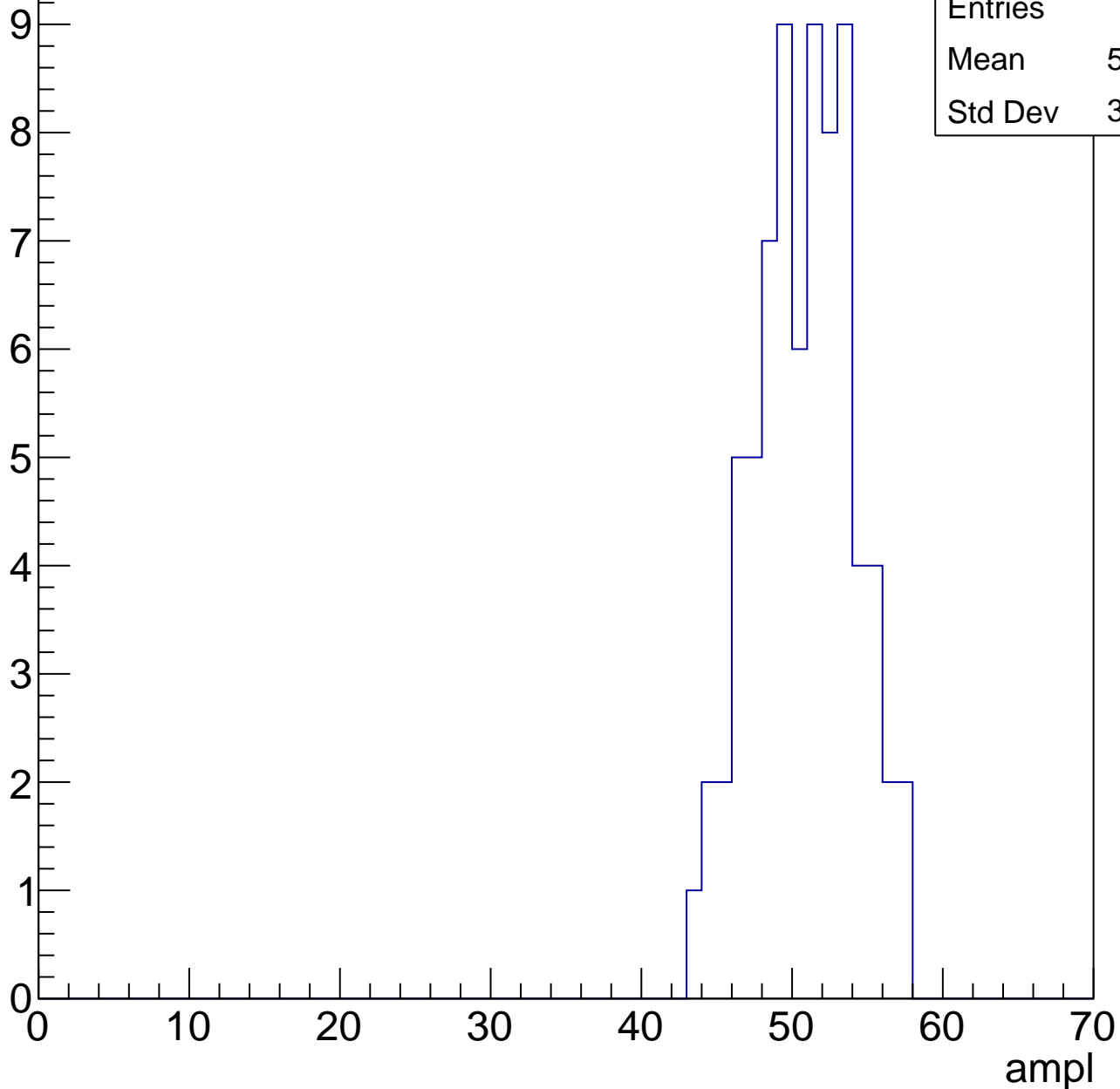
Gaus Width: 3.4157



B0L001S, U11-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

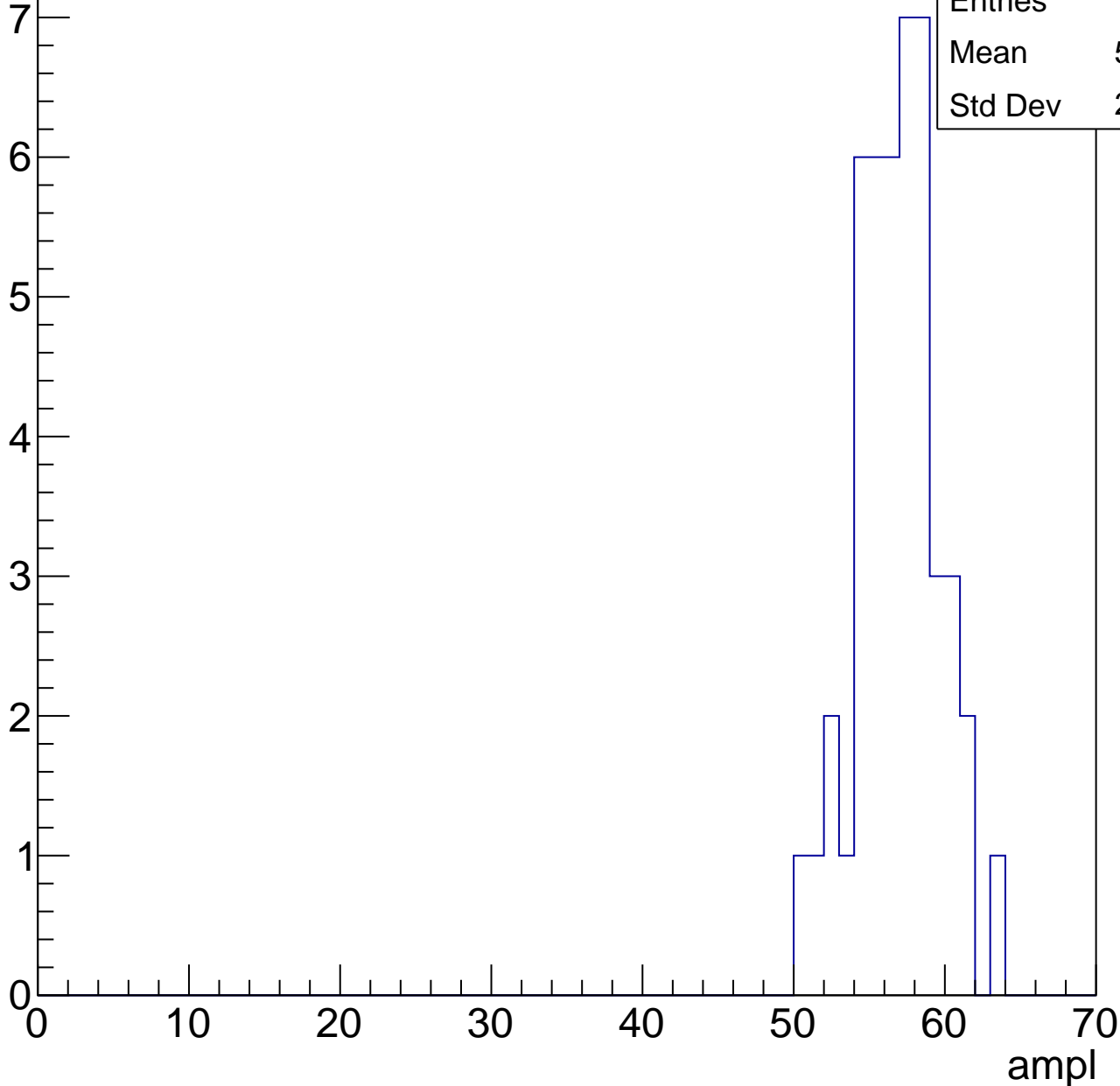


B0L001S, U11-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	56.41
Std Dev	2.691

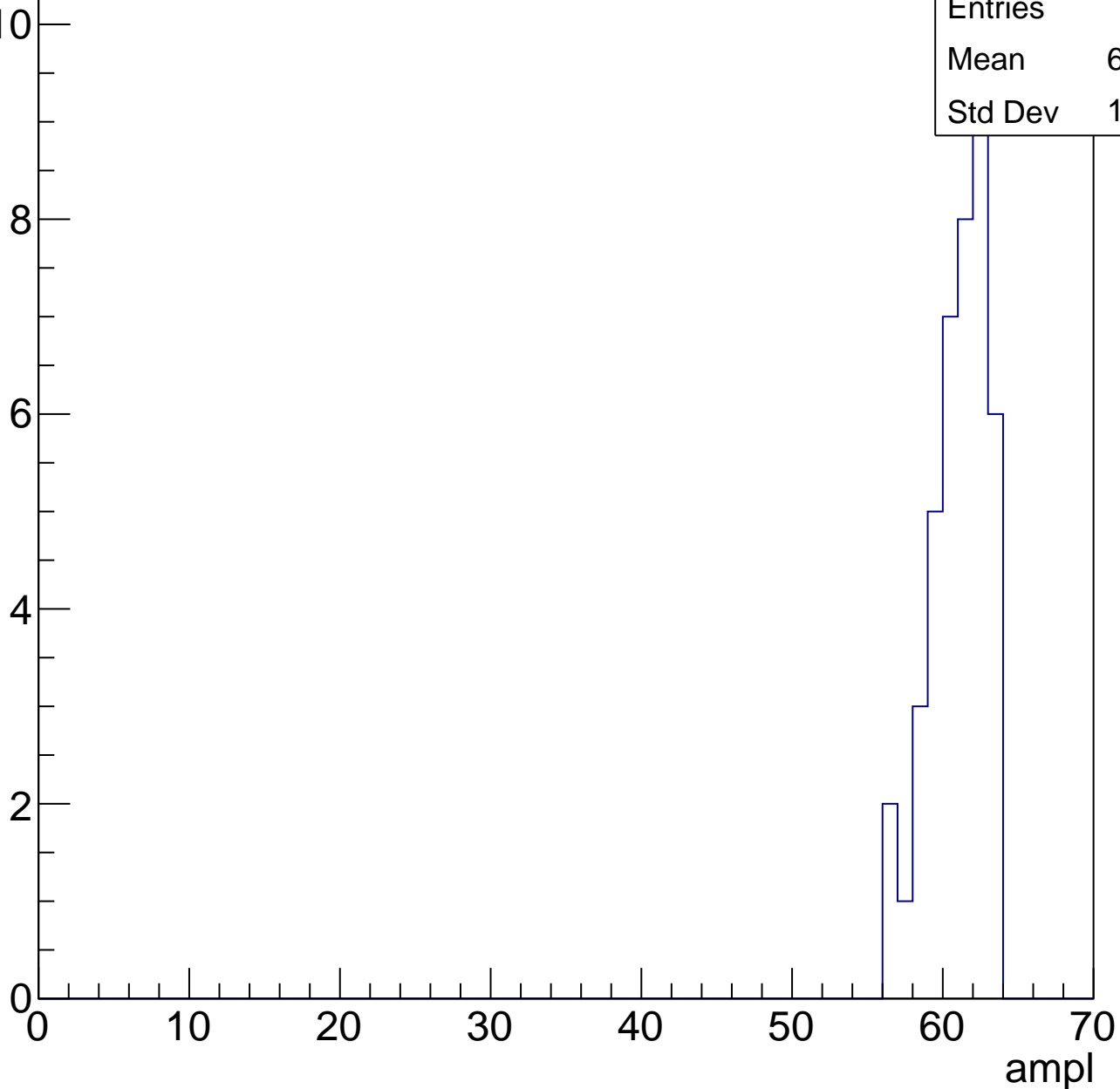


B0L001S, U11-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

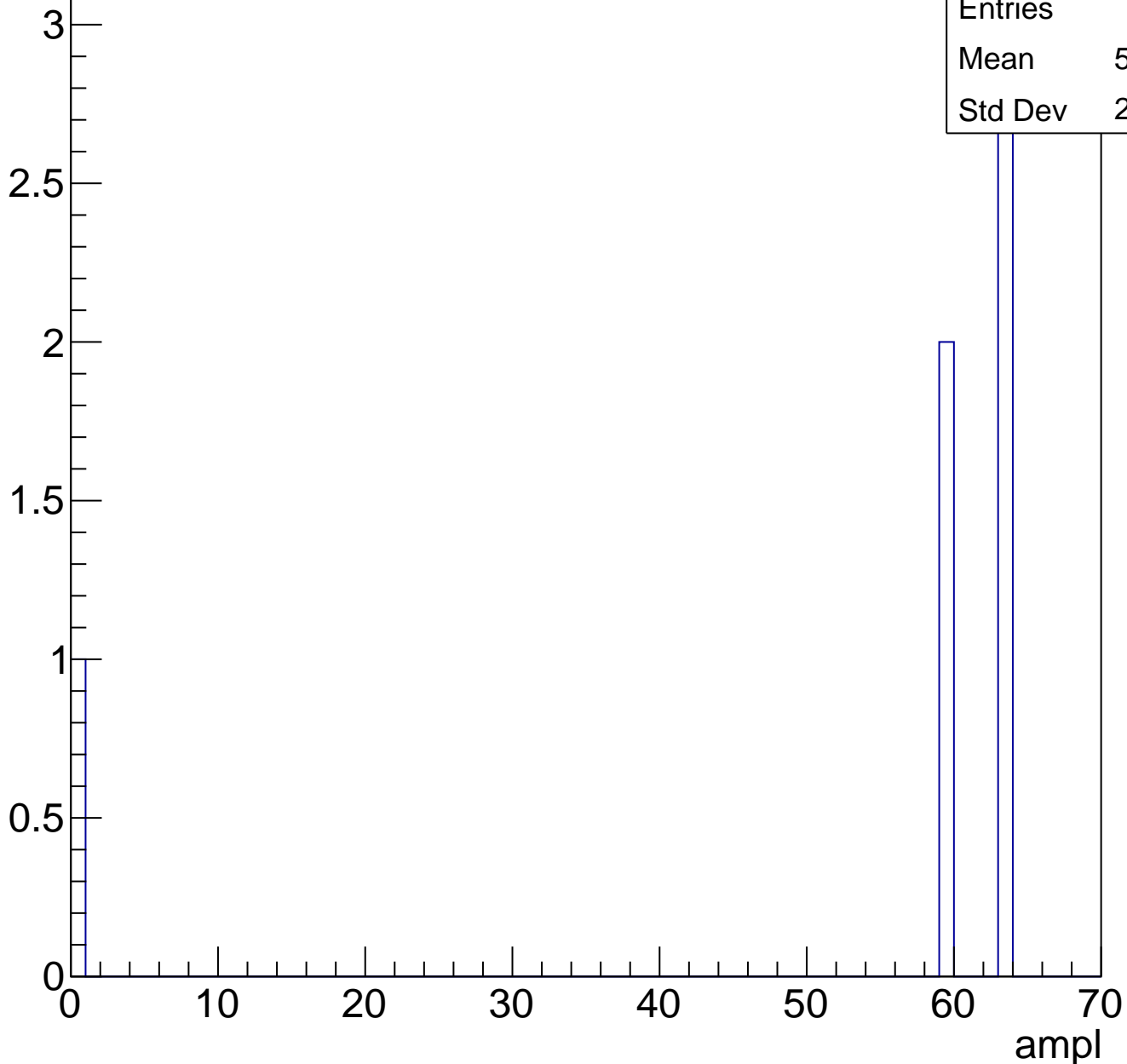
Entries	42
Mean	60.57
Std Dev	1.866



B0L001S, U11-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch81, adc0

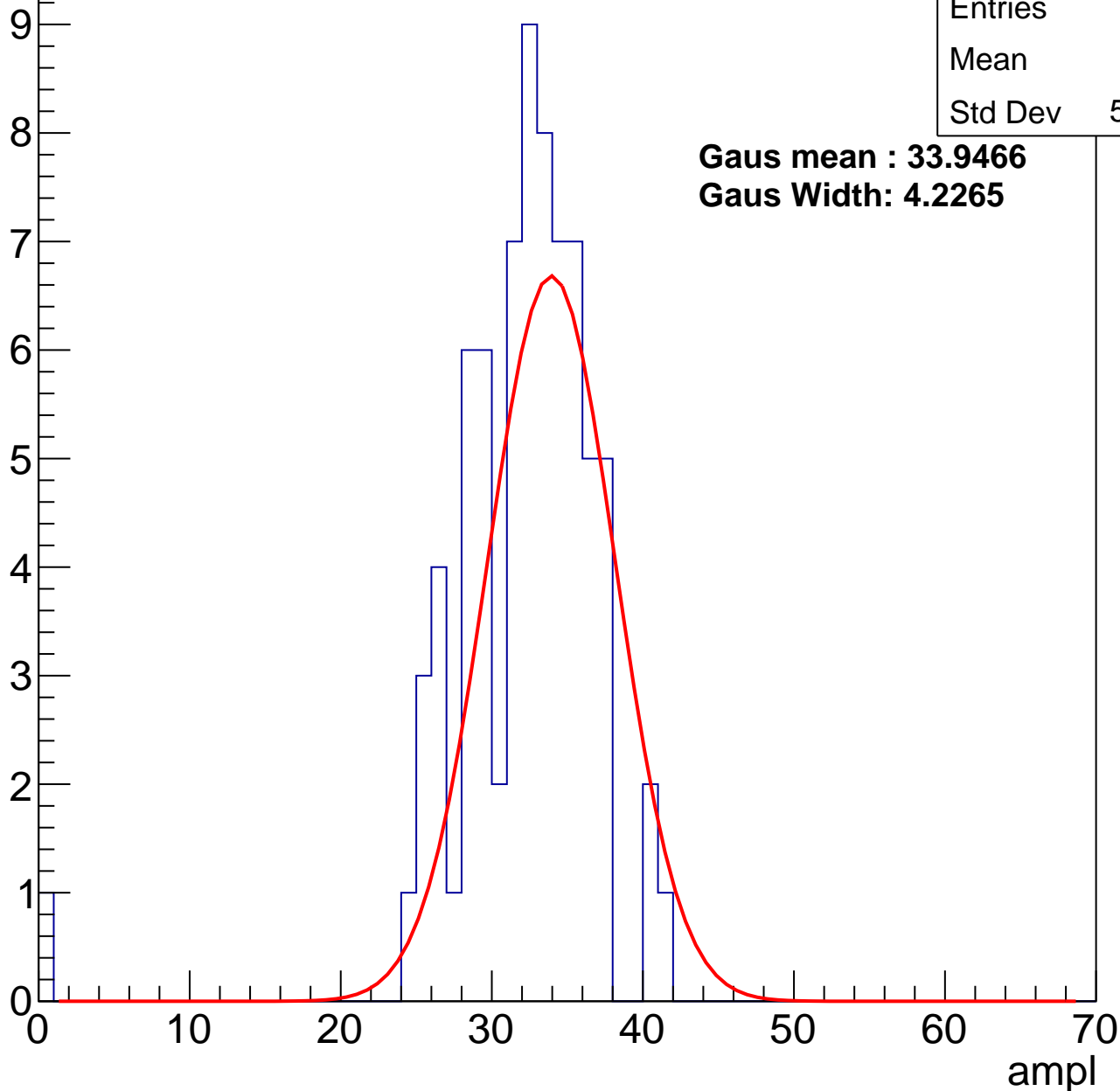
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	31.6
Std Dev	5.254

Gaus mean : 33.9466

Gaus Width: 4.2265



B0L001S, U11-ch81, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	56
Mean	38.8
Std Dev	3.259

Gaus mean : 38.7577

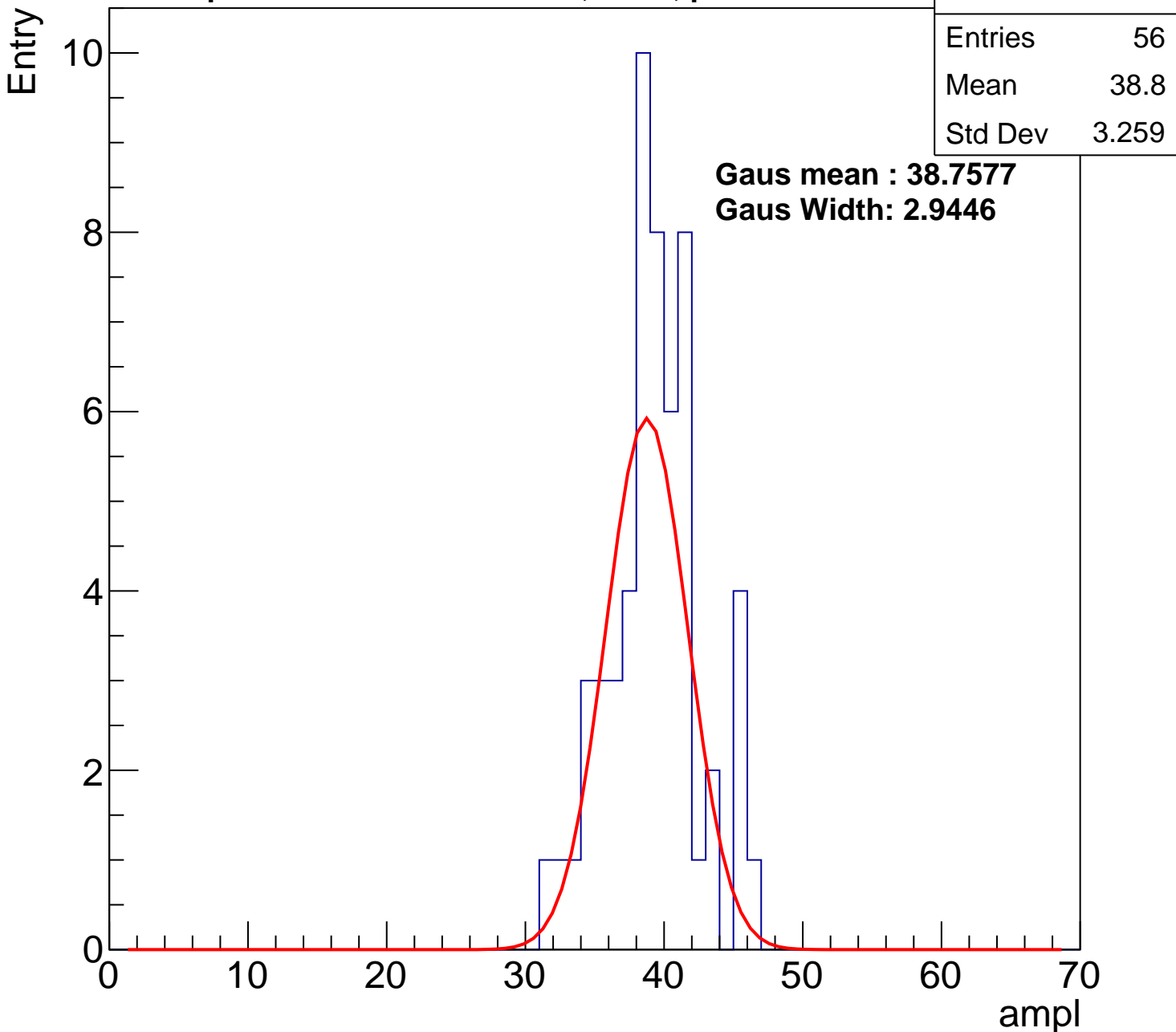
Gaus Width: 2.9446

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch81, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	44.69
Std Dev	3.516

Gaus mean : 45.7100

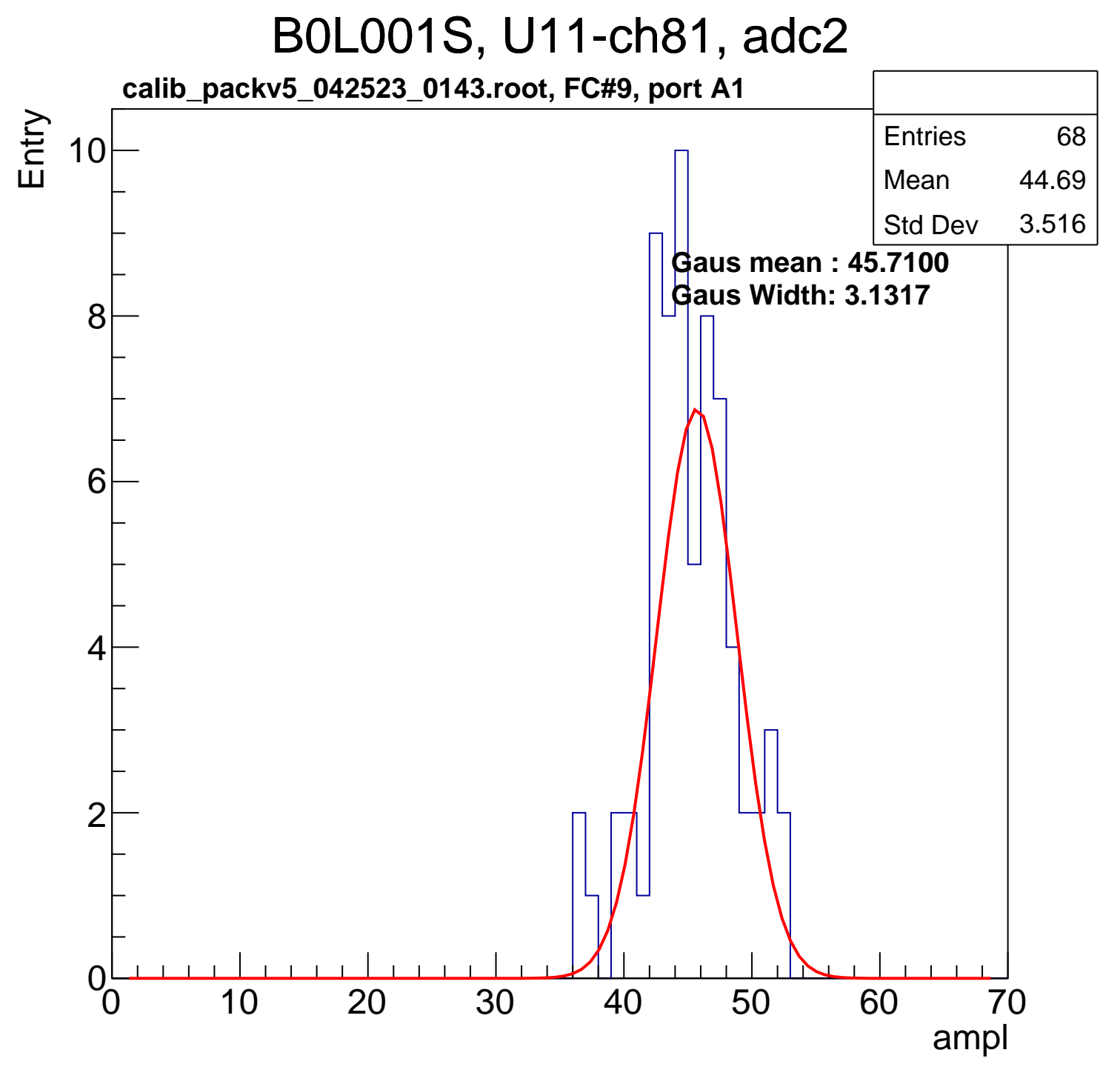
Gaus Width: 3.1317

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

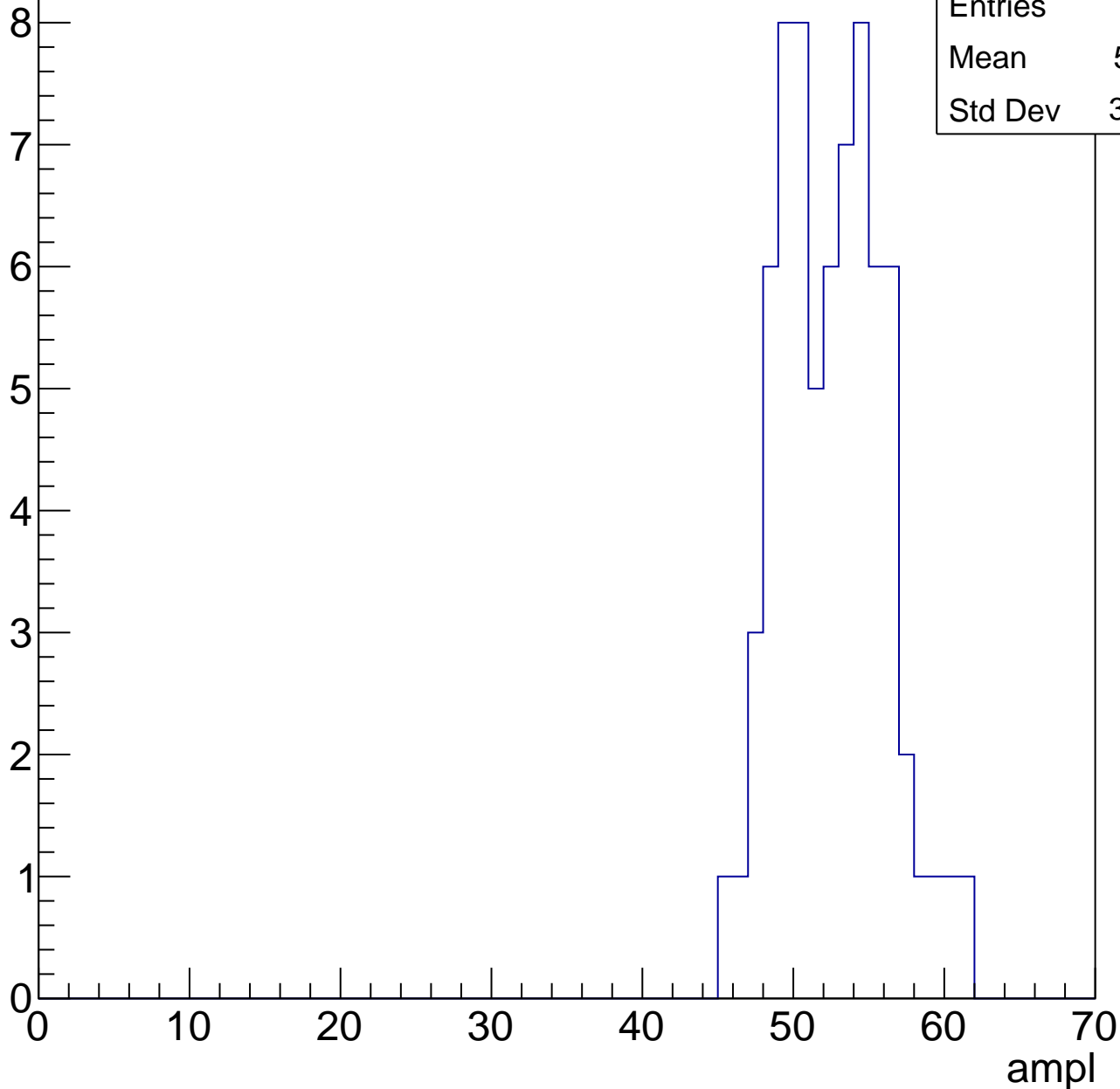


B0L001S, U11-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	52.11
Std Dev	3.425

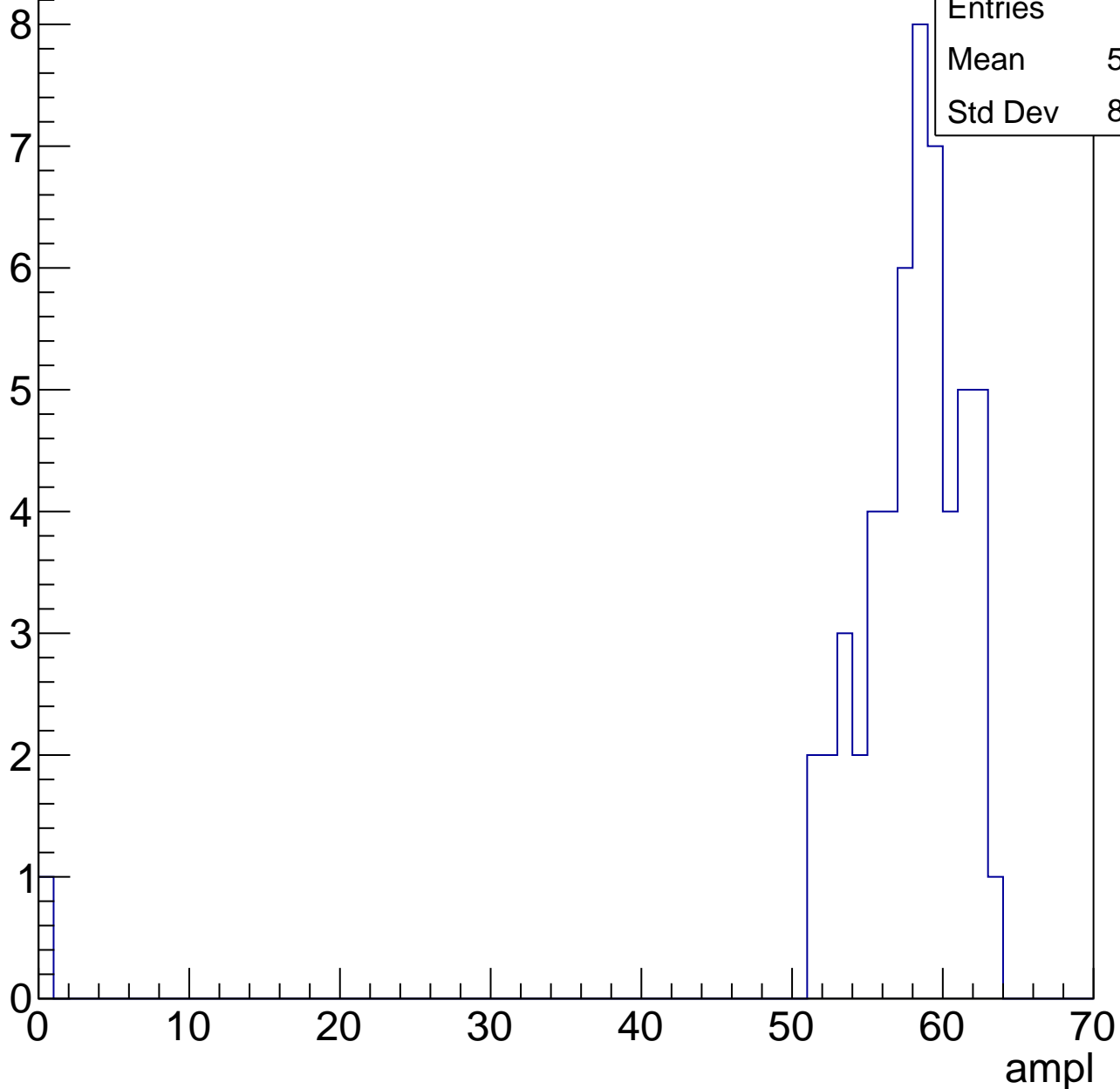


B0L001S, U11-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.56
Std Dev	8.344

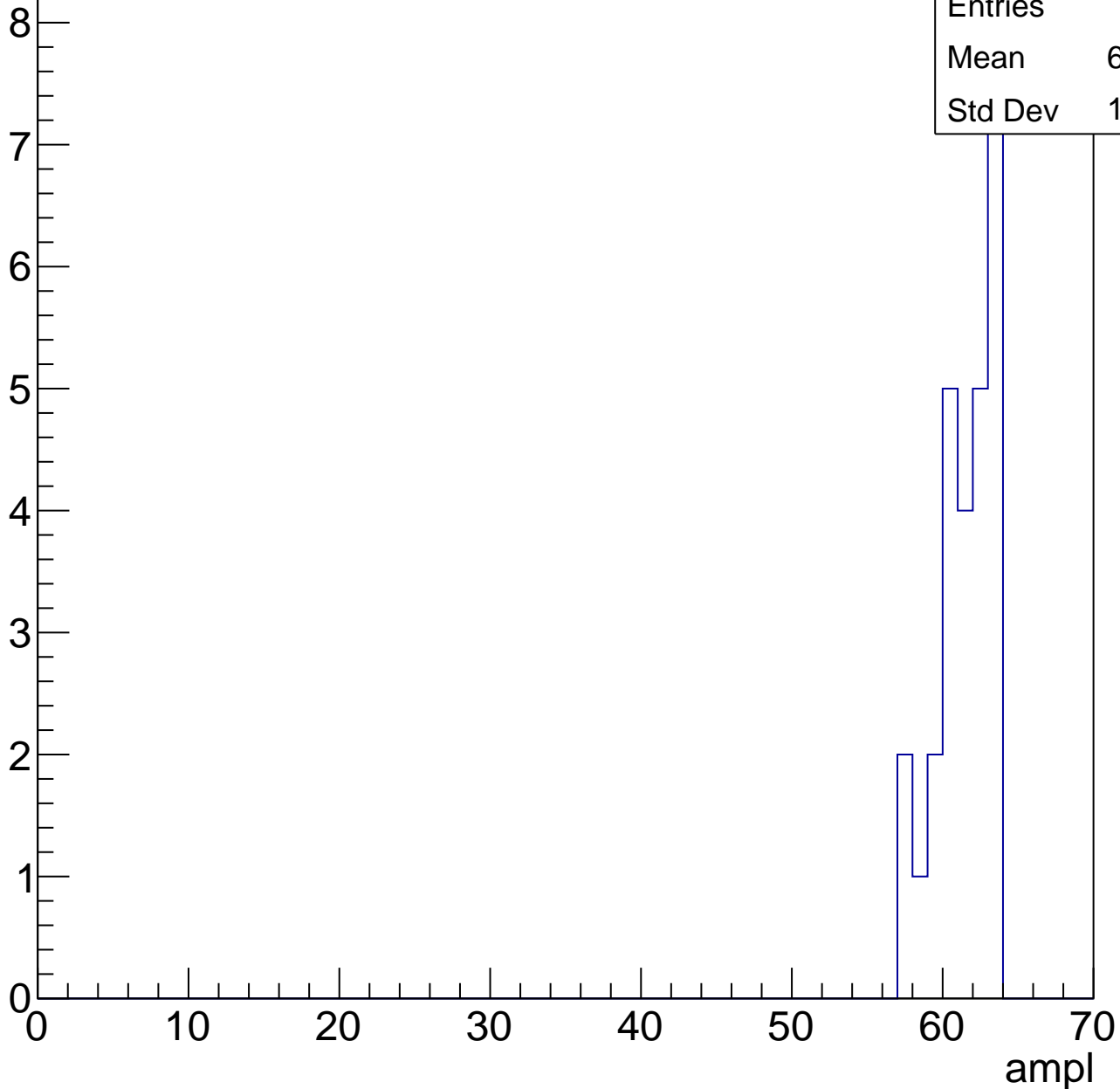


B0L001S, U11-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	61.04
Std Dev	1.835



B0L001S, U11-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch82, adc0

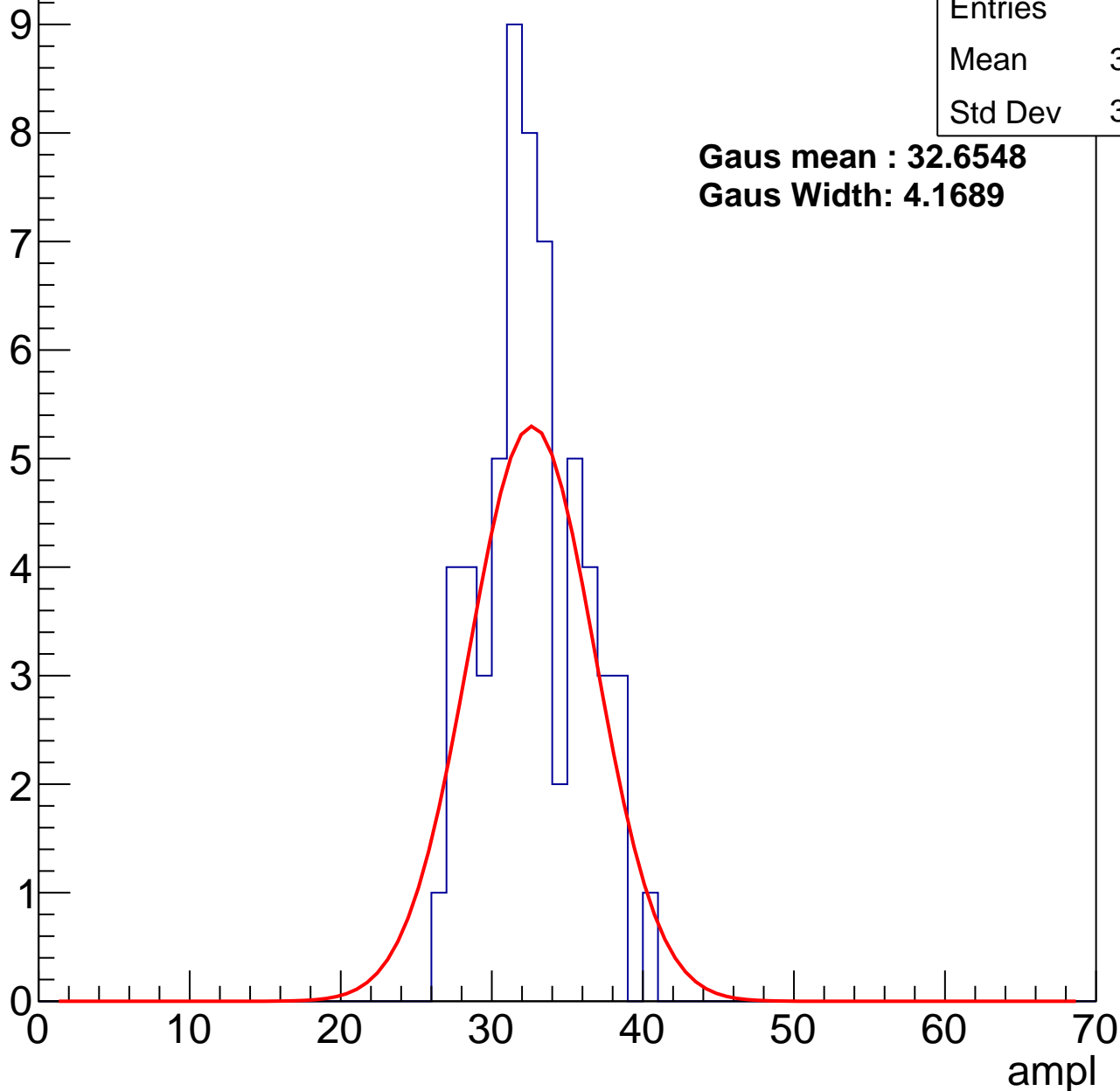
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	32.22
Std Dev	3.252

Gaus mean : 32.6548

Gaus Width: 4.1689



B0L001S, U11-ch82, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	39
Std Dev	3.773

Gaus mean : 38.9590

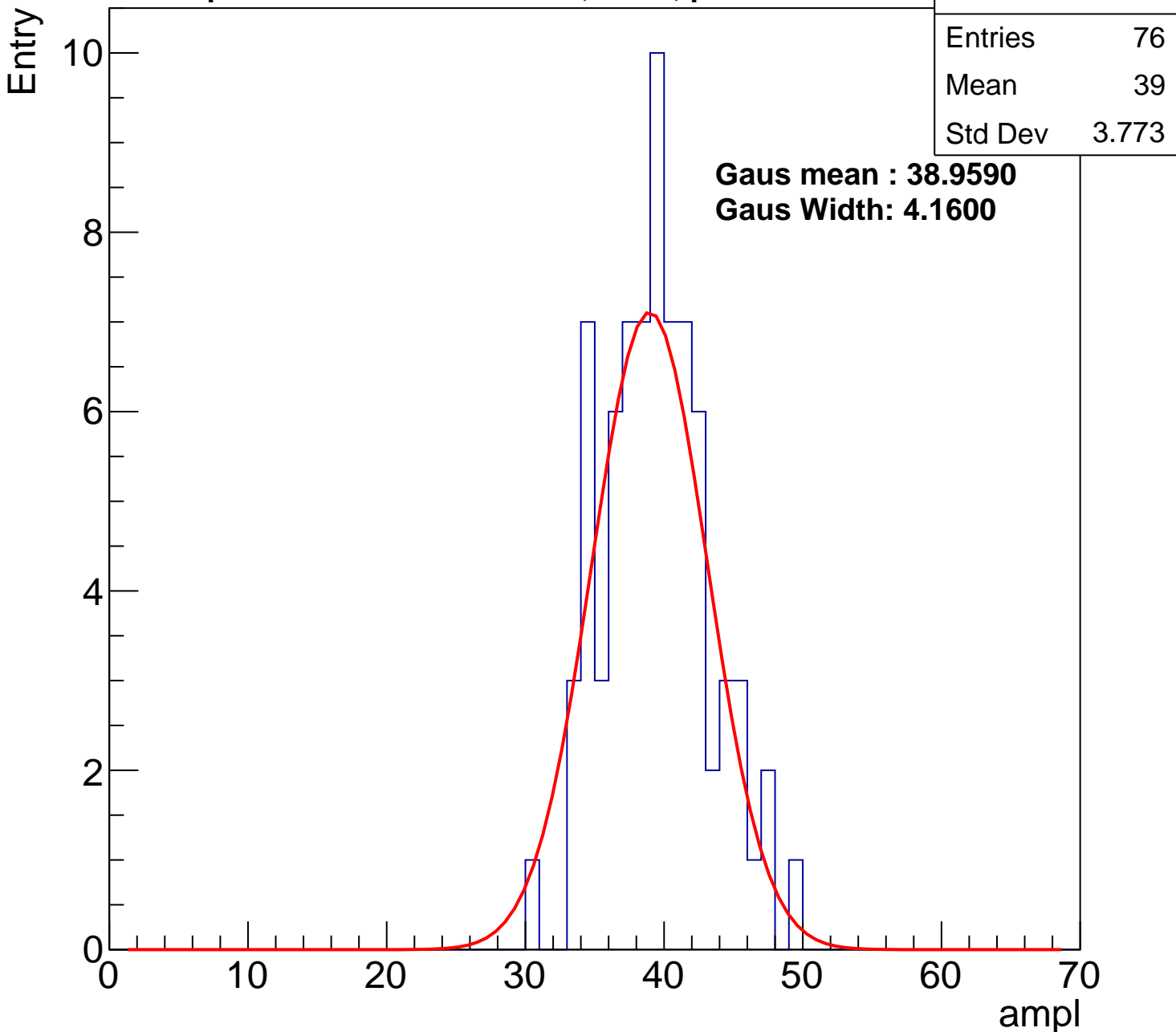
Gaus Width: 4.1600

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch82, adc2

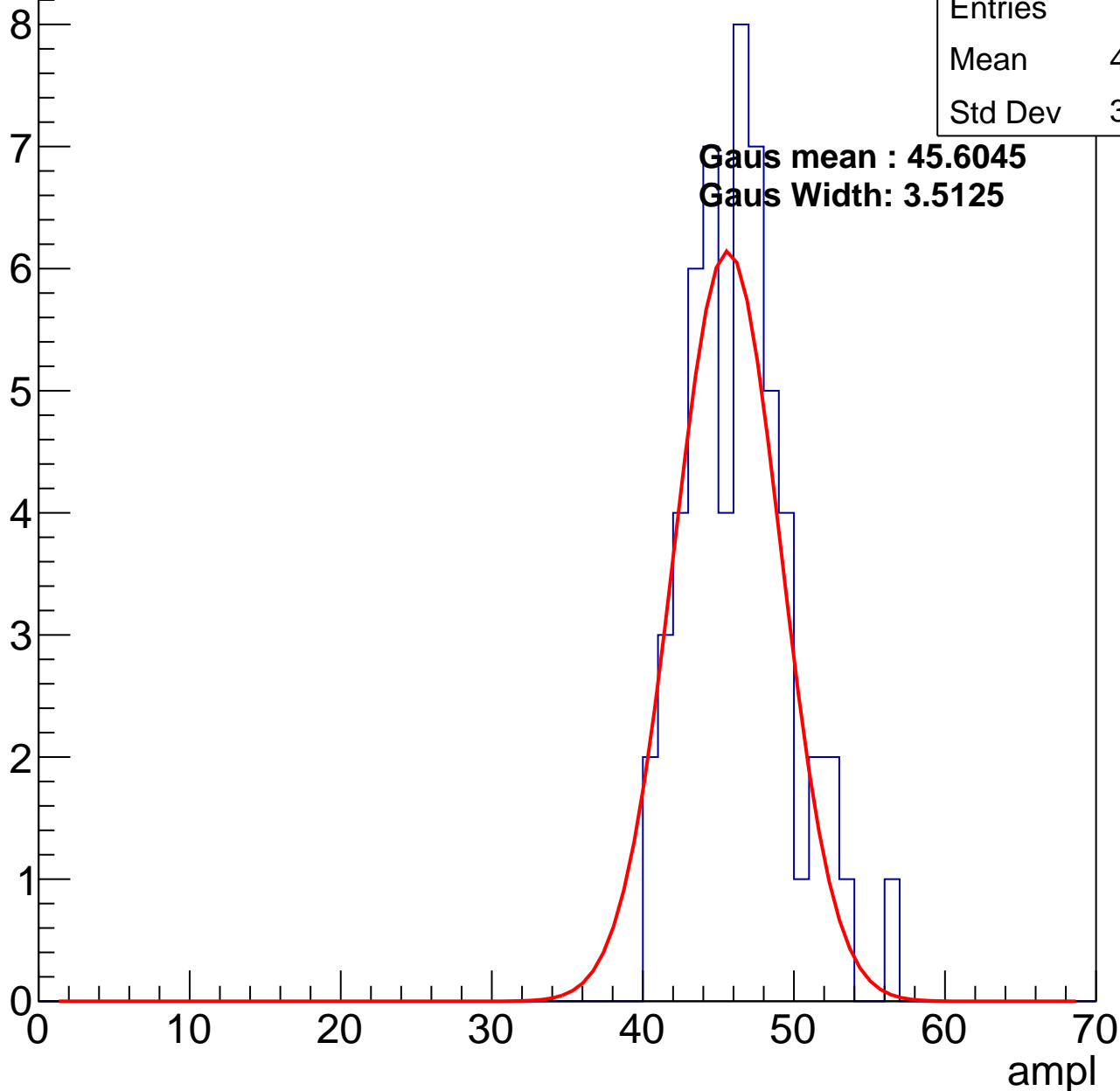
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.88
Std Dev	3.367

Gaus mean : 45.6045

Gaus Width: 3.5125

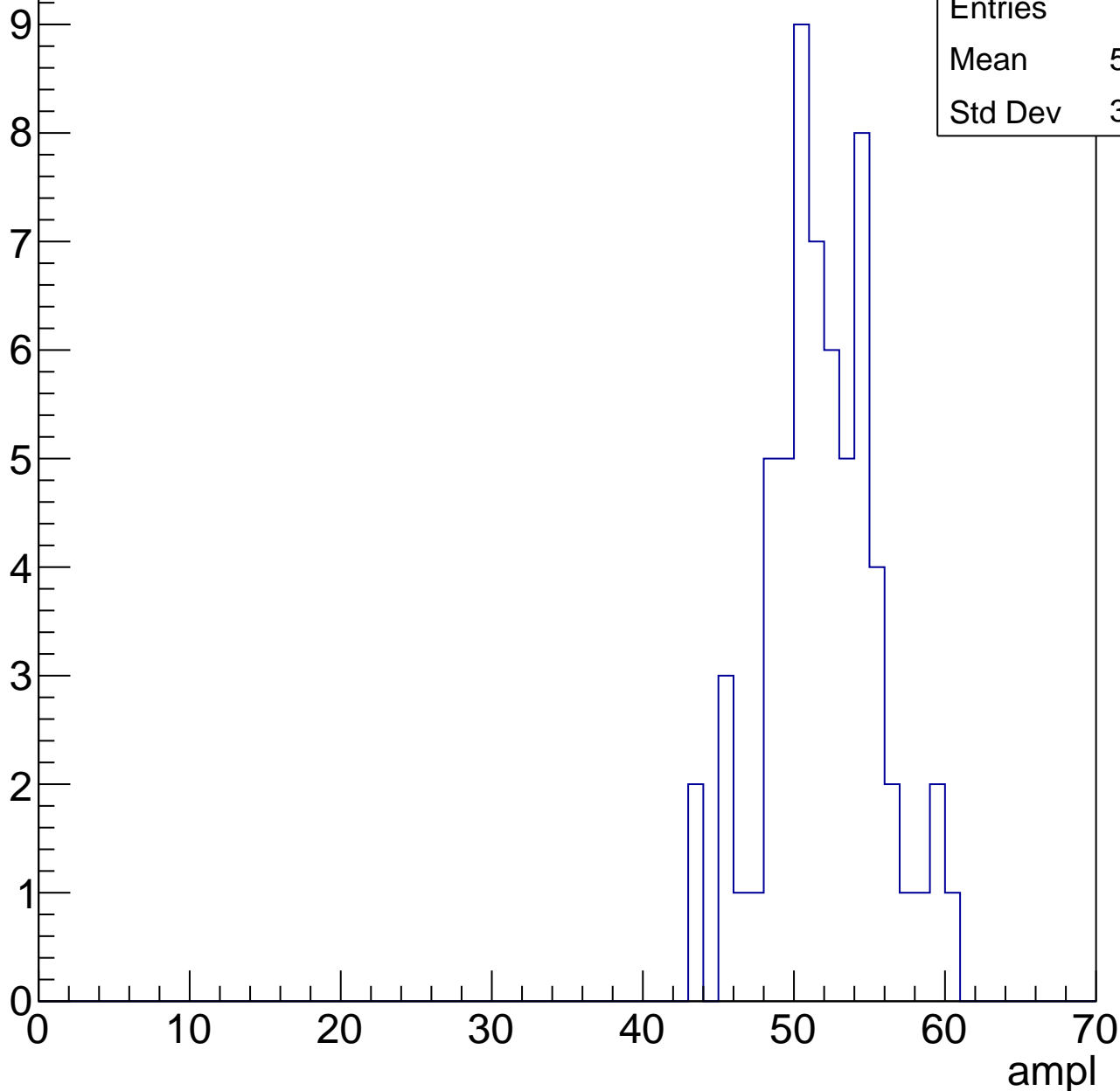


B0L001S, U11-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

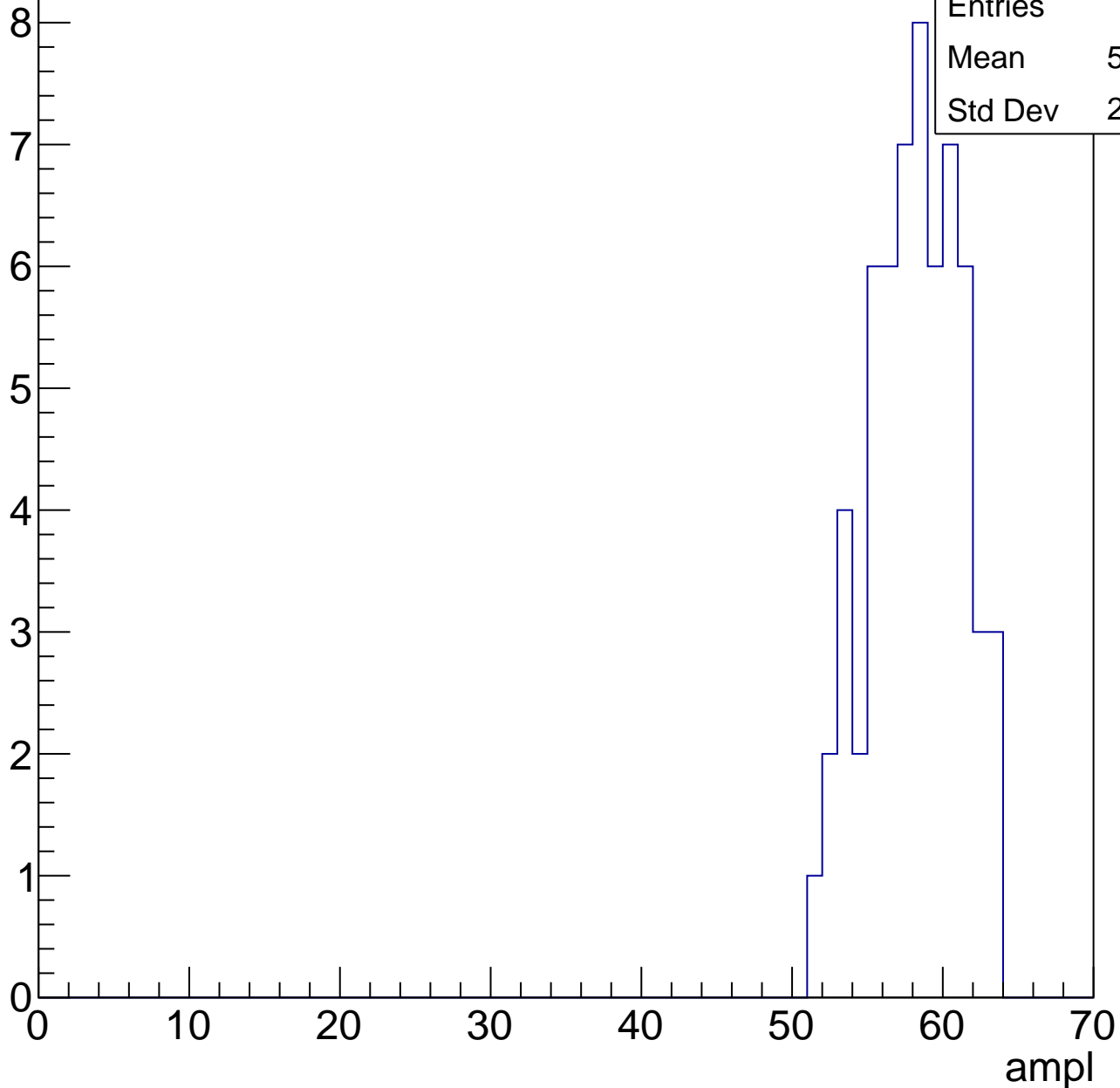
Entries	63
Mean	51.43
Std Dev	3.663



B0L001S, U11-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



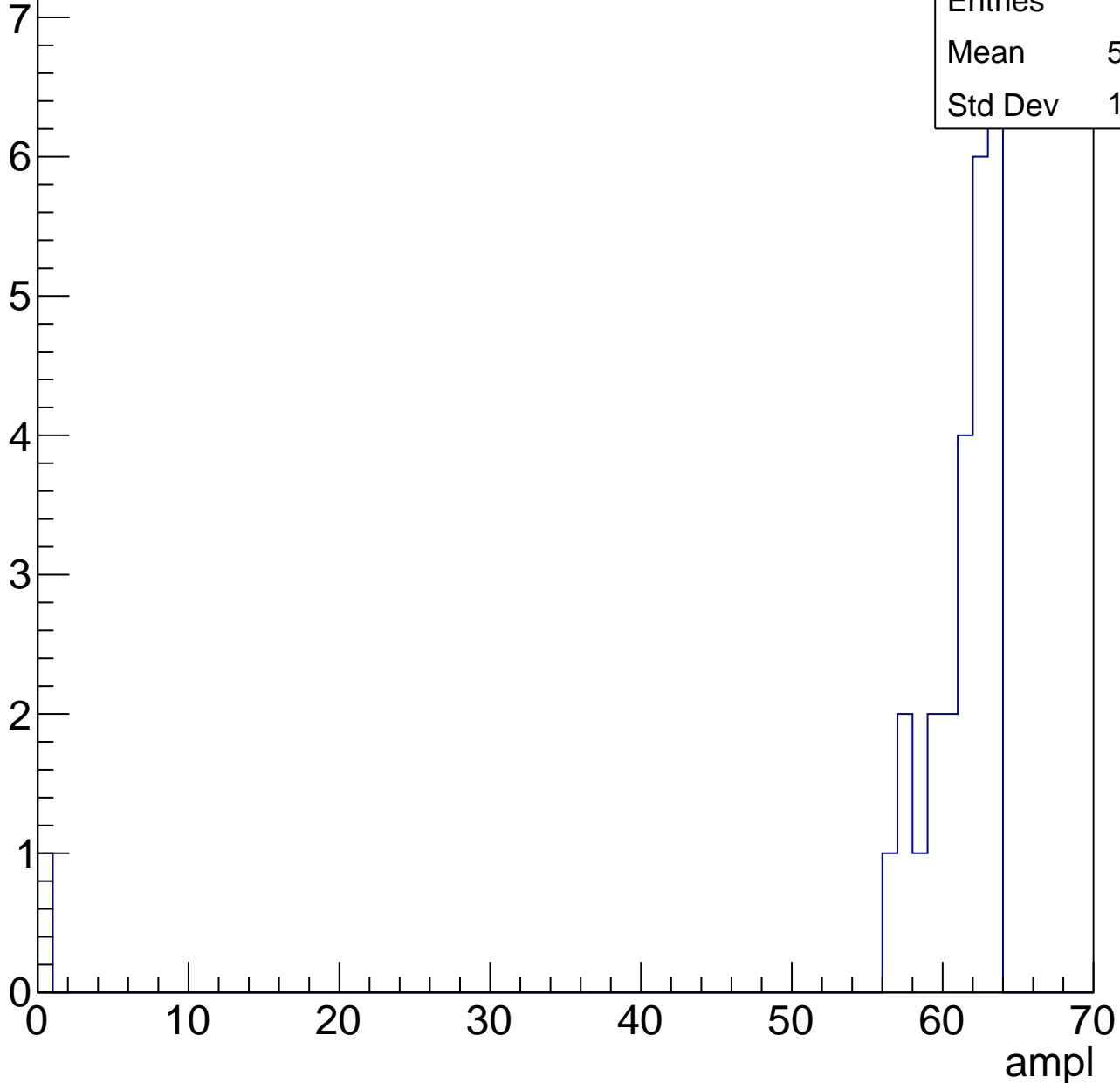
Entries	61
Mean	57.69
Std Dev	2.984

B0L001S, U11-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.58
Std Dev	11.89



B0L001S, U11-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch83, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.5
Std Dev	3.397

Gaus mean : 31.2410

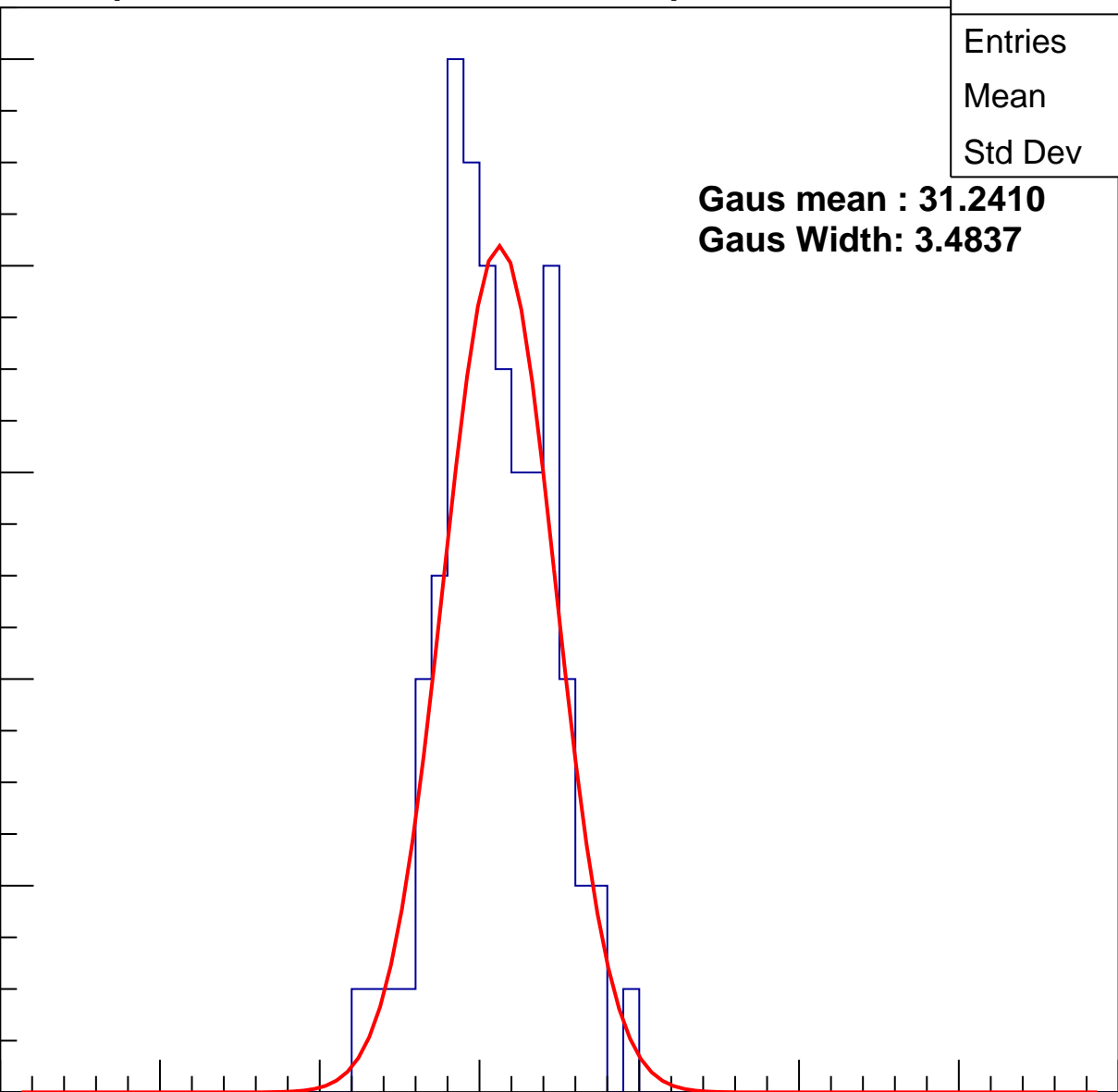
Gaus Width: 3.4837

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch83, adc1

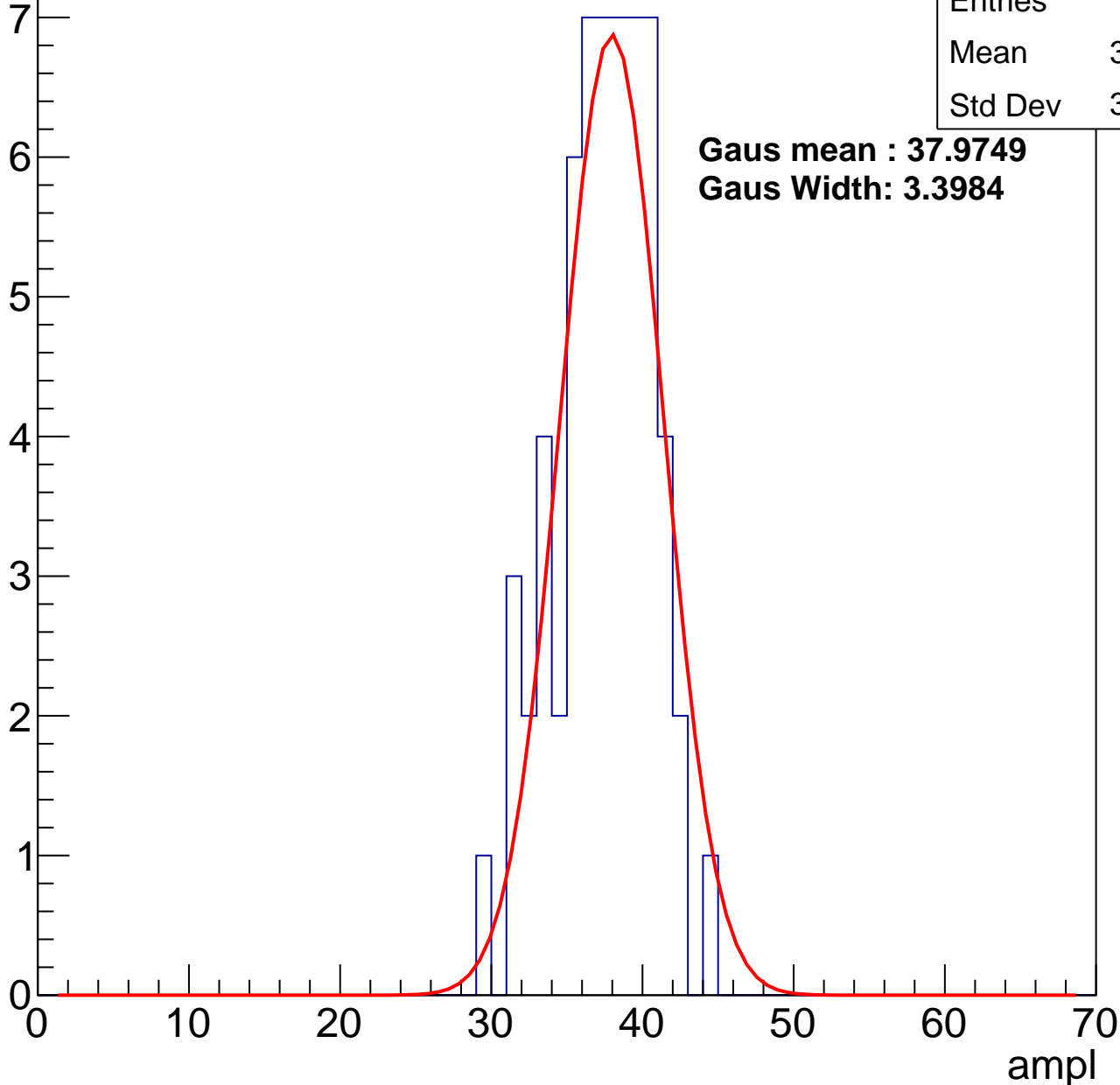
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.97
Std Dev	3.152

Gaus mean : 37.9749

Gaus Width: 3.3984



B0L001S, U11-ch83, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	44.32
Std Dev	3.756

Gaus mean : 44.6151

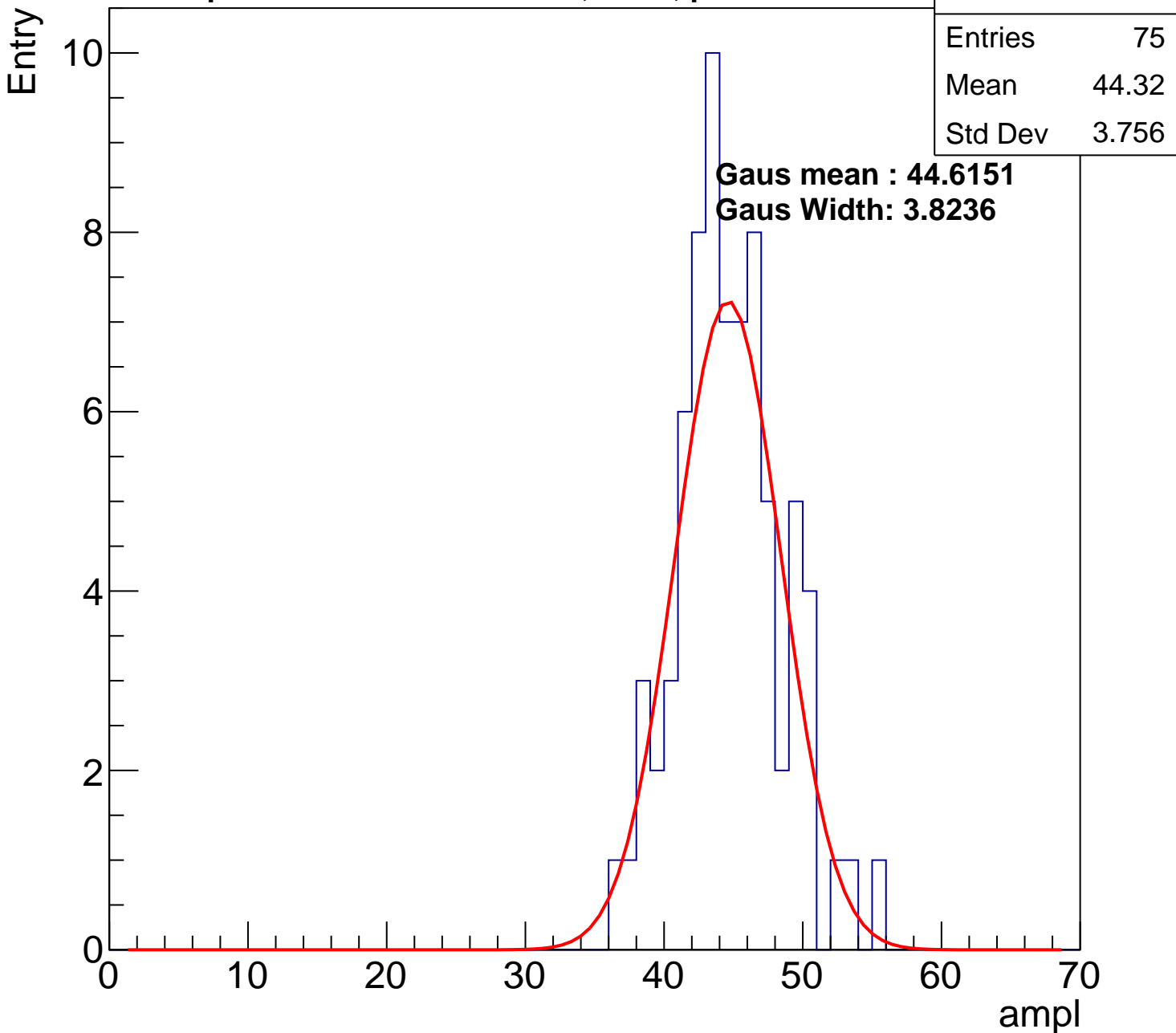
Gaus Width: 3.8236

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

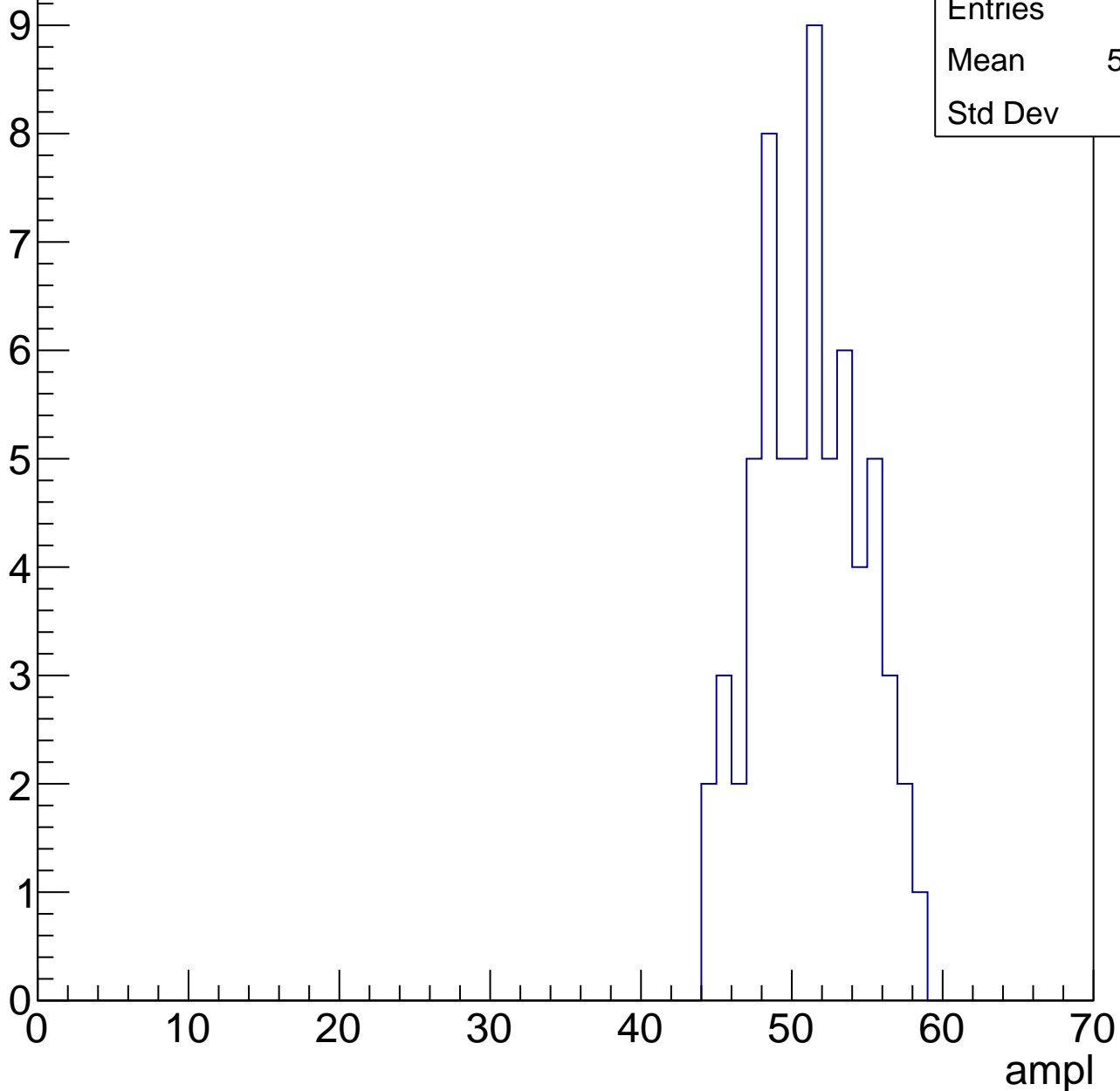


B0L001S, U11-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

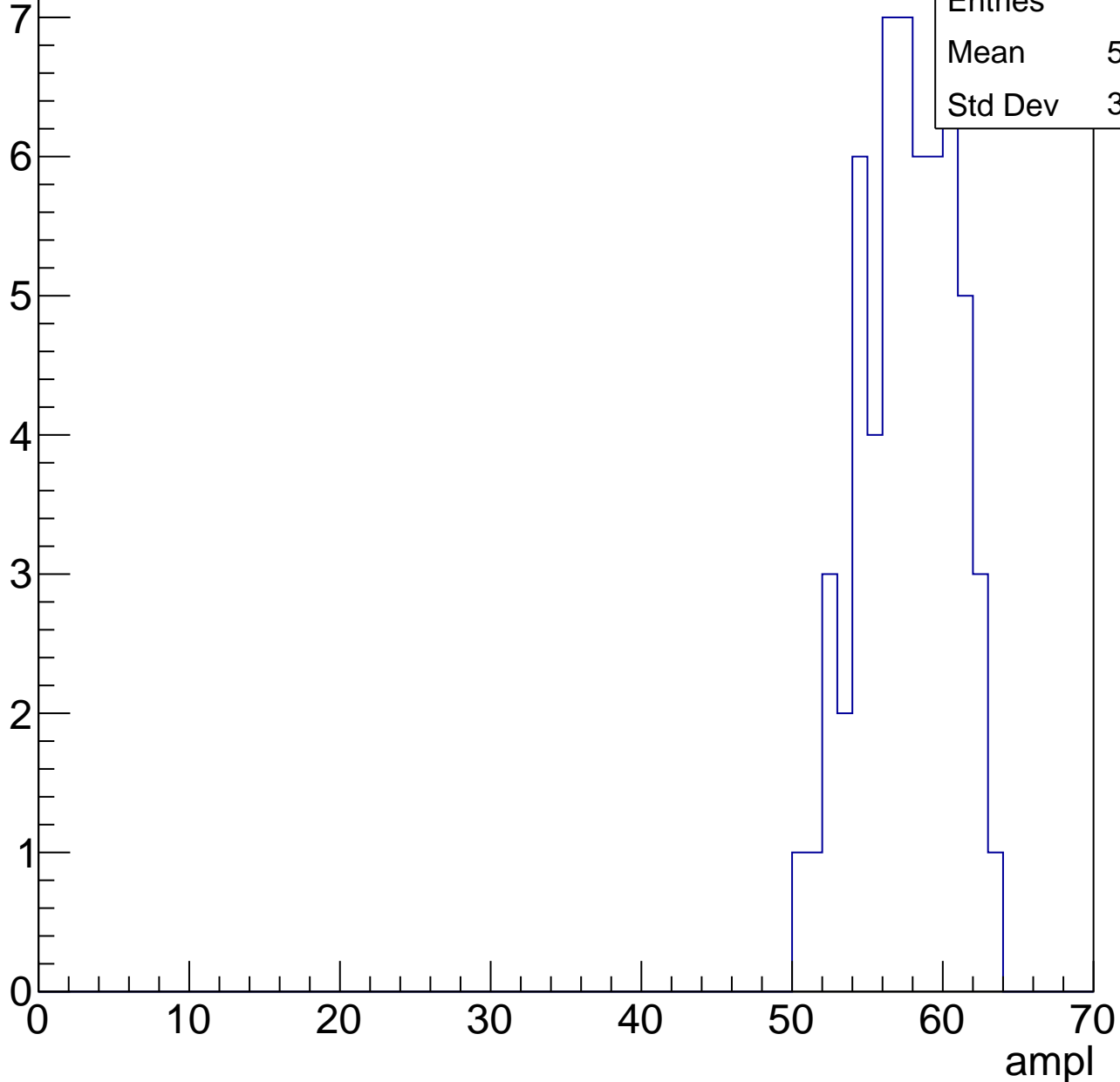
Entries	65
Mean	50.72
Std Dev	3.44



B0L001S, U11-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

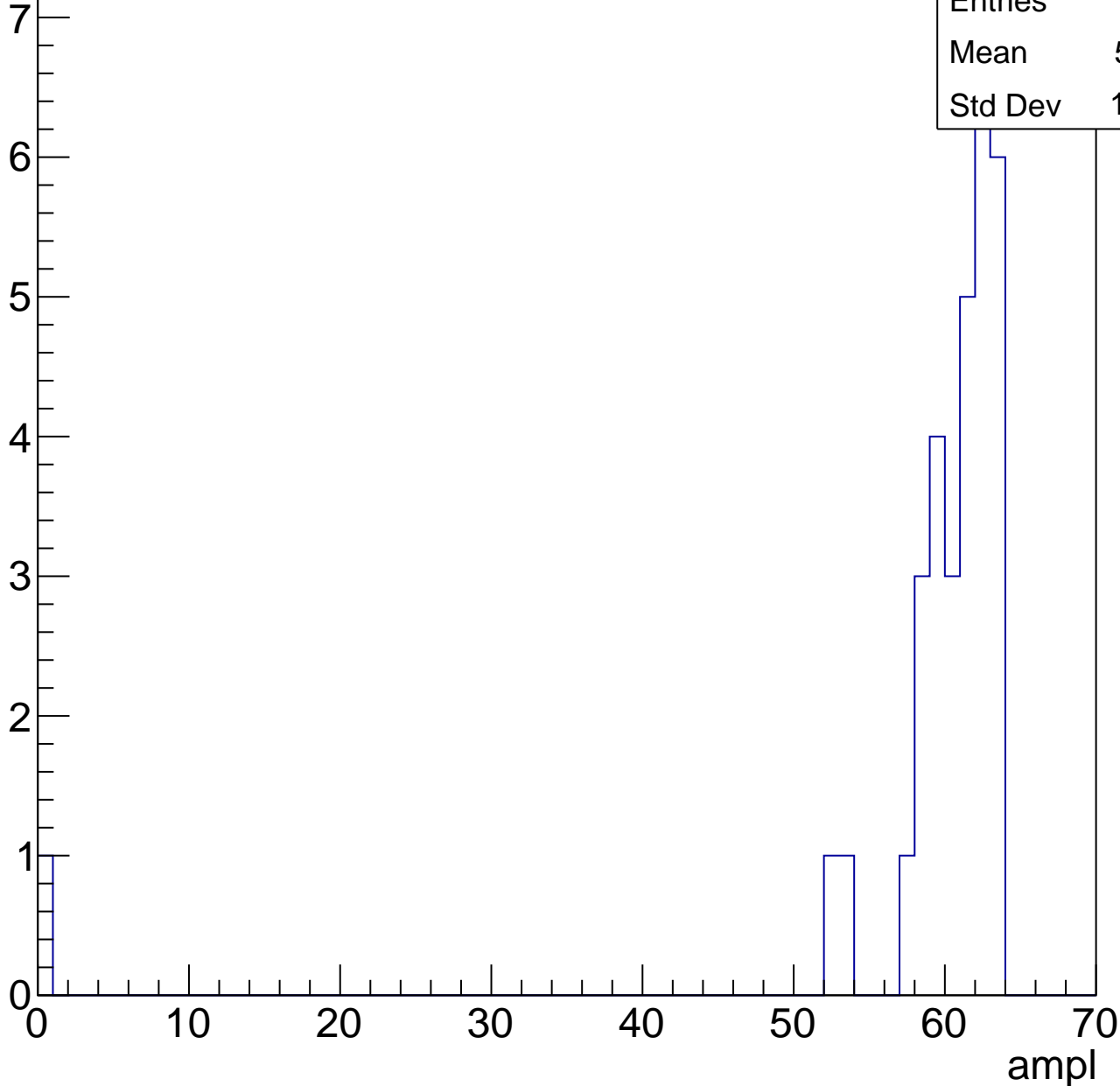


B0L001S, U11-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.41
Std Dev	10.82



B0L001S, U11-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch84, adc0

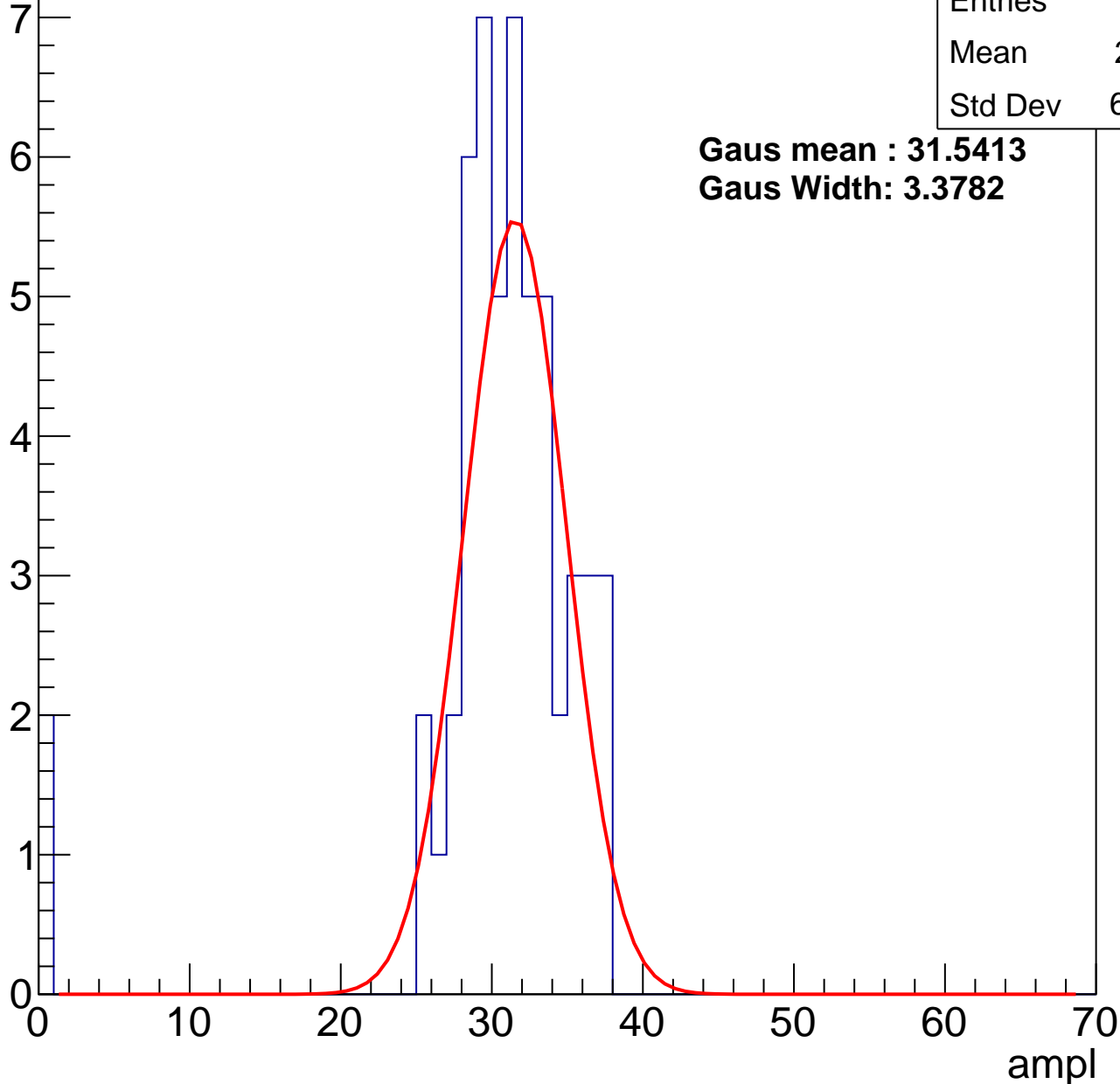
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	29.91
Std Dev	6.657

Gaus mean : 31.5413

Gaus Width: 3.3782



B0L001S, U11-ch84, adc1

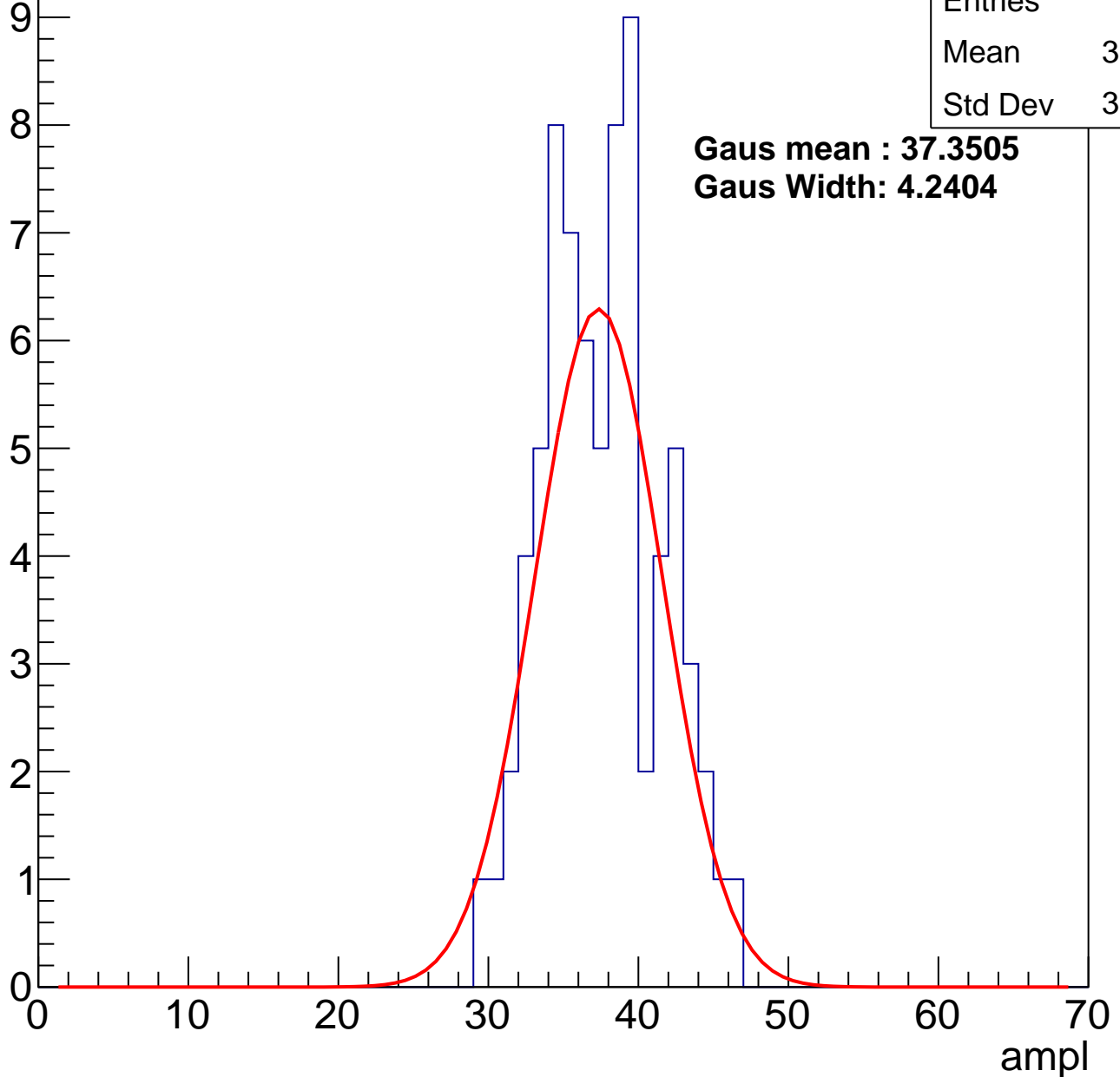
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	37.15
Std Dev	3.805

Gaus mean : 37.3505

Gaus Width: 4.2404



B0L001S, U11-ch84, adc2

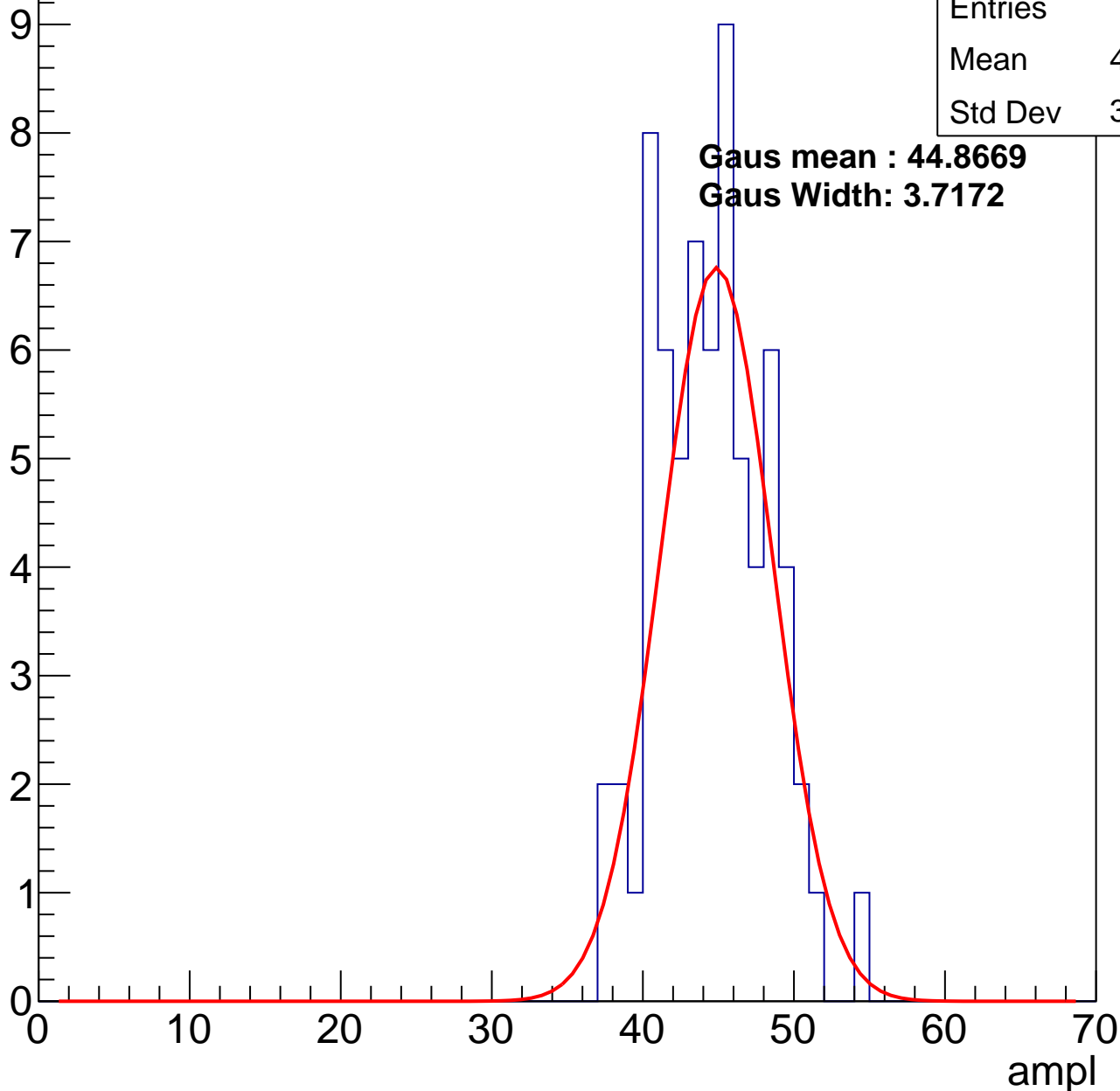
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.09
Std Dev	3.582

Gaus mean : 44.8669

Gaus Width: 3.7172

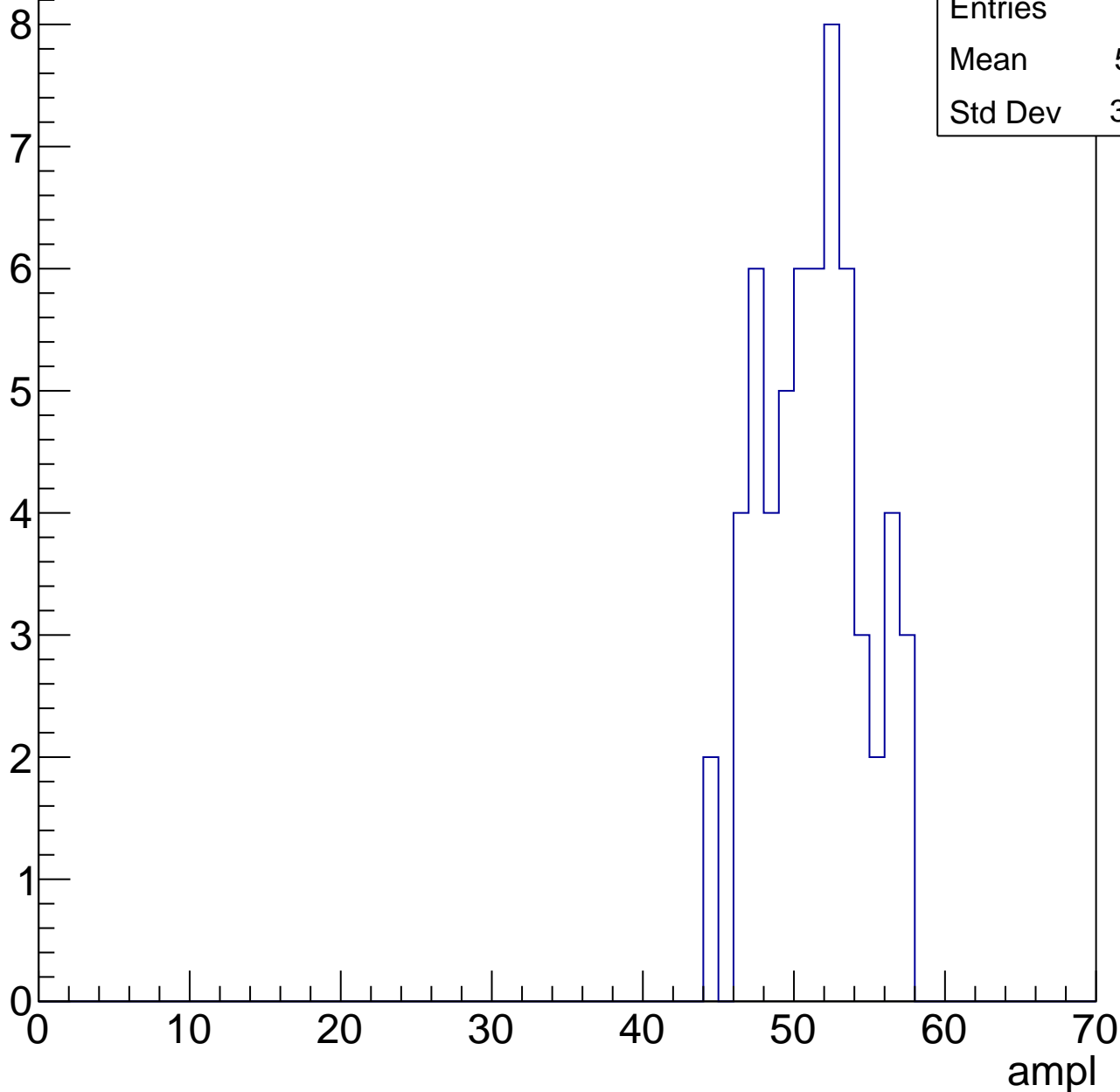


B0L001S, U11-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

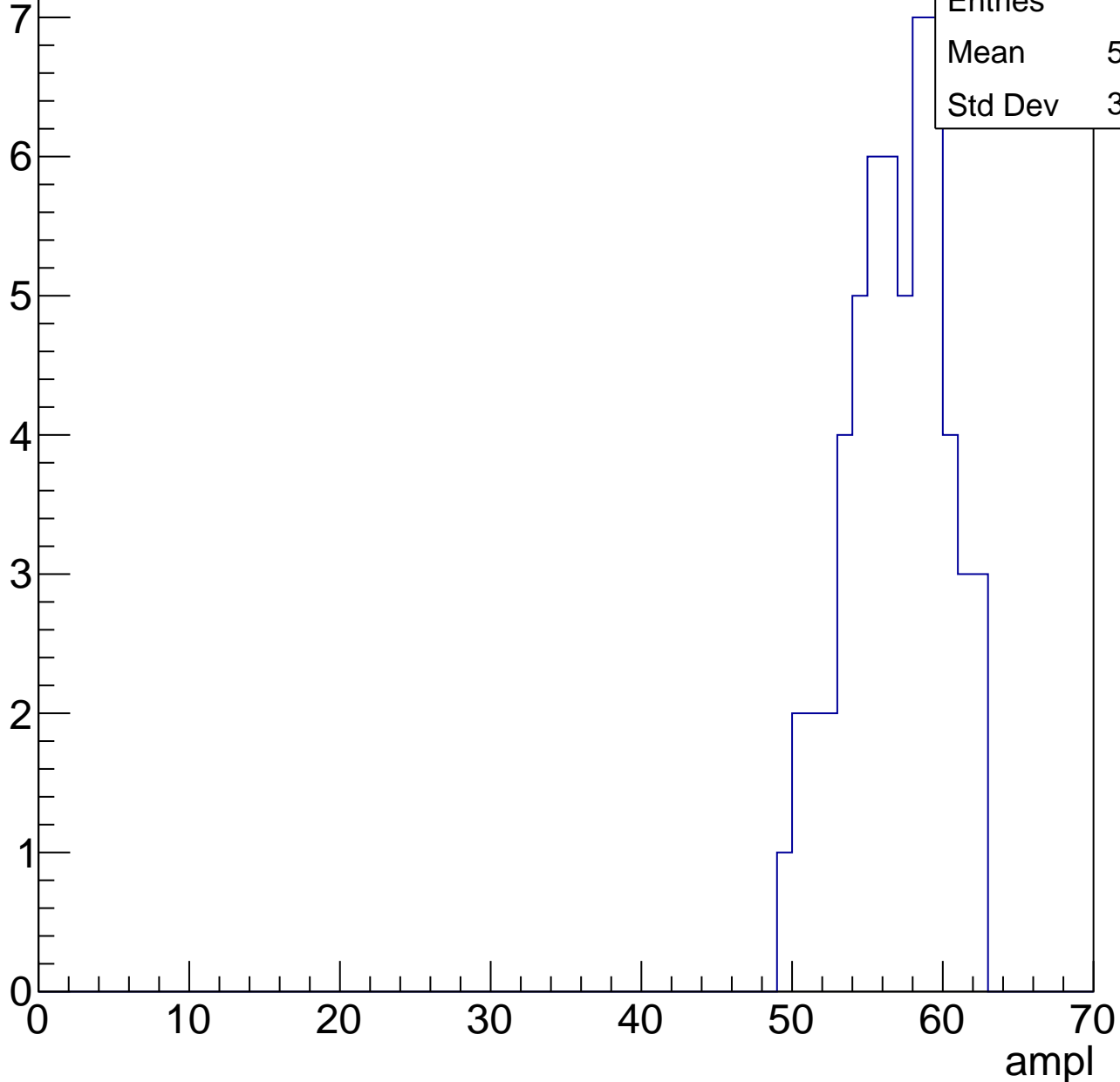
Entries	59
Mean	50.81
Std Dev	3.327



B0L001S, U11-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



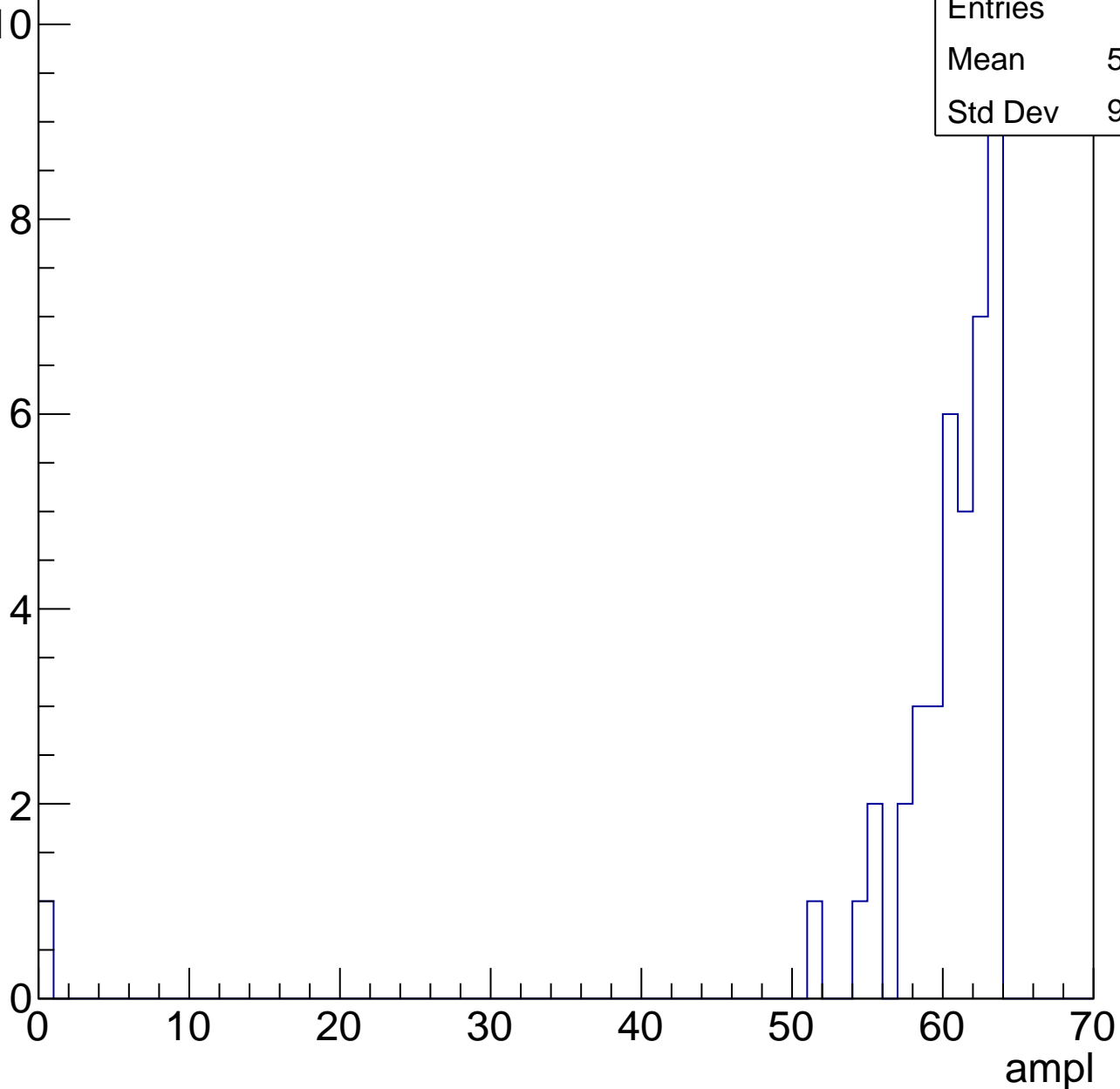
Entries	57
Mean	56.42
Std Dev	3.239

B0L001S, U11-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

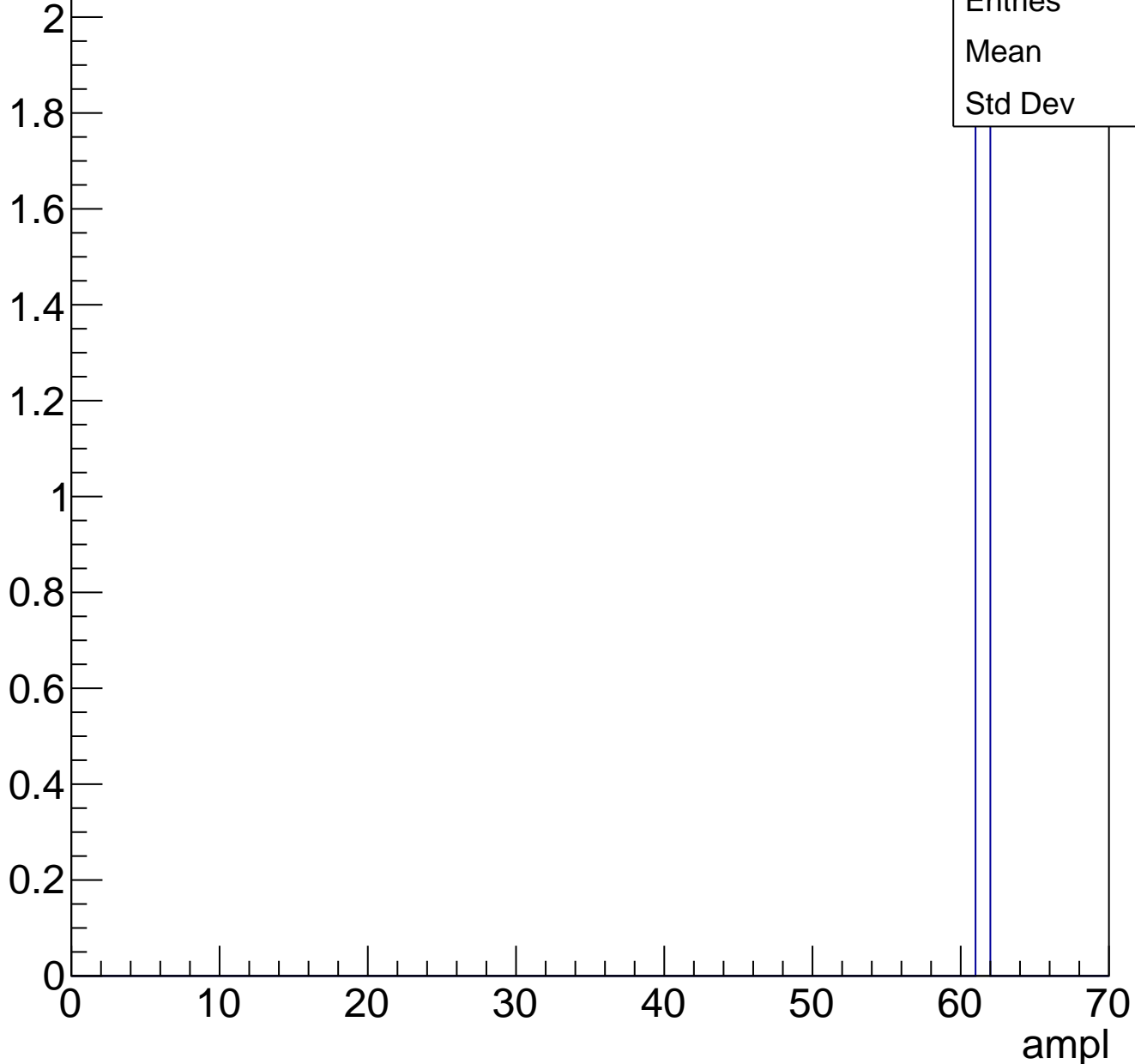
Entries	41
Mean	58.76
Std Dev	9.702



B0L001S, U11-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch85, adc0

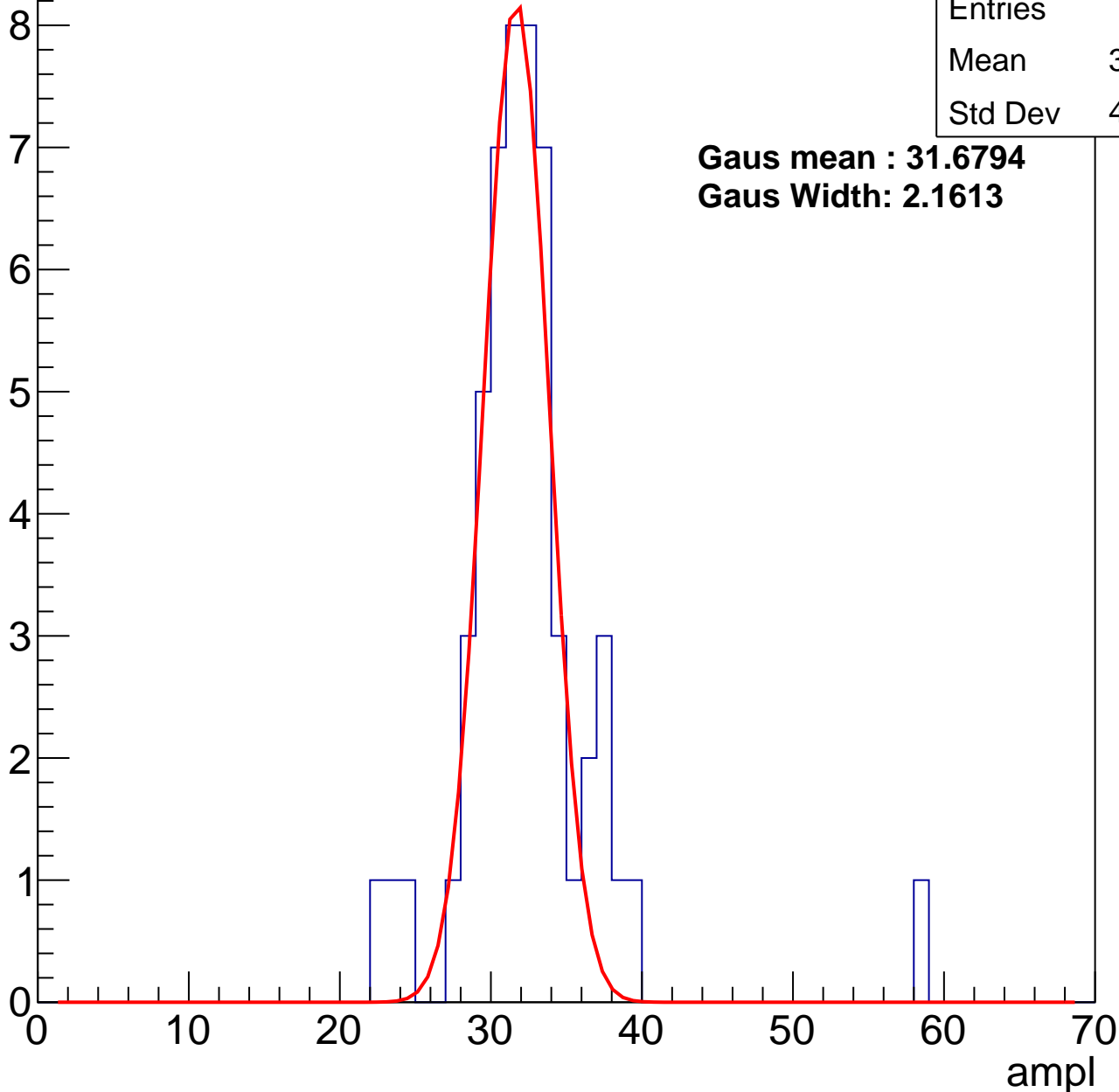
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	31.94
Std Dev	4.897

Gaus mean : 31.6794

Gaus Width: 2.1613



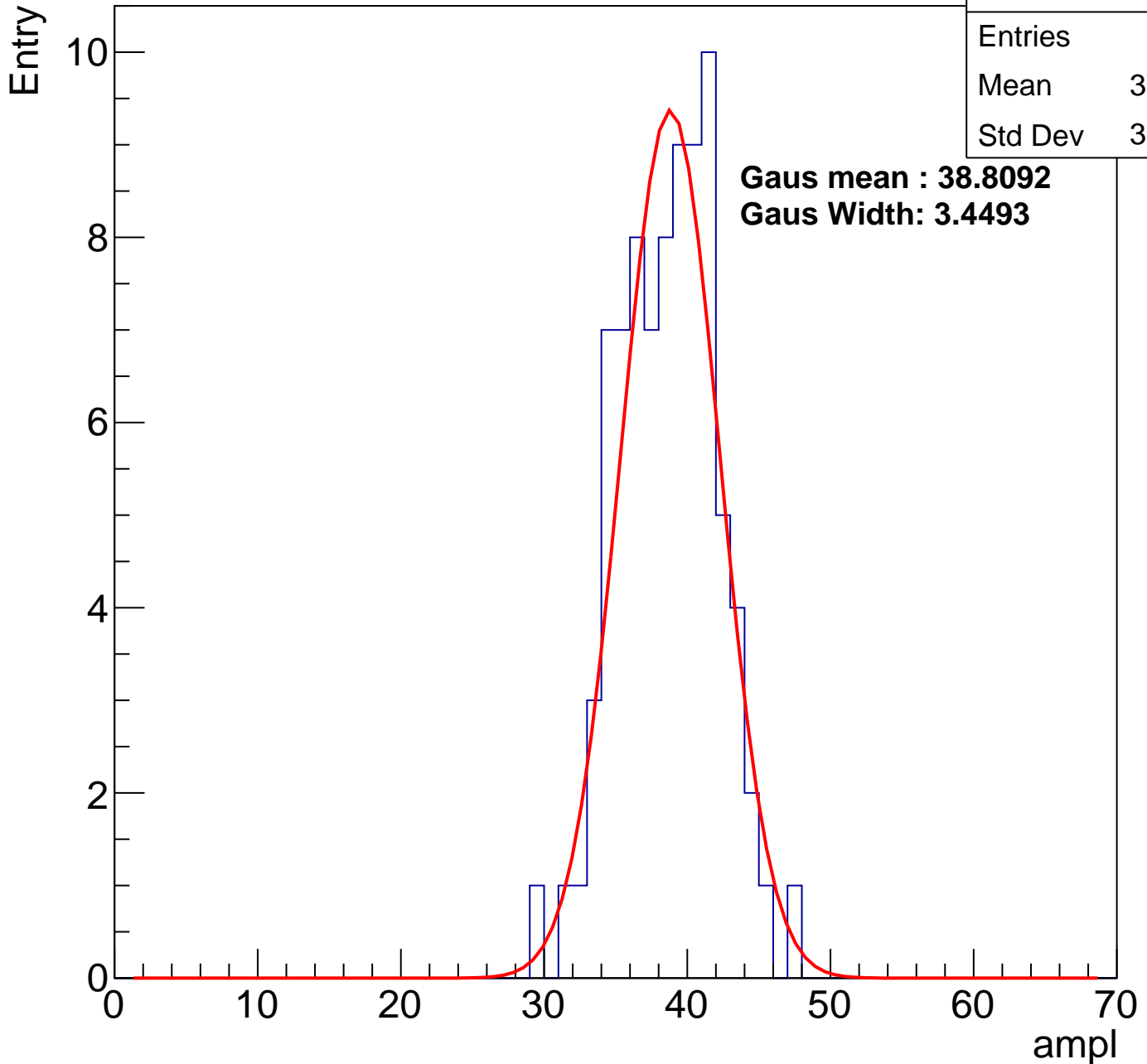
B0L001S, U11-ch85, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	38.19
Std Dev	3.396

Gaus mean : 38.8092

Gaus Width: 3.4493



B0L001S, U11-ch85, adc2

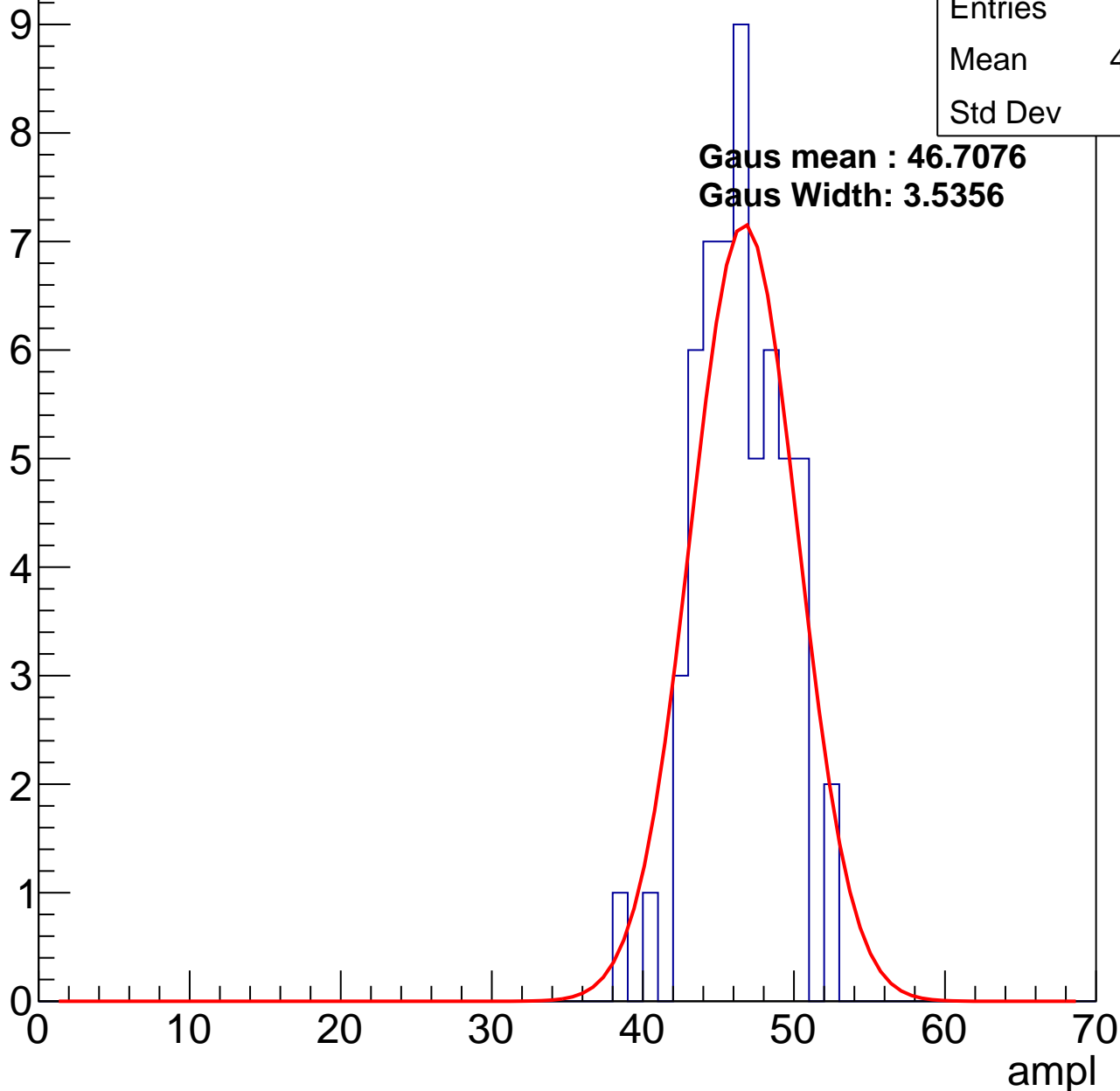
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.98
Std Dev	2.85

Gaus mean : 46.7076

Gaus Width: 3.5356



B0L001S, U11-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	51.79
Std Dev	3.969

Entry

10

8

6

4

2

0

0

10

20

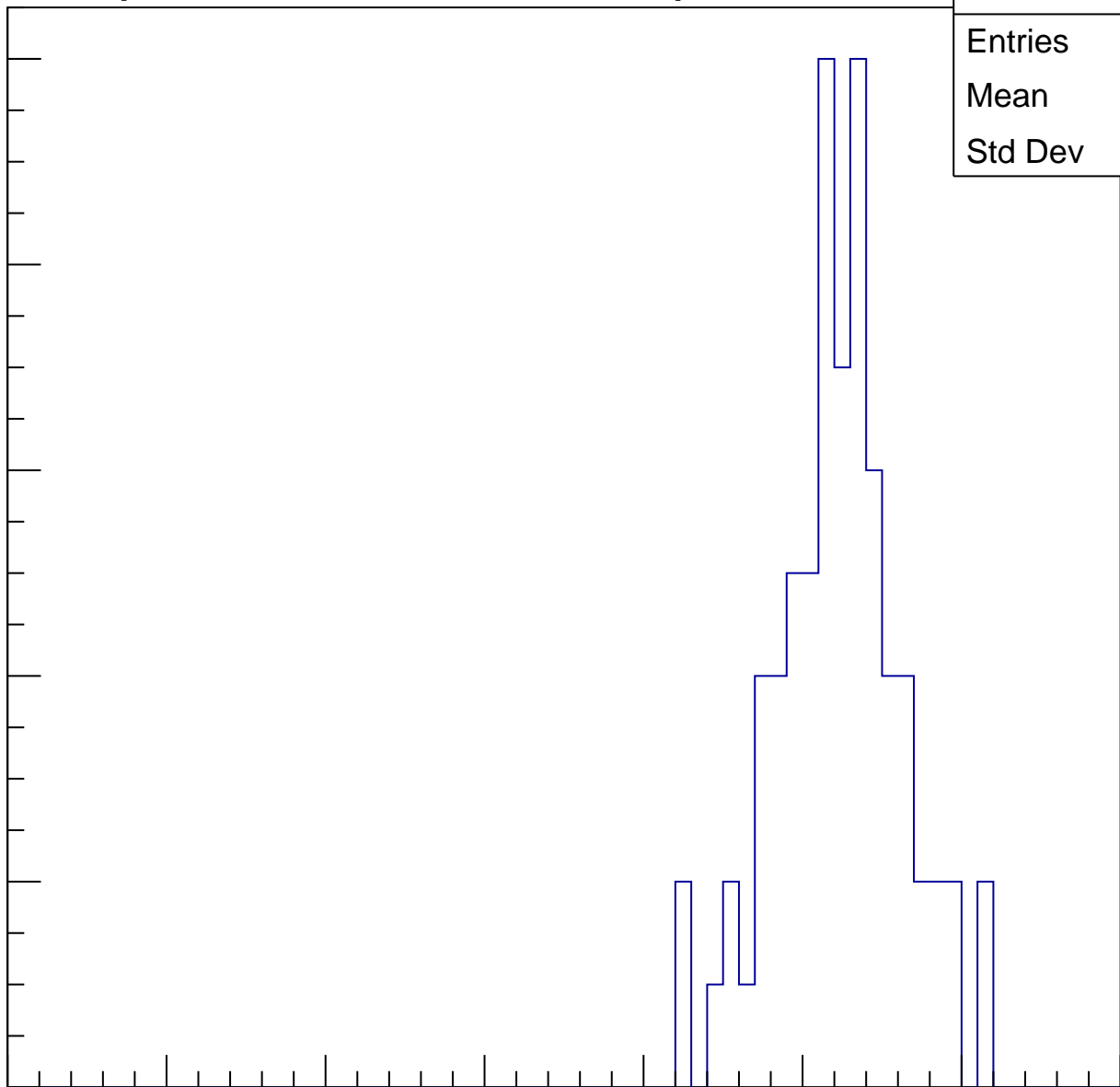
30

40

50

60

ampl

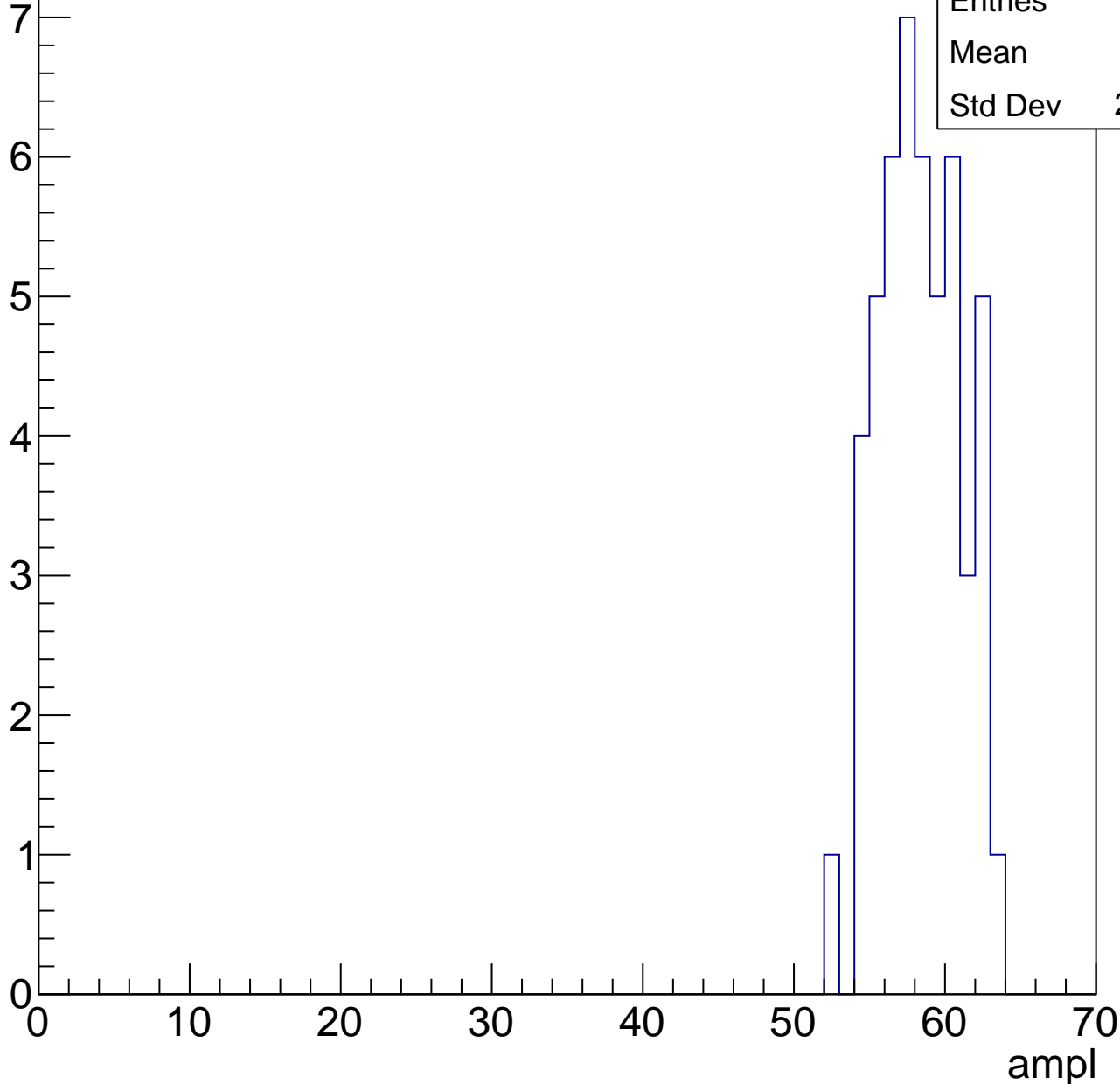


B0L001S, U11-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	57.9
Std Dev	2.621

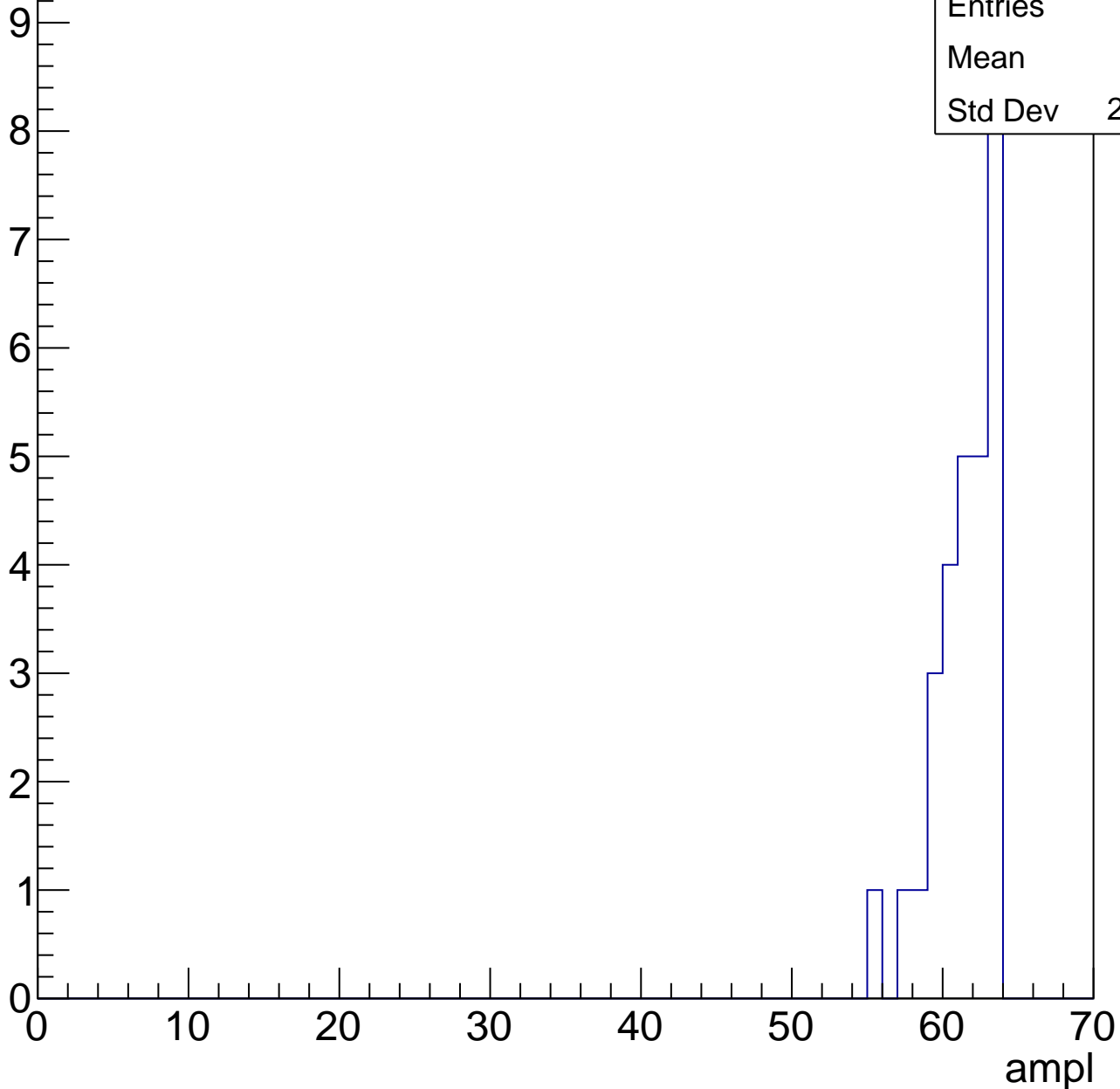


B0L001S, U11-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	61
Std Dev	2.017



B0L001S, U11-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch86, adc0

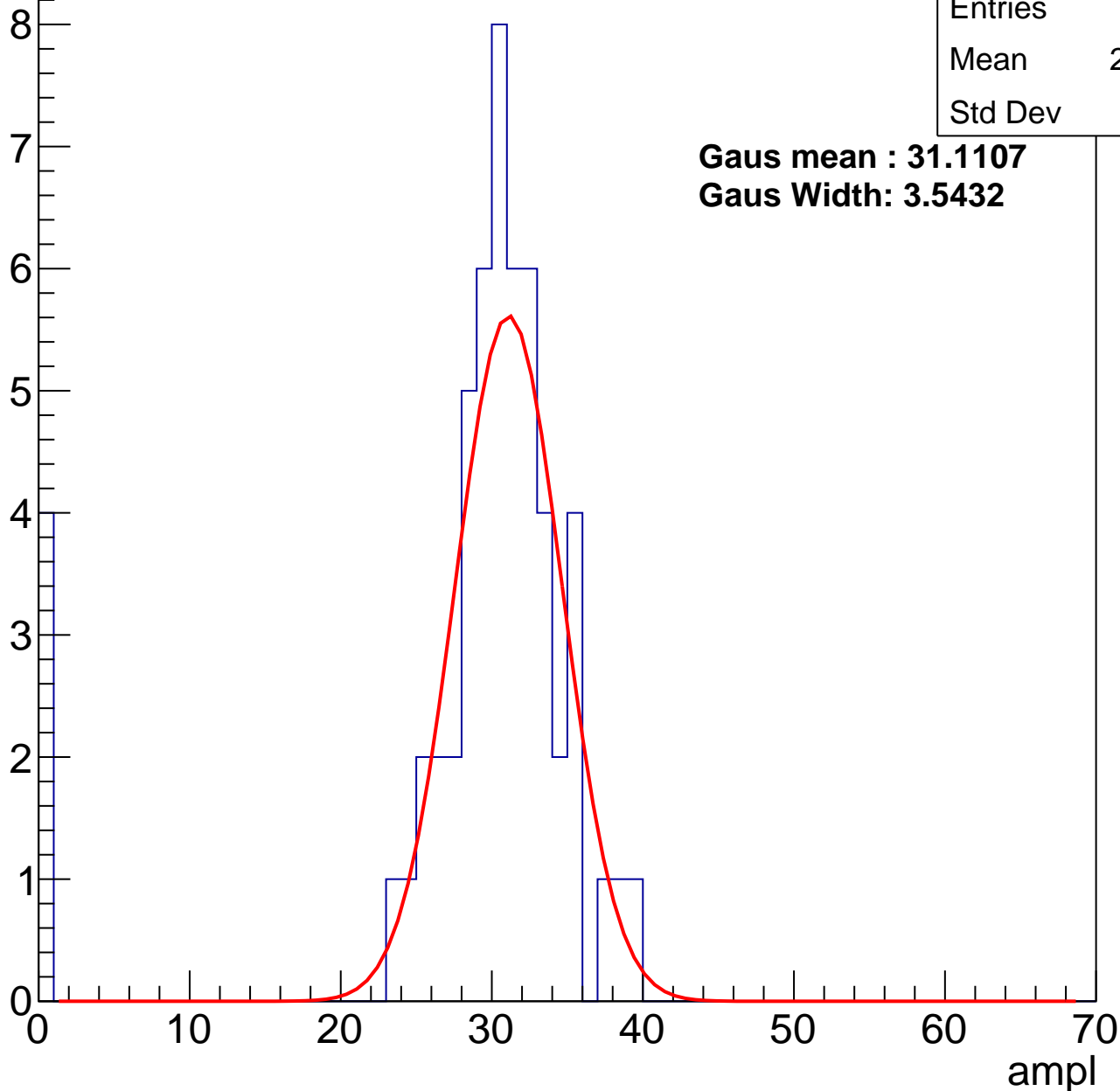
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	28.38
Std Dev	8.51

Gaus mean : 31.1107

Gaus Width: 3.5432



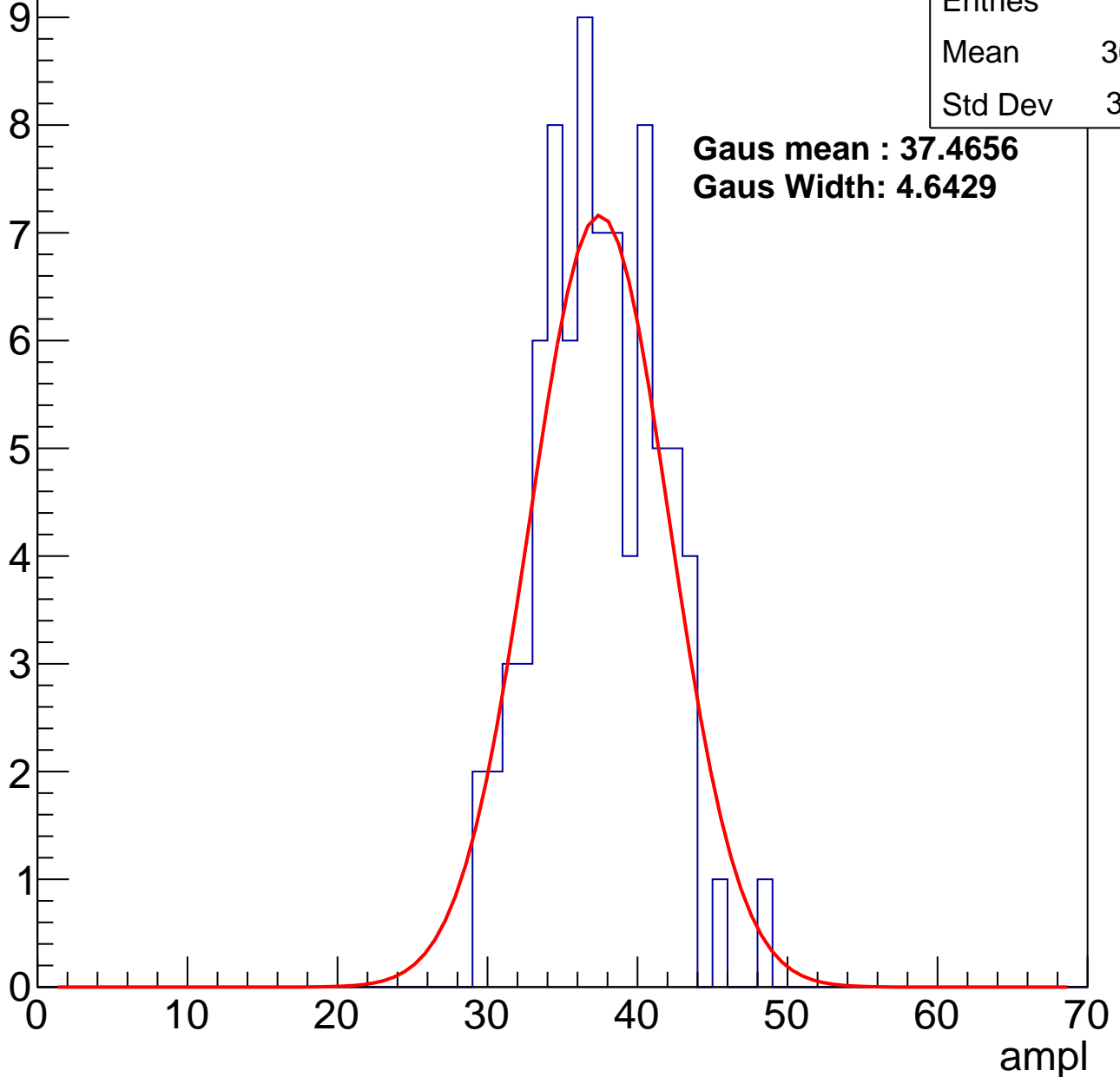
B0L001S, U11-ch86, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	36.94
Std Dev	3.911

Gaus mean : 37.4656
Gaus Width: 4.6429



B0L001S, U11-ch86, adc2

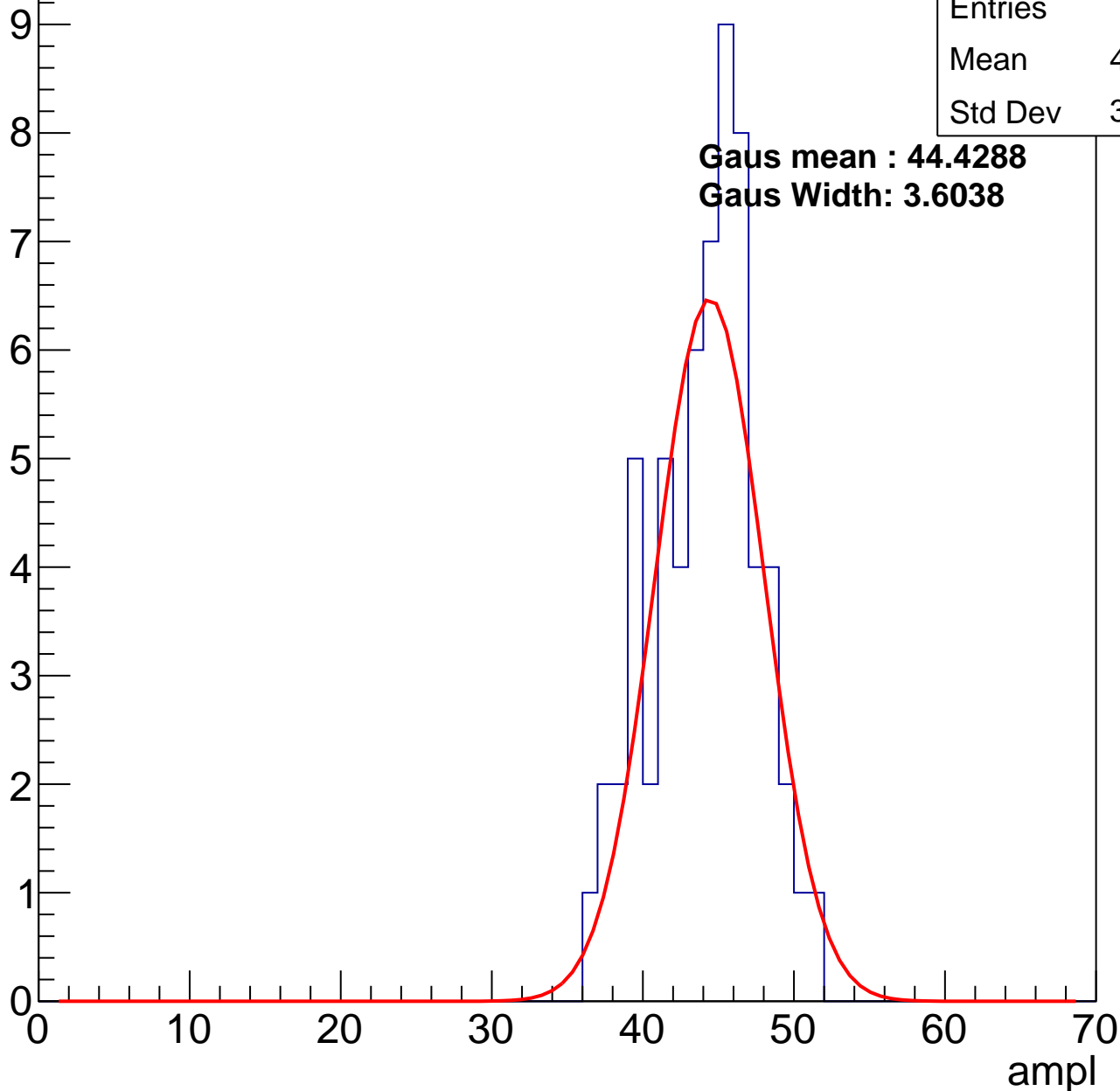
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.68
Std Dev	3.399

Gaus mean : 44.4288

Gaus Width: 3.6038

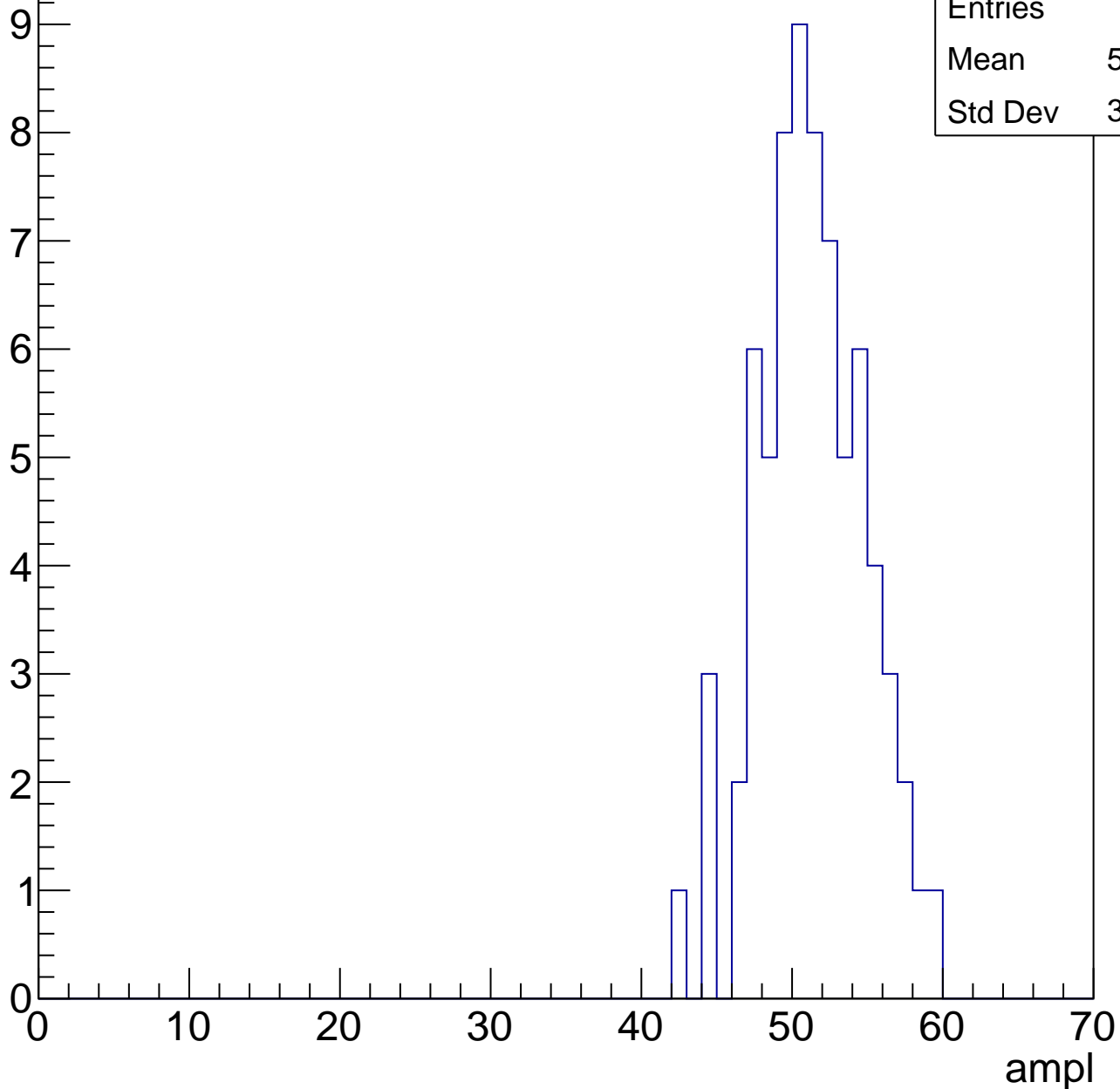


B0L001S, U11-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

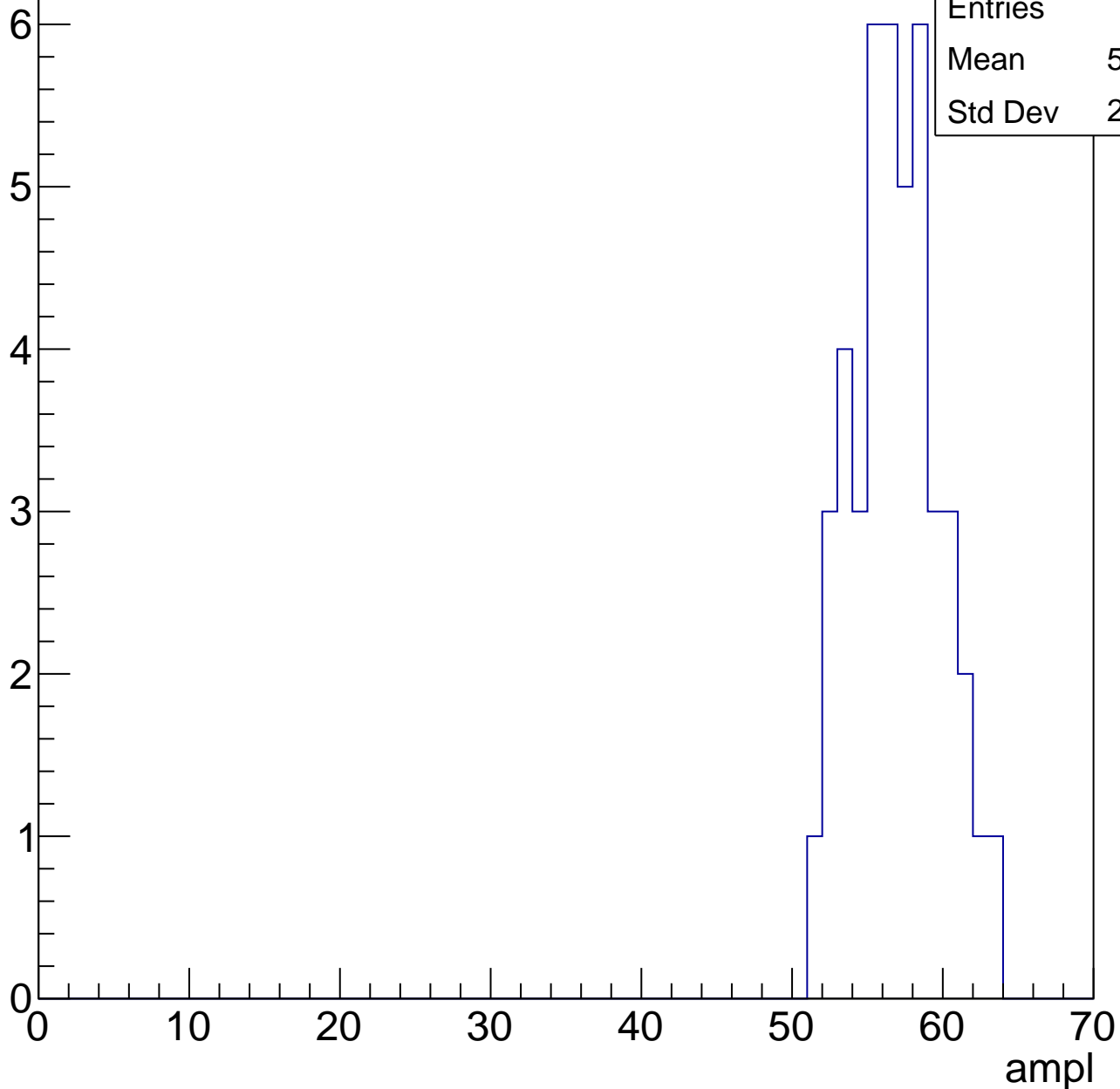
Entries	71
Mean	50.85
Std Dev	3.487



B0L001S, U11-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

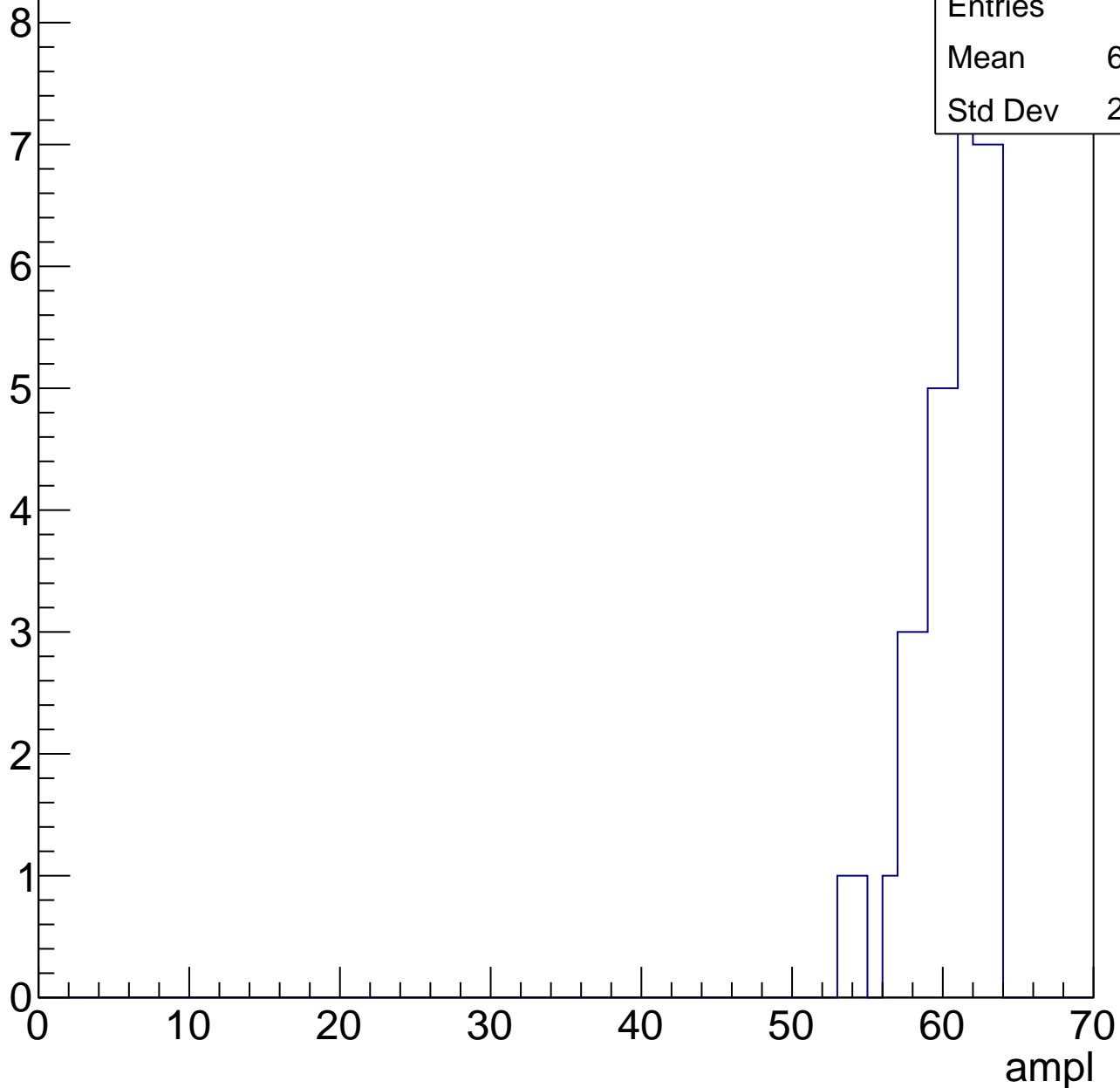


Entries	44
Mean	56.45
Std Dev	2.848

B0L001S, U11-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.62
Std Dev	20.32

ampl

0 10 20 30 40 50 60 70

B0L001S, U11-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch87, adc0

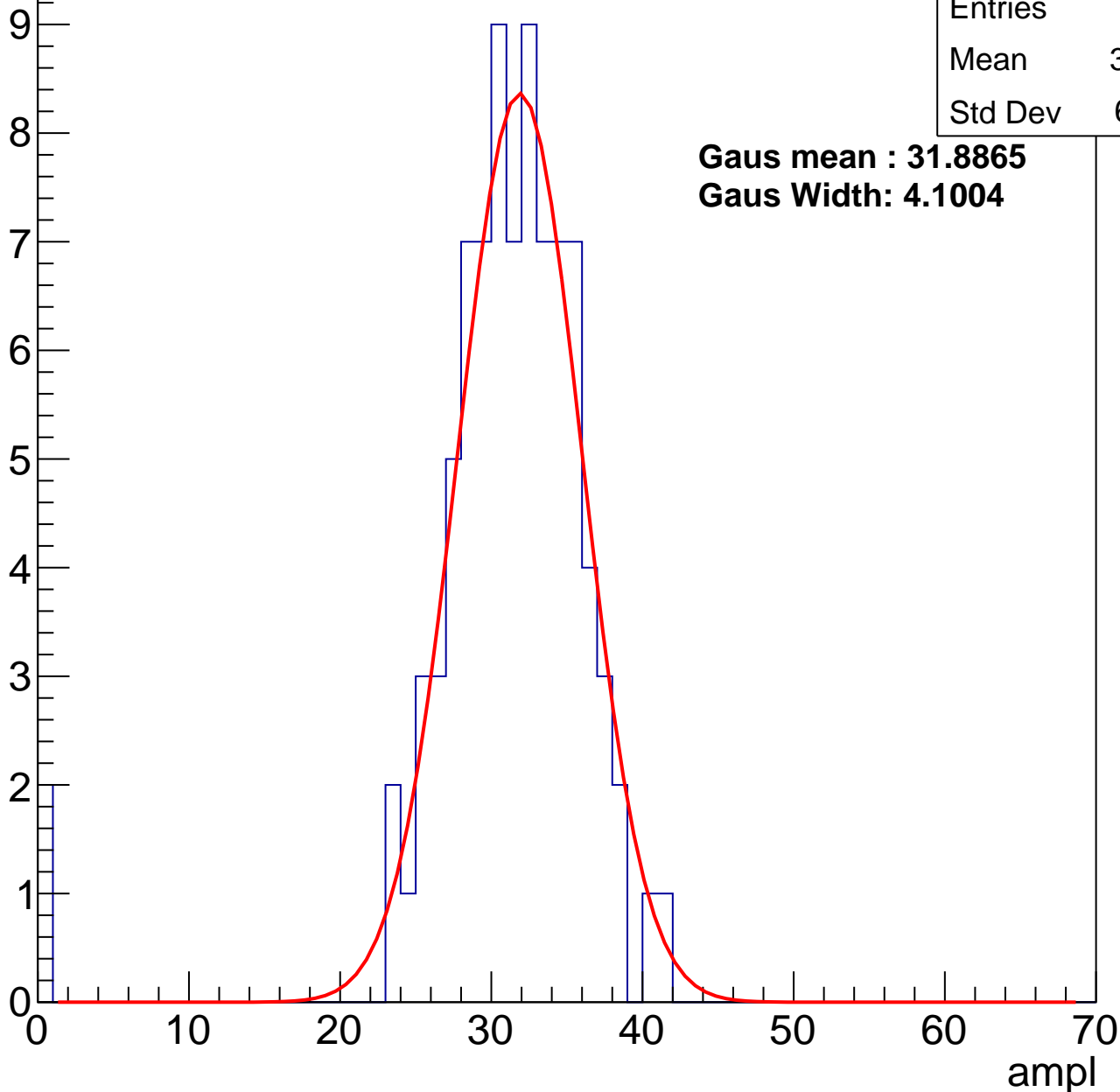
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	30.55
Std Dev	6.011

Gaus mean : 31.8865

Gaus Width: 4.1004



B0L001S, U11-ch87, adc1

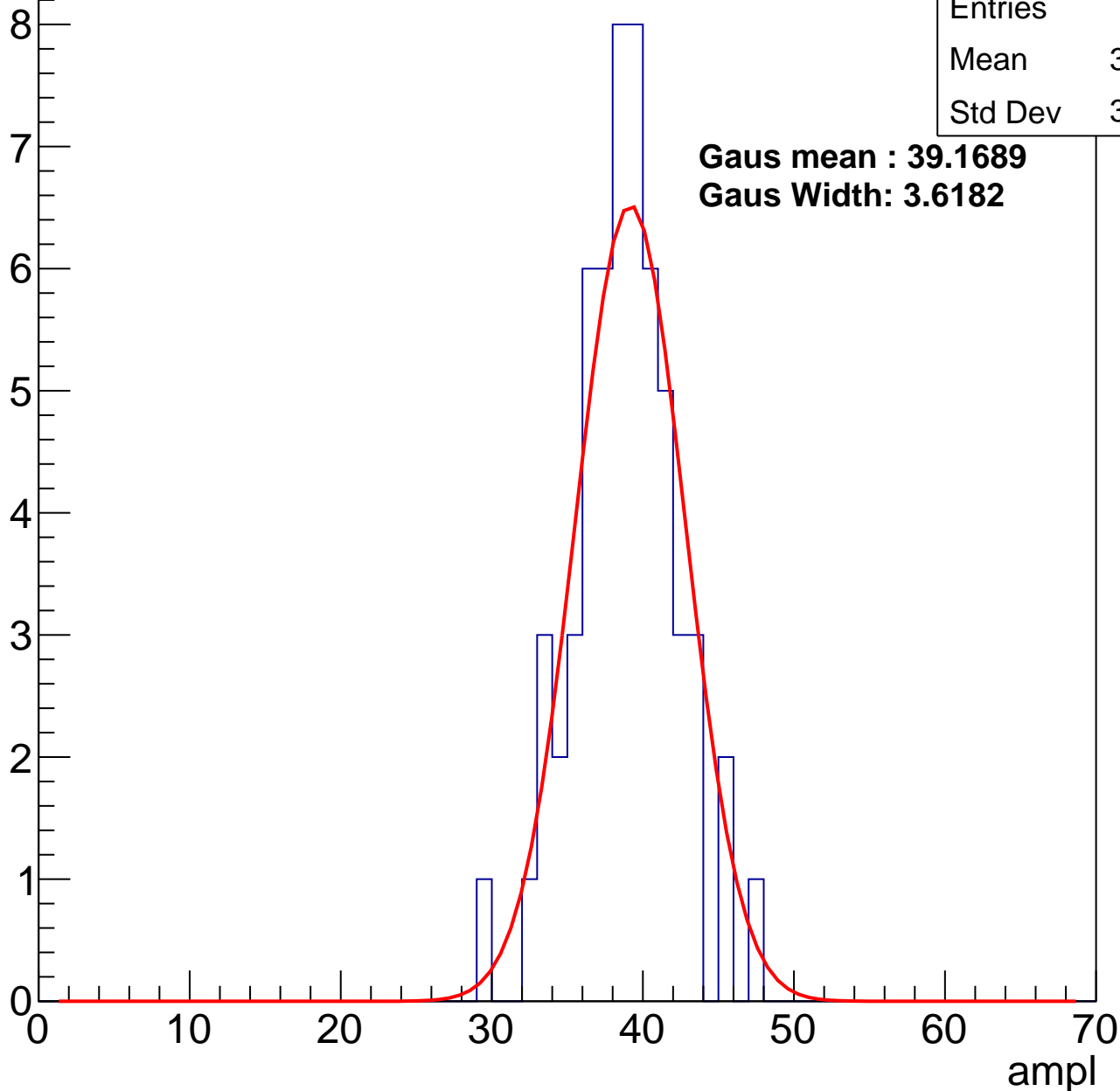
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	38.34
Std Dev	3.356

Gaus mean : 39.1689

Gaus Width: 3.6182



B0L001S, U11-ch87, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	45.56
Std Dev	3.605

Gaus mean : 45.7258

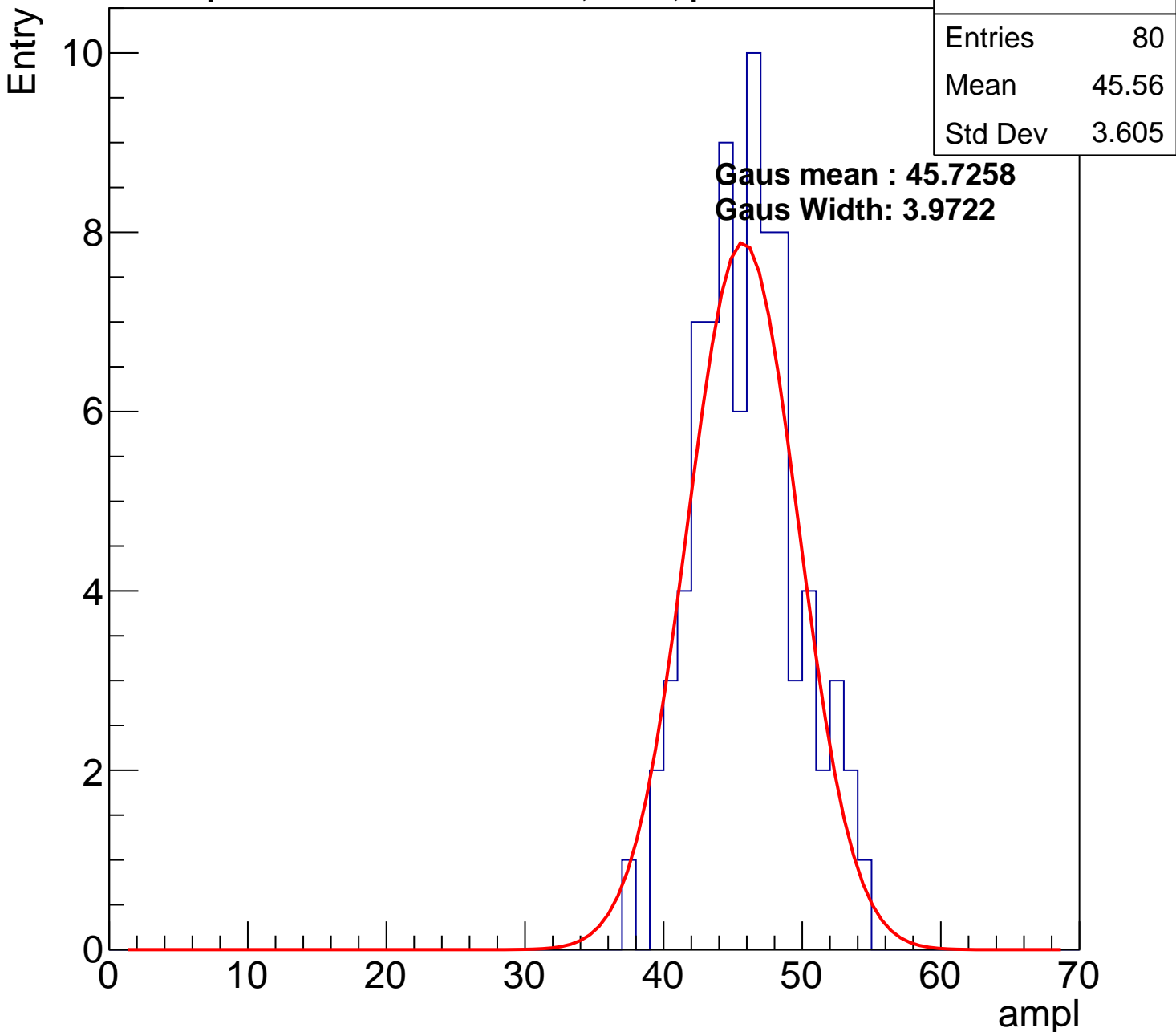
Gaus Width: 3.9722

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

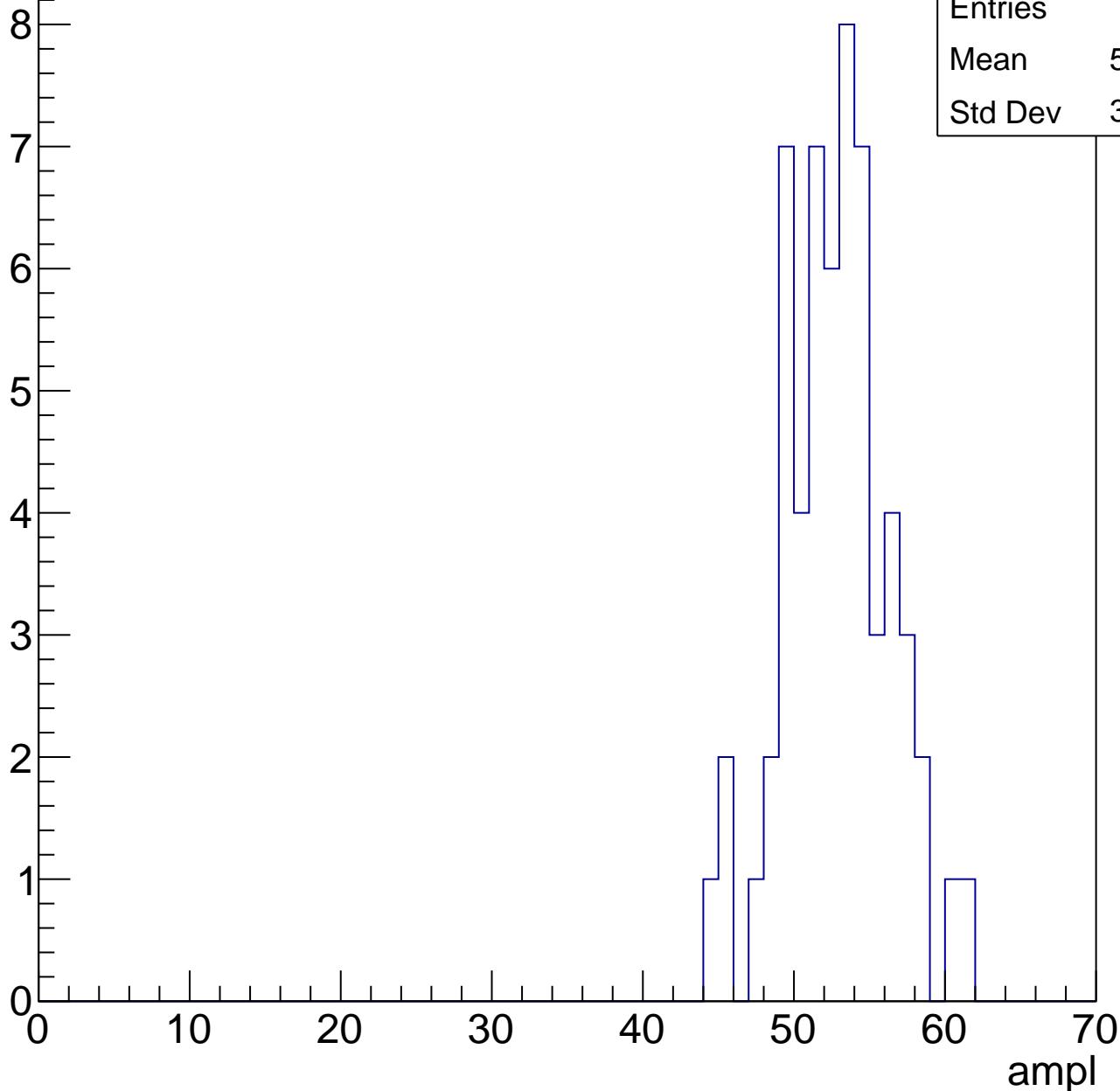


B0L001S, U11-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	52.34
Std Dev	3.487

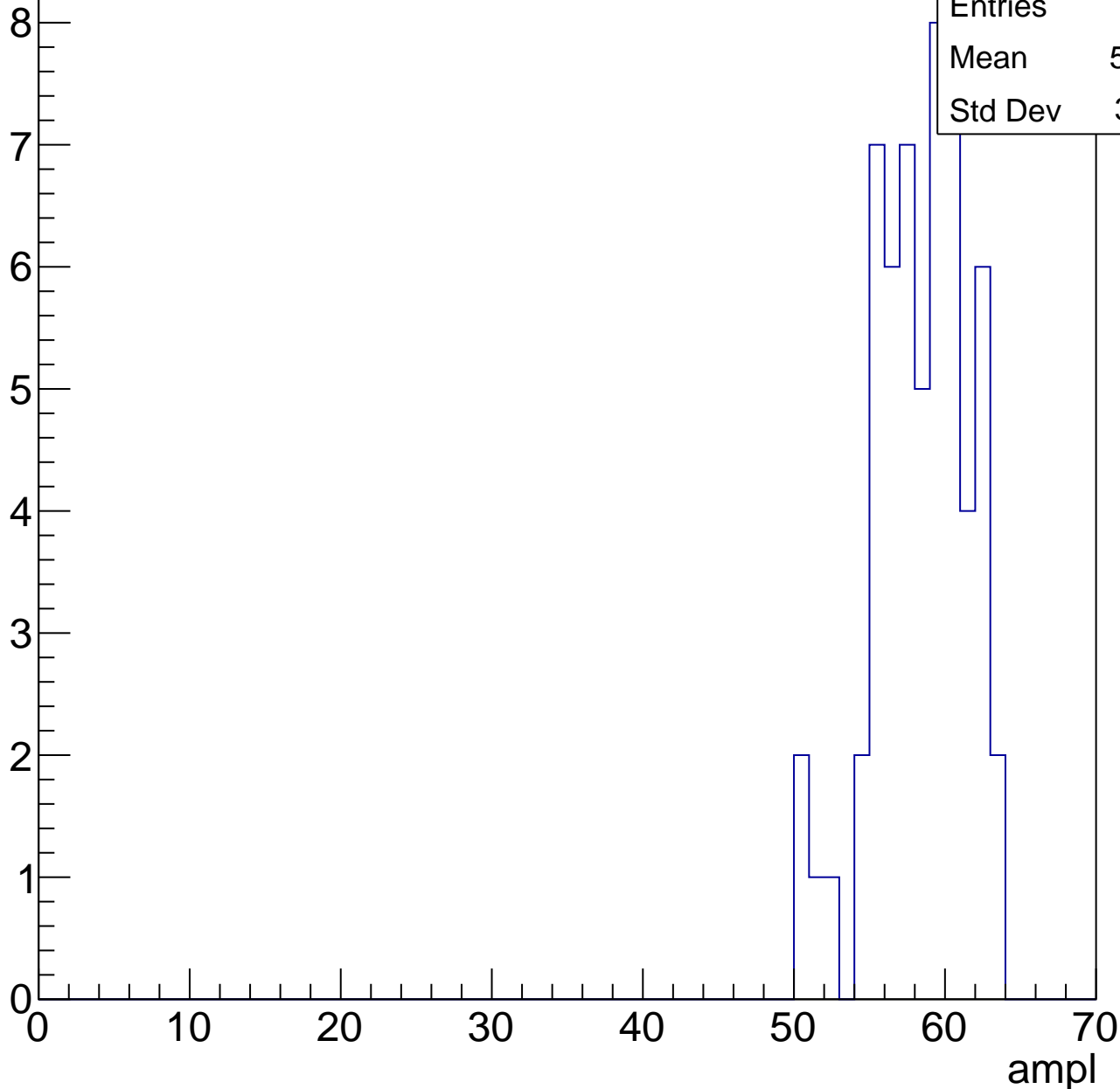


B0L001S, U11-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

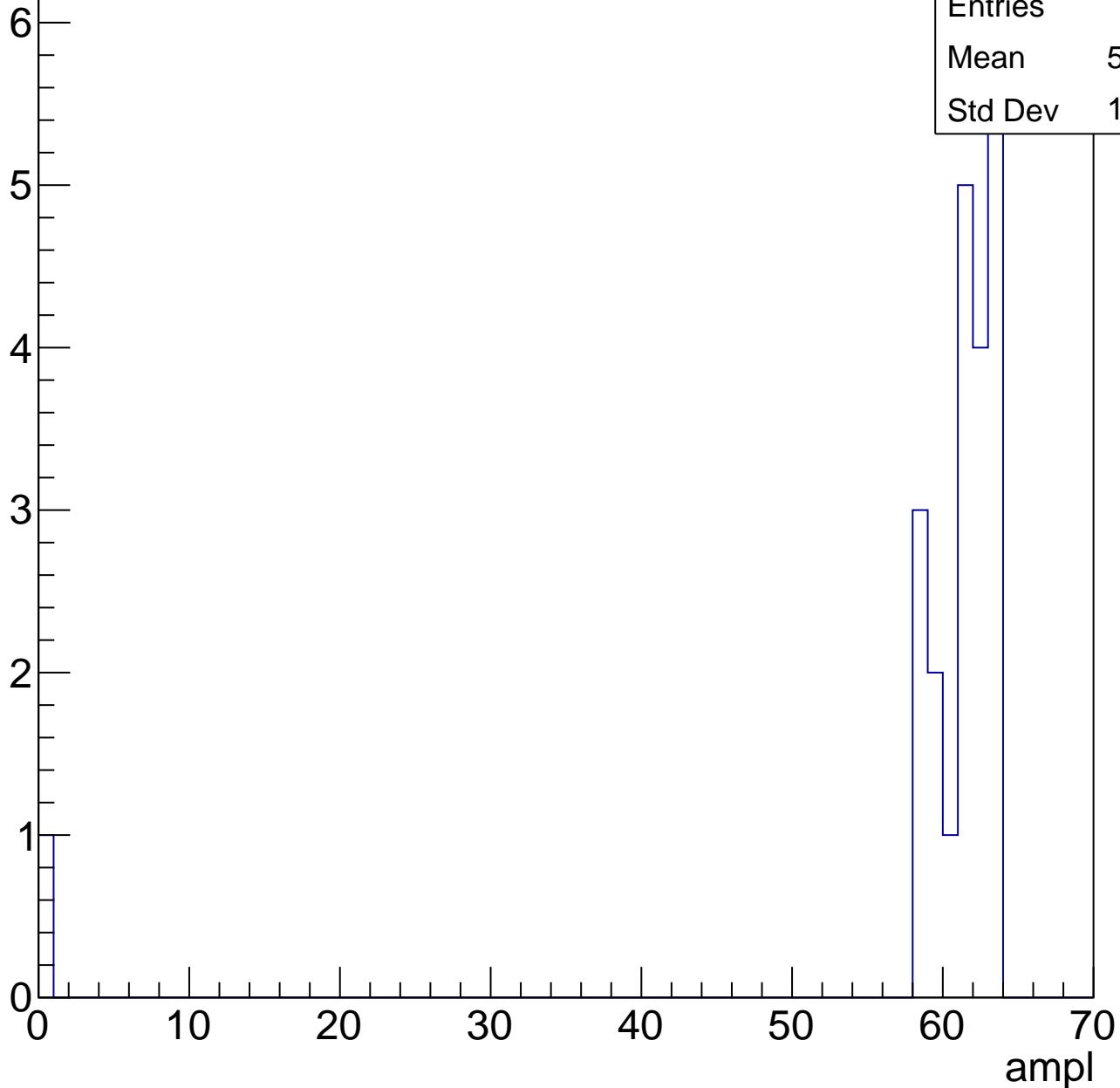
Entries	59
Mean	57.88
Std Dev	3.081



B0L001S, U11-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch88, adc0

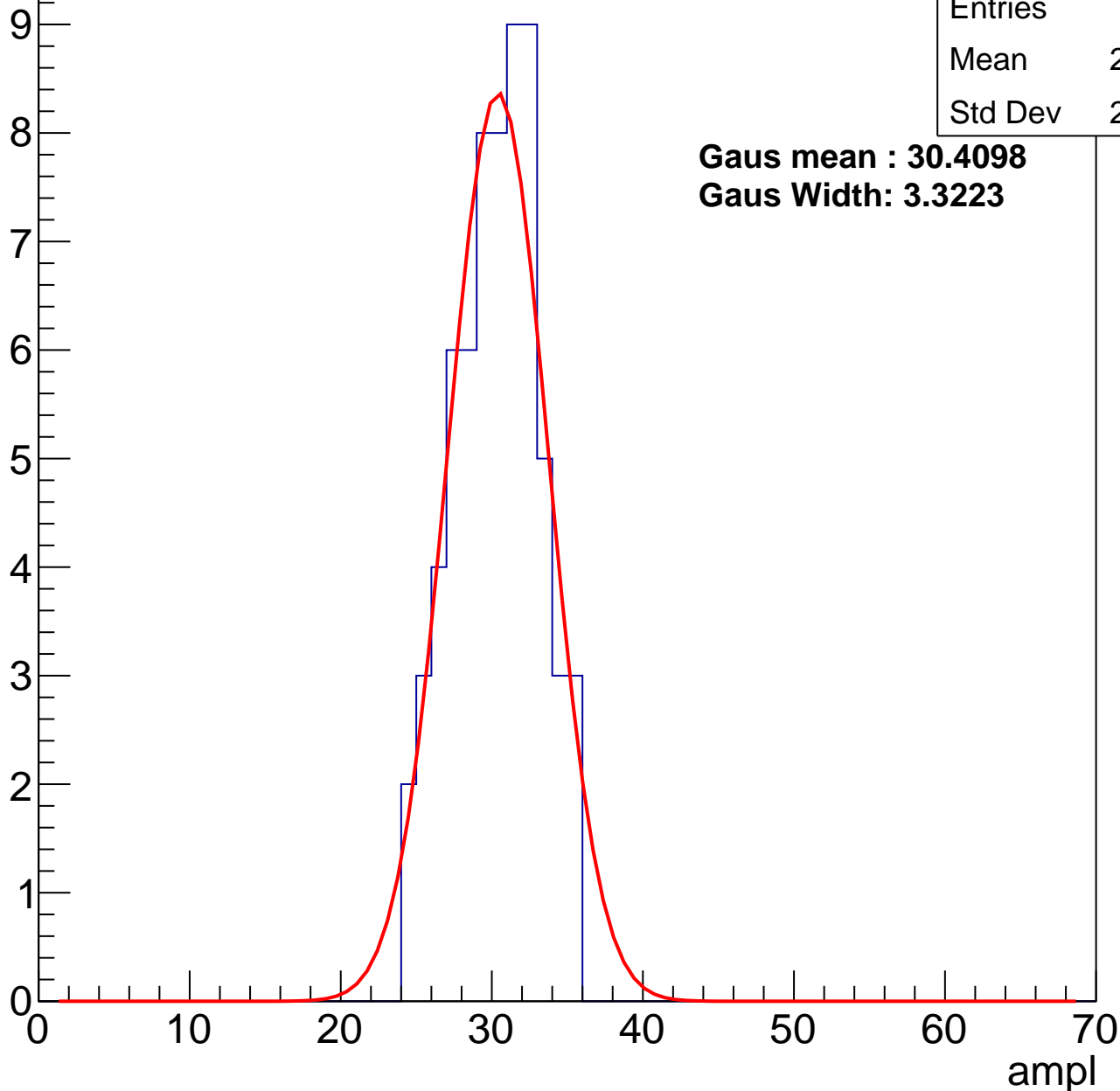
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.82
Std Dev	2.774

Gaus mean : 30.4098

Gaus Width: 3.3223



B0L001S, U11-ch88, adc1

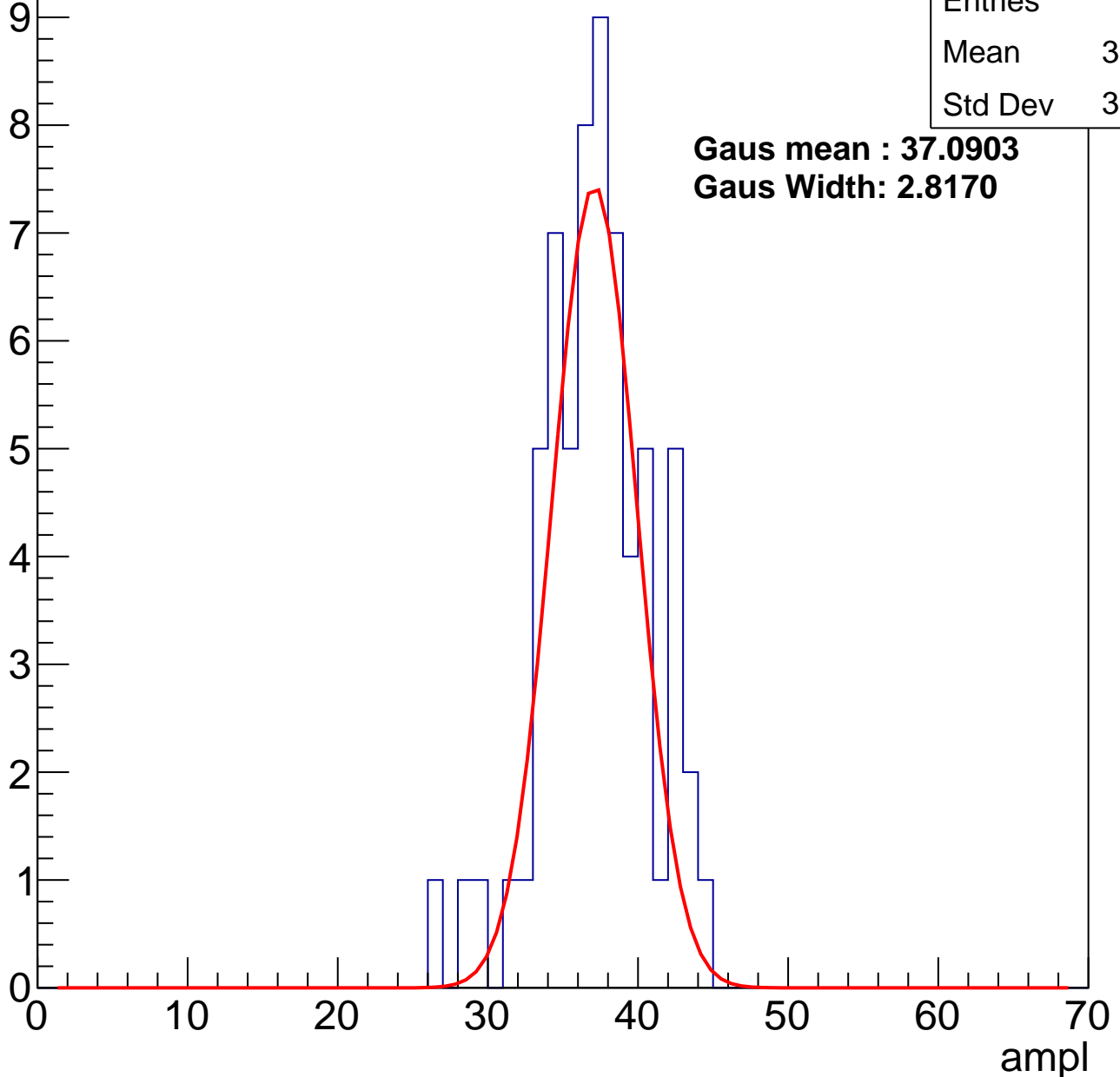
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	36.69
Std Dev	3.583

Gaus mean : 37.0903

Gaus Width: 2.8170



B0L001S, U11-ch88, adc2

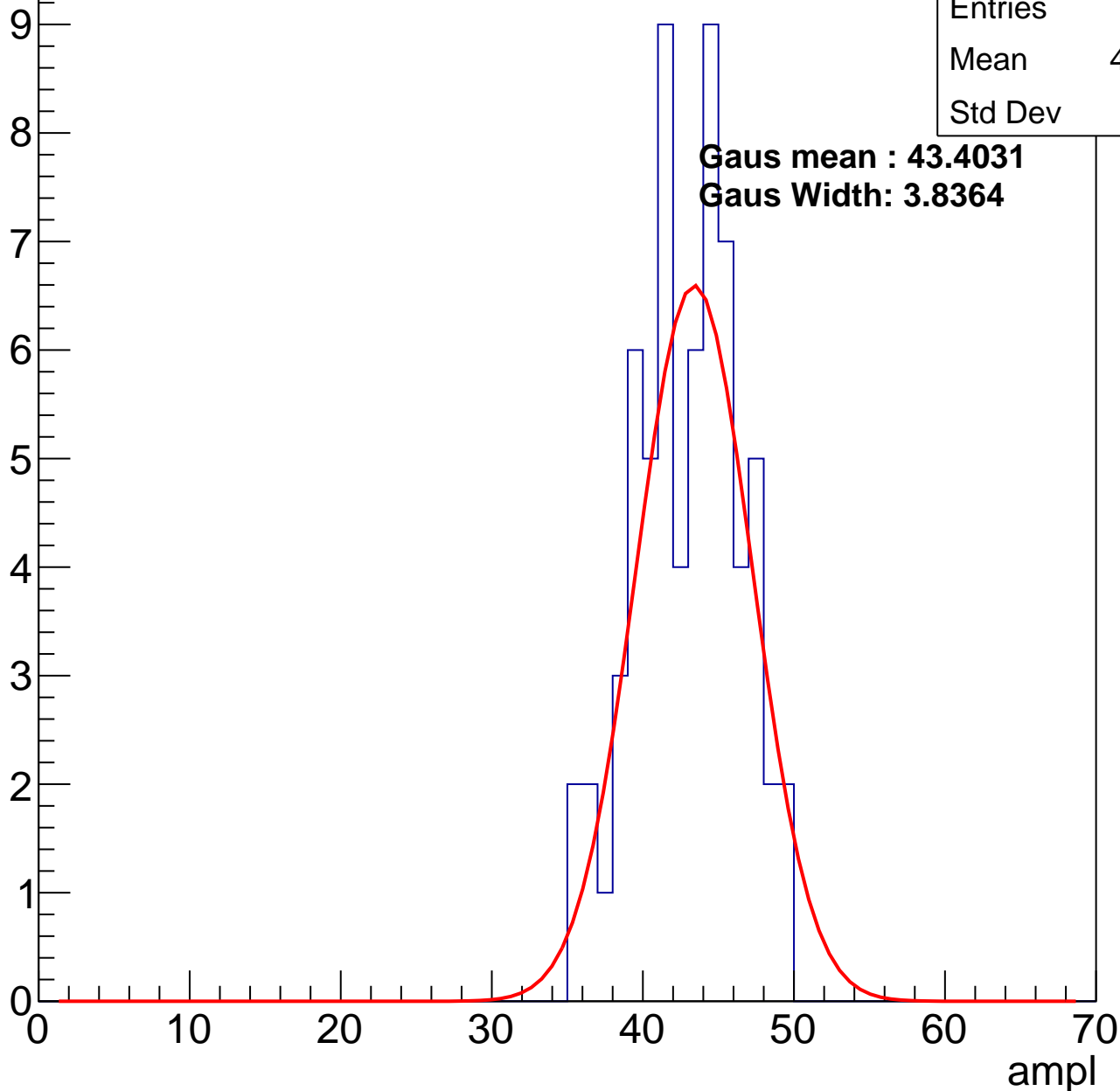
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.48
Std Dev	3.4

Gaus mean : 43.4031

Gaus Width: 3.8364

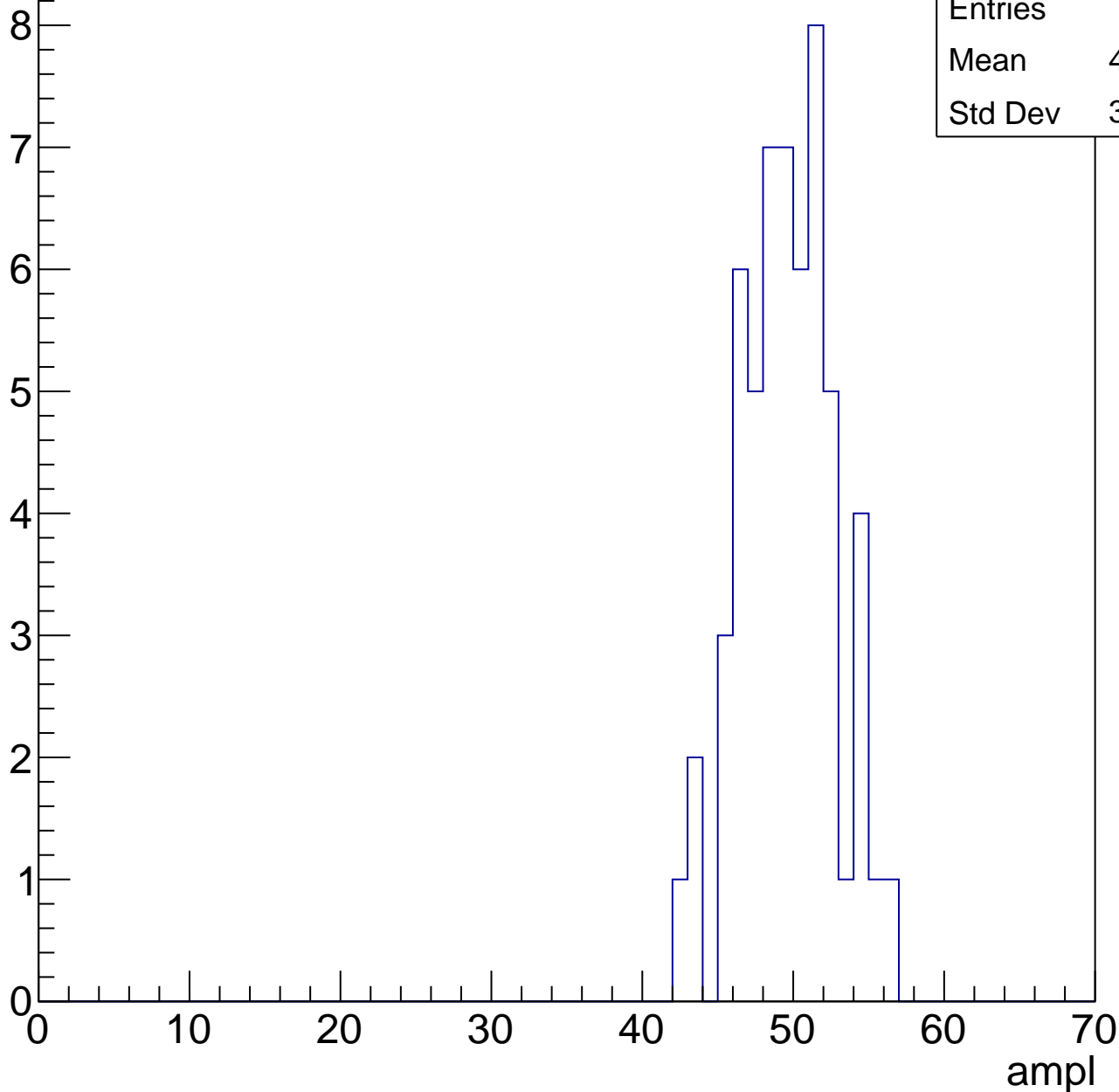


B0L001S, U11-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

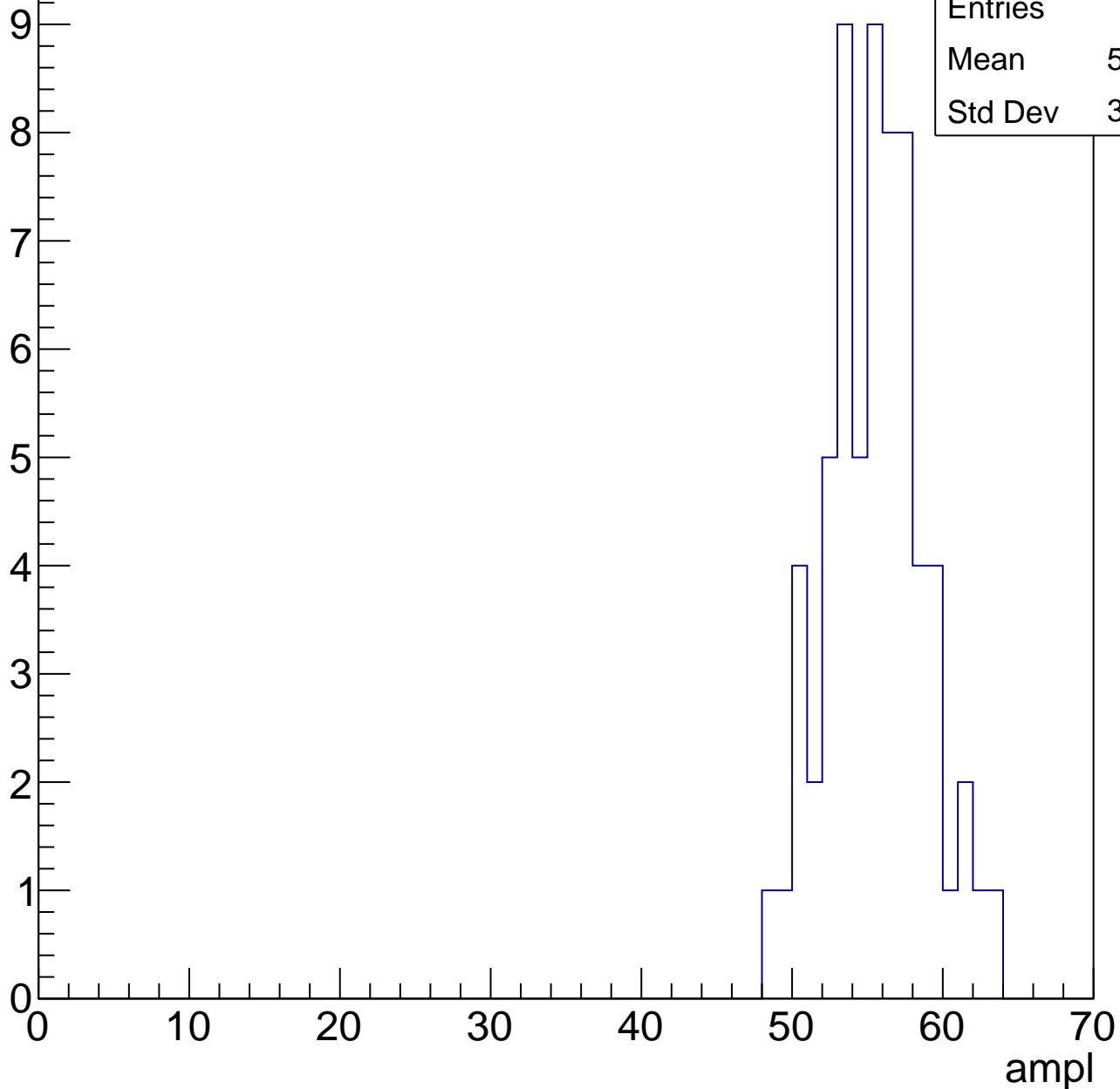
Entries	57
Mean	49.14
Std Dev	3.058



B0L001S, U11-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

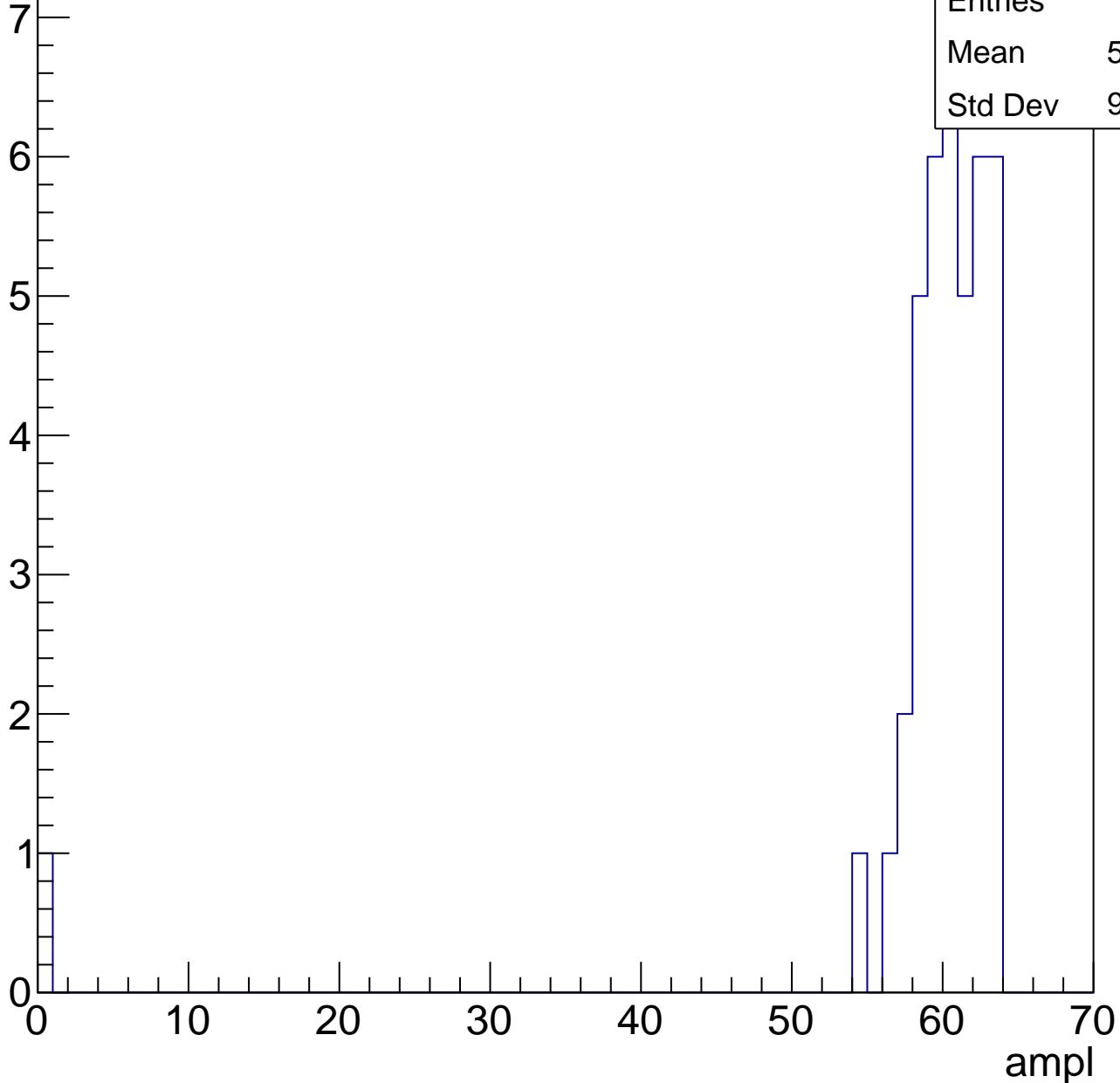


B0L001S, U11-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

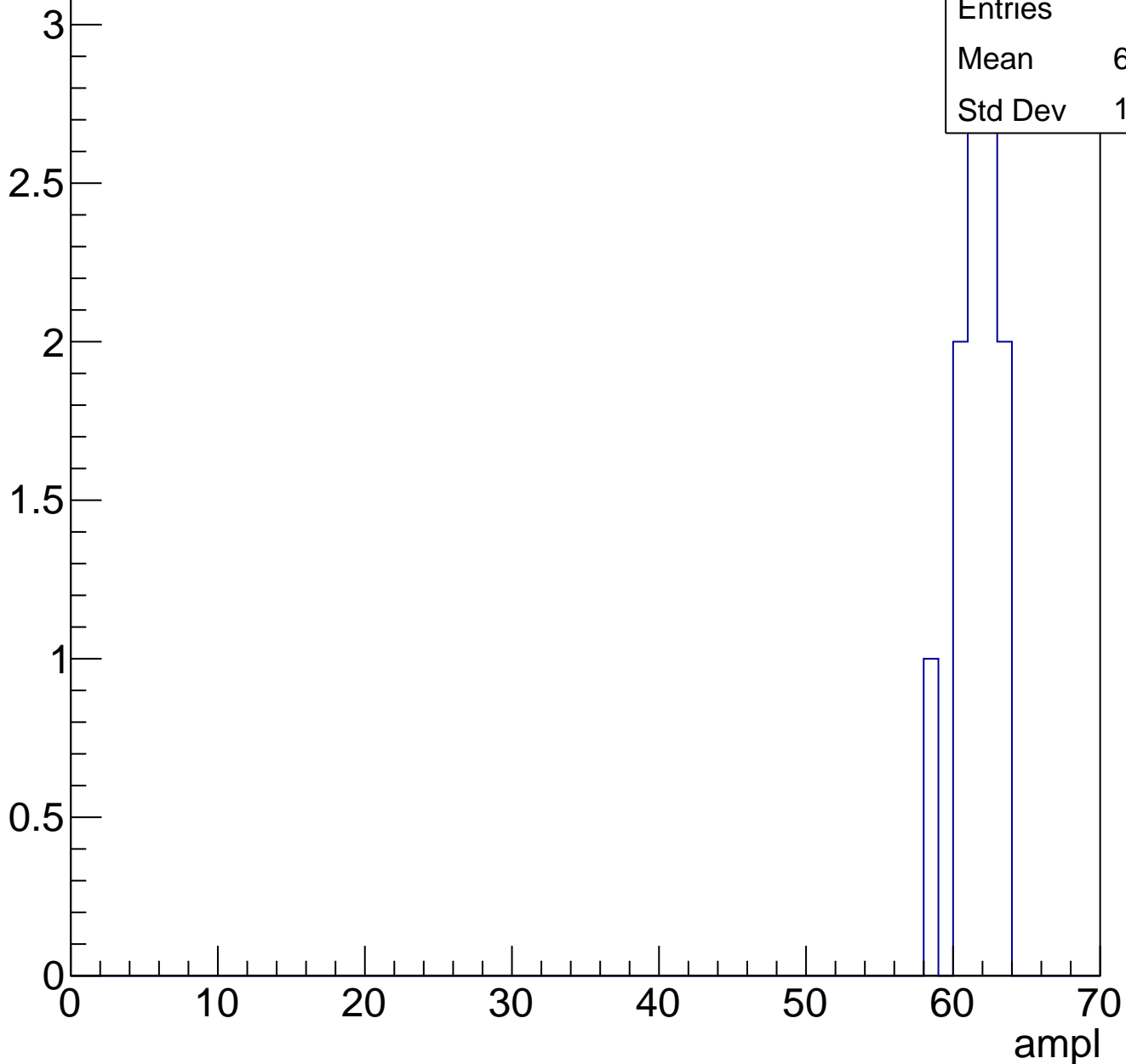
Entries	40
Mean	58.58
Std Dev	9.615



B0L001S, U11-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

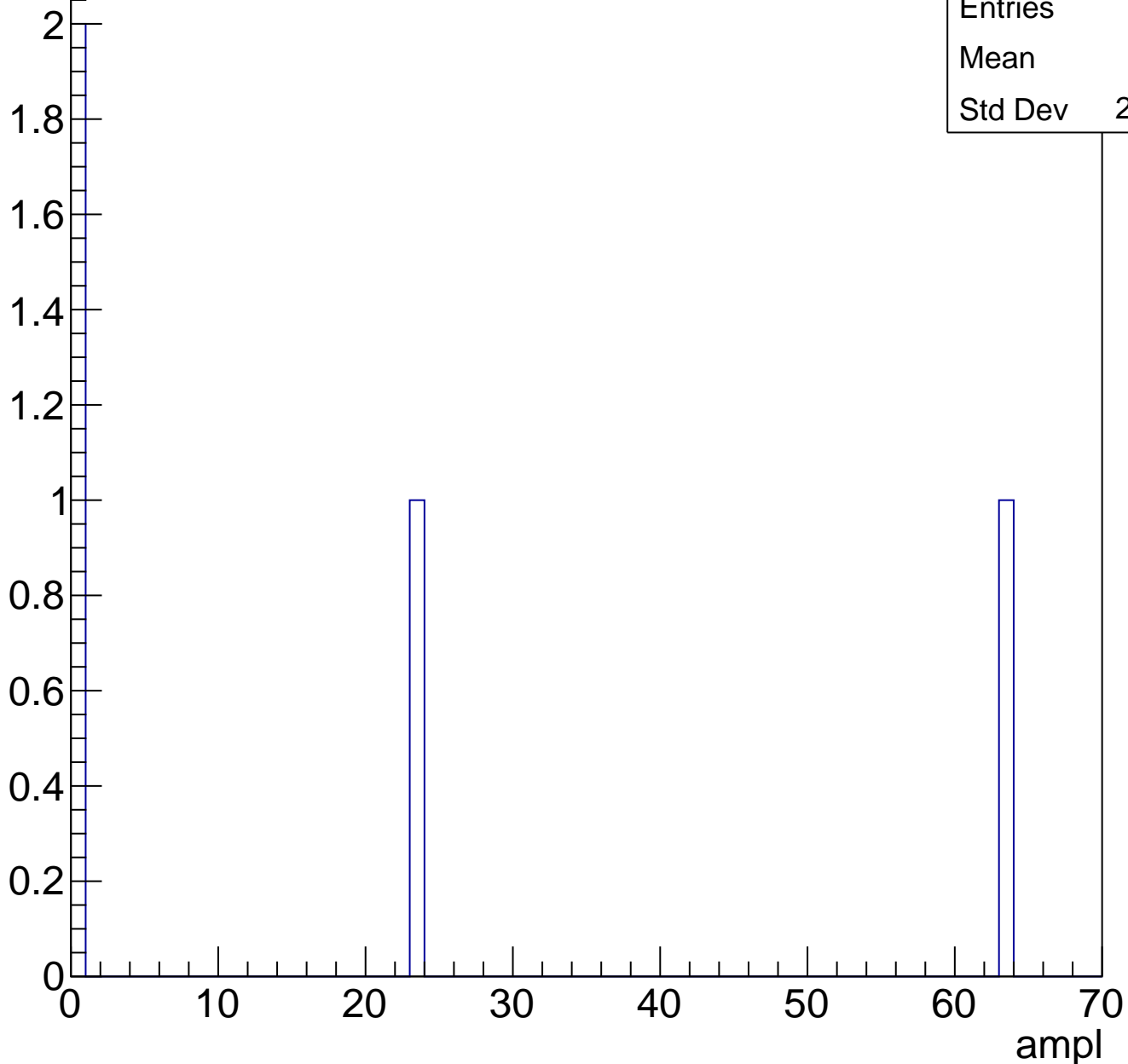
Entry



B0L001S, U11-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	21.5
Std Dev	25.73

B0L001S, U11-ch89, adc0

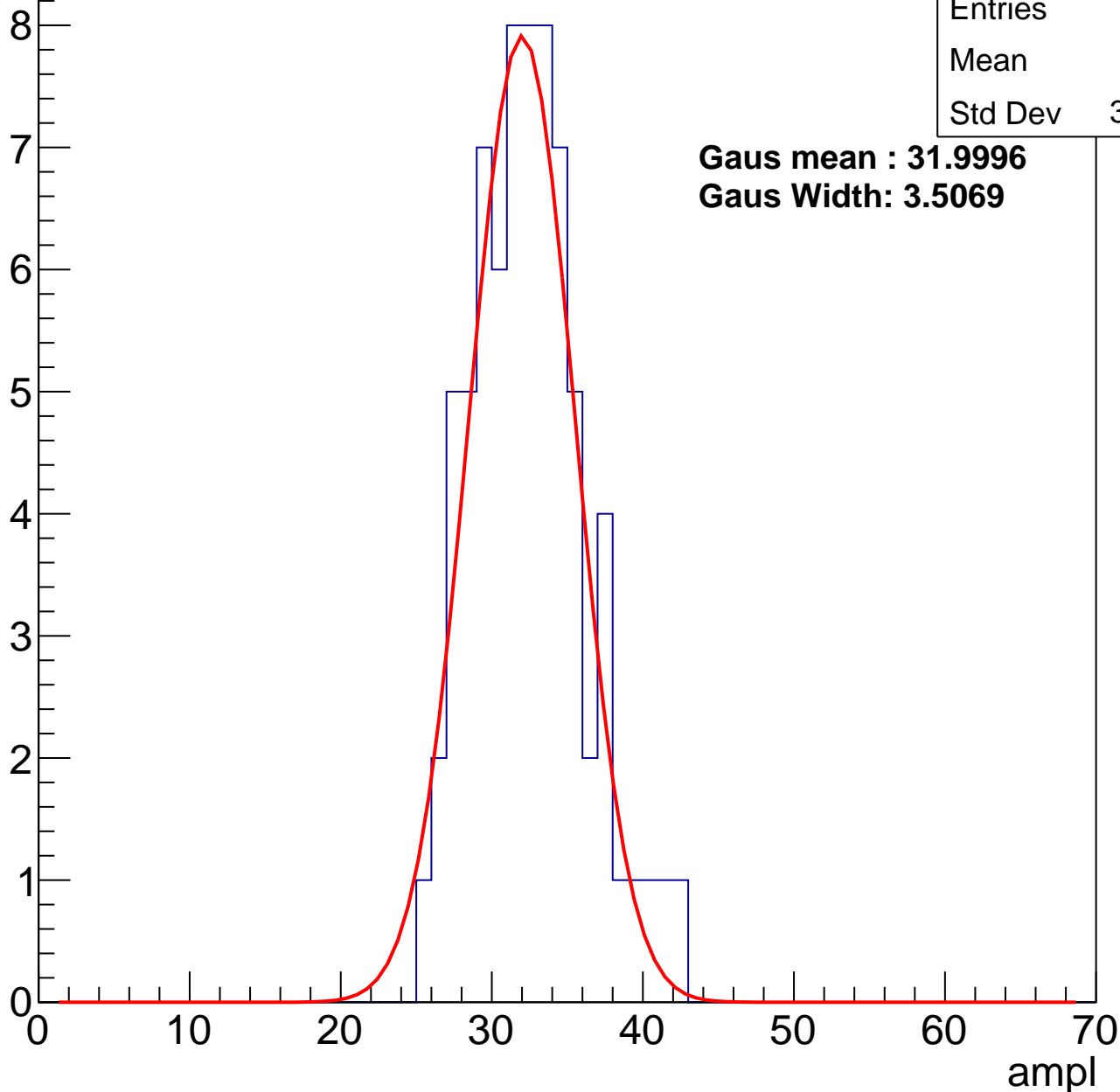
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	32
Std Dev	3.634

Gaus mean : 31.9996

Gaus Width: 3.5069



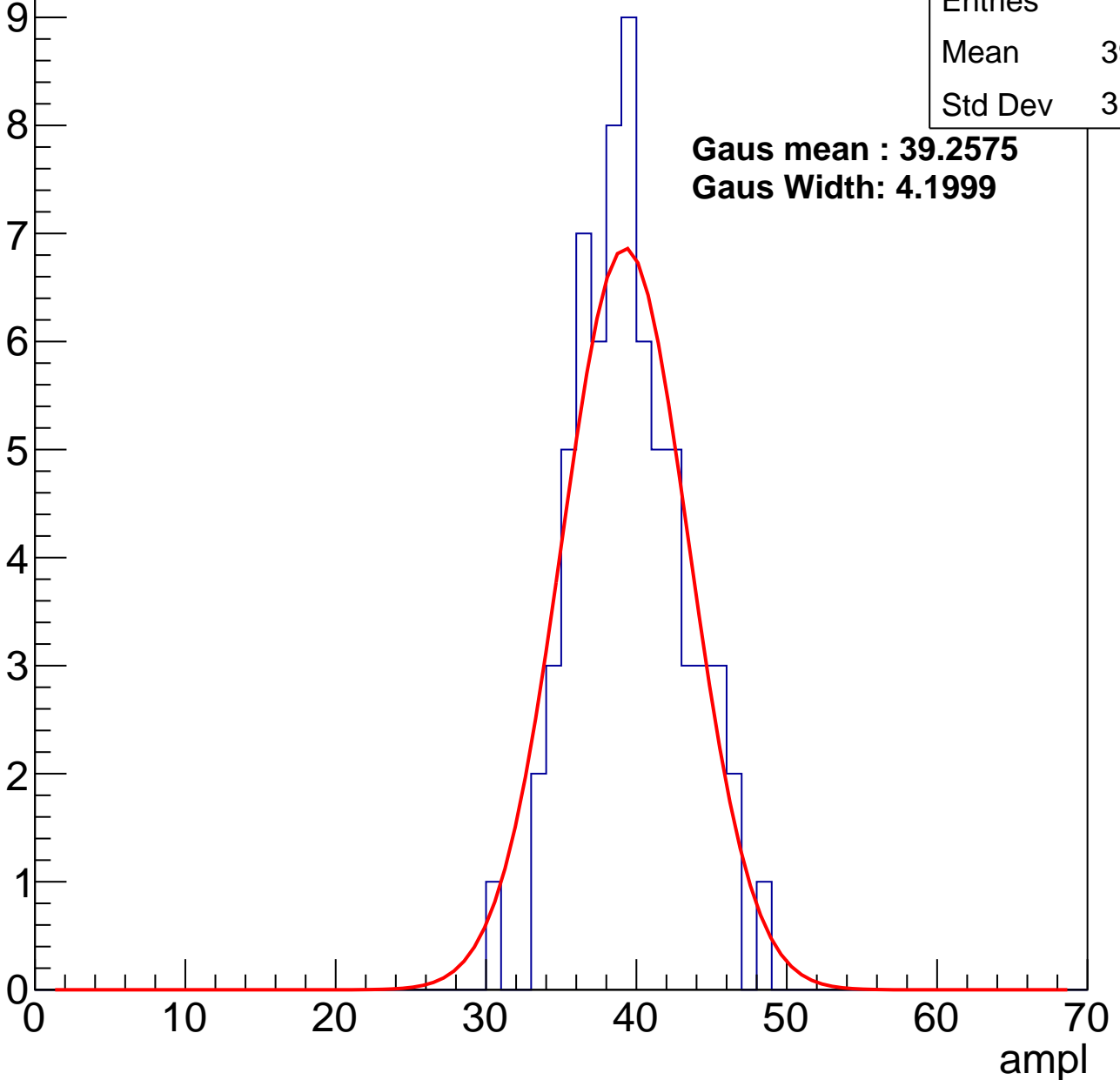
B0L001S, U11-ch89, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	39.03
Std Dev	3.579

Gaus mean : 39.2575
Gaus Width: 4.1999



B0L001S, U11-ch89, adc2

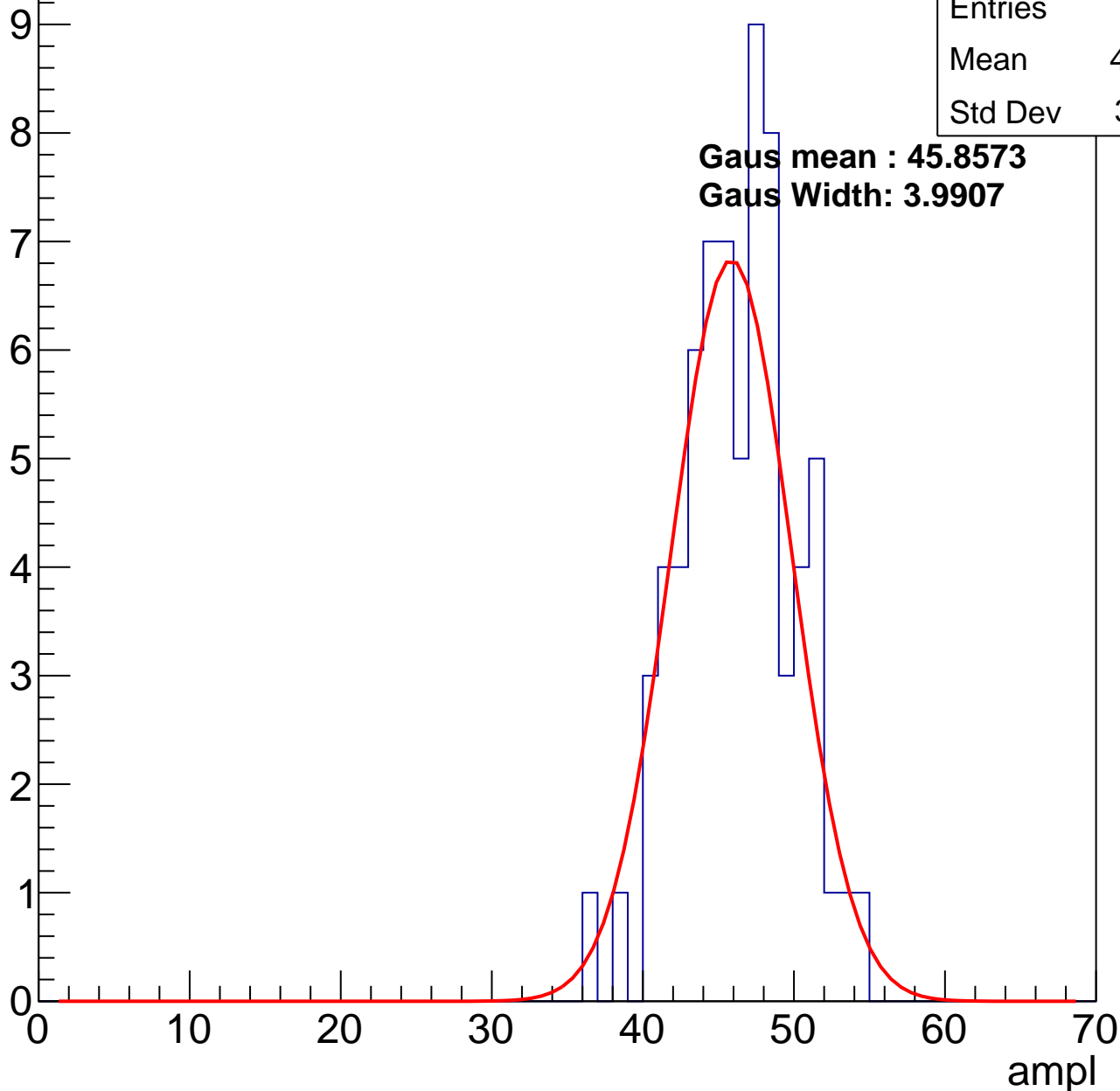
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	45.79
Std Dev	3.641

Gaus mean : 45.8573

Gaus Width: 3.9907

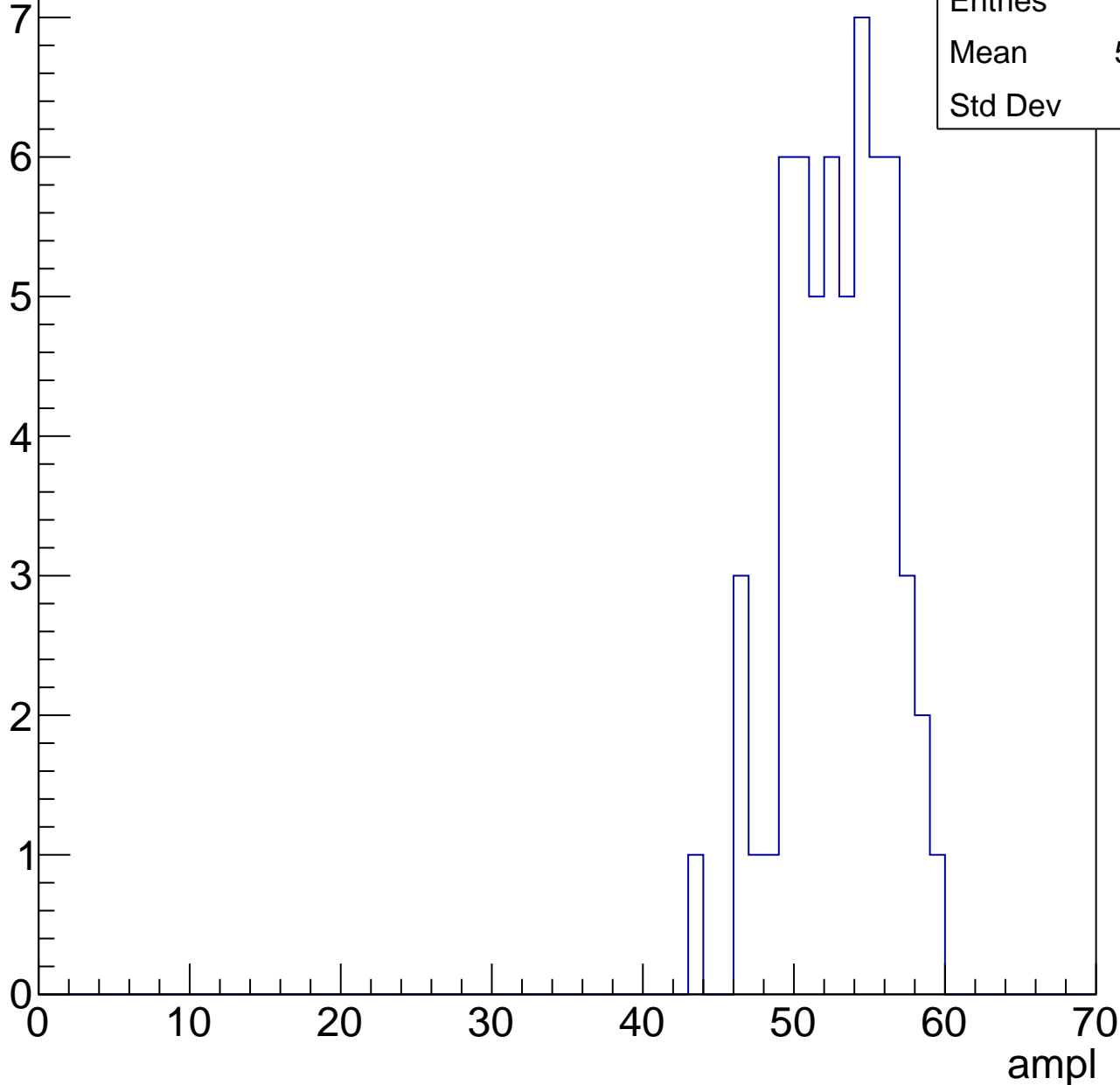


B0L001S, U11-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	52.41
Std Dev	3.4

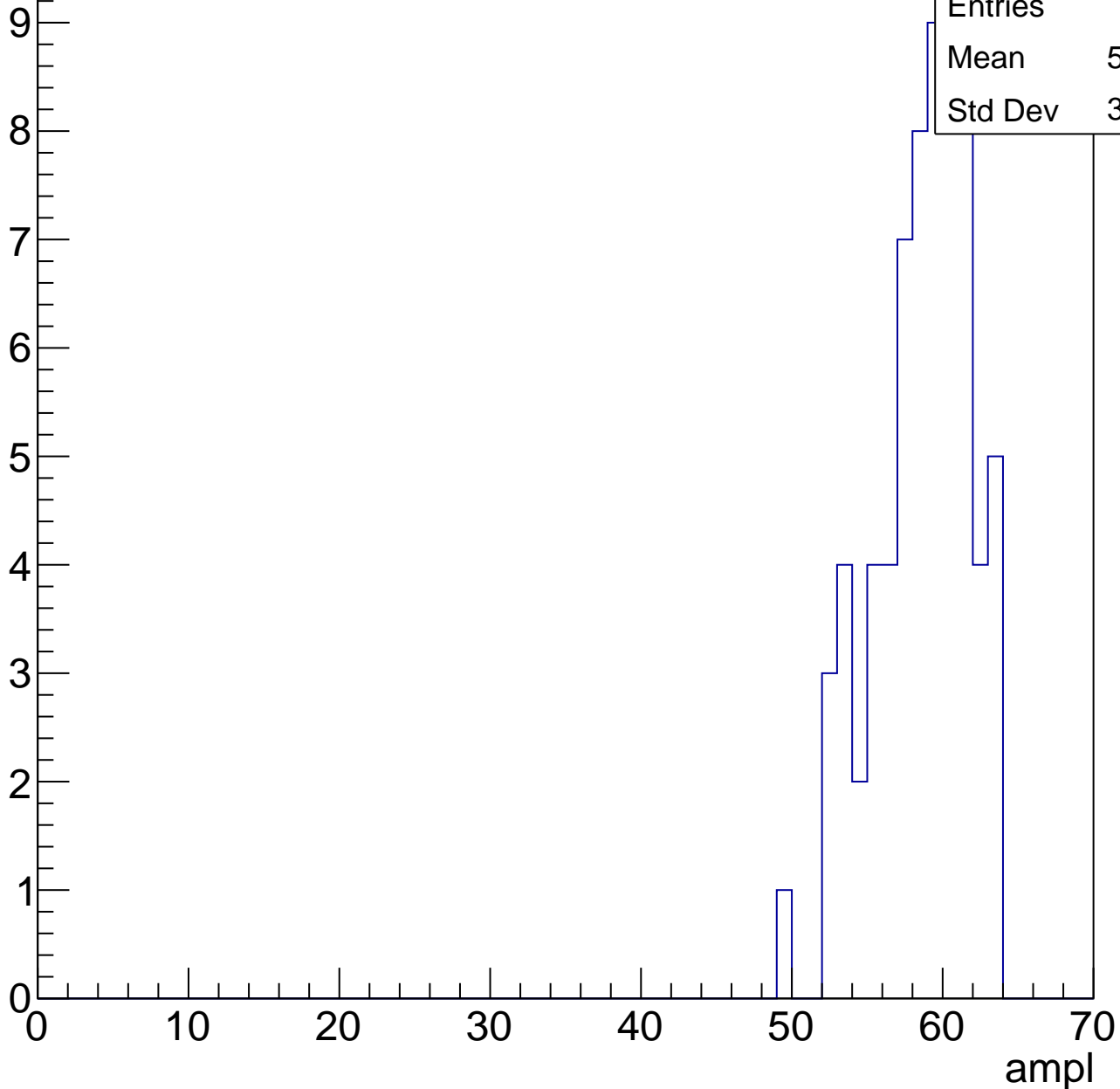


B0L001S, U11-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	58.12
Std Dev	3.188

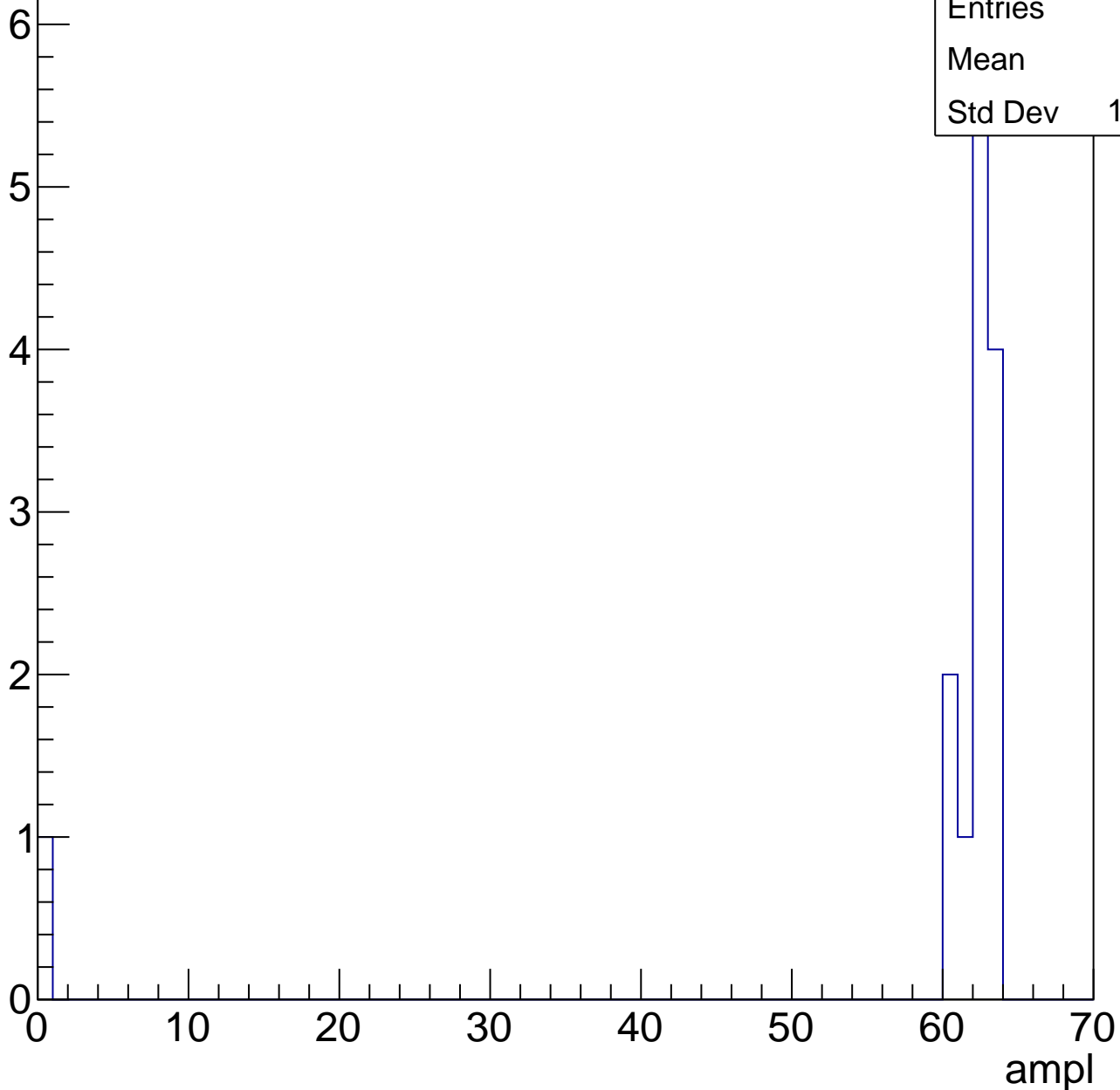


B0L001S, U11-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	14
Mean	57.5
Std Dev	15.98



B0L001S, U11-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch89, adc7

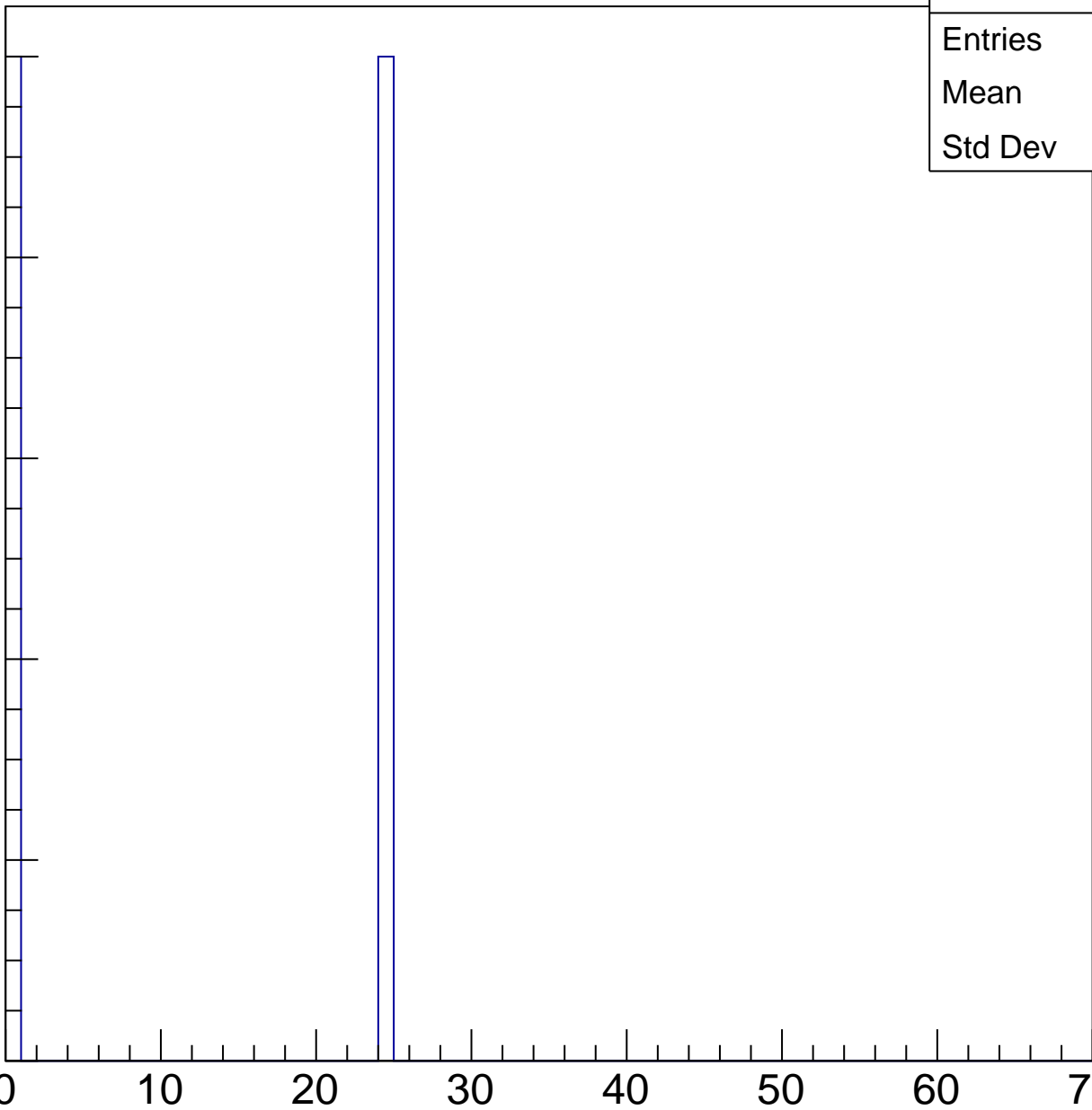
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B0L001S, U11-ch90, adc0

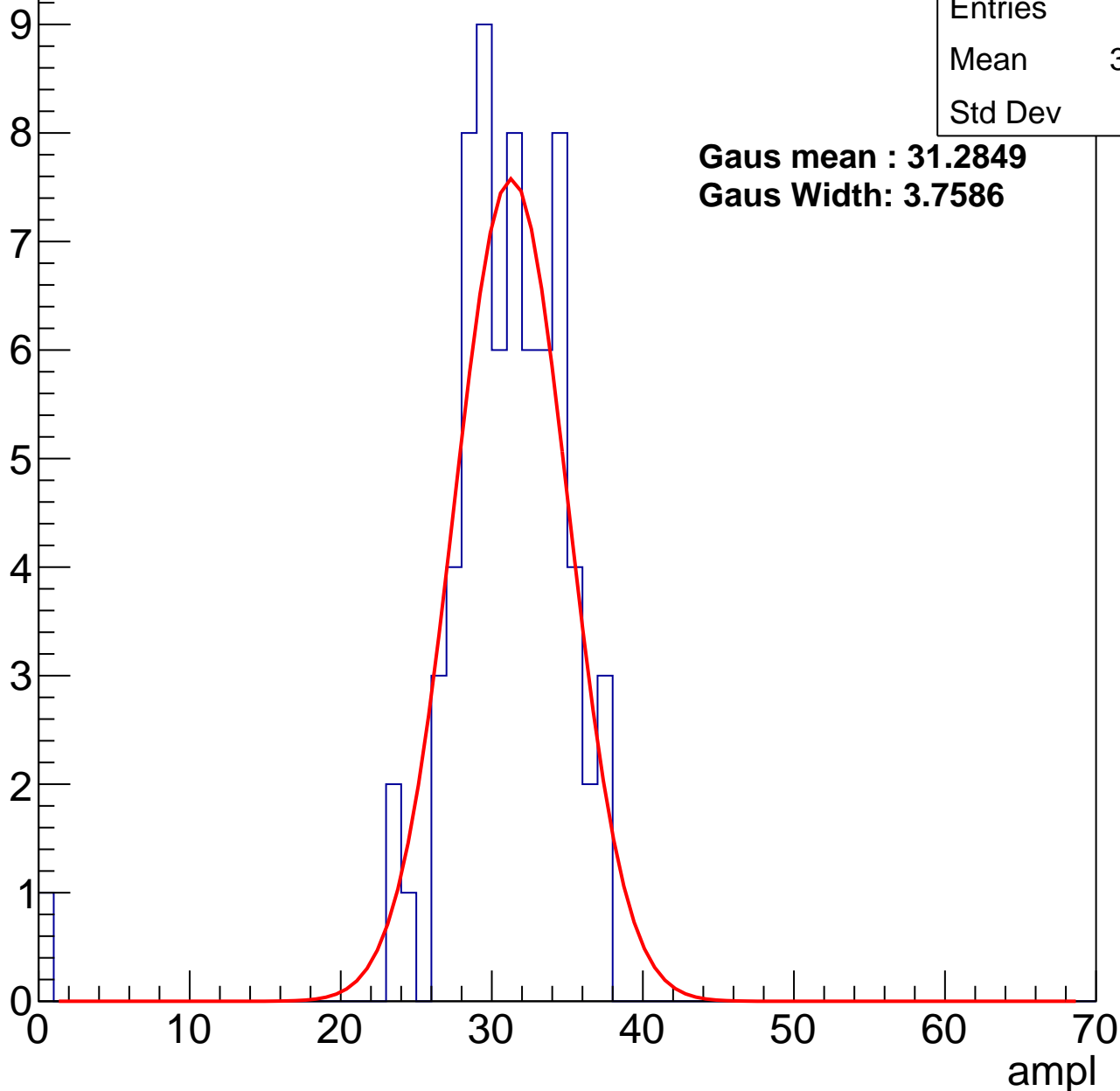
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	30.34
Std Dev	4.87

Gaus mean : 31.2849

Gaus Width: 3.7586



B0L001S, U11-ch90, adc1

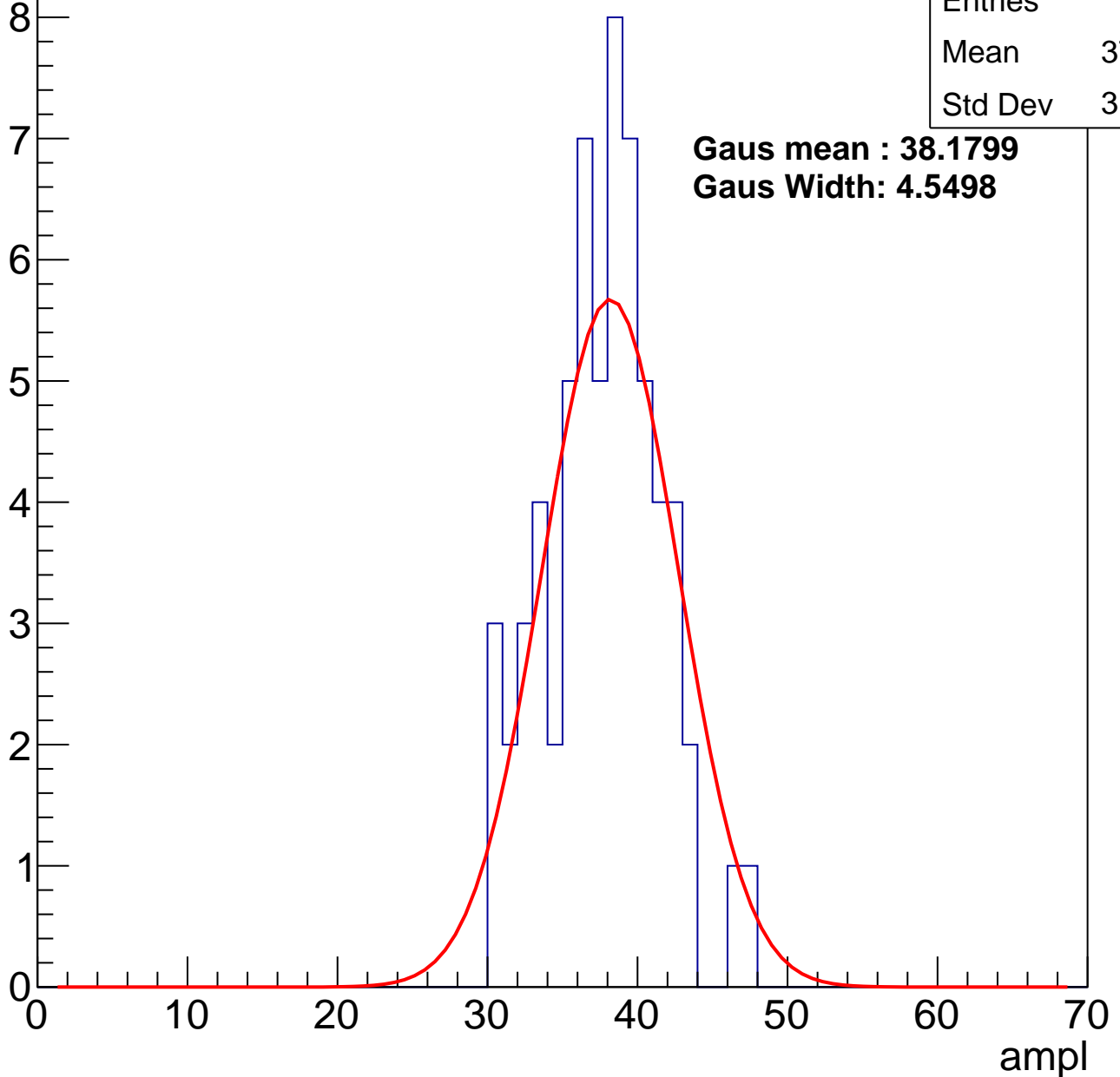
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.27
Std Dev	3.772

Gaus mean : 38.1799

Gaus Width: 4.5498



B0L001S, U11-ch90, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	43.56
Std Dev	3.516

Gaus mean : 44.0375

Gaus Width: 3.6689

Entry

10

8

6

4

2

0

0

10

20

30

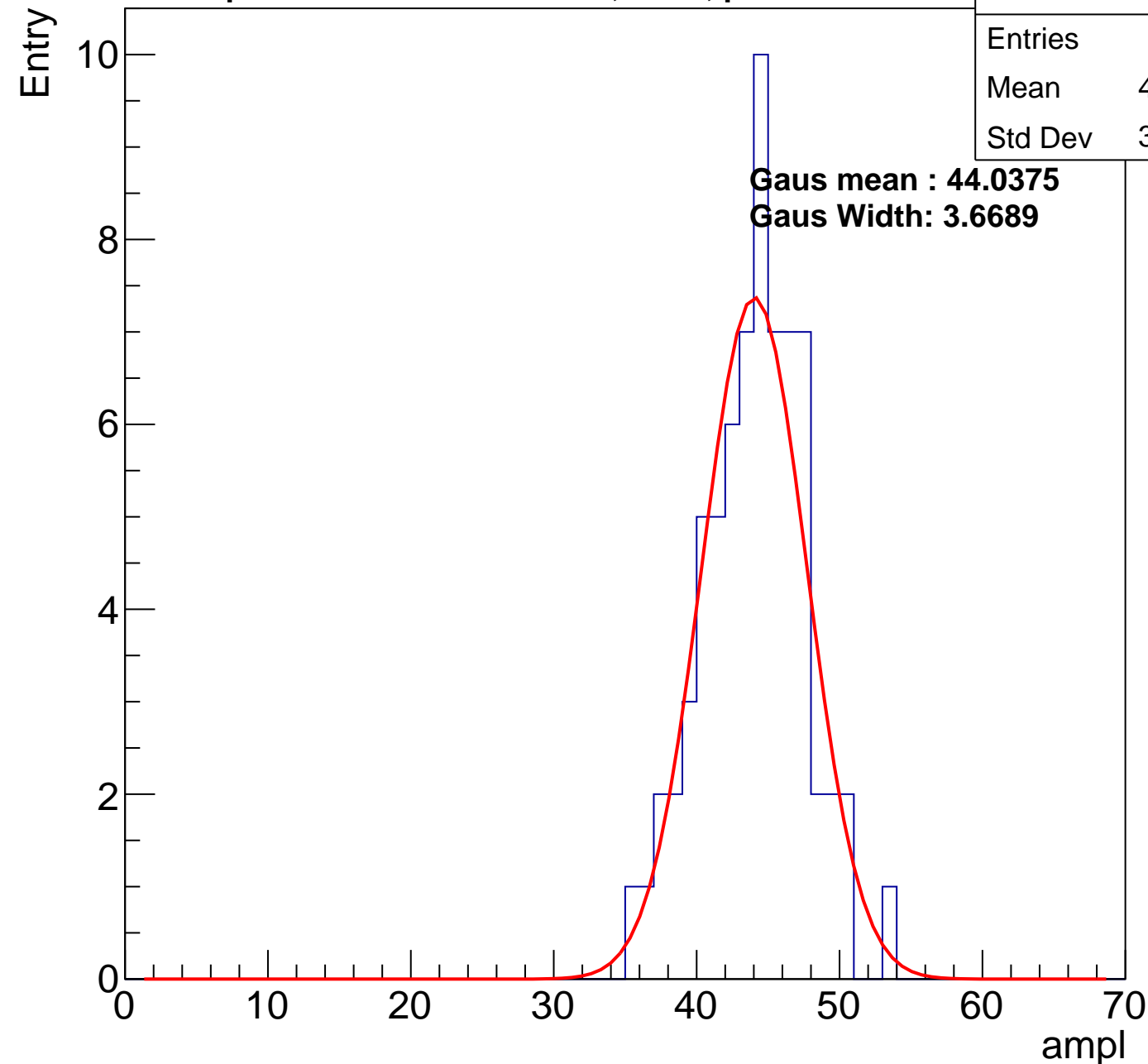
40

50

60

70

ampl

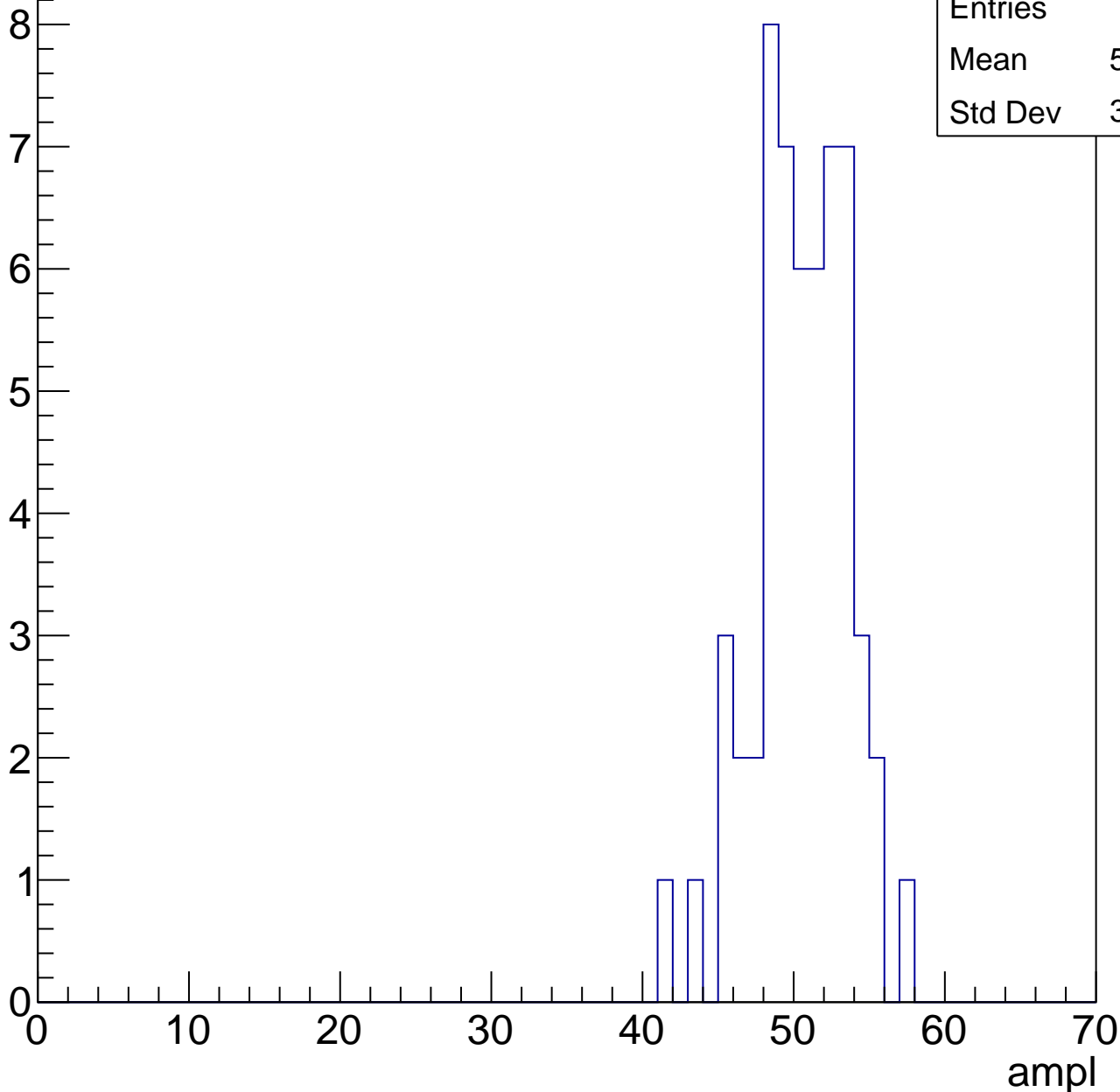


B0L001S, U11-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

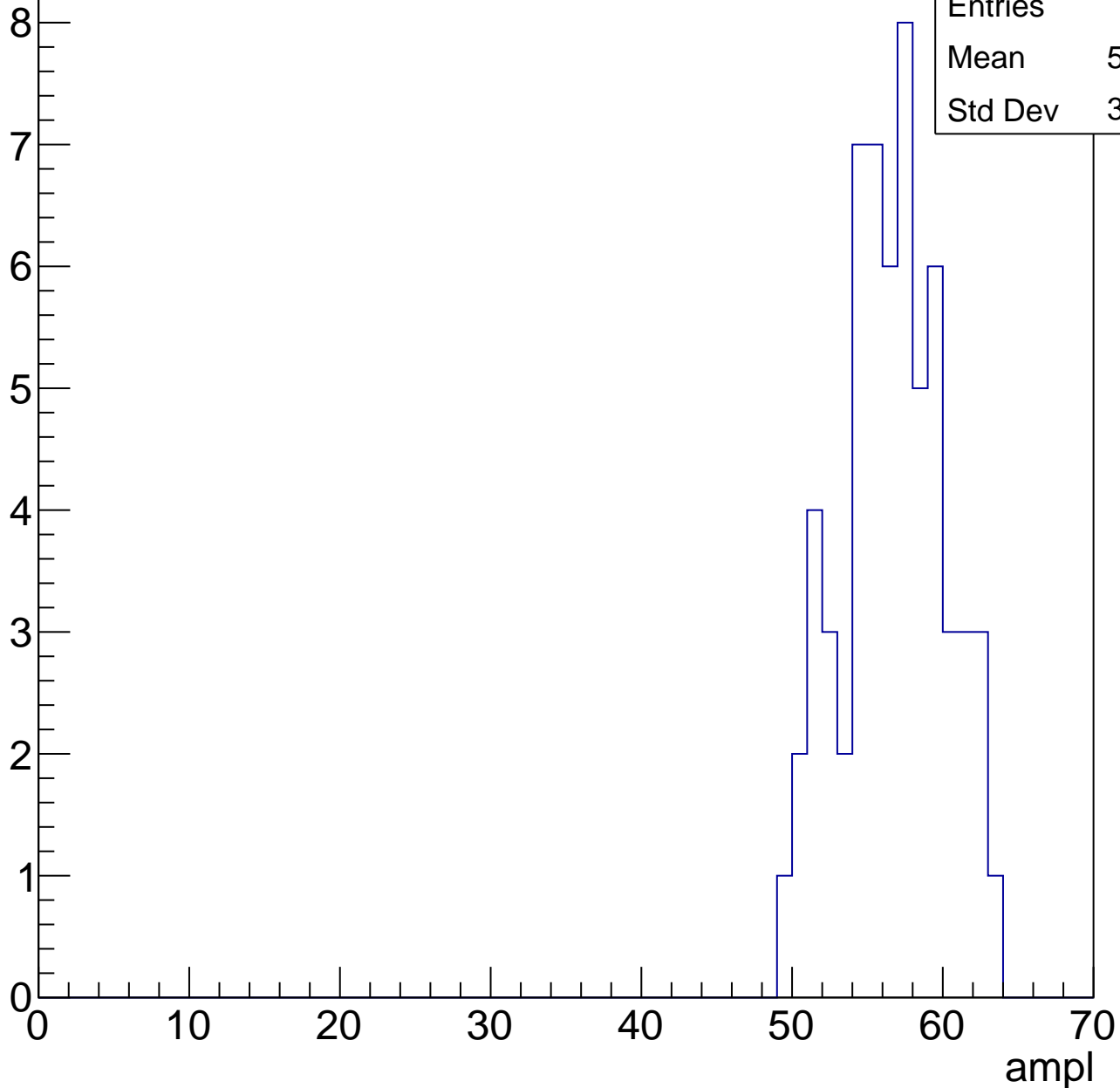
Entries	56
Mean	50.04
Std Dev	3.099



B0L001S, U11-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	61
Mean	56.16
Std Dev	3.345

B0L001S, U11-ch90, adc5

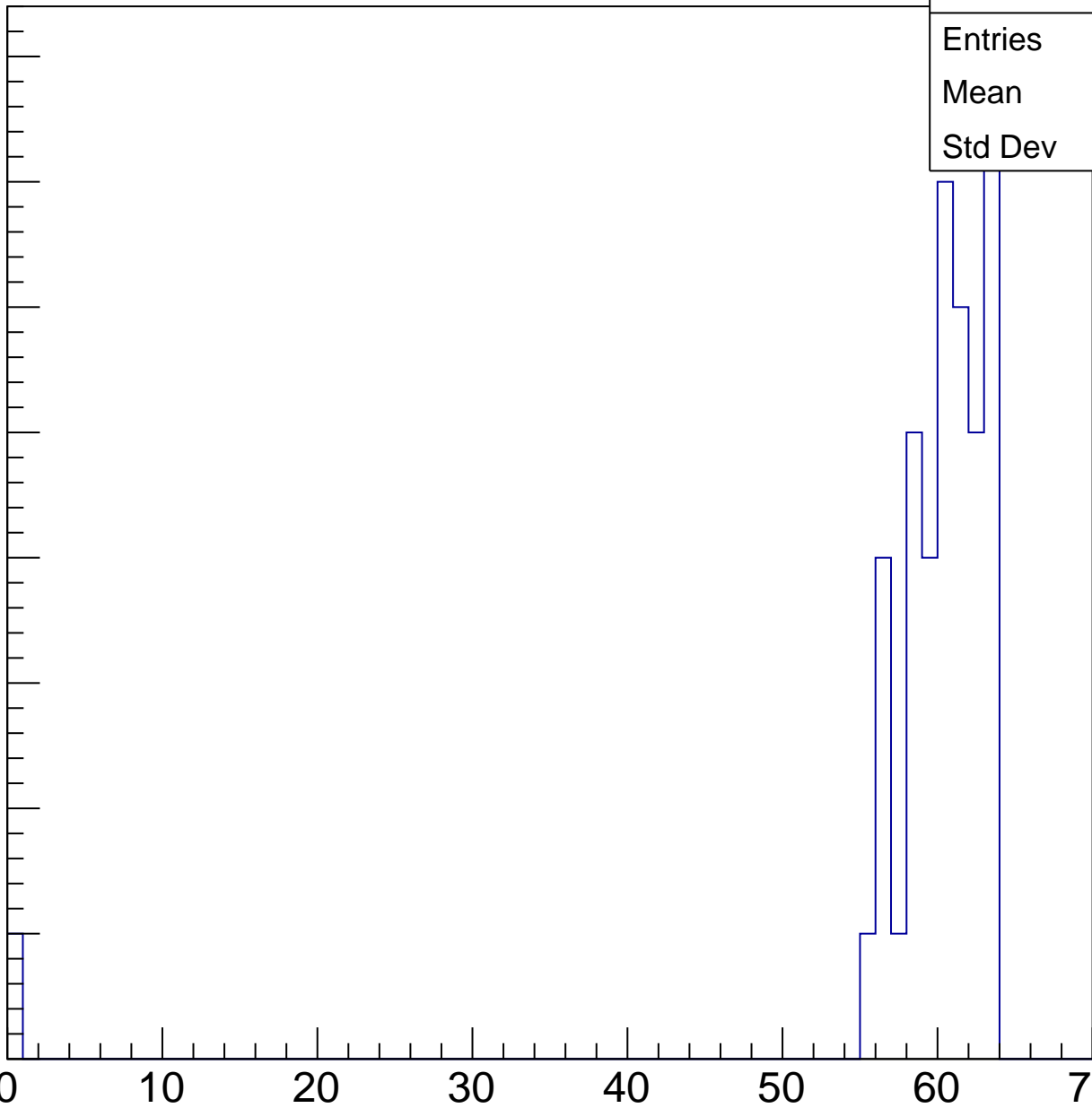
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.62
Std Dev	9.436

ampl



B0L001S, U11-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch91, adc0

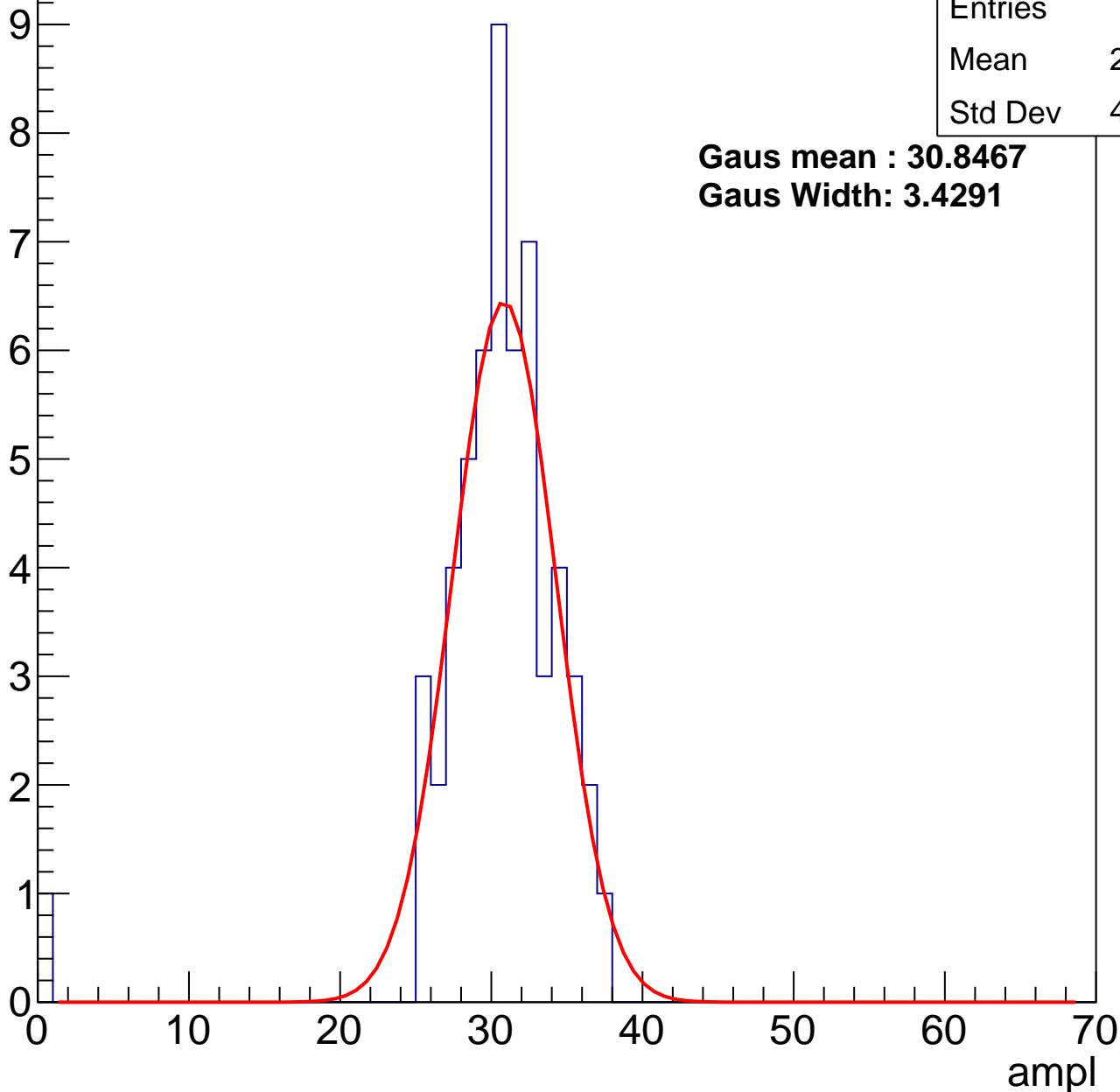
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.96
Std Dev	4.982

Gaus mean : 30.8467

Gaus Width: 3.4291



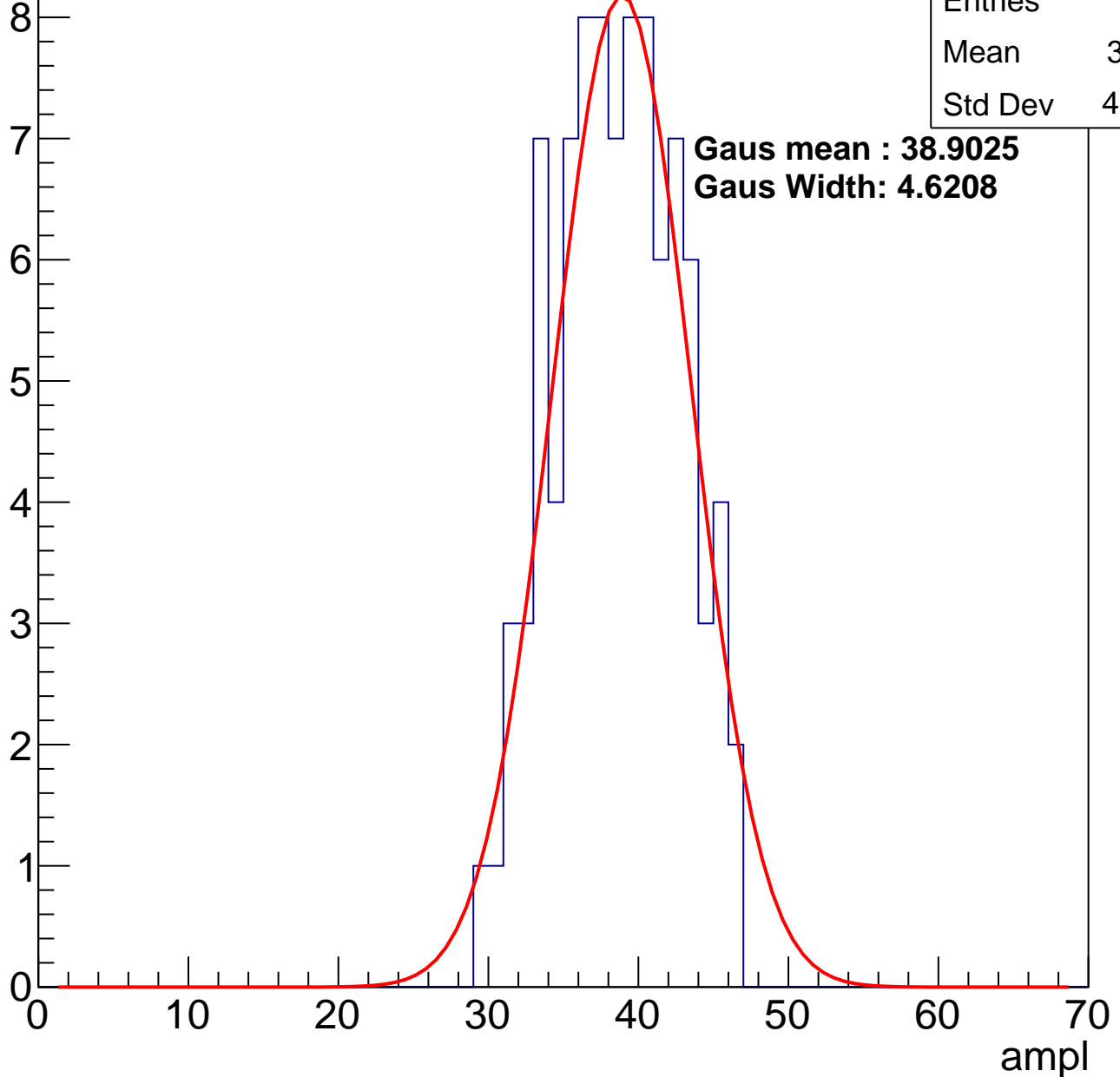
B0L001S, U11-ch91, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	93
Mean	38.11
Std Dev	4.055

Gaus mean : 38.9025
Gaus Width: 4.6208



B0L001S, U11-ch91, adc2

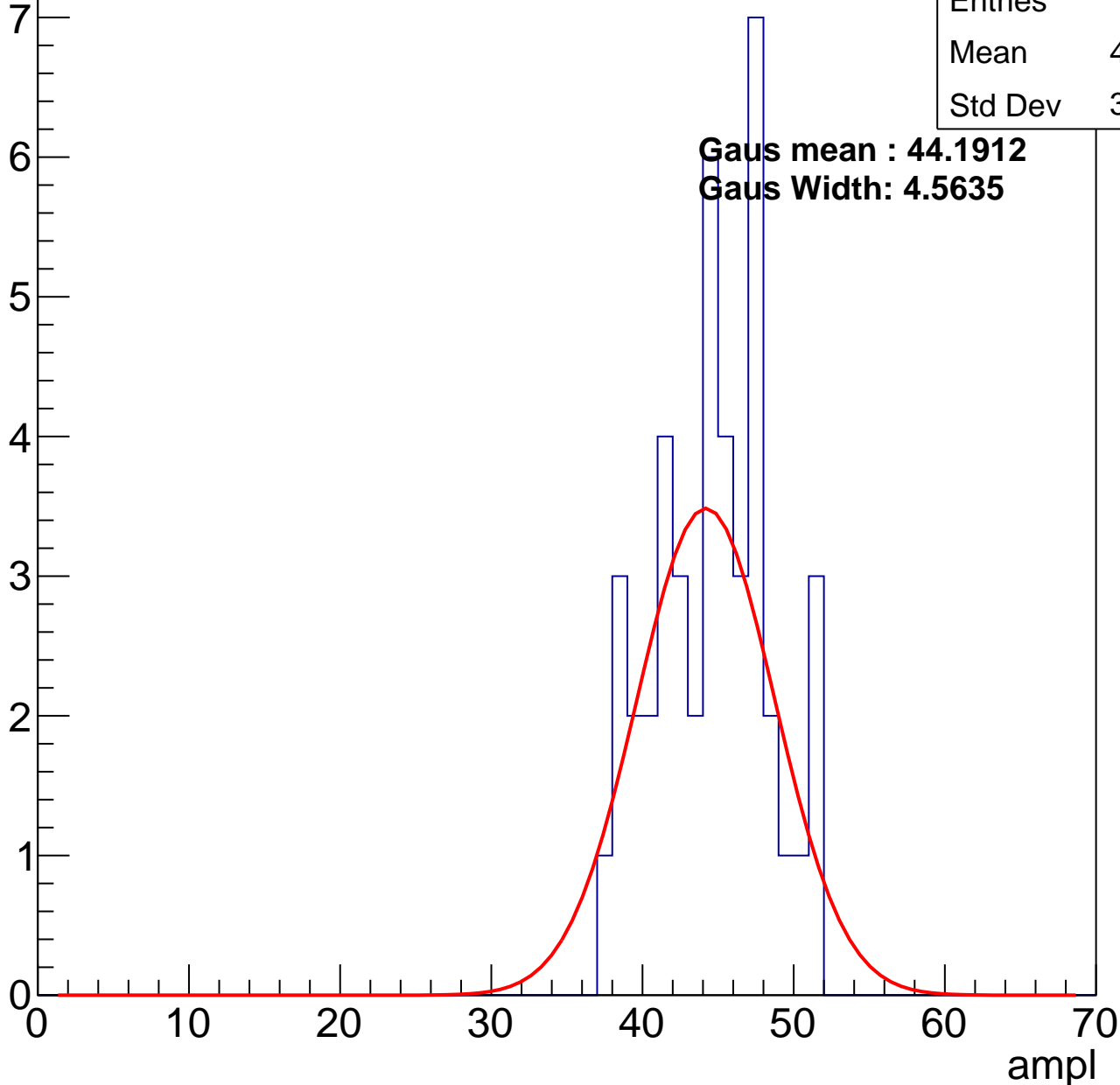
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	44.18
Std Dev	3.713

Gaus mean : 44.1912

Gaus Width: 4.5635

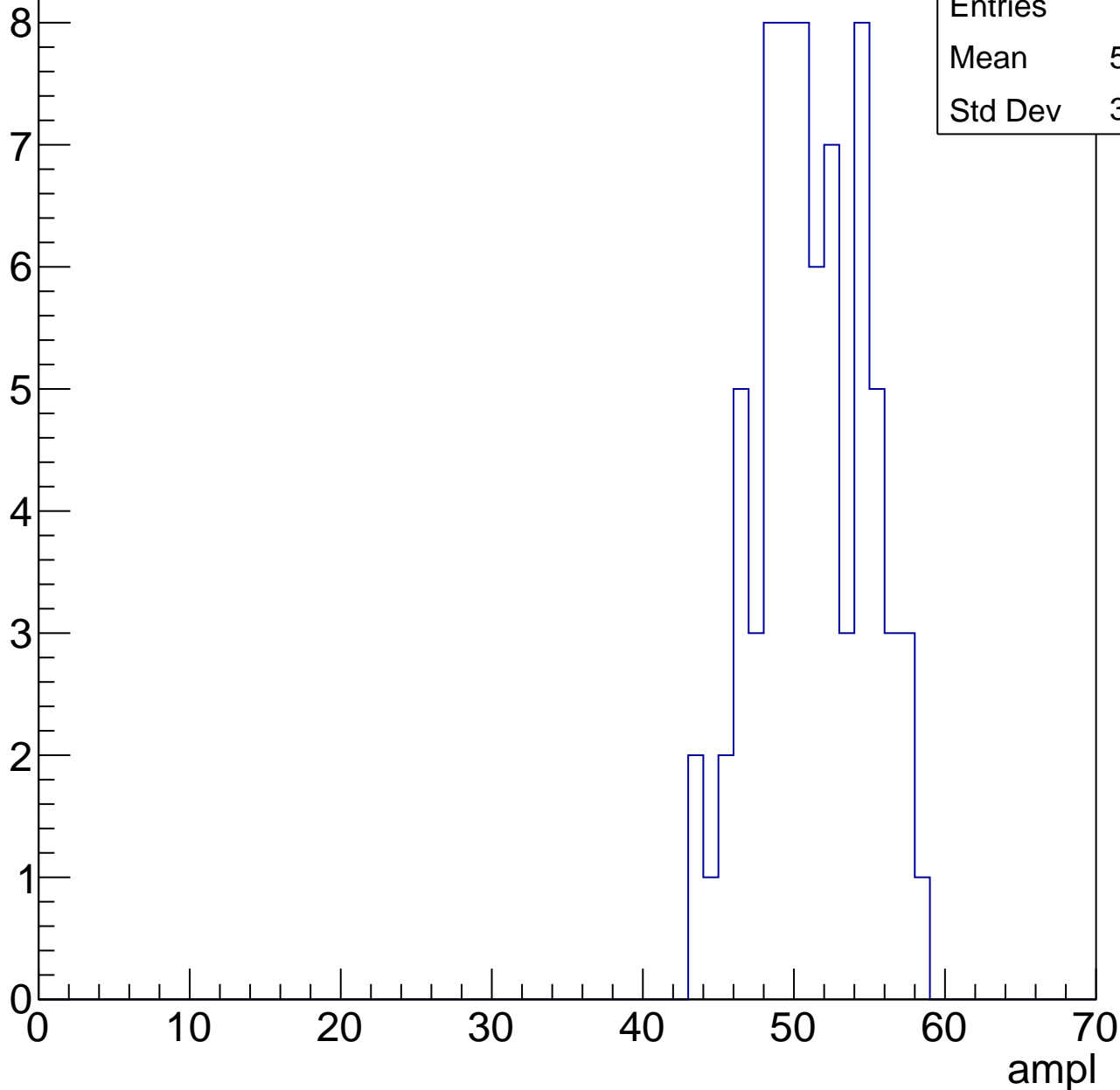


B0L001S, U11-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.68
Std Dev	3.569

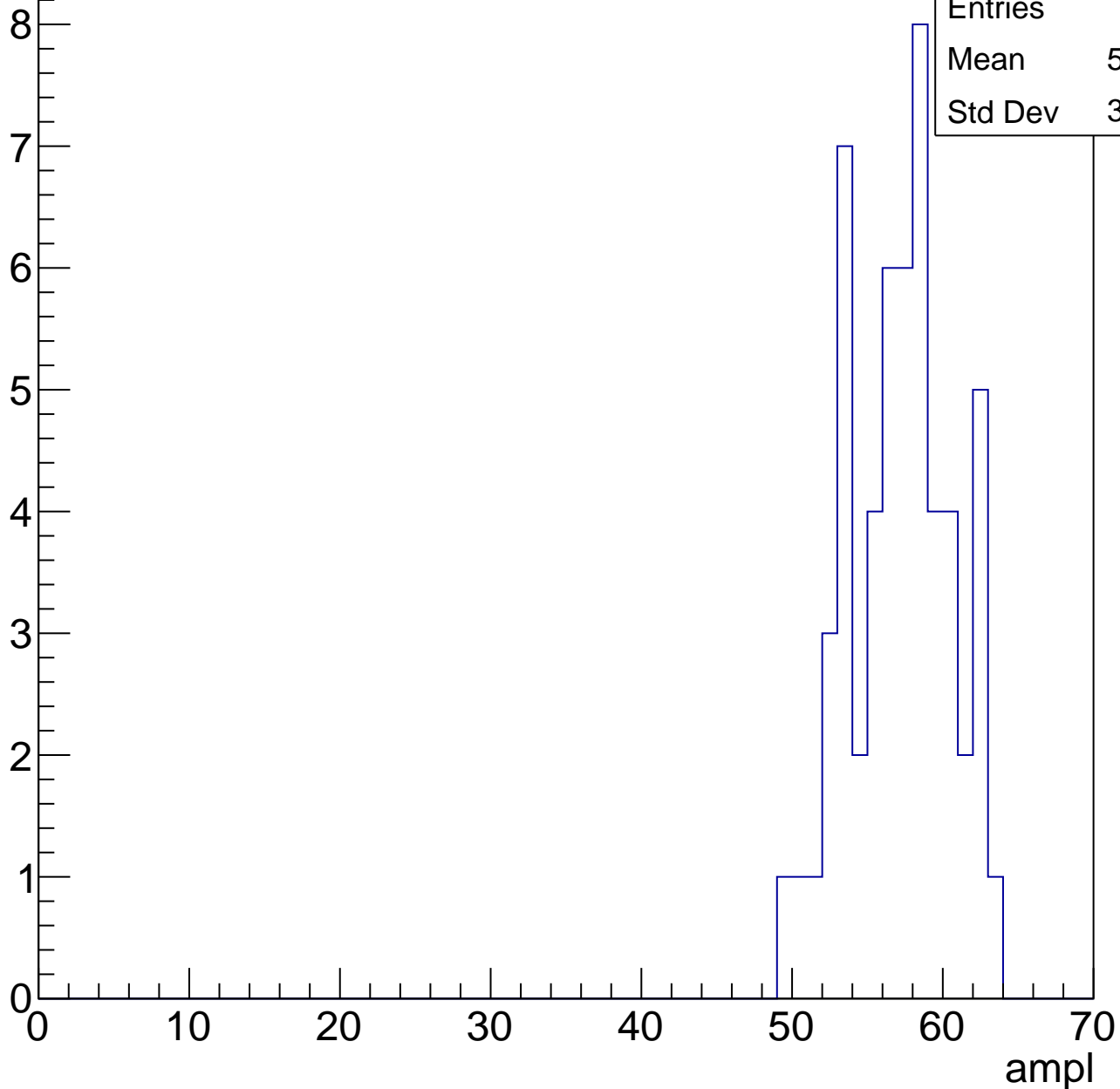


B0L001S, U11-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.69
Std Dev	3.368

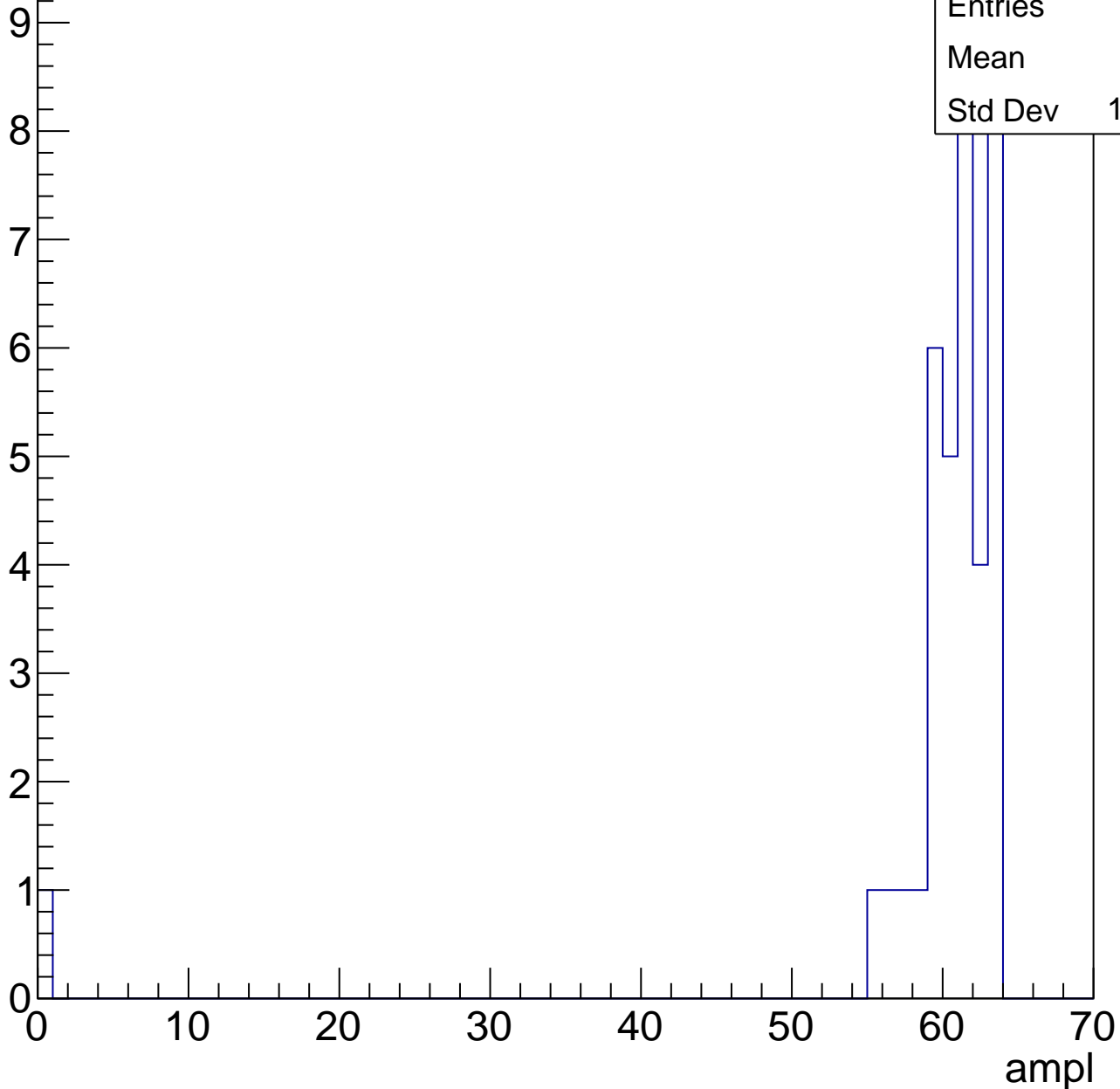


B0L001S, U11-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	59
Std Dev	10.04



B0L001S, U11-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

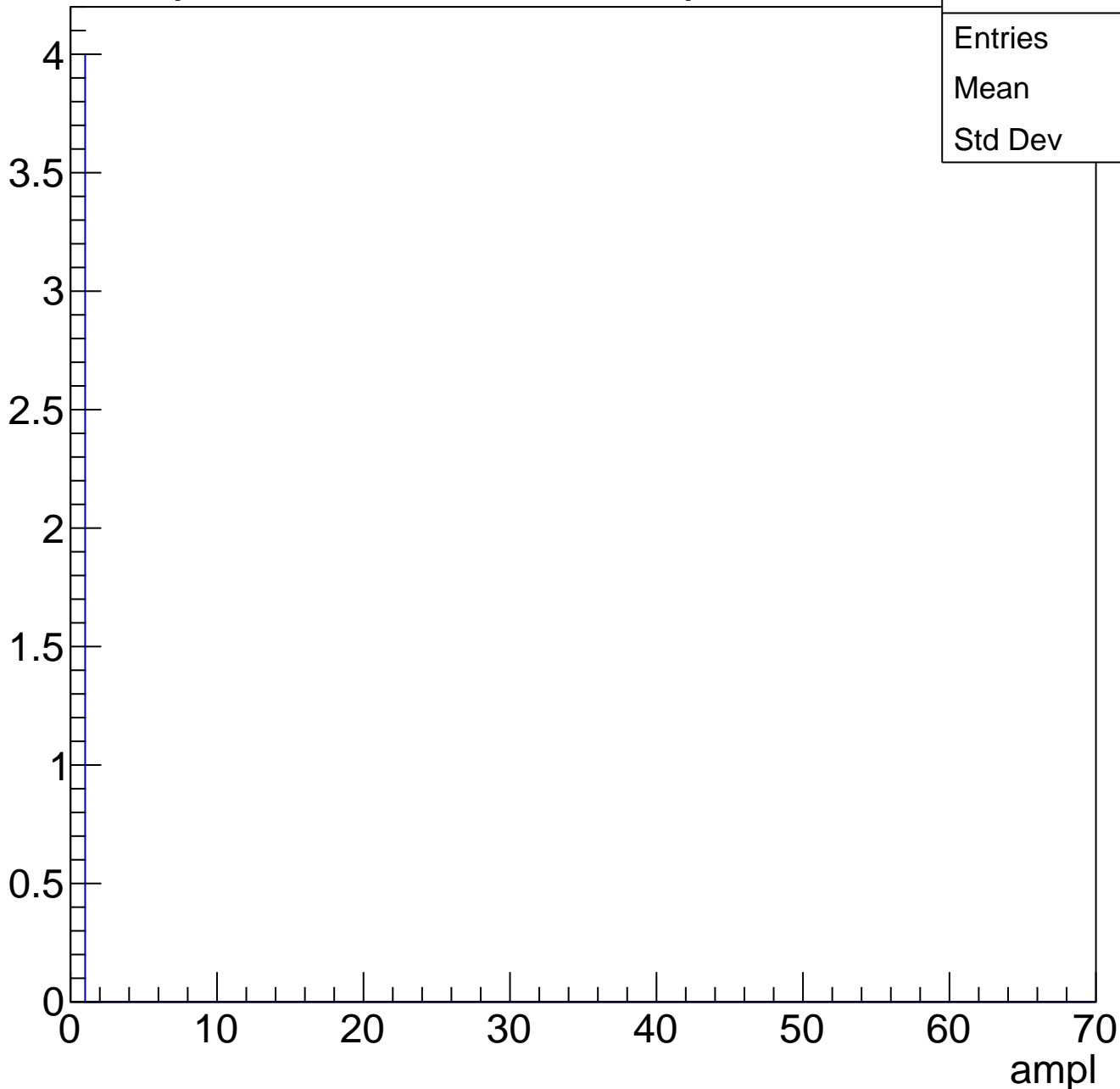
Entry



B0L001S, U11-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U11-ch92, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	29.25
Std Dev	4.568

Gaus mean : 30.2433

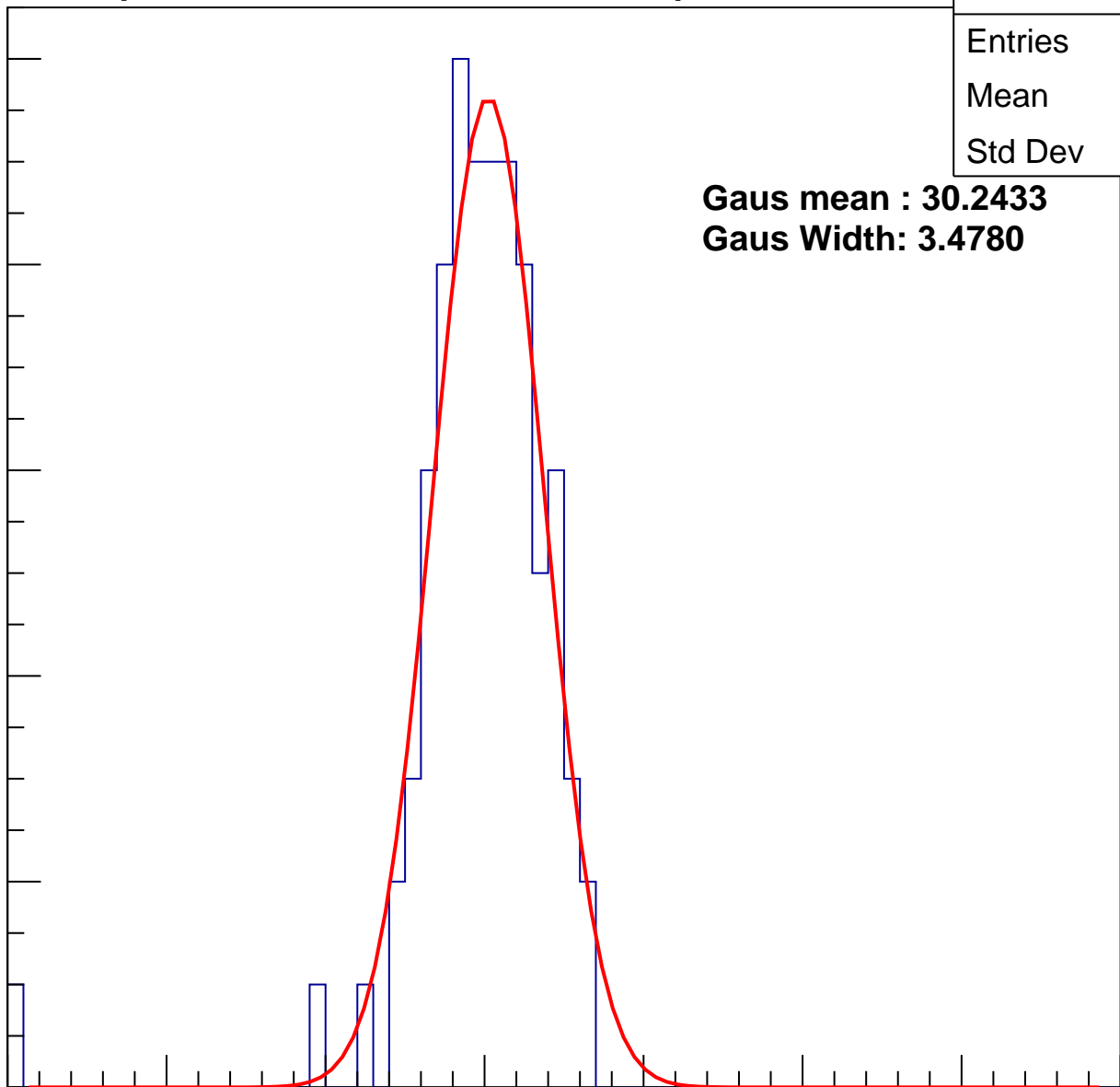
Gaus Width: 3.4780

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch92, adc1

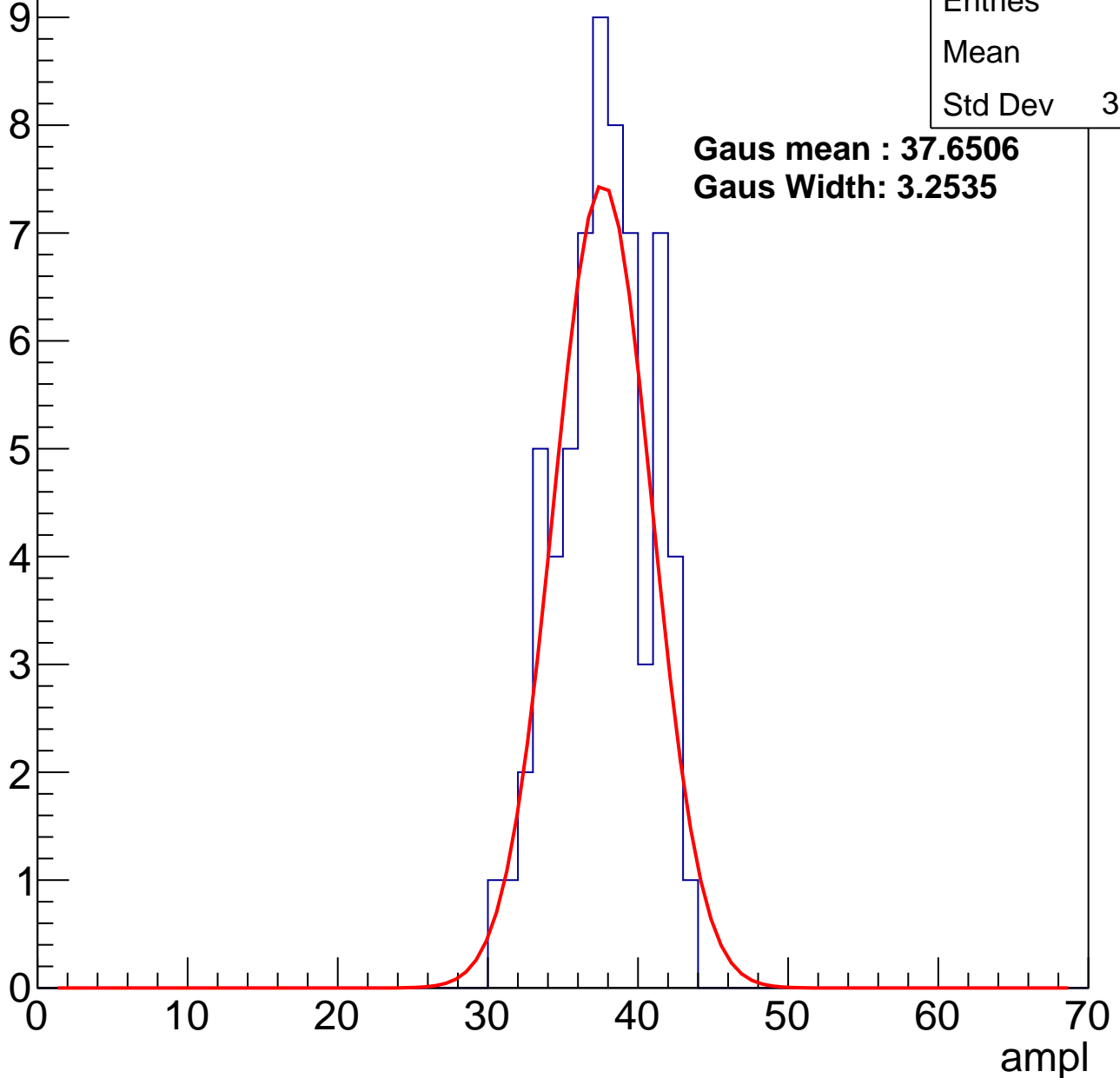
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.2
Std Dev	3.027

Gaus mean : 37.6506

Gaus Width: 3.2535



B0L001S, U11-ch92, adc2

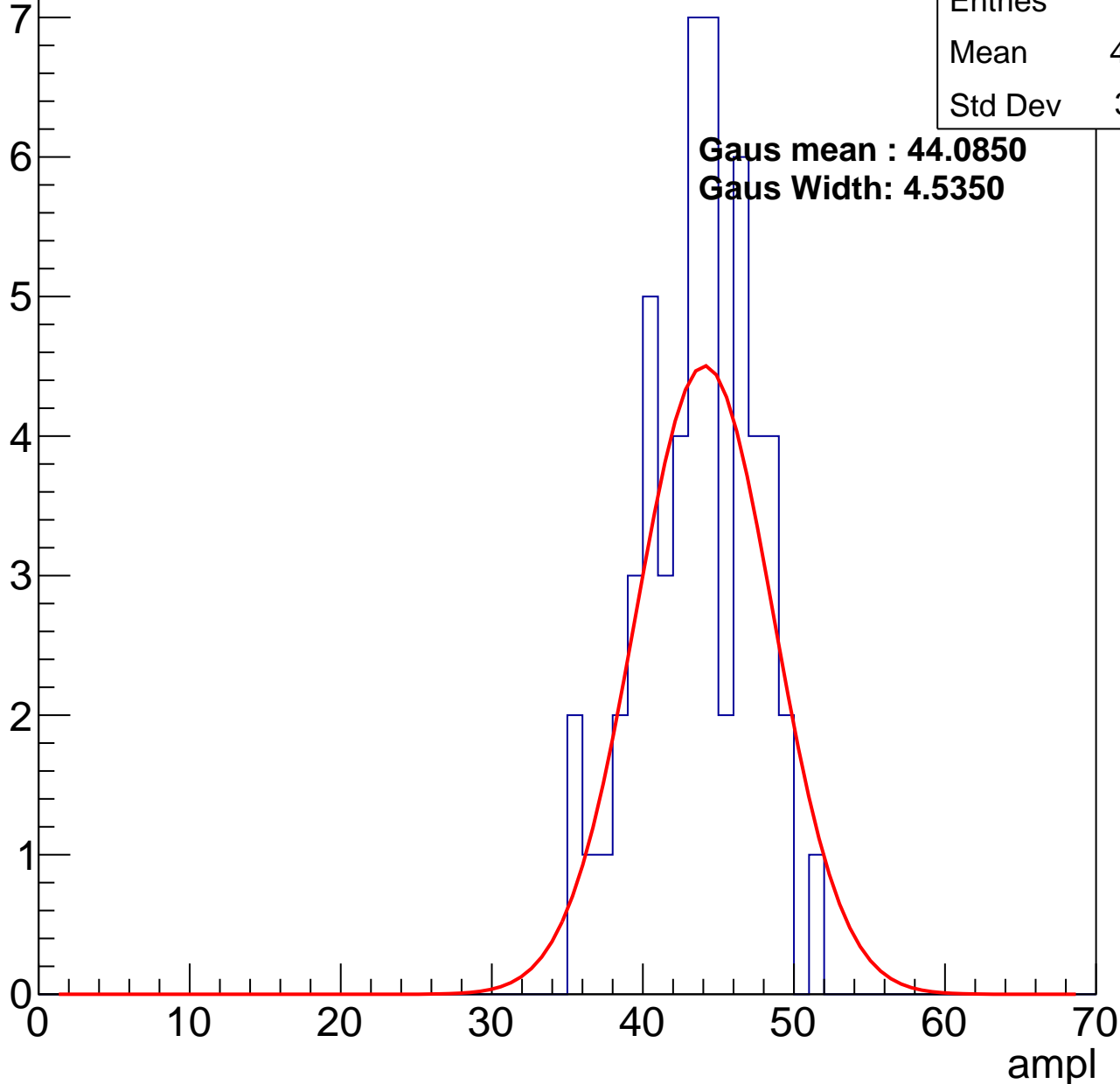
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.17
Std Dev	3.701

Gaus mean : 44.0850

Gaus Width: 4.5350

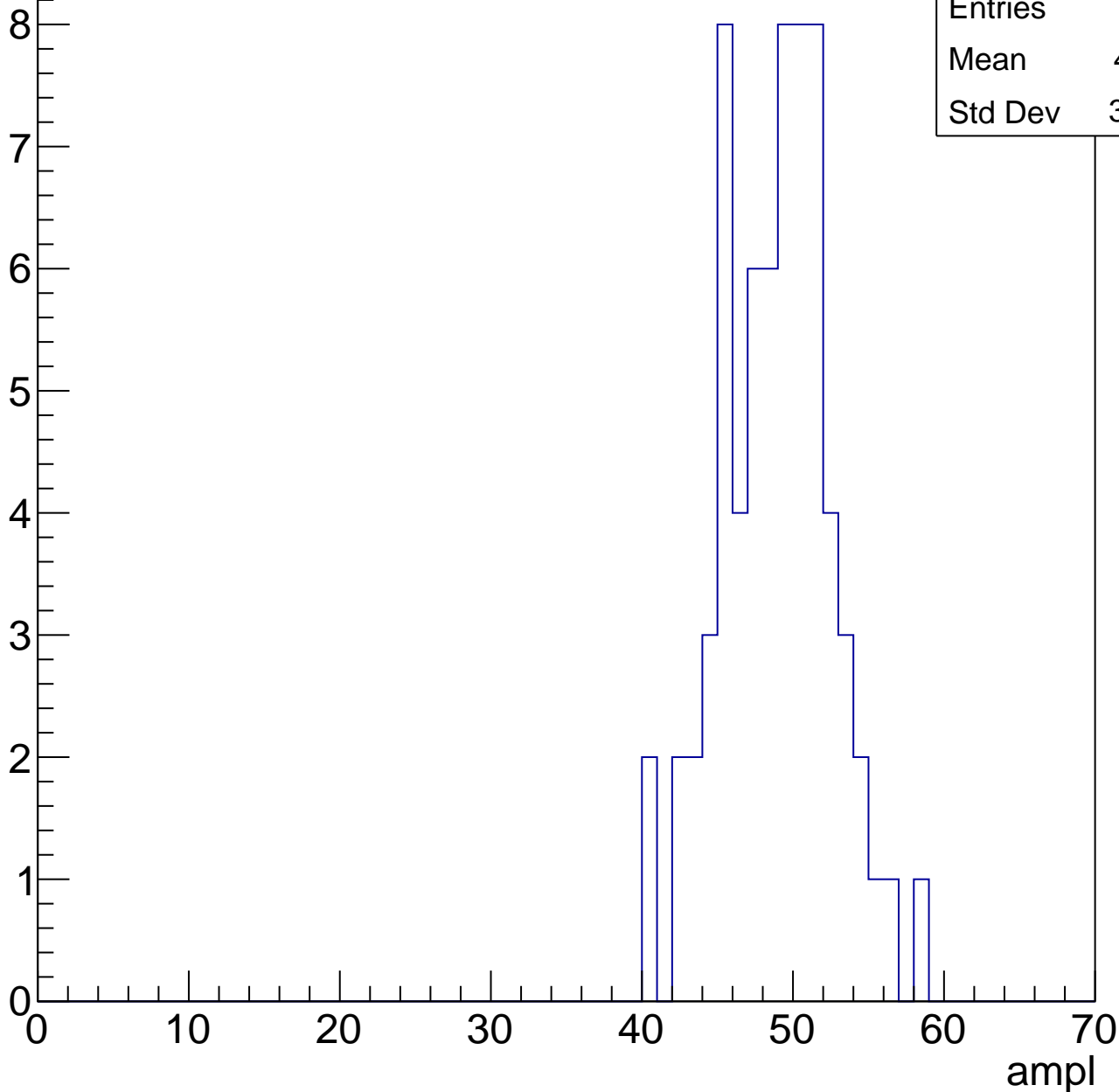


B0L001S, U11-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

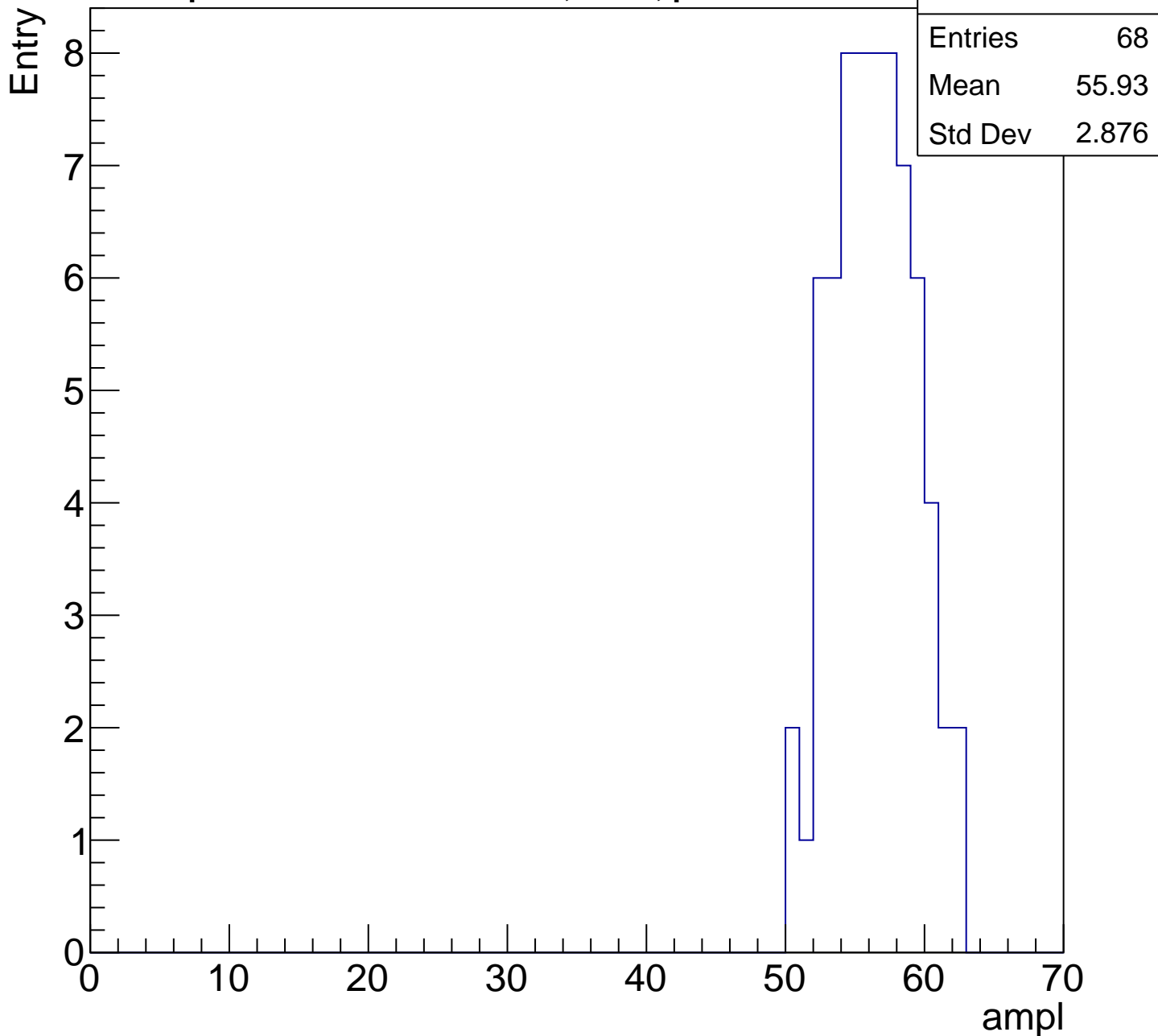
Entry

Entries	69
Mean	48.41
Std Dev	3.645



B0L001S, U11-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

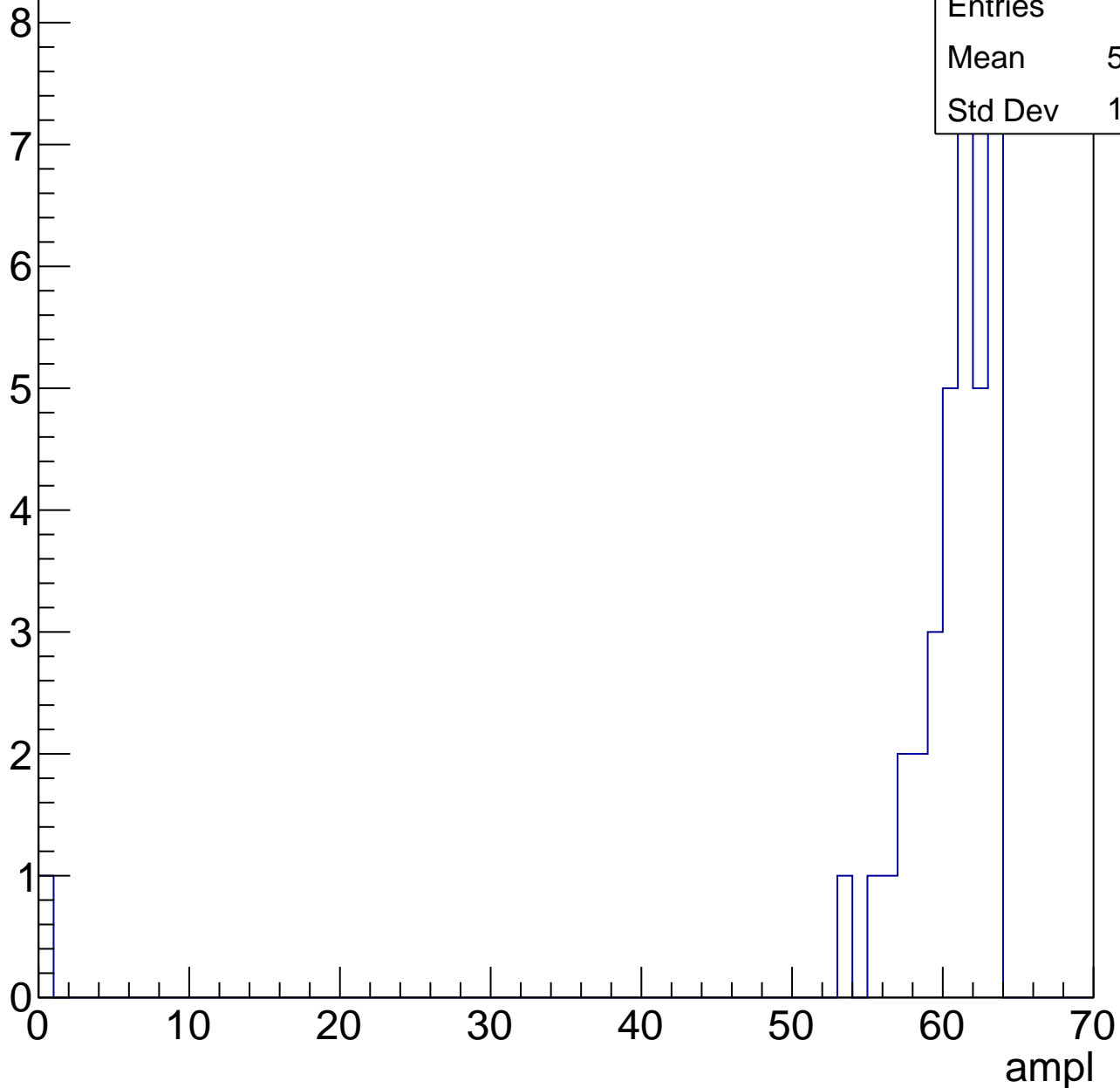


B0L001S, U11-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

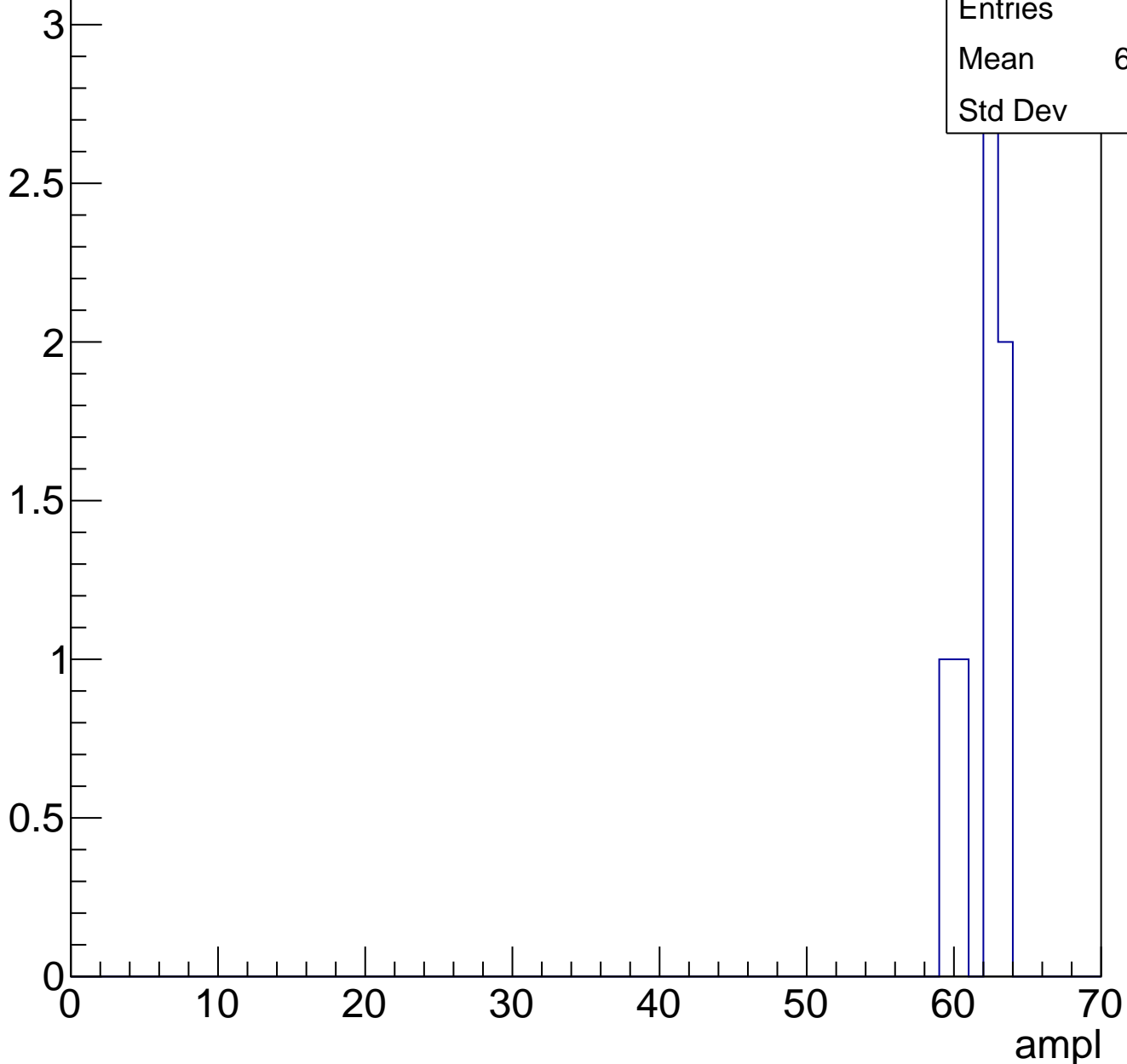
Entries	37
Mean	58.73
Std Dev	10.08



B0L001S, U11-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch93, adc0

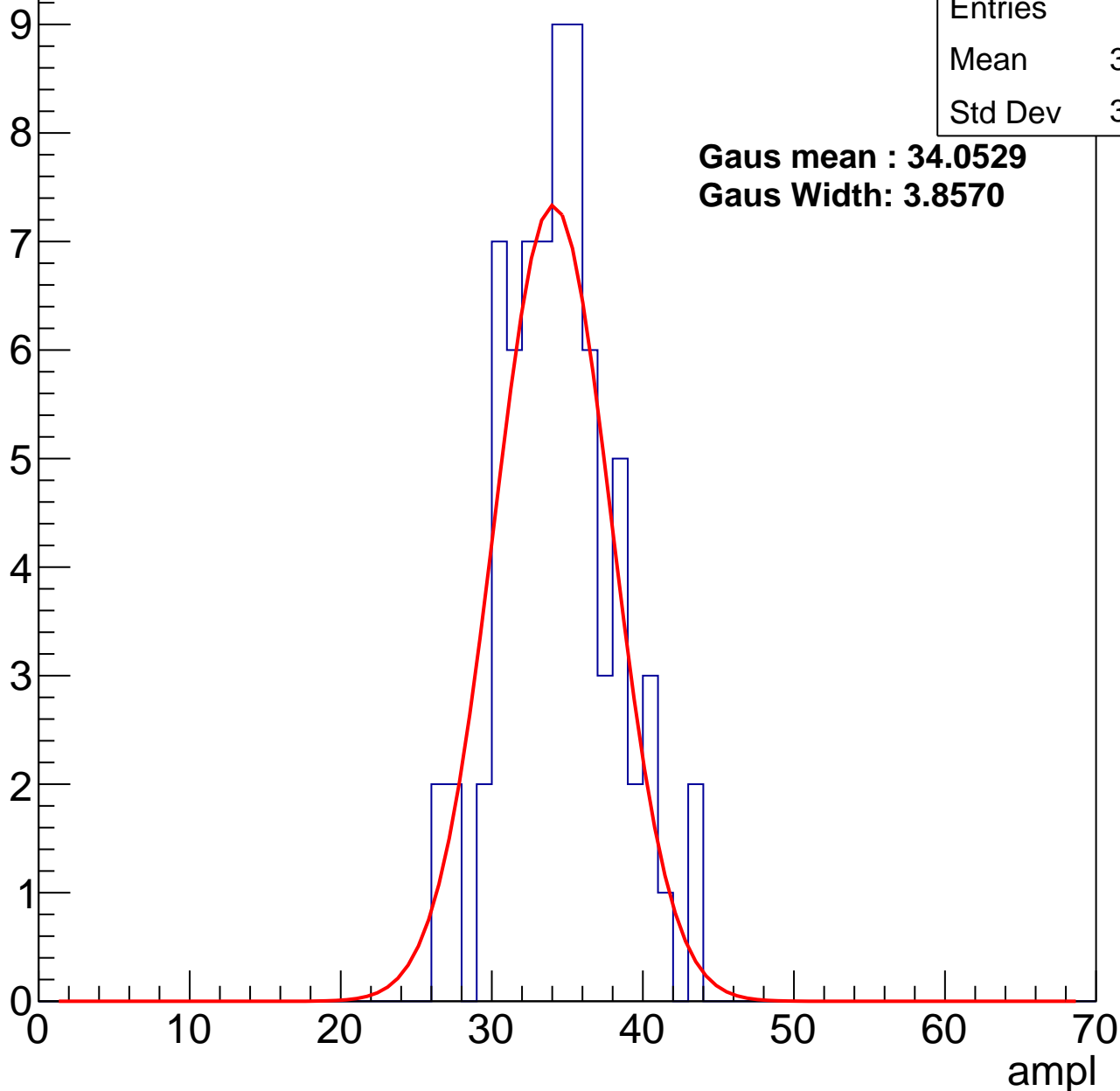
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	33.95
Std Dev	3.675

Gaus mean : 34.0529

Gaus Width: 3.8570



B0L001S, U11-ch93, adc1

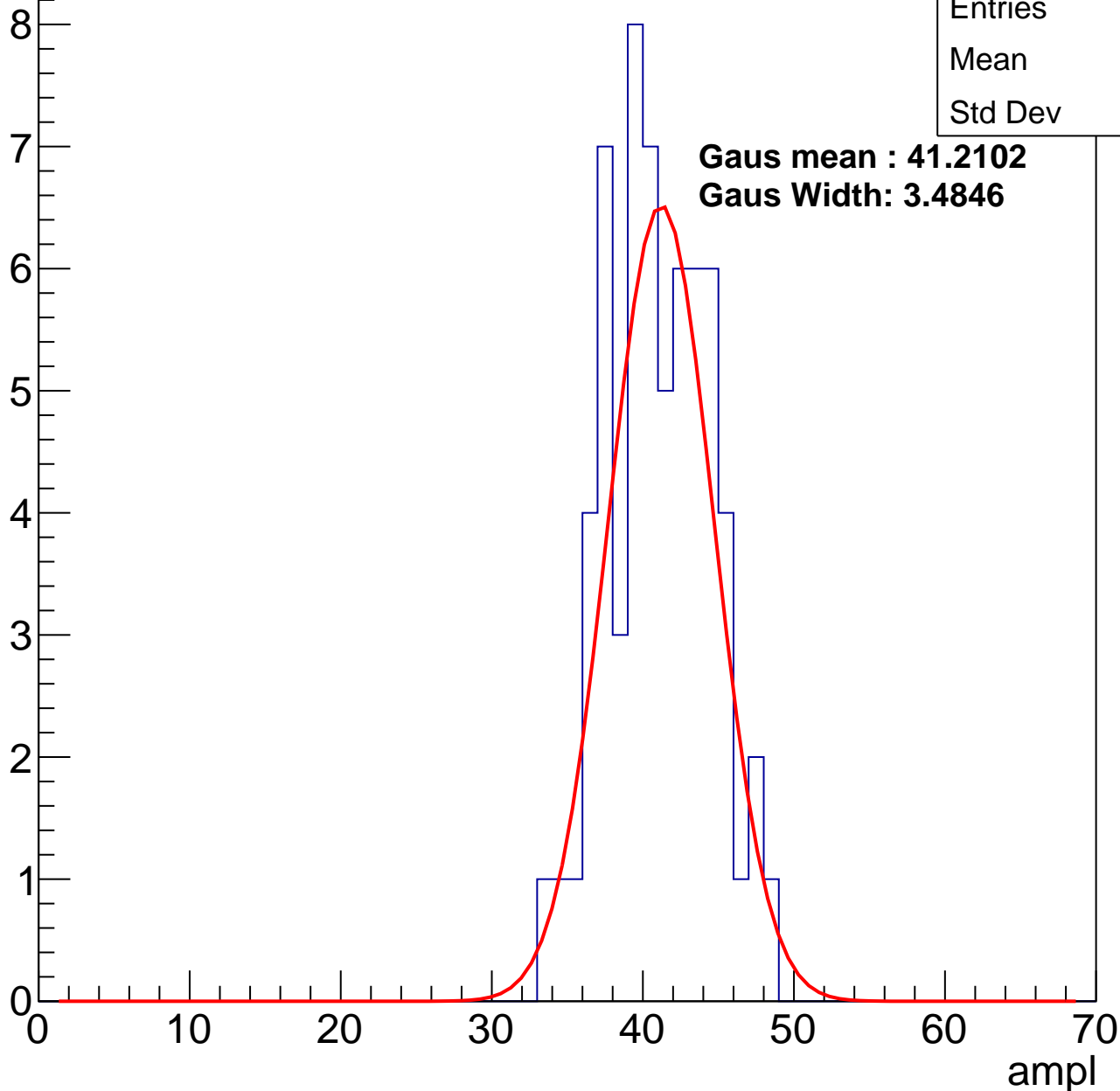
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	40.6
Std Dev	3.36

Gaus mean : 41.2102

Gaus Width: 3.4846



B0L001S, U11-ch93, adc2

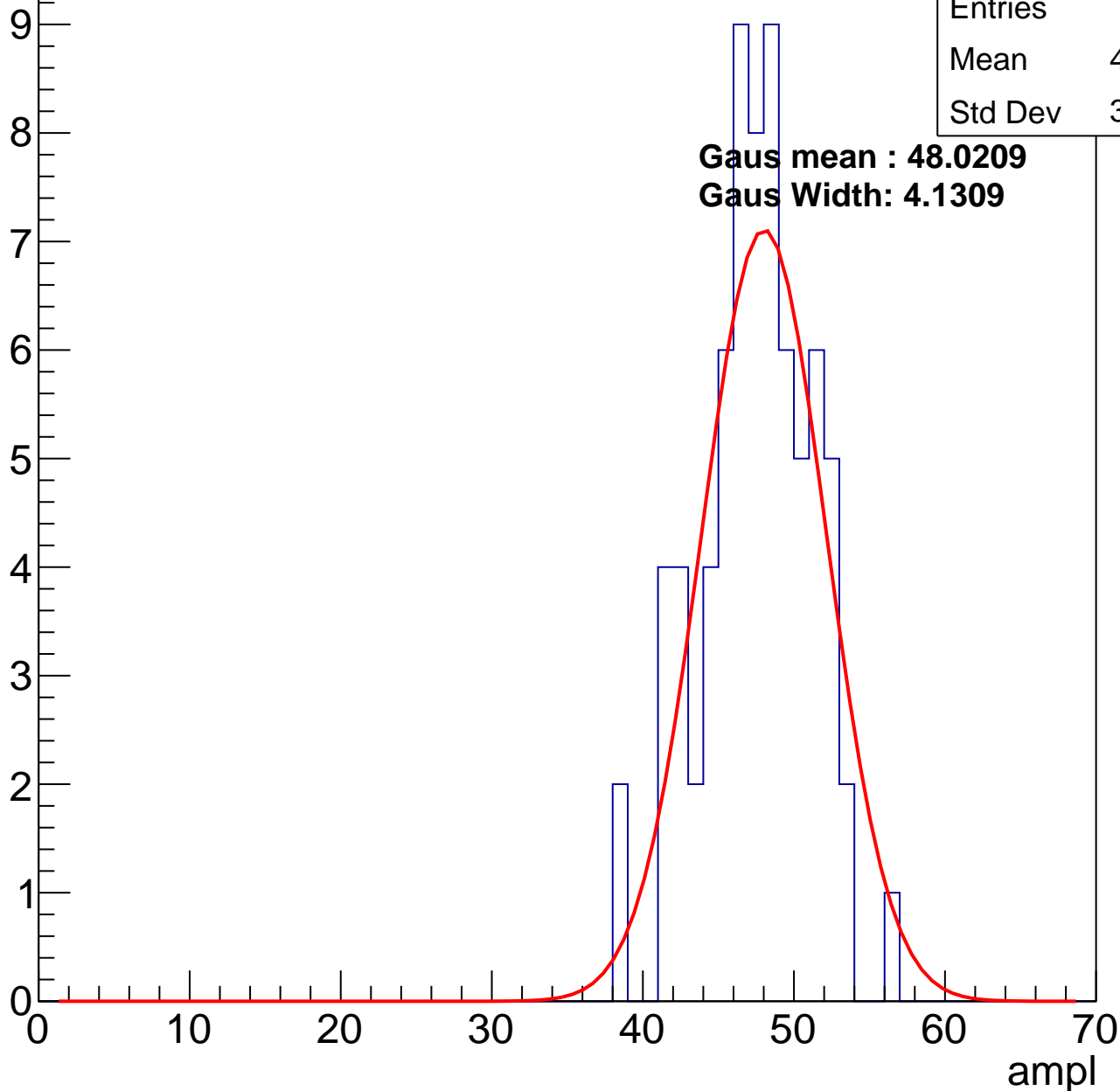
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	47.04
Std Dev	3.624

Gaus mean : 48.0209

Gaus Width: 4.1309

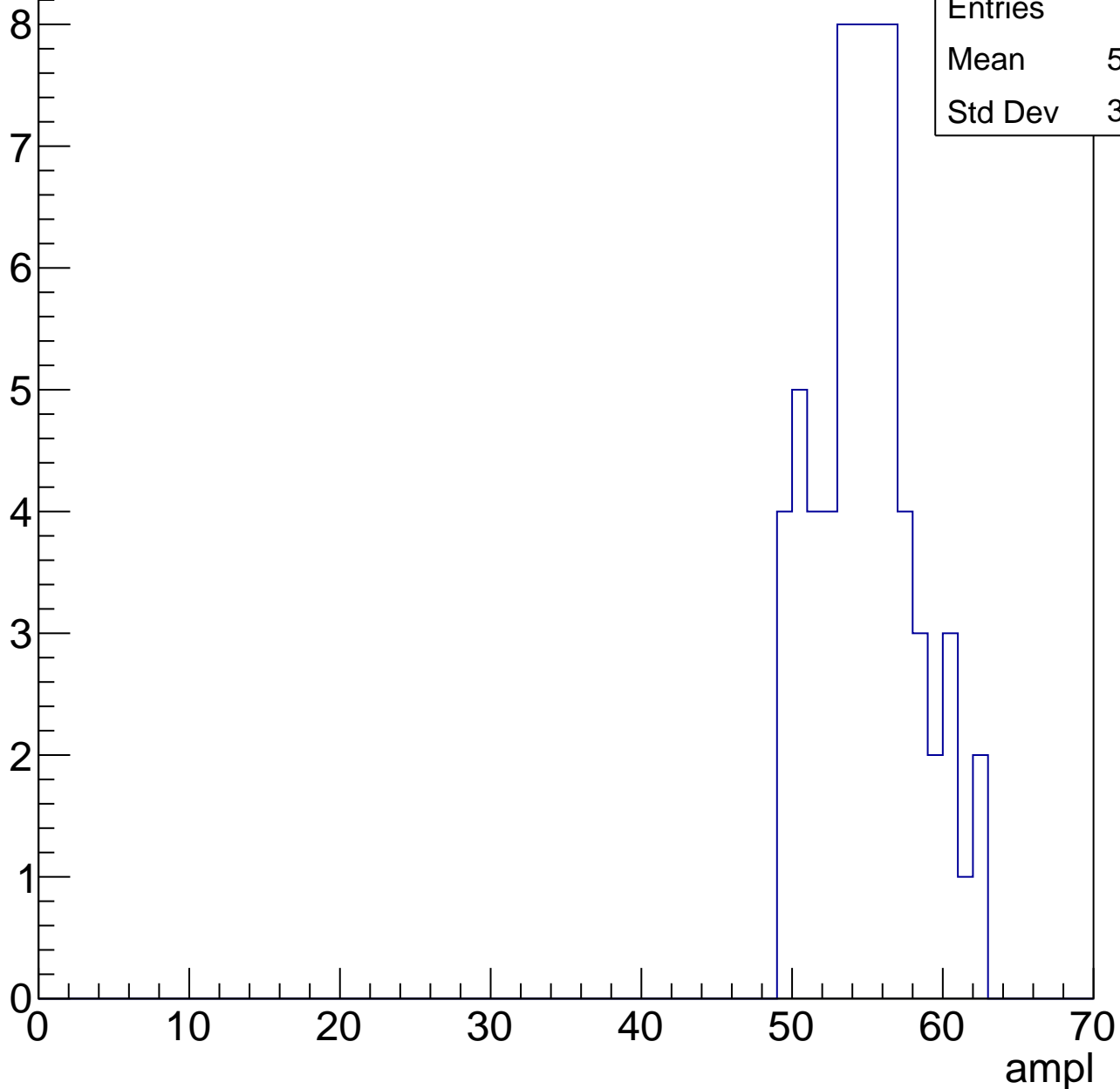


B0L001S, U11-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	54.48
Std Dev	3.269

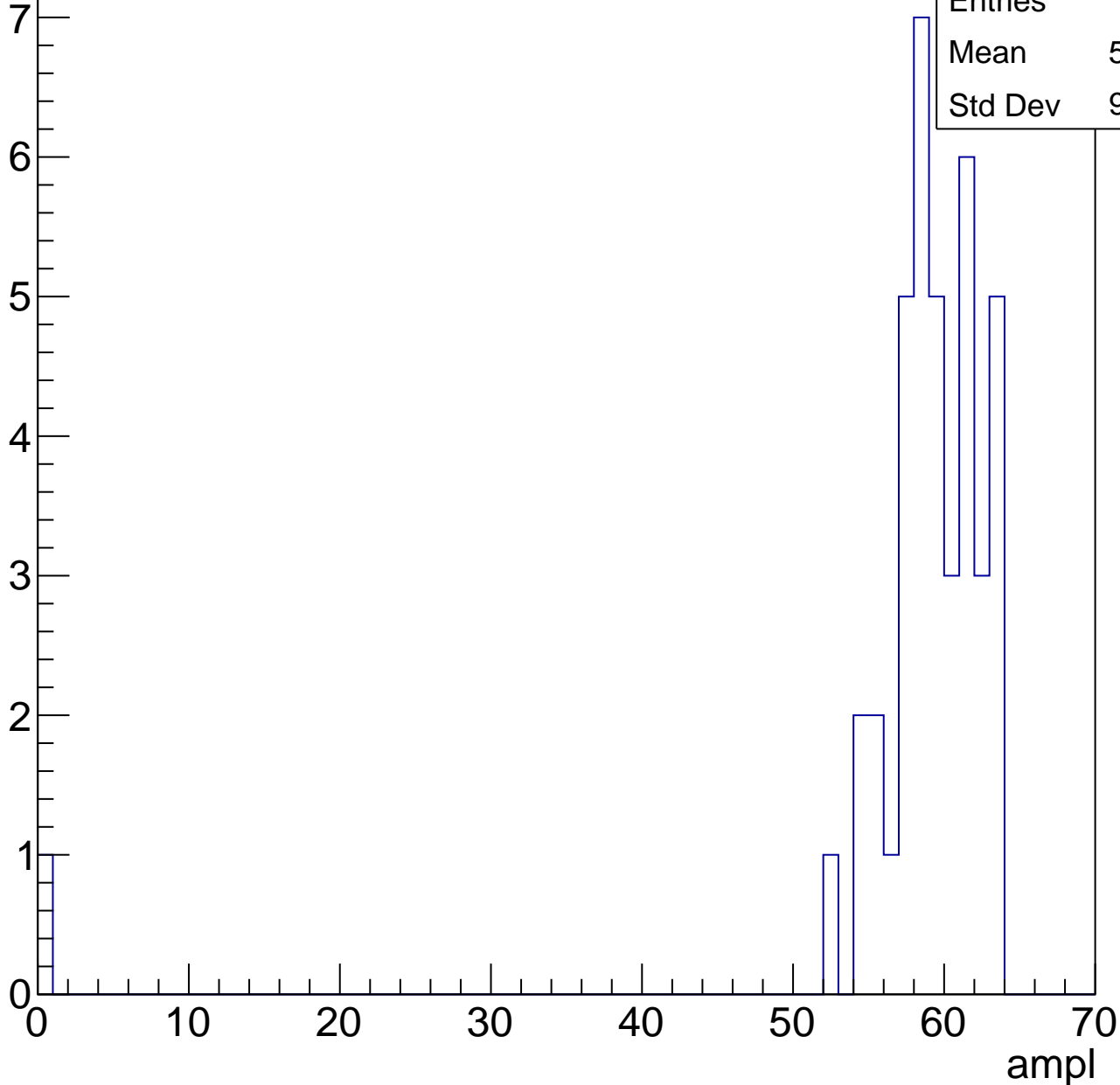


B0L001S, U11-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	57.54
Std Dev	9.492

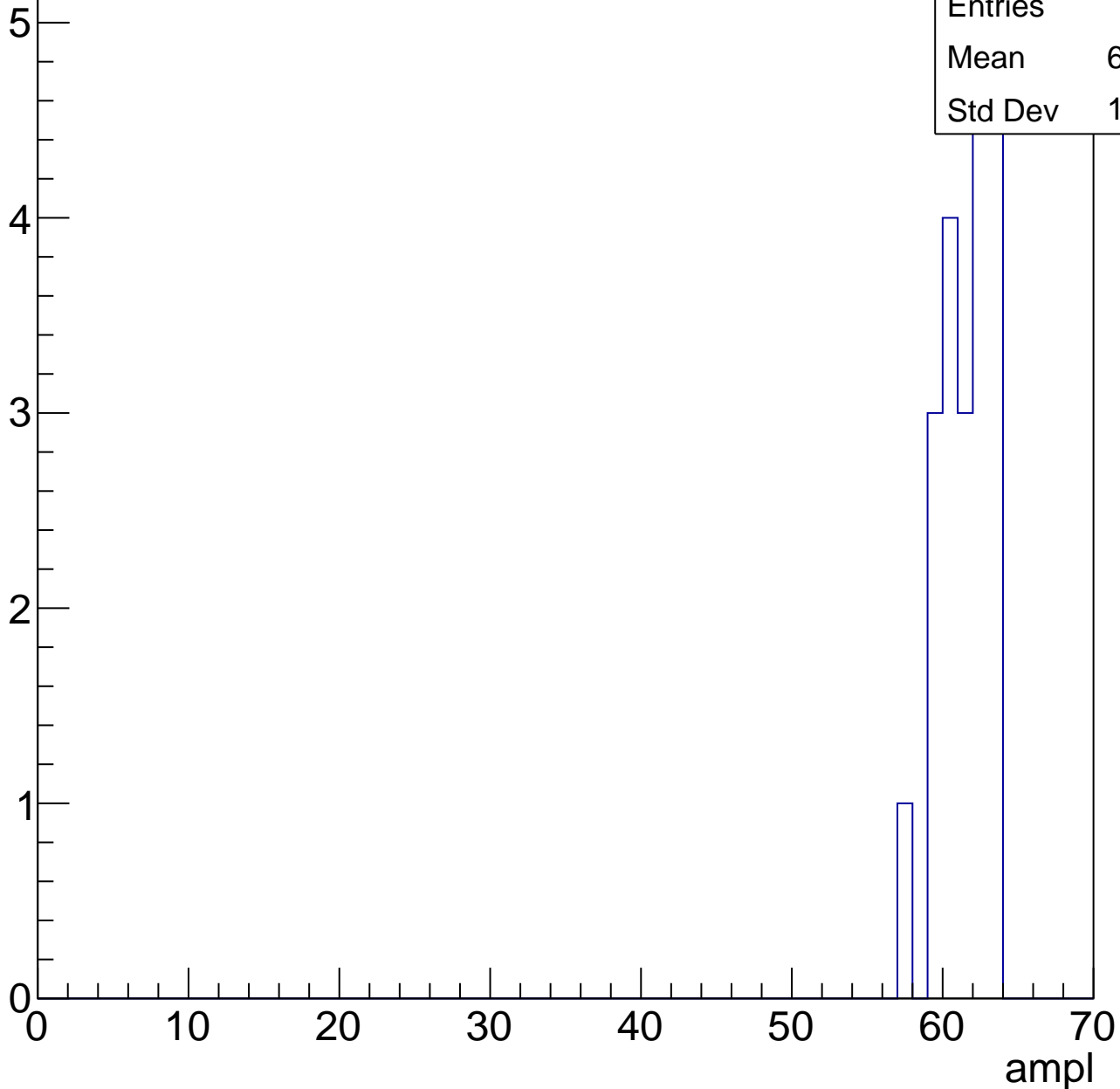


B0L001S, U11-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	61.05
Std Dev	1.647



B0L001S, U11-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch94, adc0

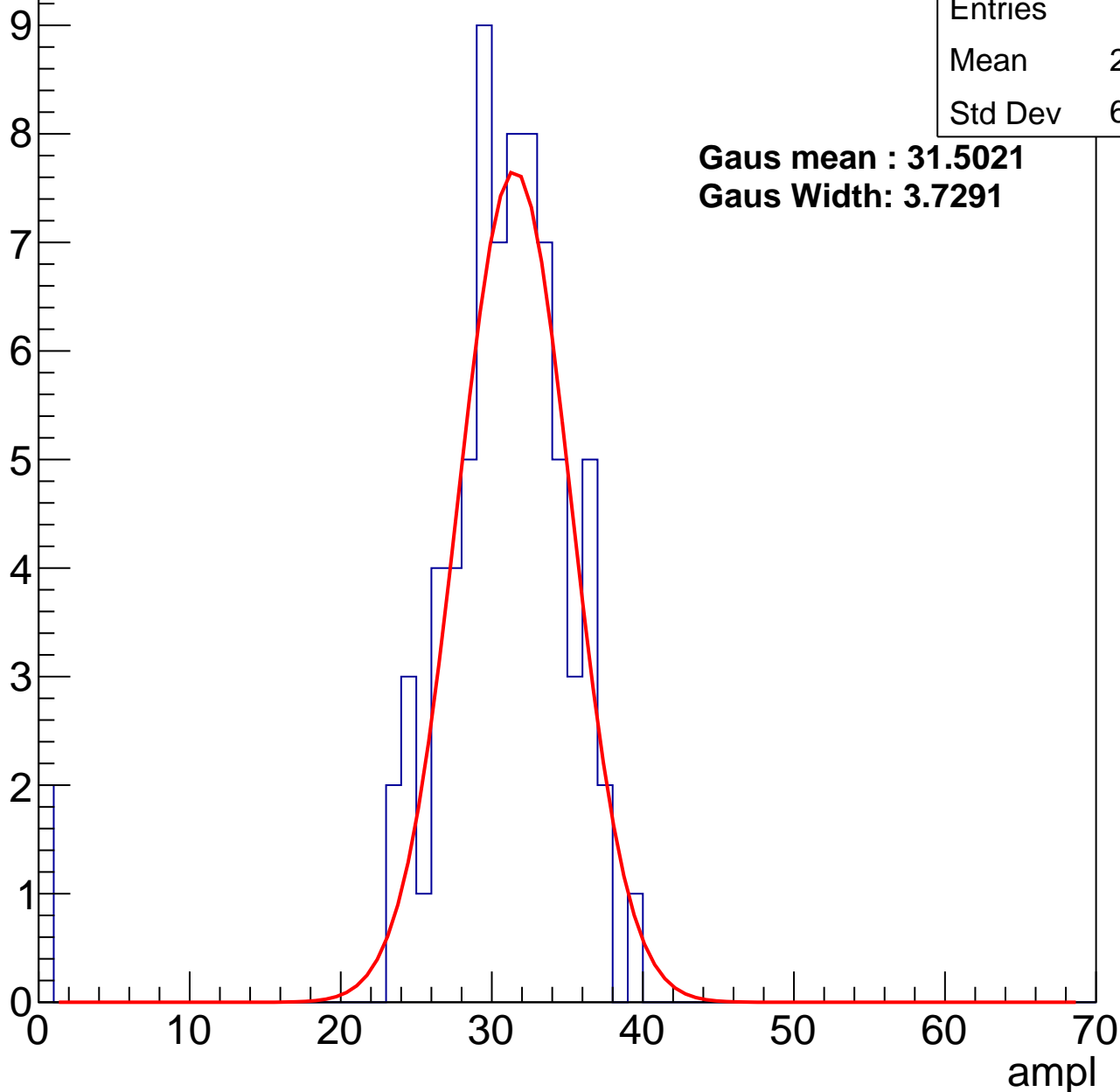
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	29.86
Std Dev	6.043

Gaus mean : 31.5021

Gaus Width: 3.7291



B0L001S, U11-ch94, adc1

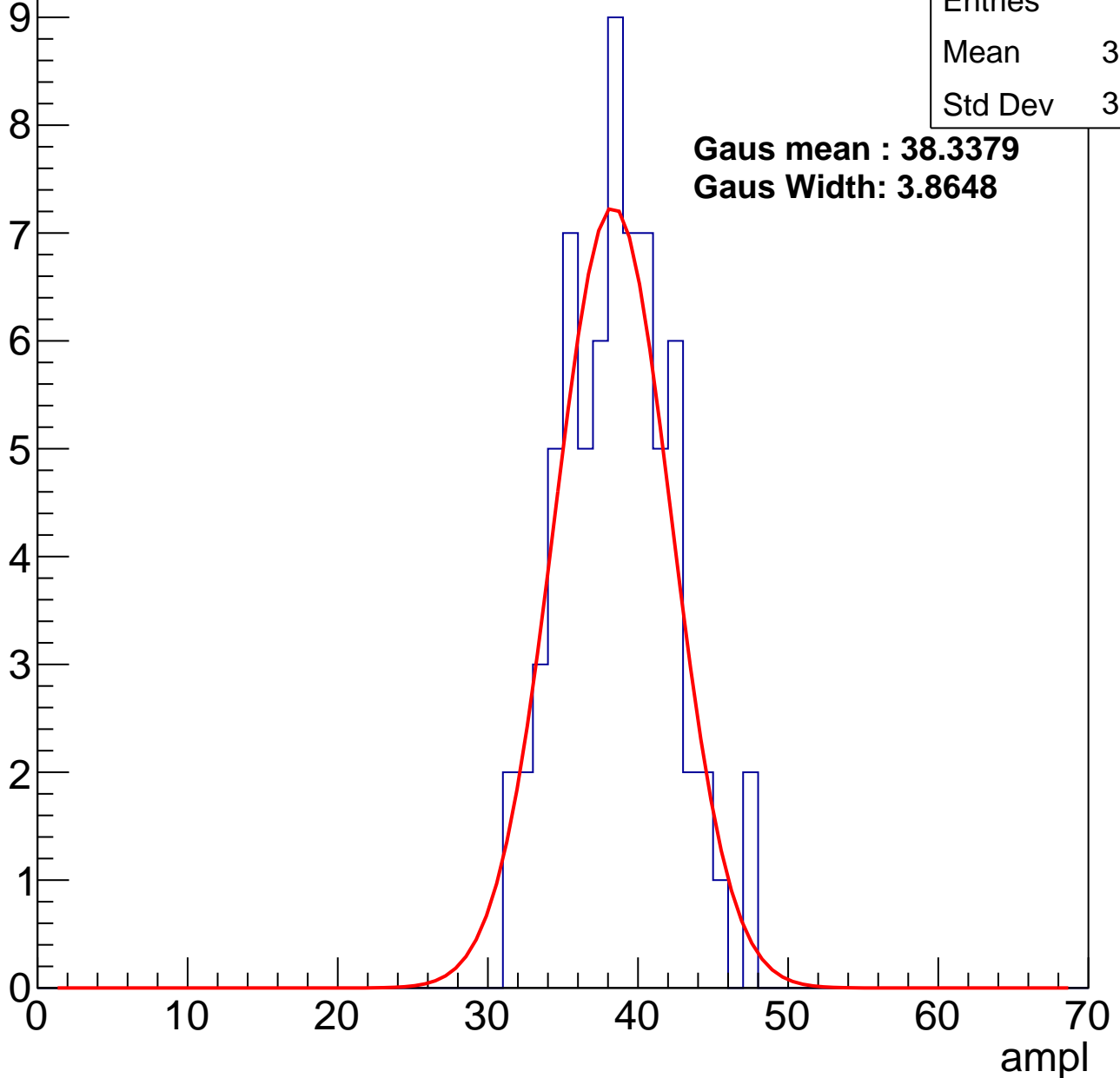
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.13
Std Dev	3.603

Gaus mean : 38.3379

Gaus Width: 3.8648



B0L001S, U11-ch94, adc2

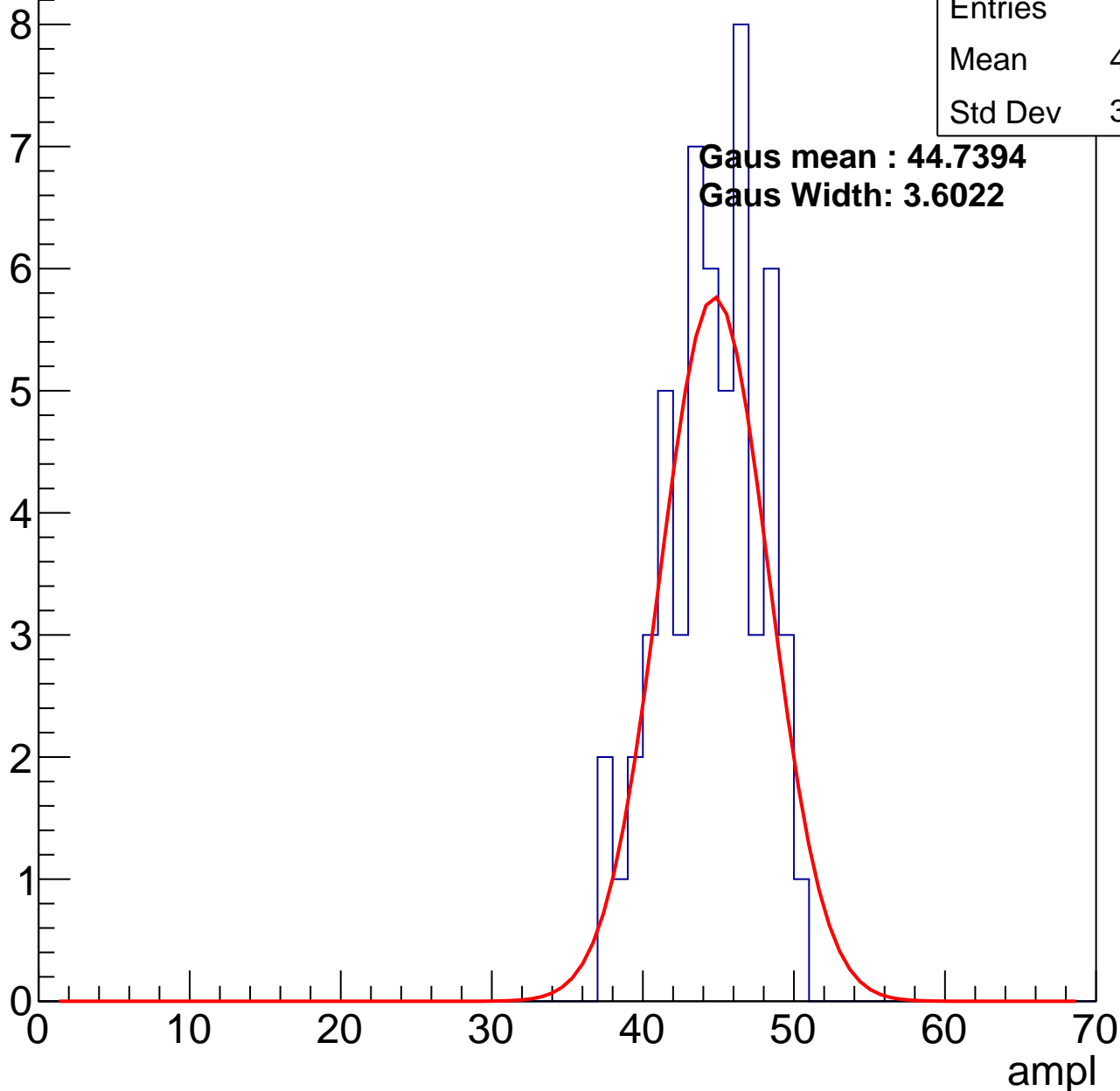
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.09
Std Dev	3.209

Gaus mean : 44.7394

Gaus Width: 3.6022

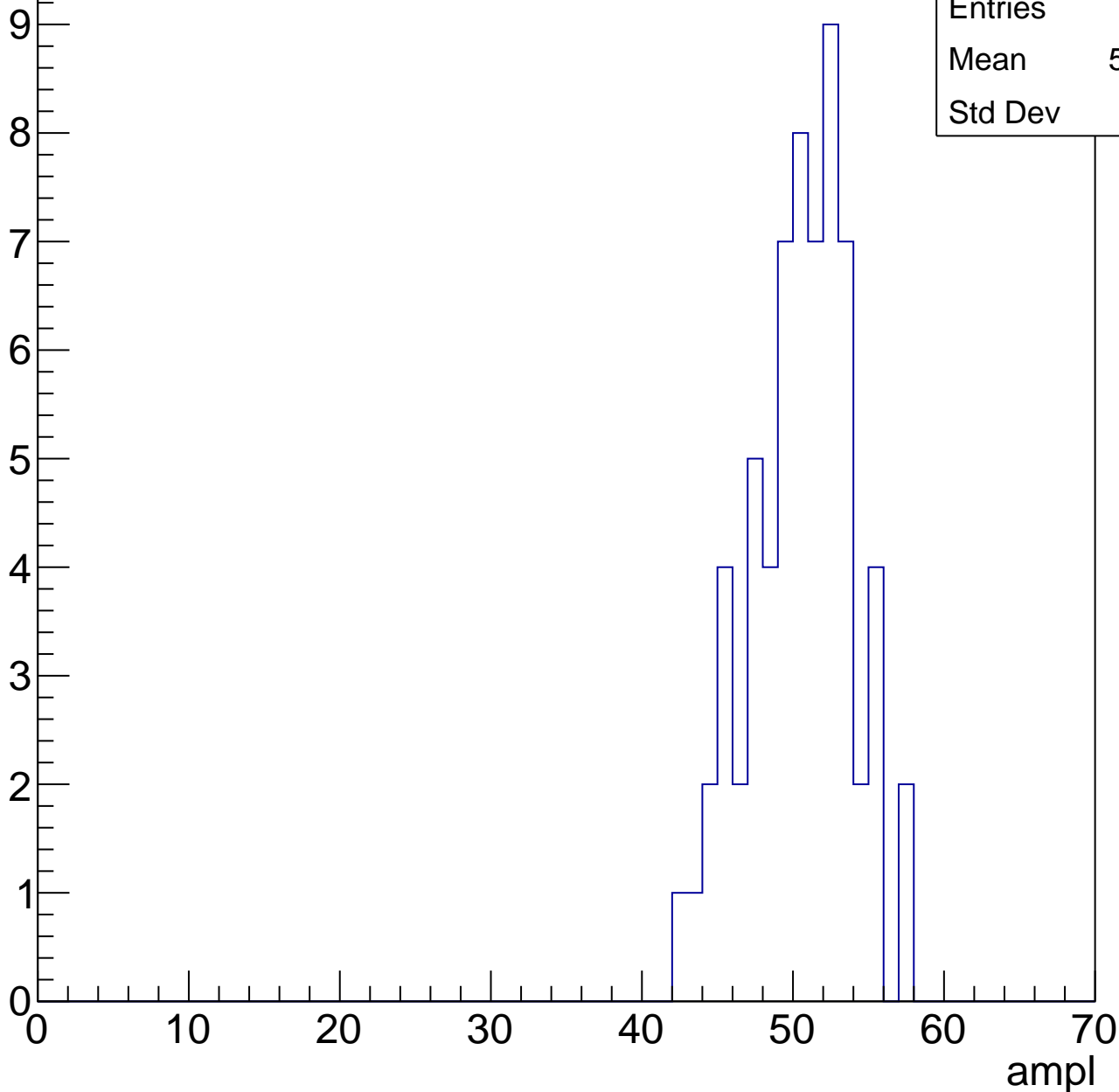


B0L001S, U11-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.05
Std Dev	3.33

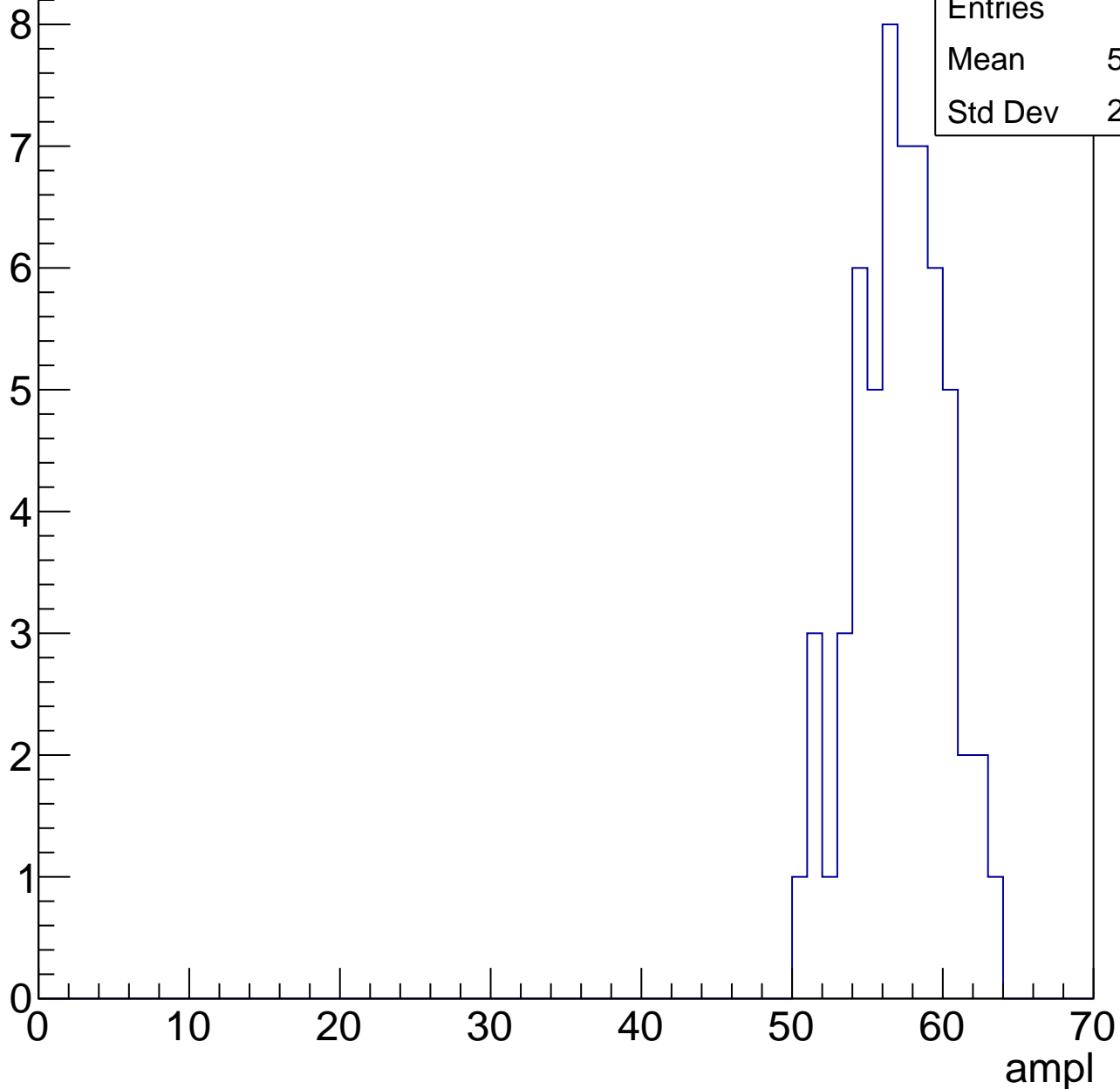


B0L001S, U11-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.65
Std Dev	2.959

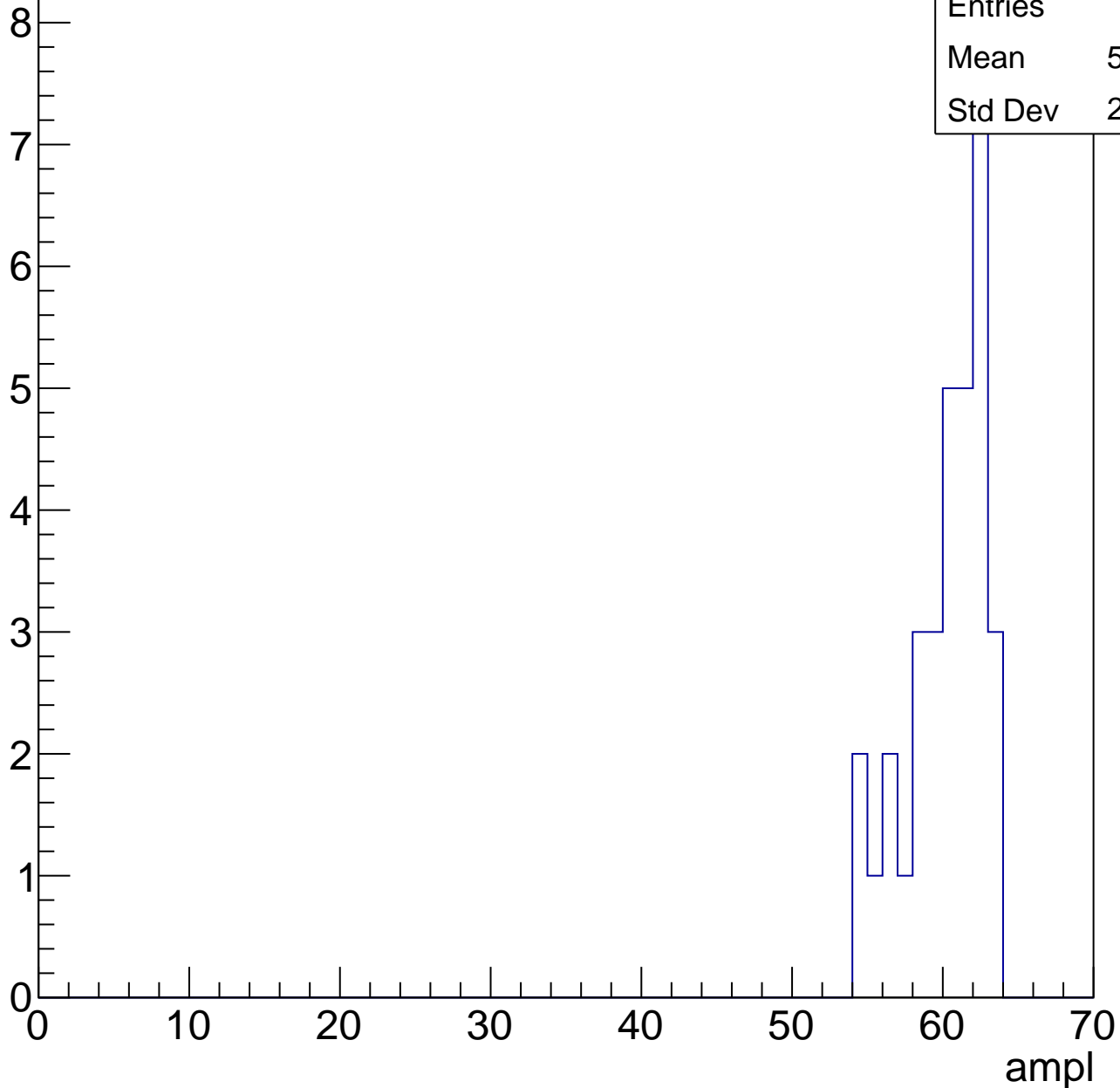


B0L001S, U11-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.79
Std Dev	2.556

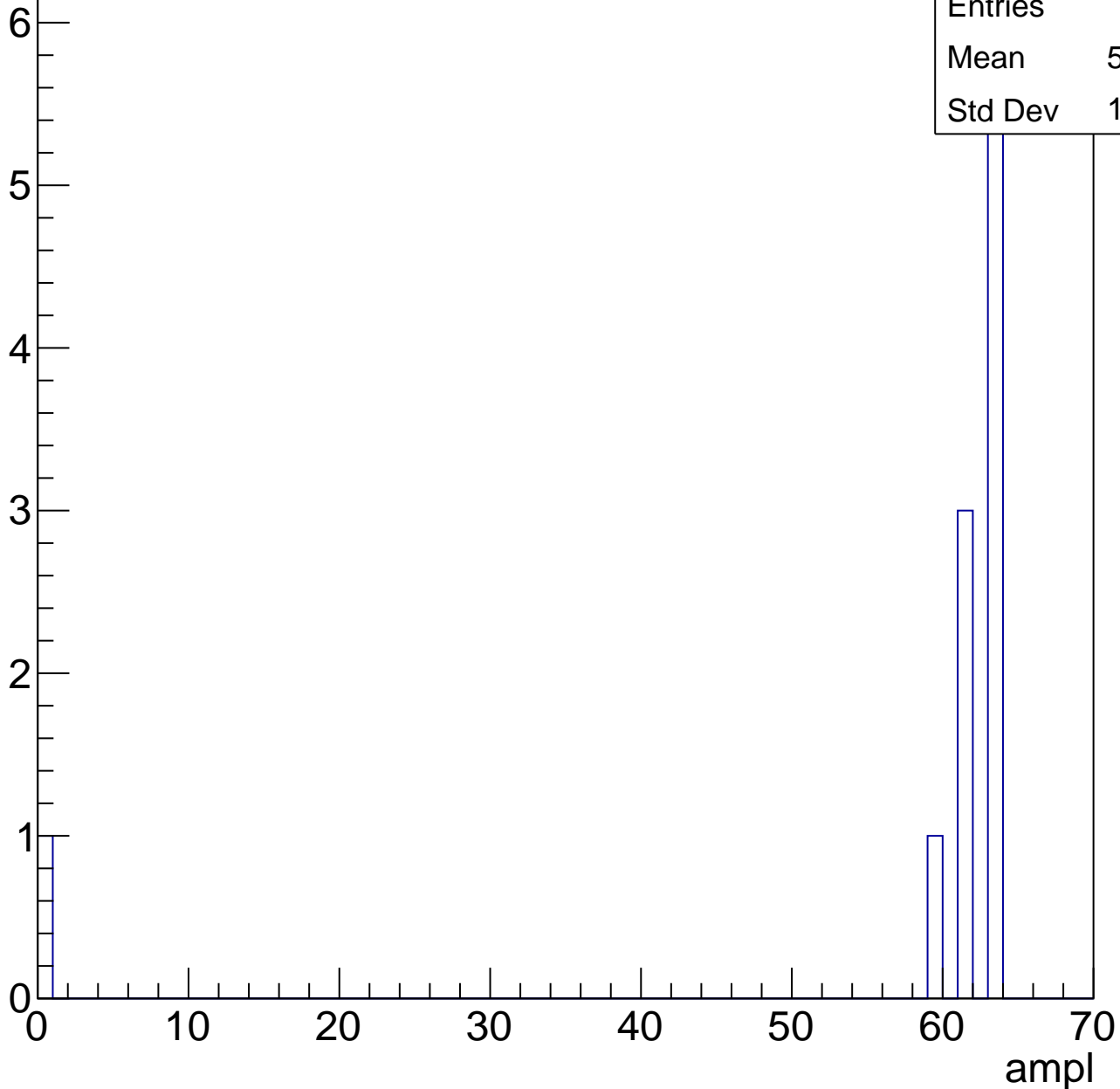


B0L001S, U11-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	11
Mean	56.36
Std Dev	17.87



B0L001S, U11-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch95, adc0

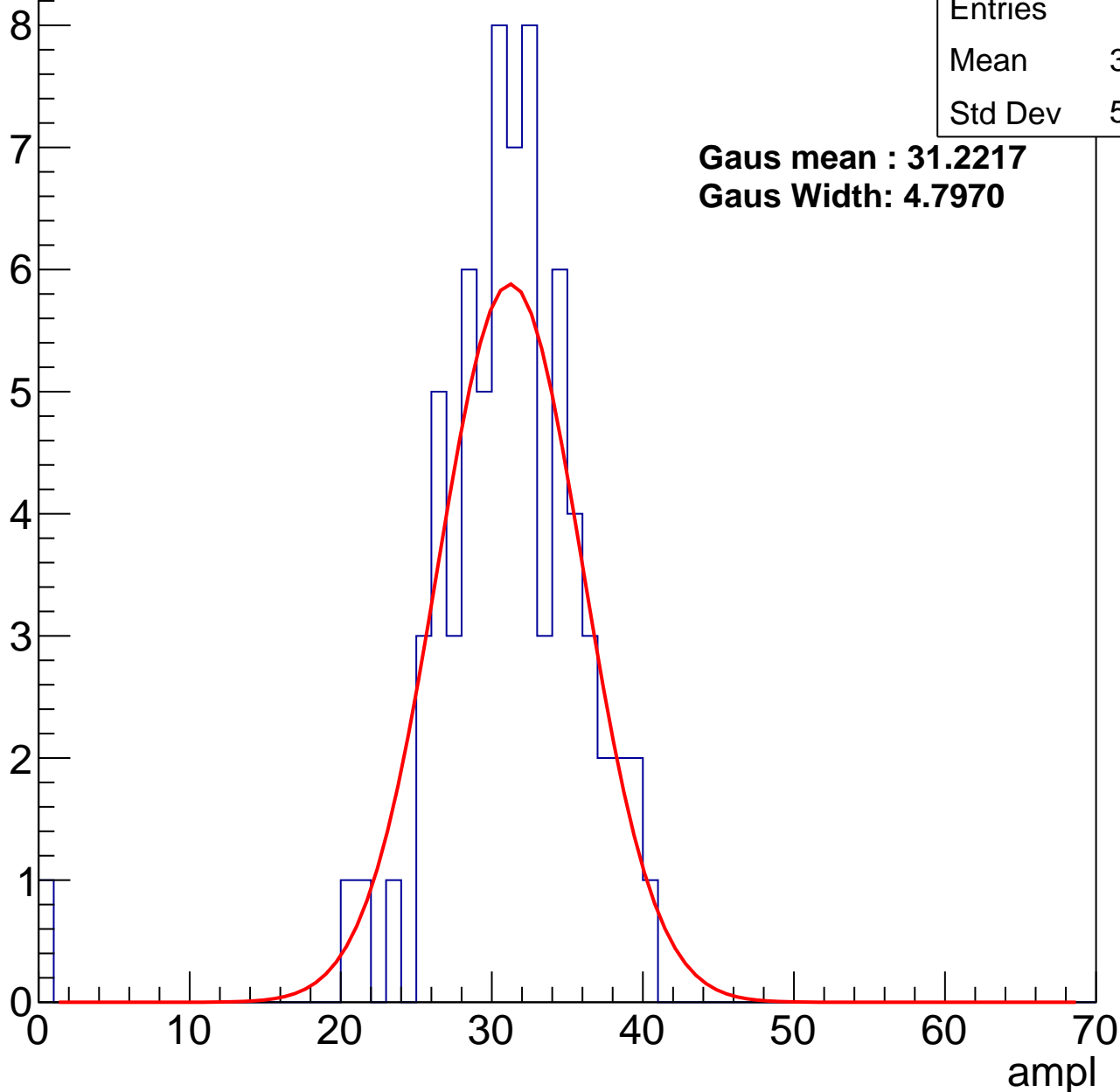
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	30.49
Std Dev	5.505

Gaus mean : 31.2217

Gaus Width: 4.7970



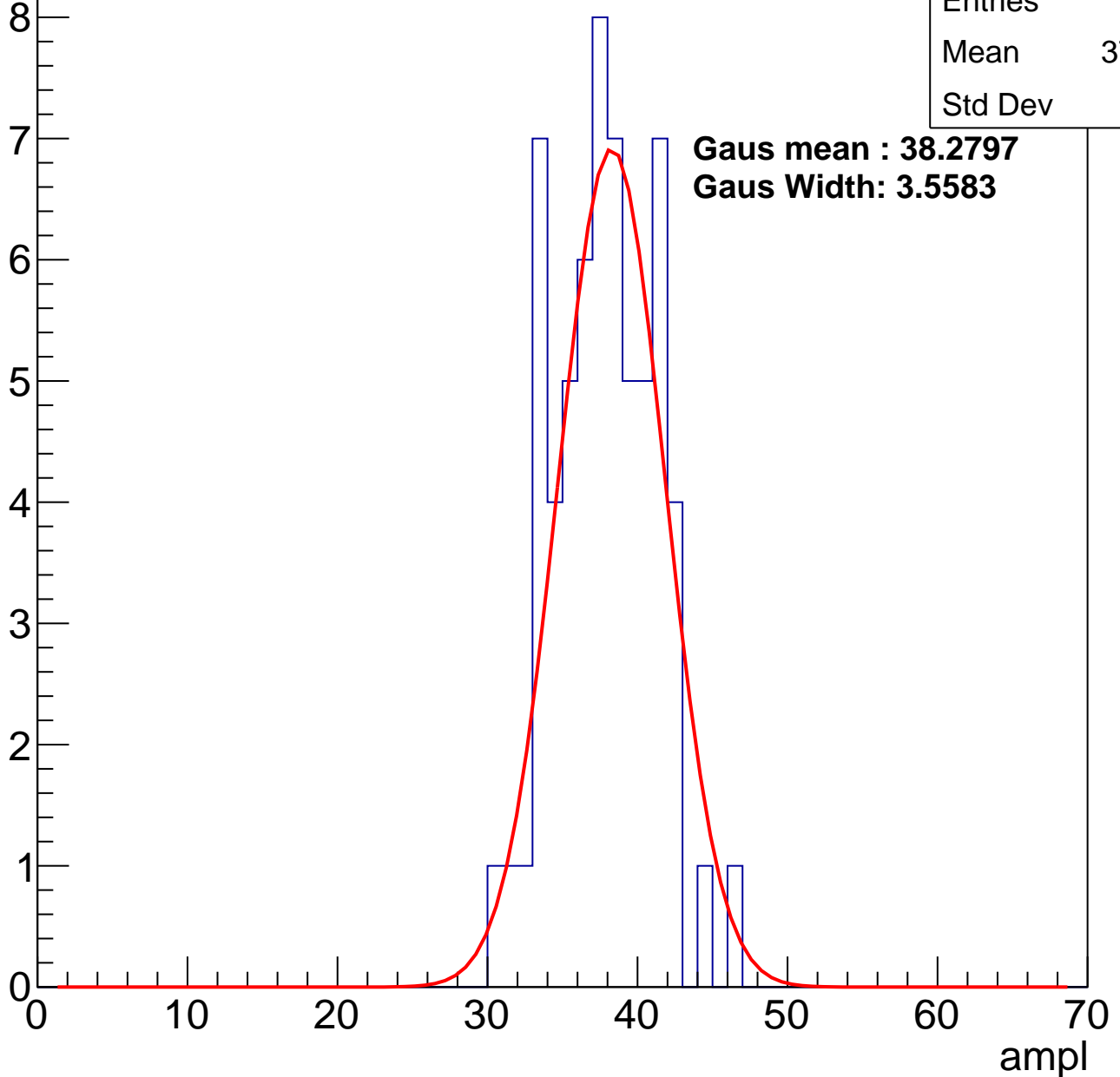
B0L001S, U11-ch95, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.35
Std Dev	3.31

Gaus mean : 38.2797
Gaus Width: 3.5583



B0L001S, U11-ch95, adc2

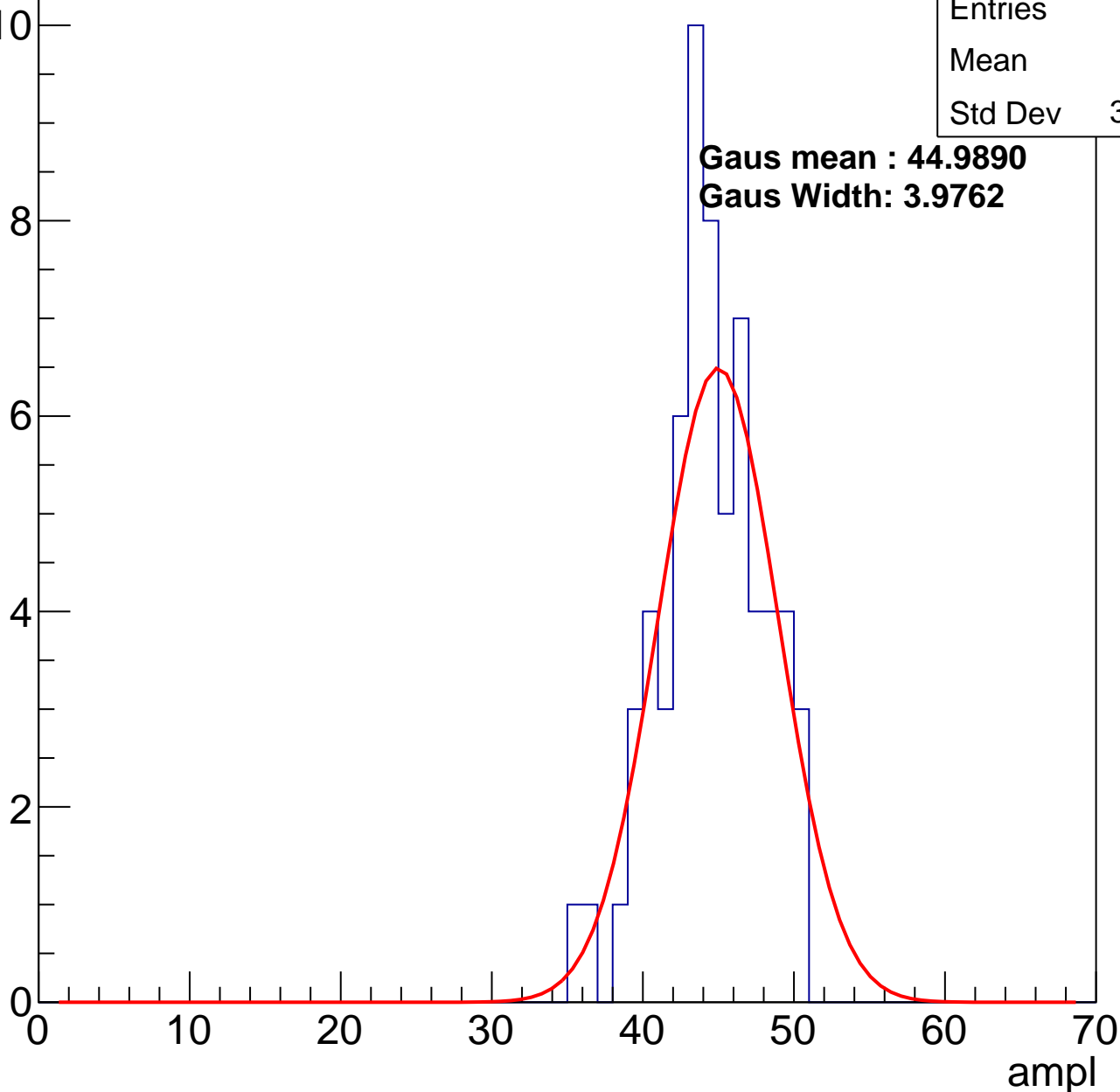
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44
Std Dev	3.359

Gaus mean : 44.9890

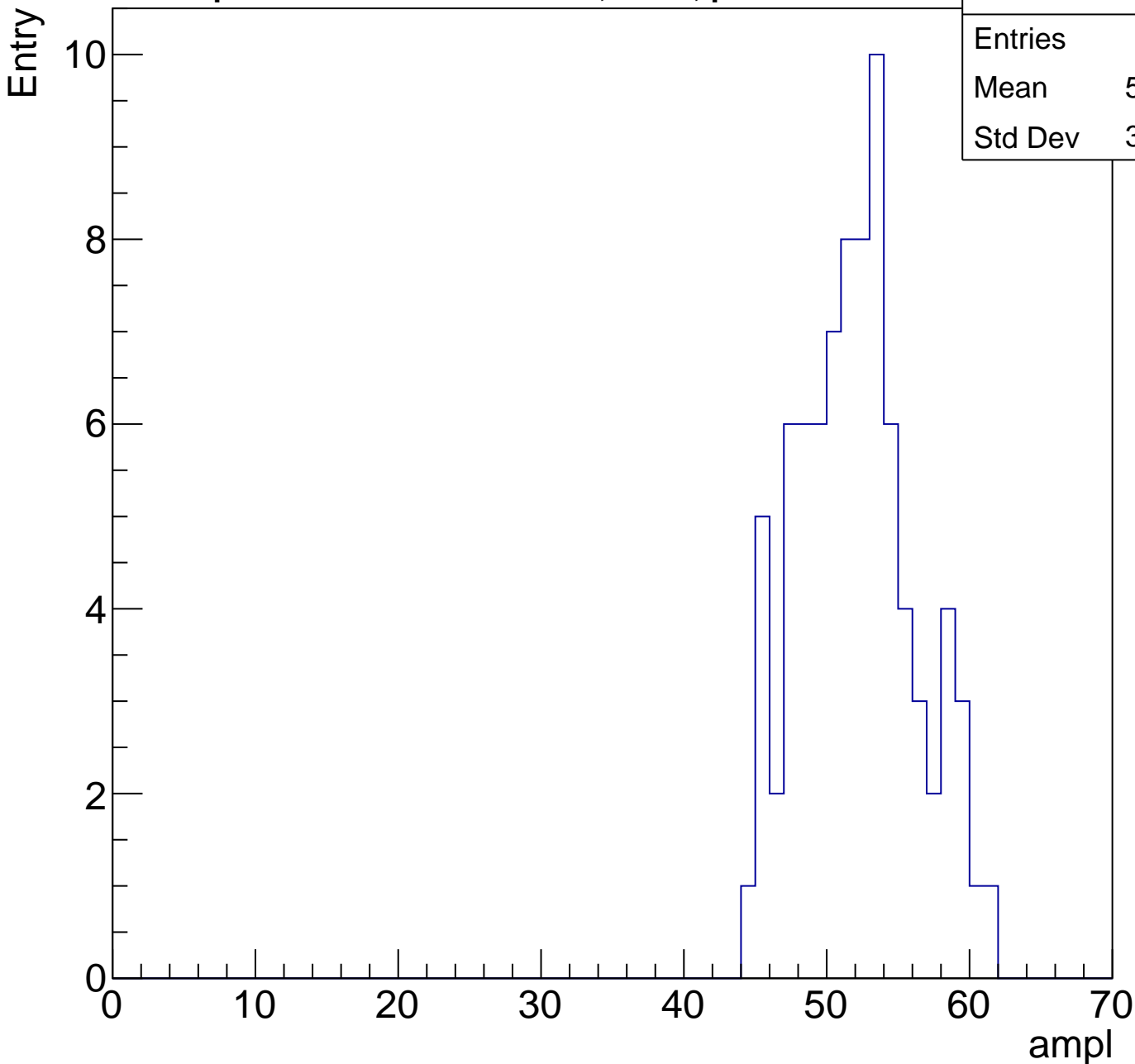
Gaus Width: 3.9762



B0L001S, U11-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

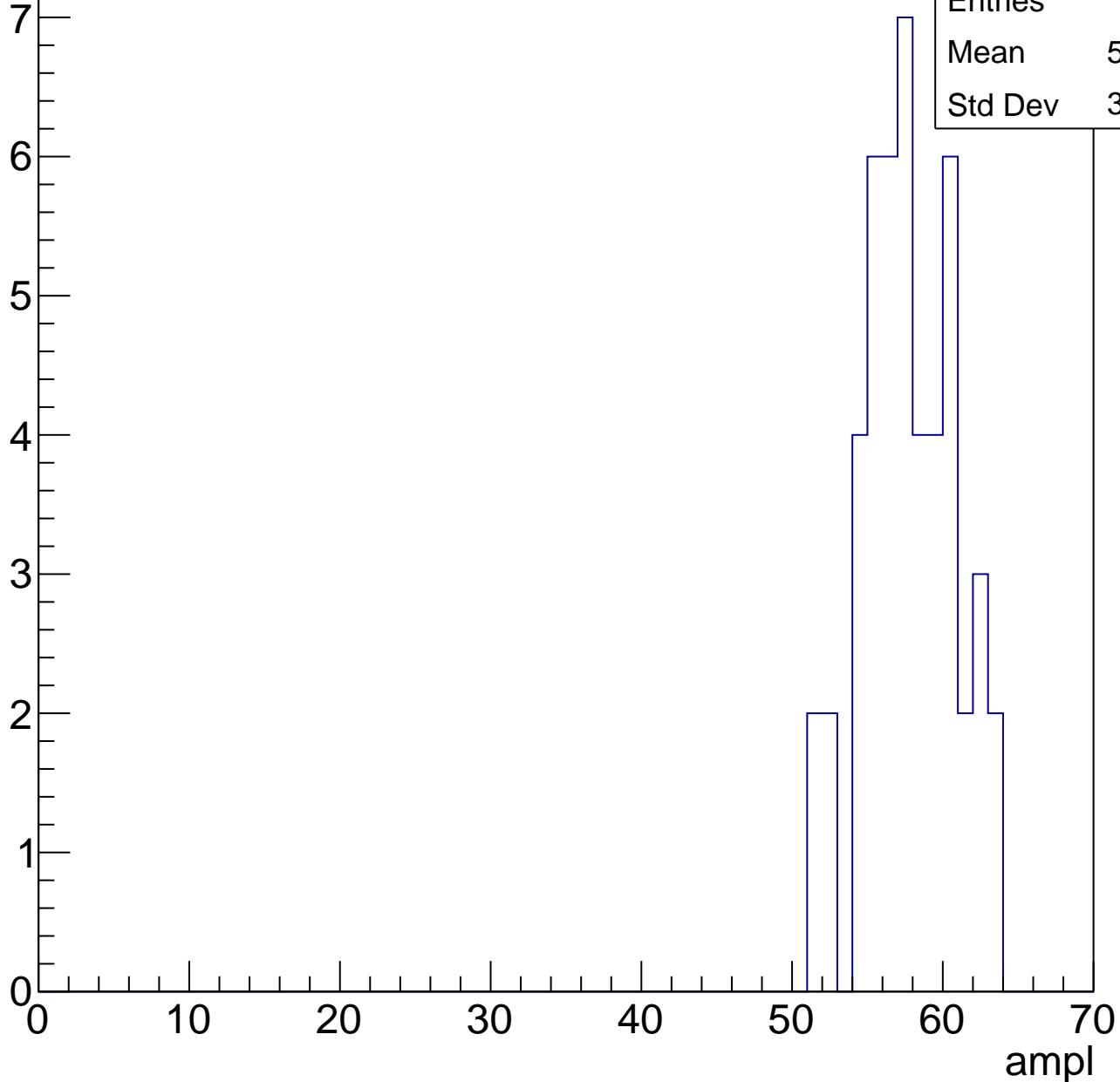
Entries	83
Mean	51.63
Std Dev	3.972



B0L001S, U11-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

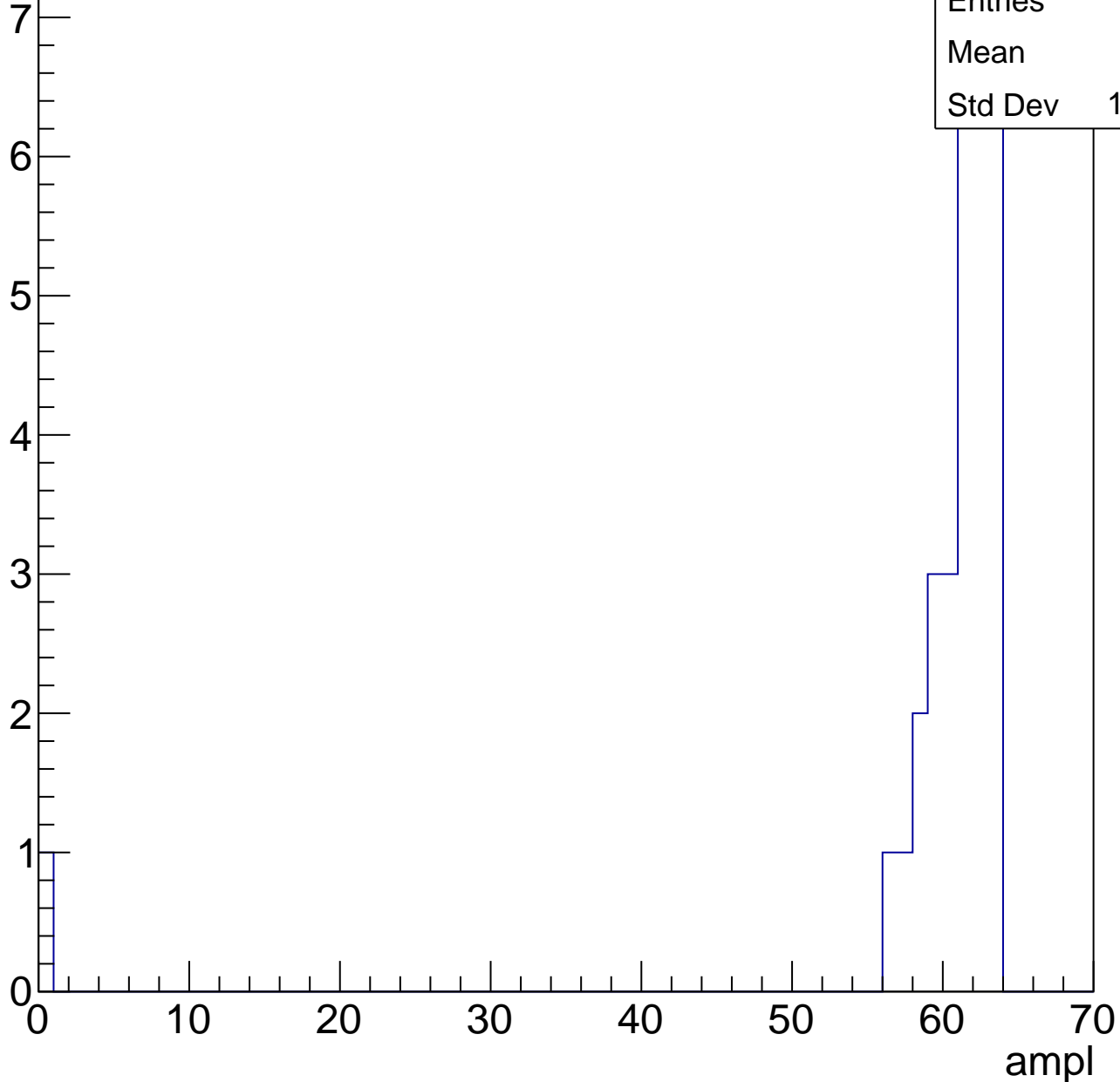


Entries	48
Mean	57.27
Std Dev	3.019

B0L001S, U11-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



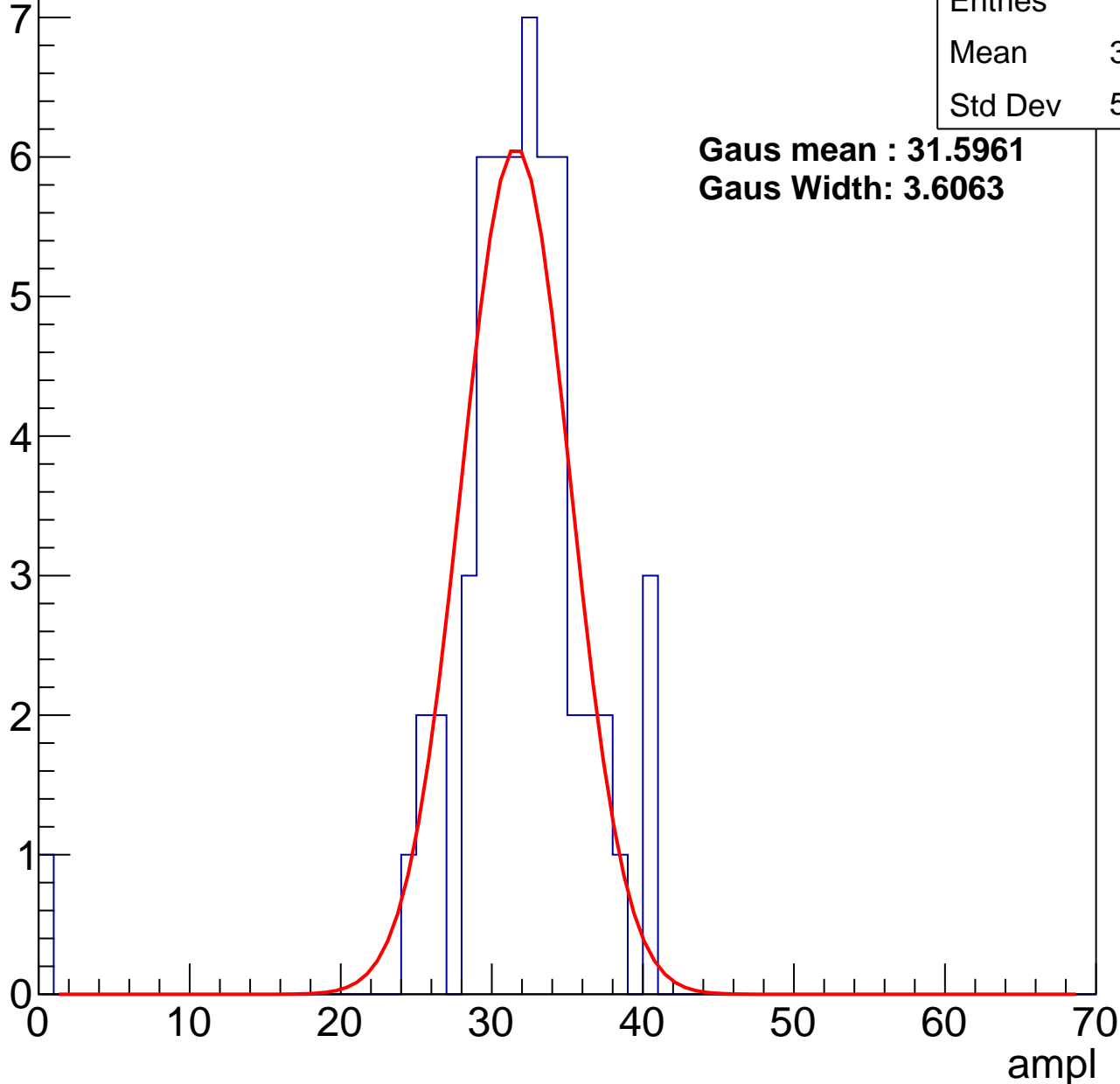
B0L001S, U11-ch96, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	31.25
Std Dev	5.536

Gaus mean : 31.5961
Gaus Width: 3.6063



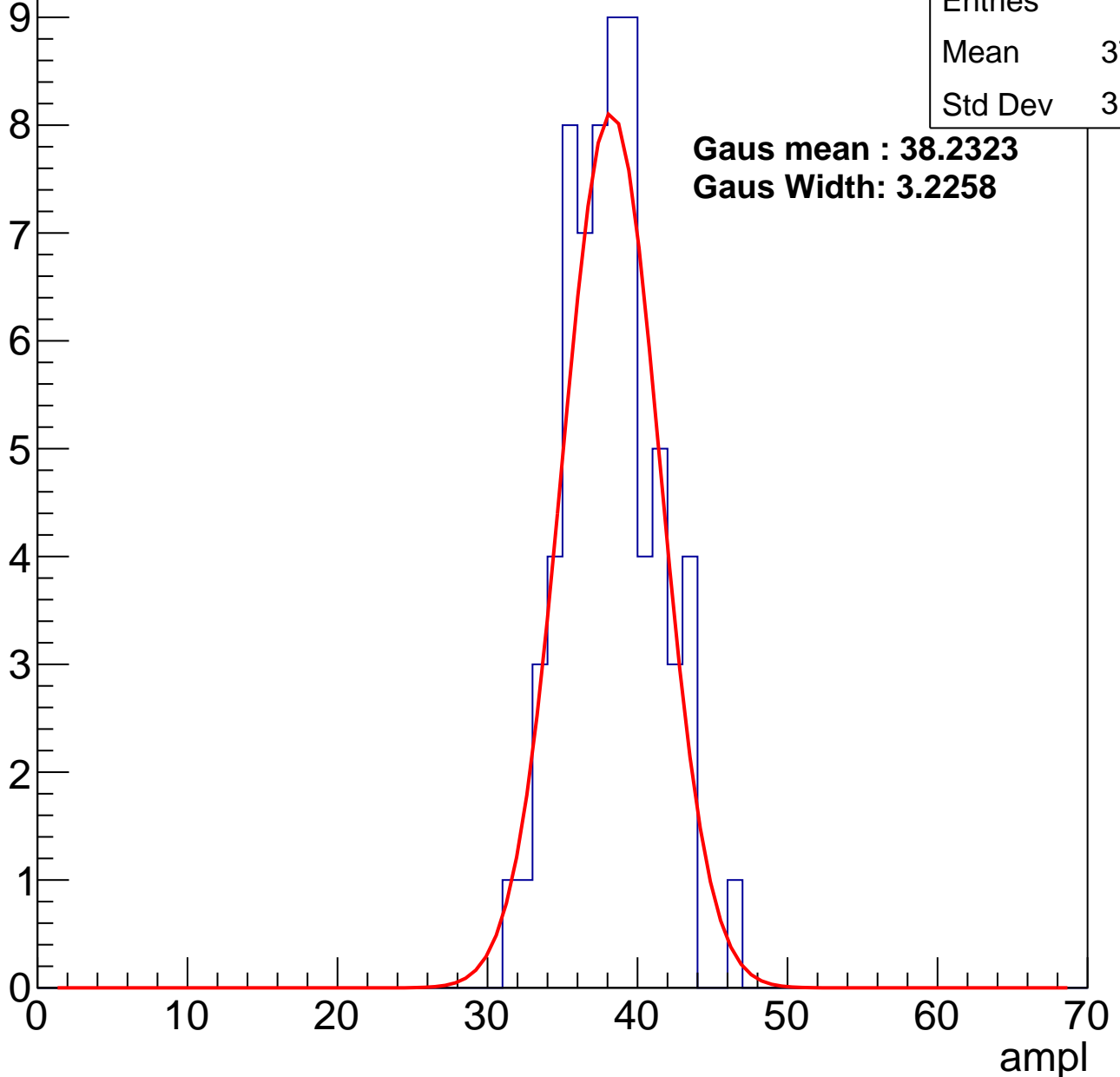
B0L001S, U11-ch96, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.73
Std Dev	3.015

Gaus mean : 38.2323
Gaus Width: 3.2258



B0L001S, U11-ch96, adc2

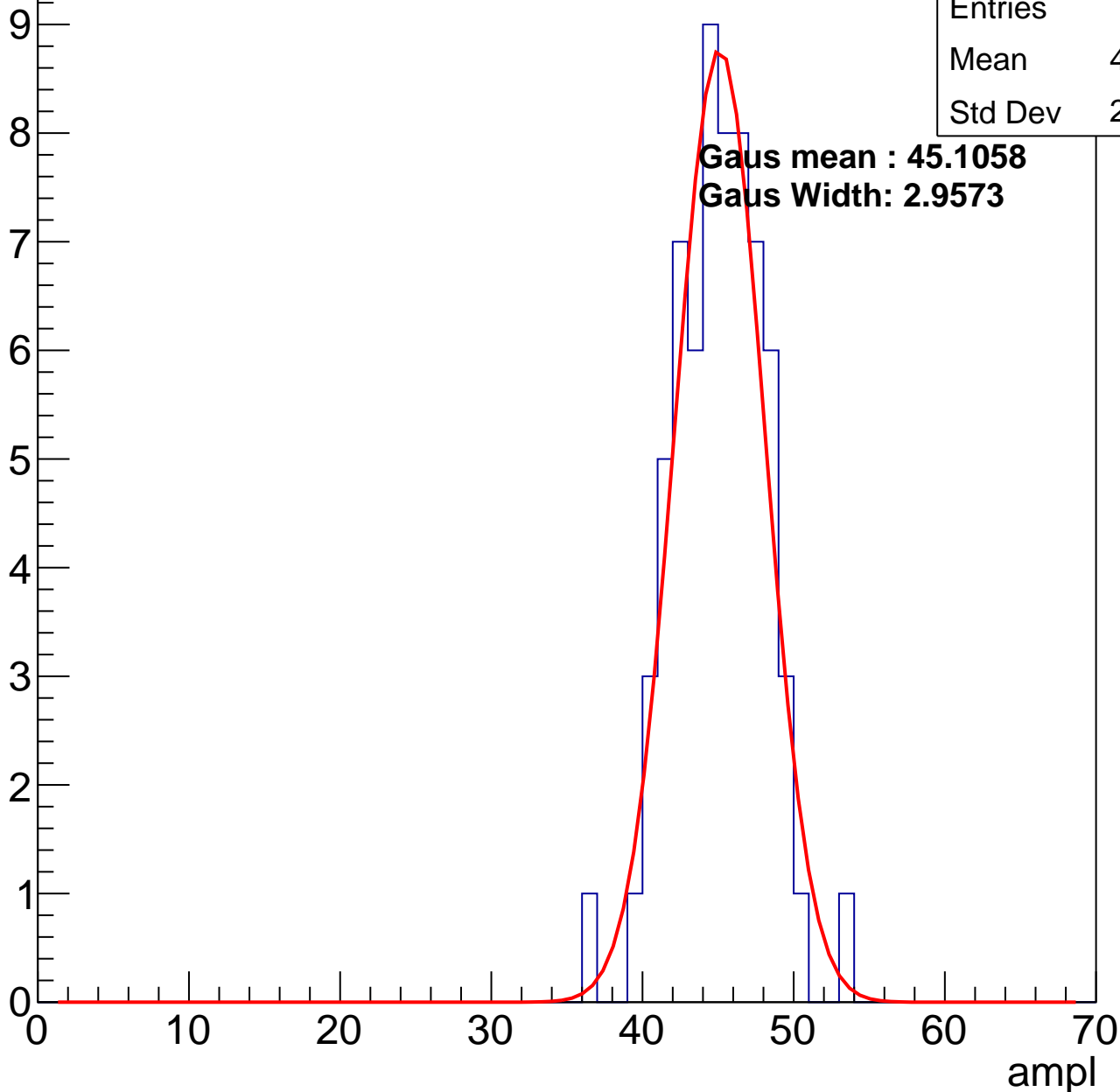
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.59
Std Dev	2.975

Gaus mean : 45.1058

Gaus Width: 2.9573

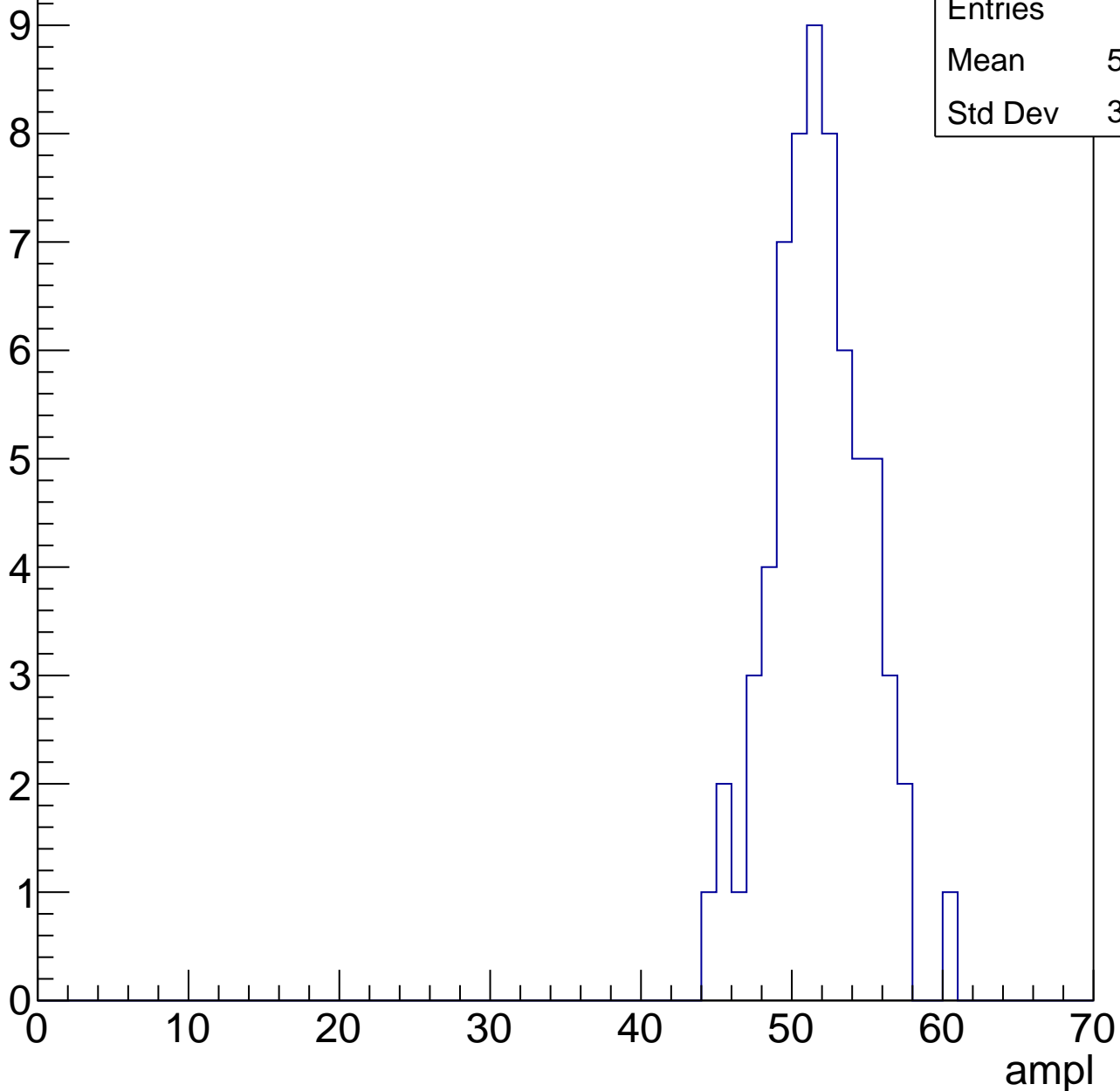


B0L001S, U11-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.32
Std Dev	3.148

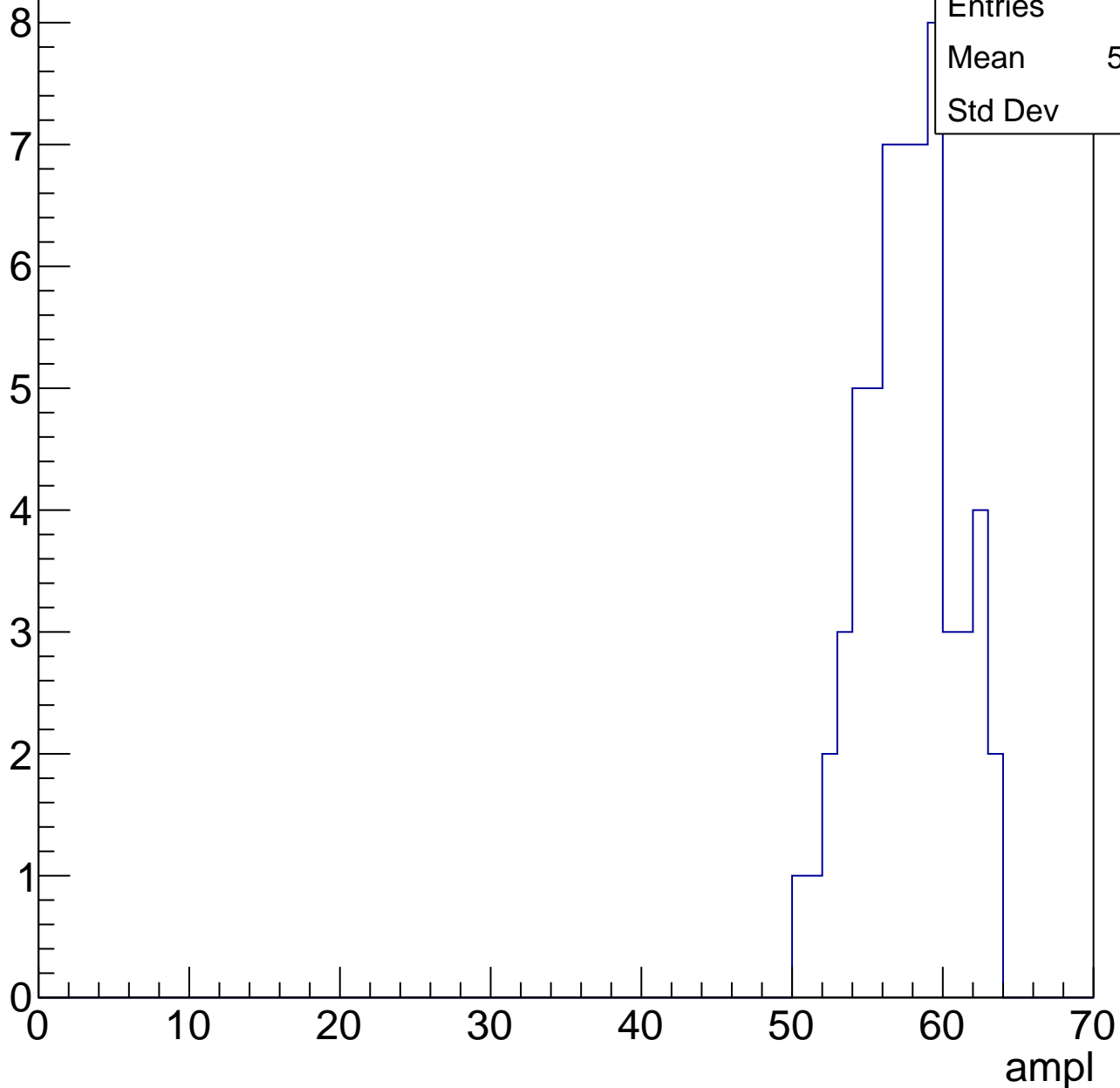


B0L001S, U11-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.16
Std Dev	3.05

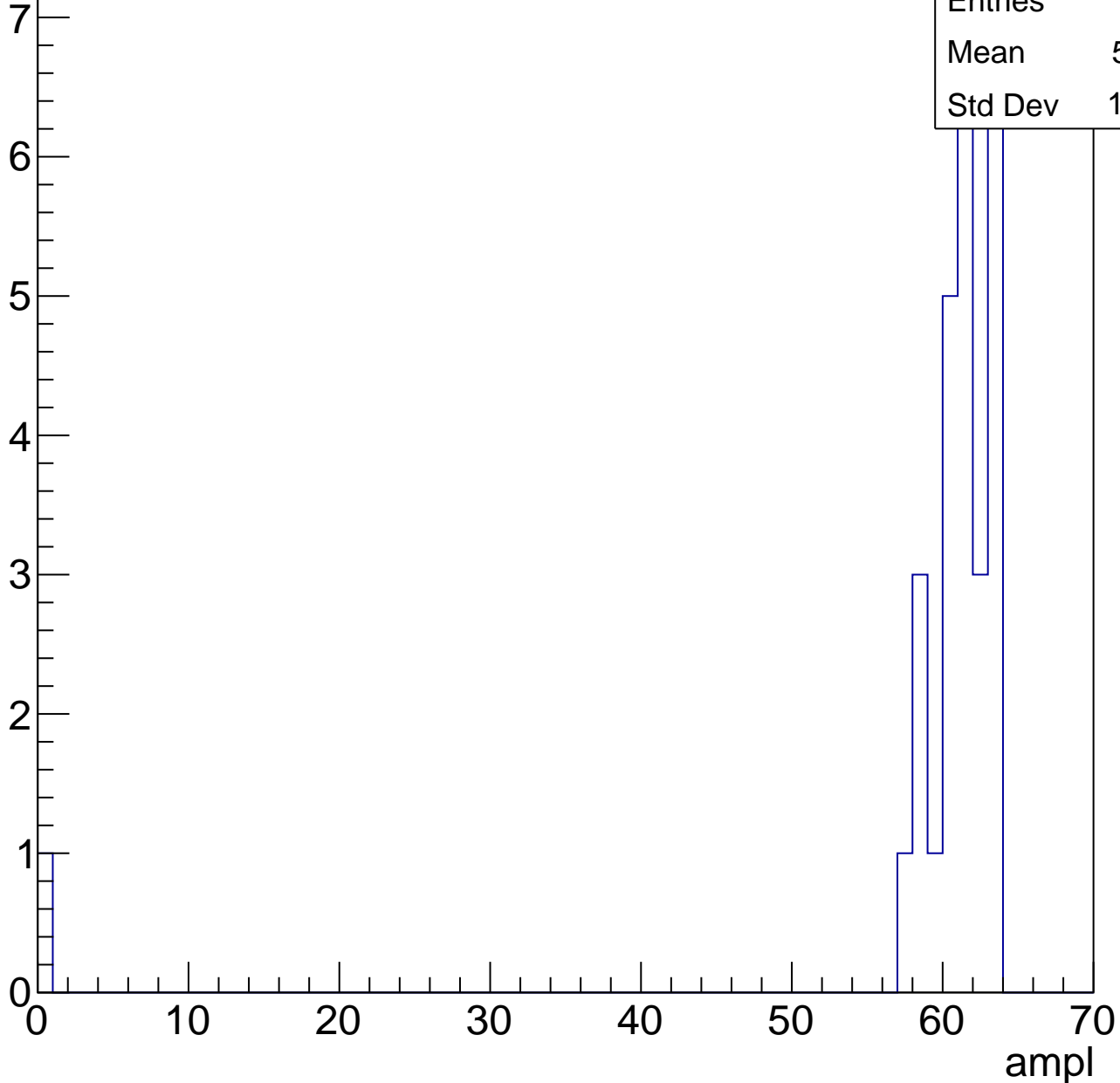


B0L001S, U11-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

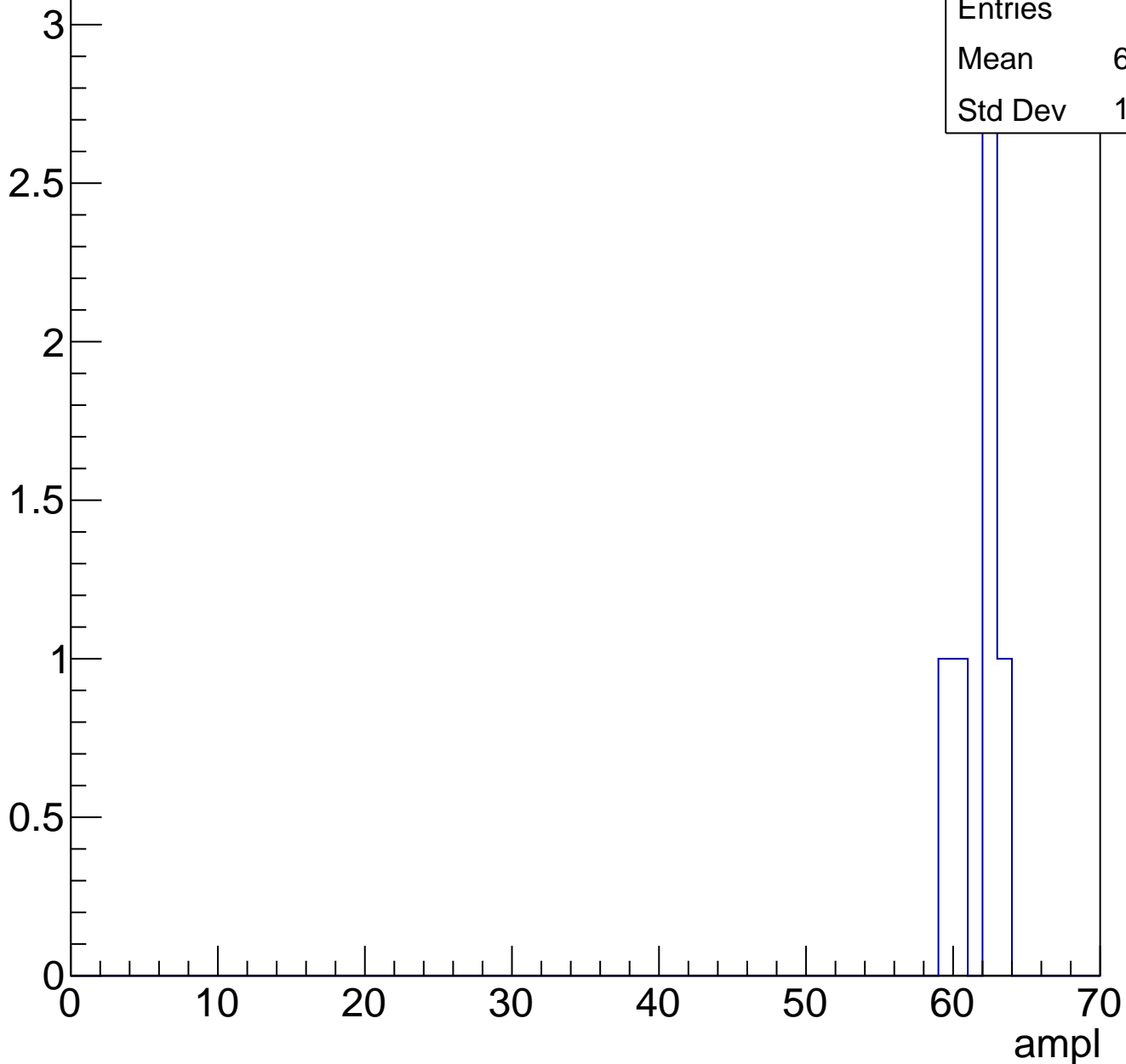
Entries	28
Mean	58.71
Std Dev	11.43



B0L001S, U11-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch97, adc0

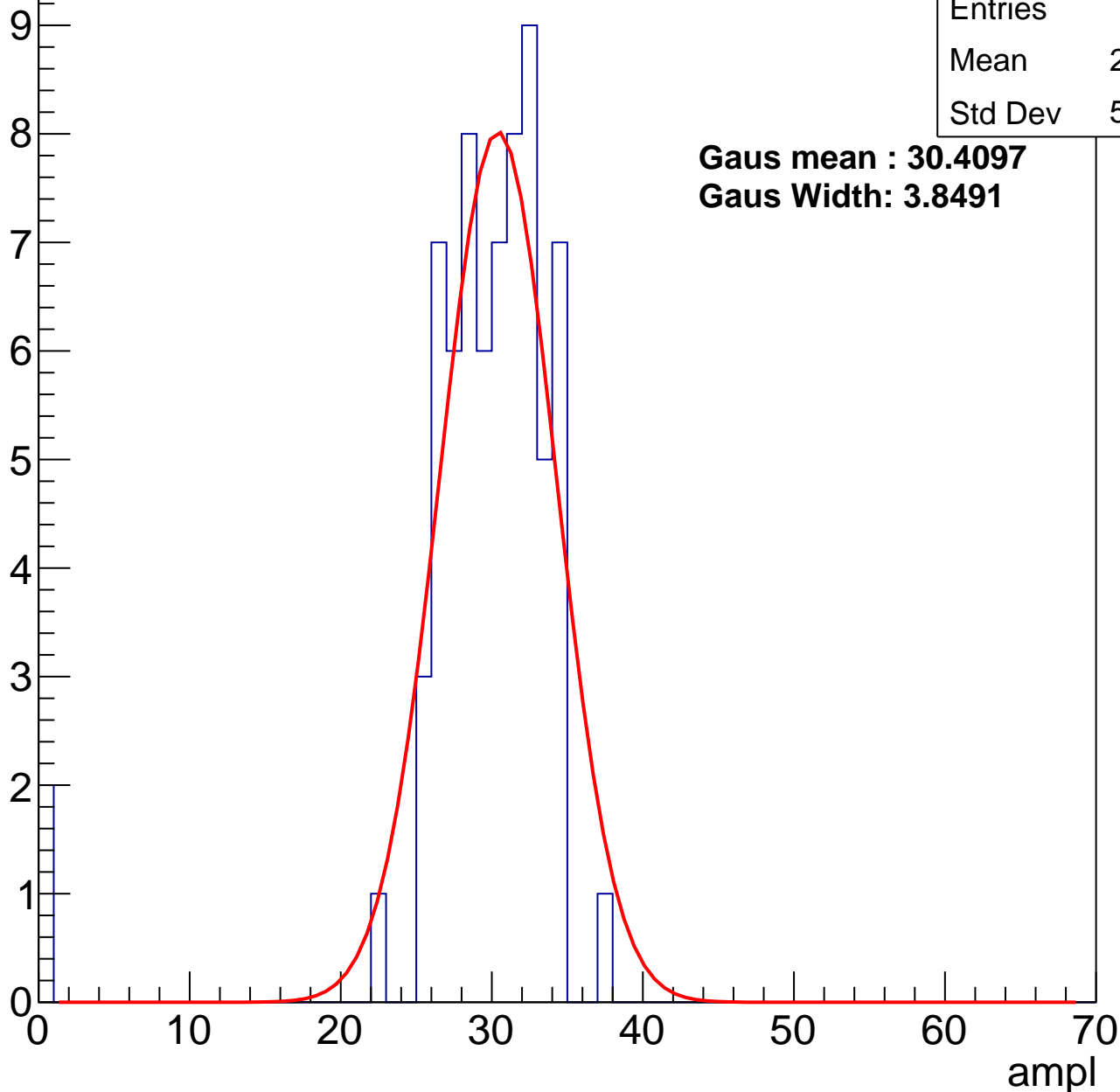
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	28.93
Std Dev	5.748

Gaus mean : 30.4097

Gaus Width: 3.8491



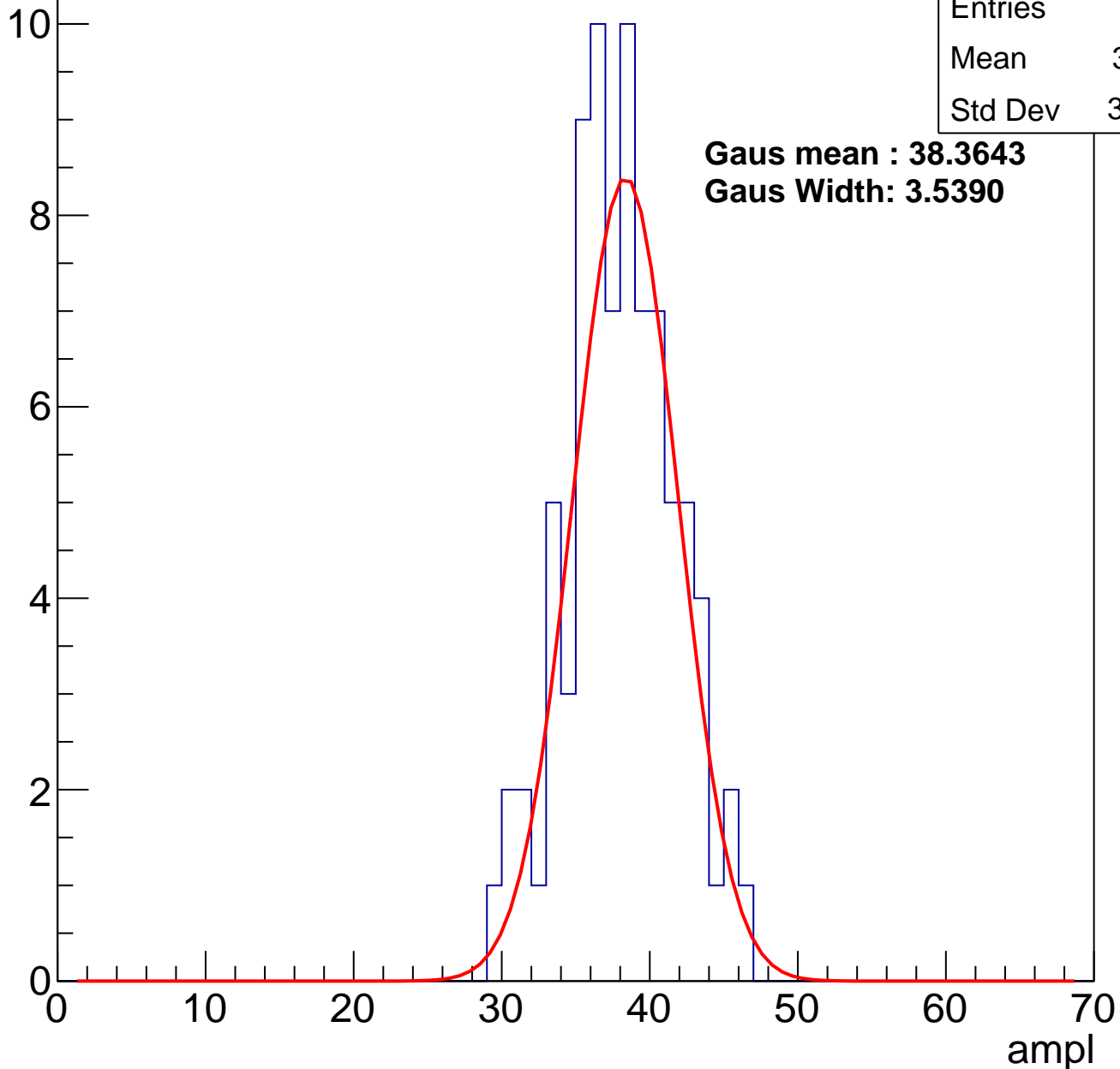
B0L001S, U11-ch97, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	37.61
Std Dev	3.645

Gaus mean : 38.3643
Gaus Width: 3.5390

Entry



B0L001S, U11-ch97, adc2

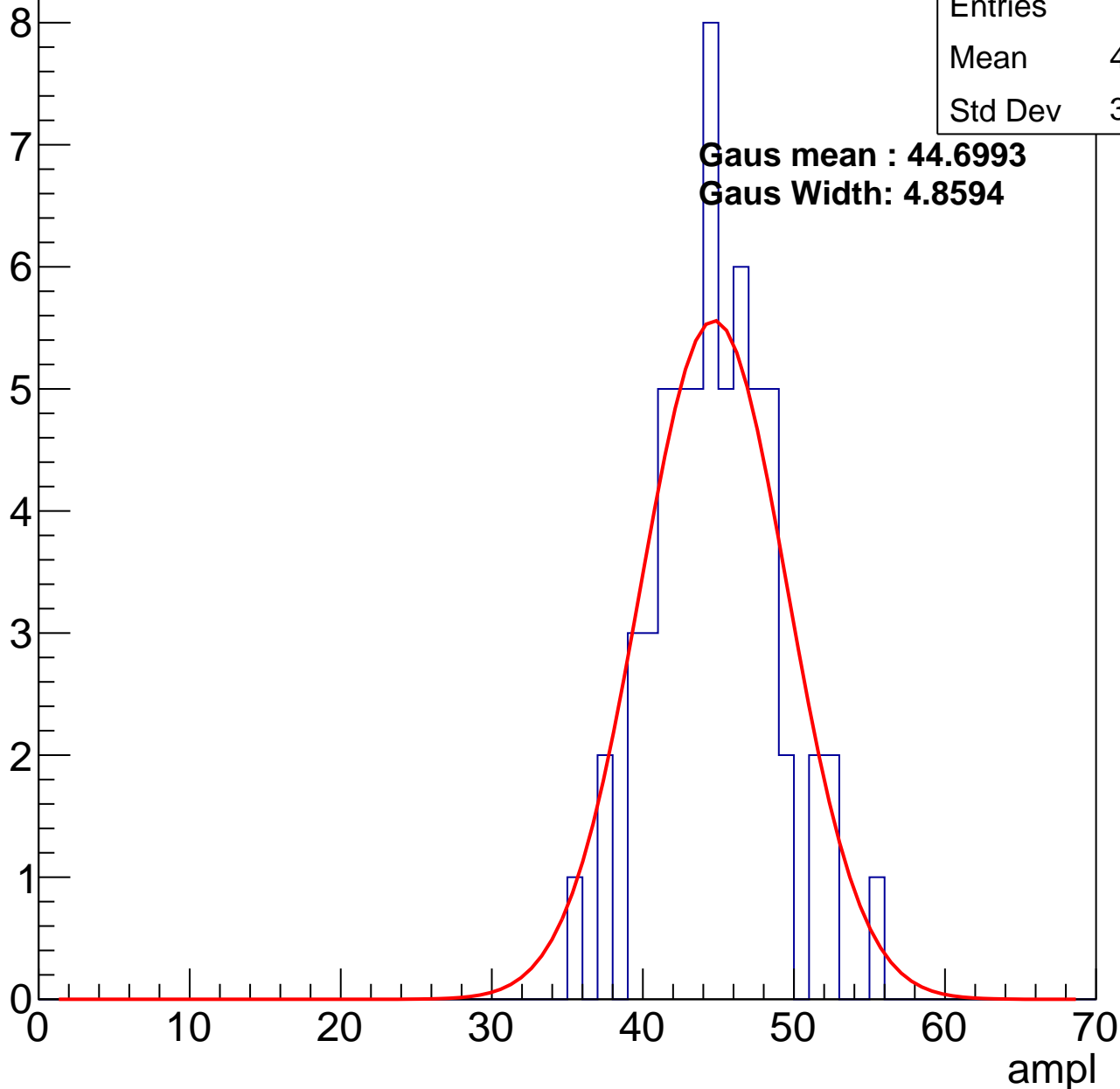
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.38
Std Dev	3.903

Gaus mean : 44.6993

Gaus Width: 4.8594

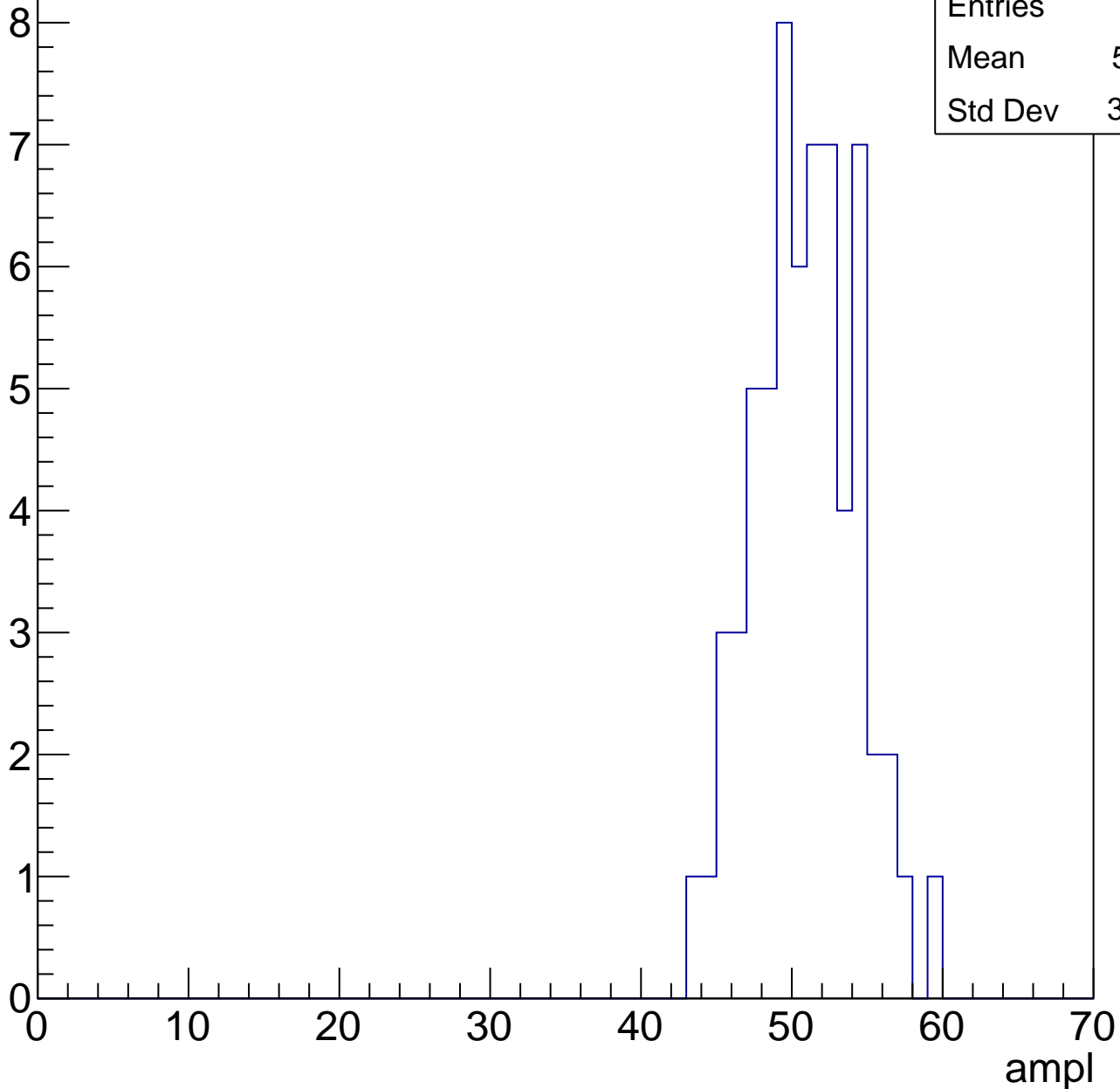


B0L001S, U11-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

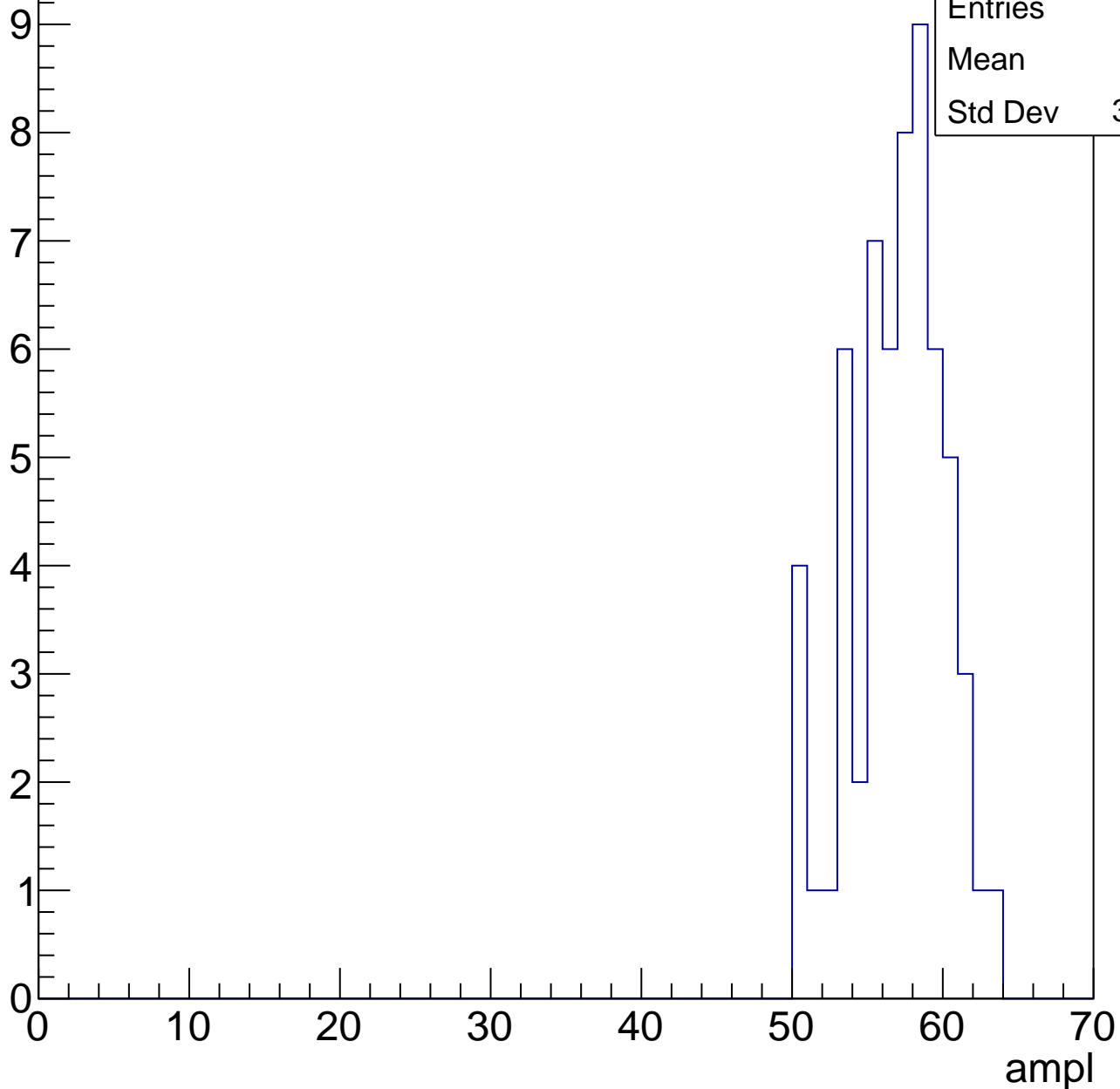
Entries	63
Mean	50.41
Std Dev	3.346



B0L001S, U11-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

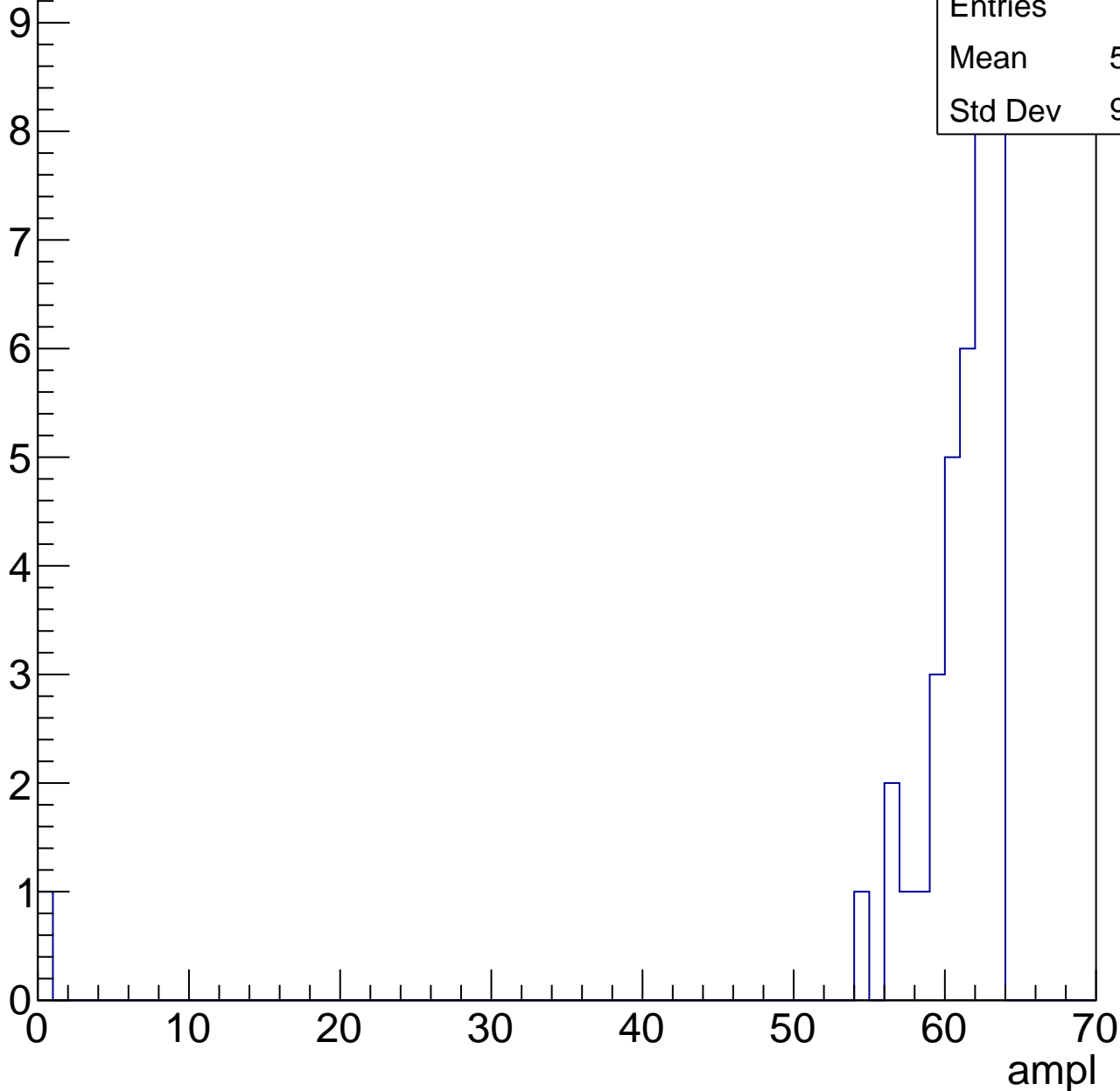


B0L001S, U11-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	59.18
Std Dev	9.976



B0L001S, U11-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	61
Std Dev	0

B0L001S, U11-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch98, adc0

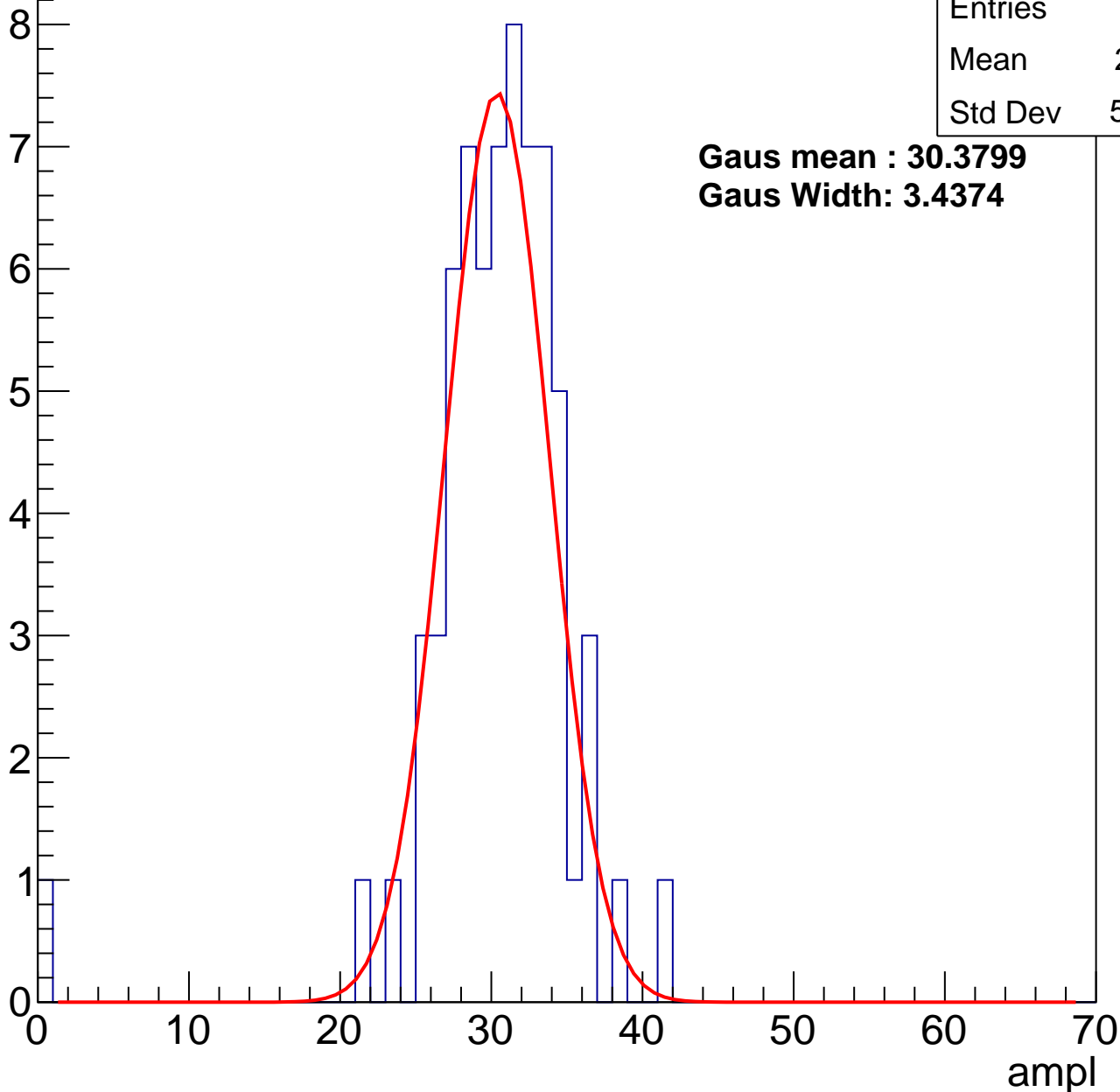
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.91
Std Dev	5.055

Gaus mean : 30.3799

Gaus Width: 3.4374



B0L001S, U11-ch98, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	36.83
Std Dev	2.874

Gaus mean : 37.7853

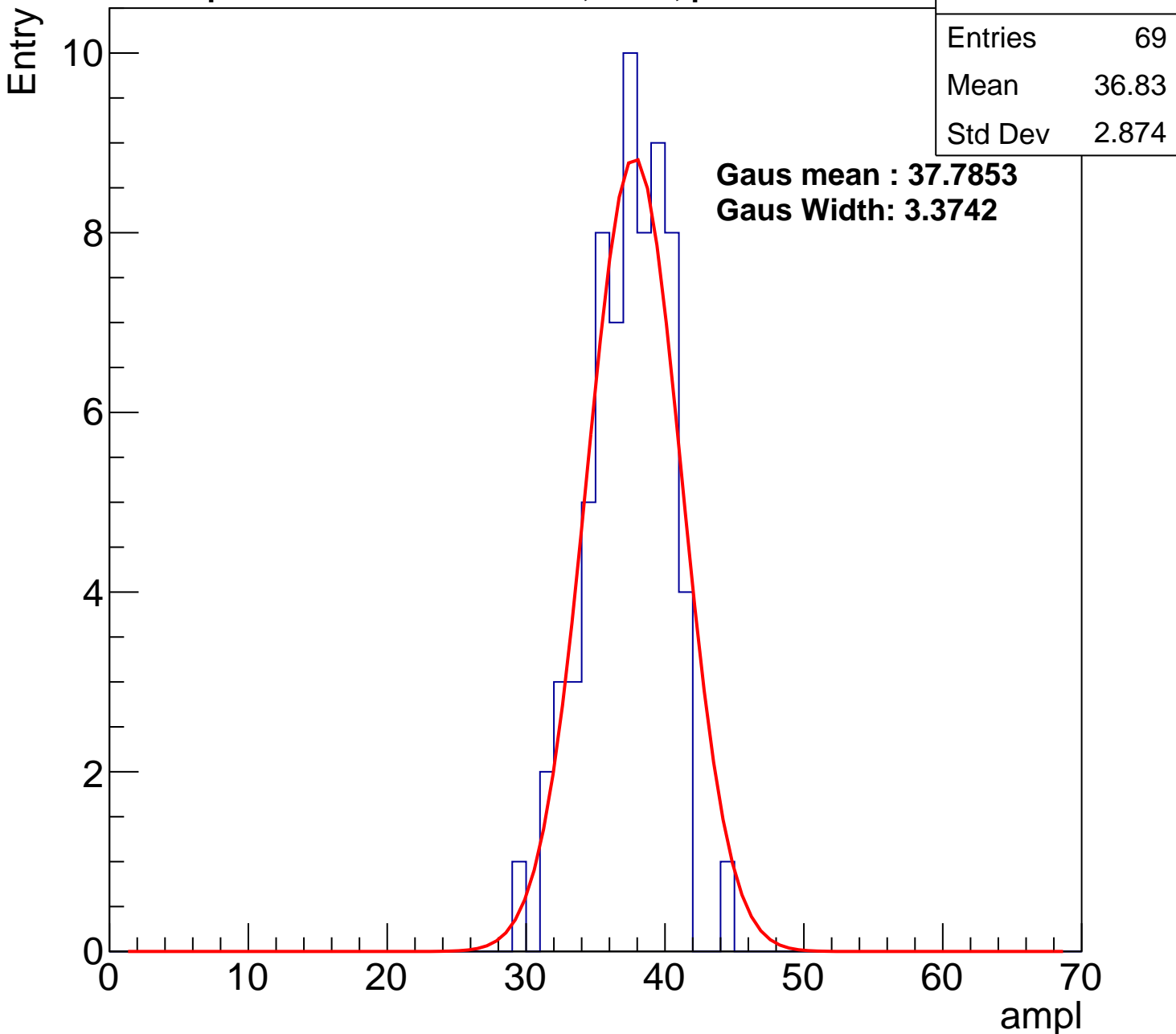
Gaus Width: 3.3742

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch98, adc2

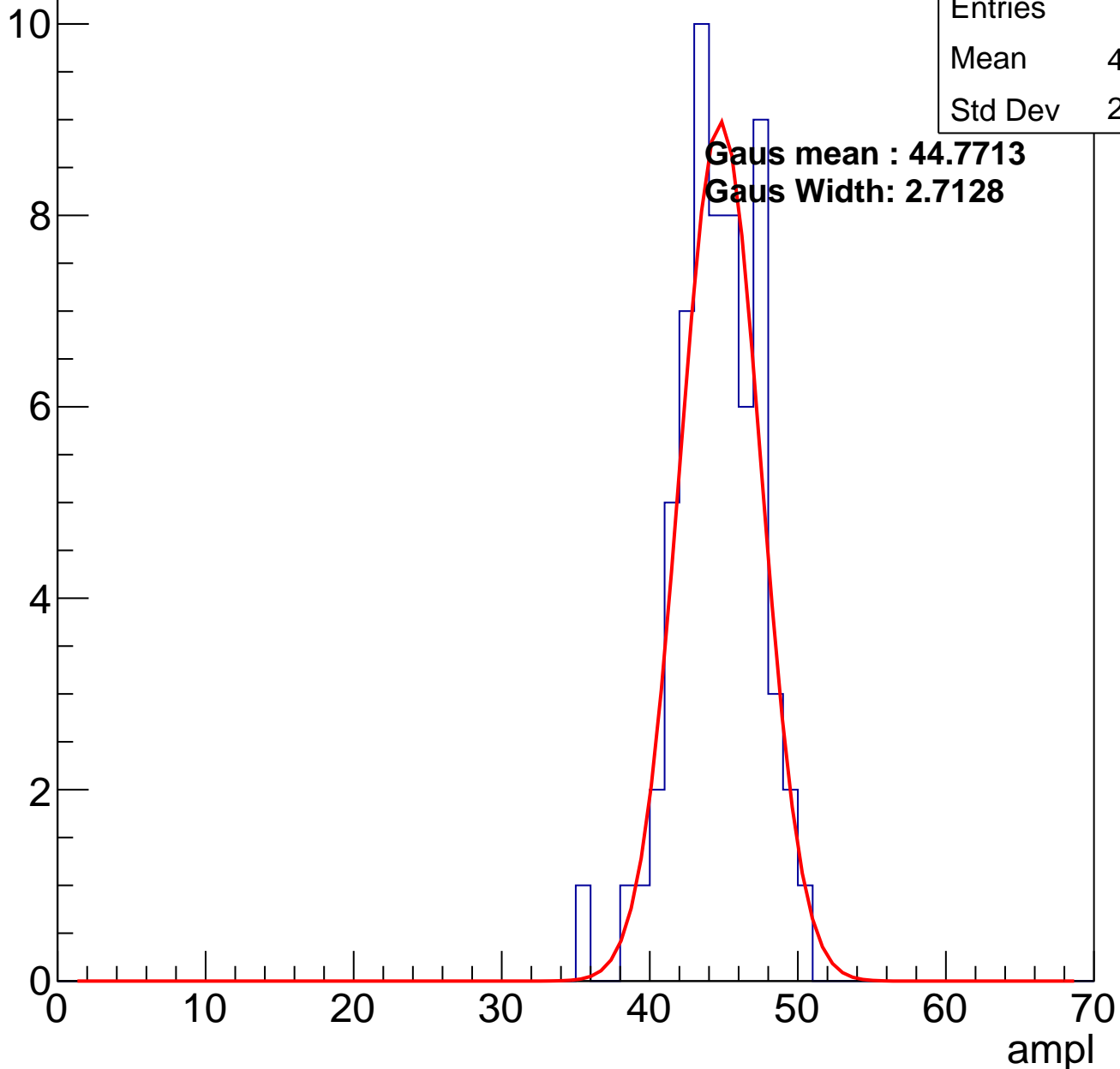
calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	44.12
Std Dev	2.803

Gaus mean : 44.7713

Gaus Width: 2.7128

Entry

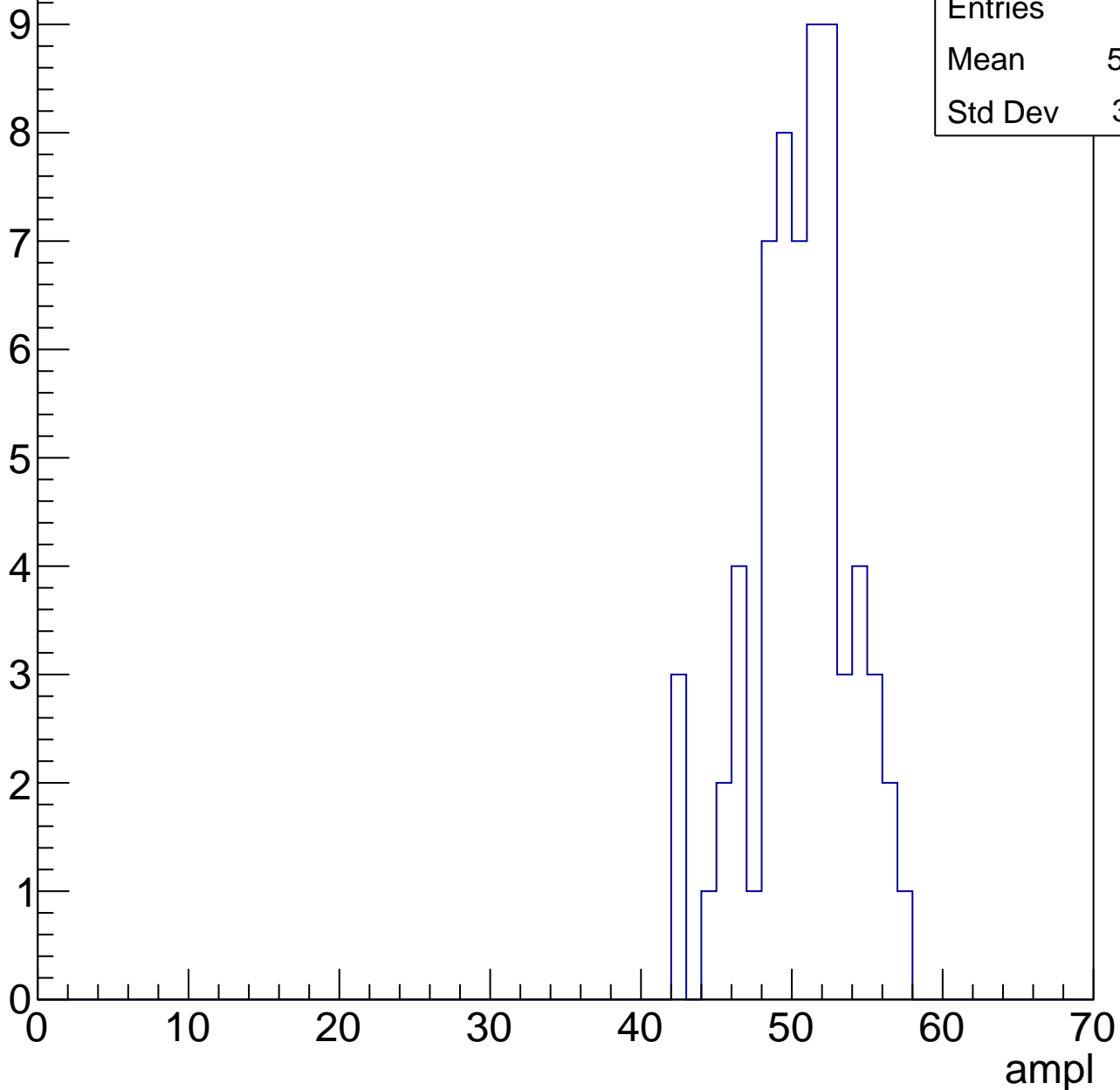


B0L001S, U11-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

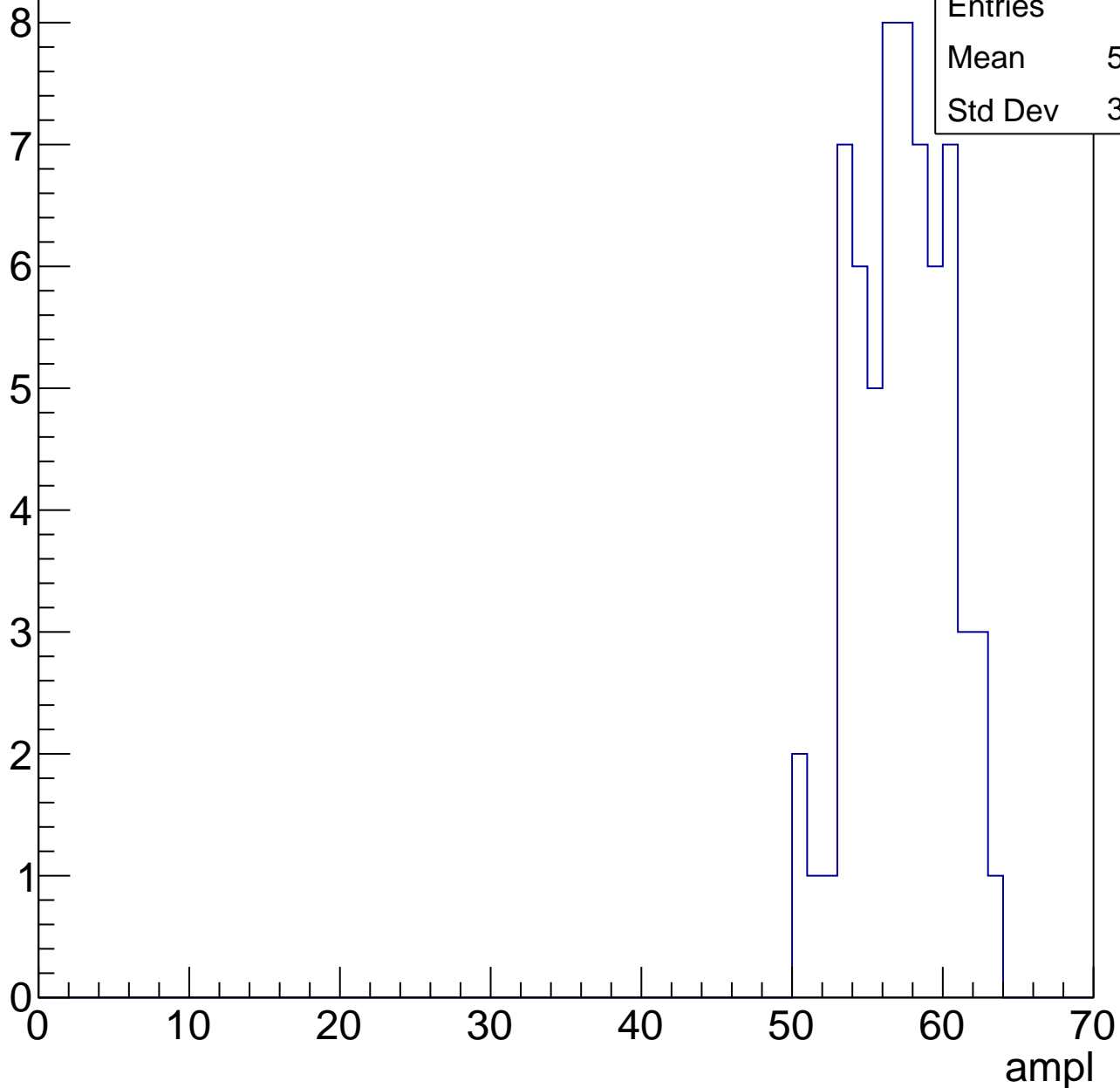
Entries	64
Mean	50.08
Std Dev	3.351



B0L001S, U11-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



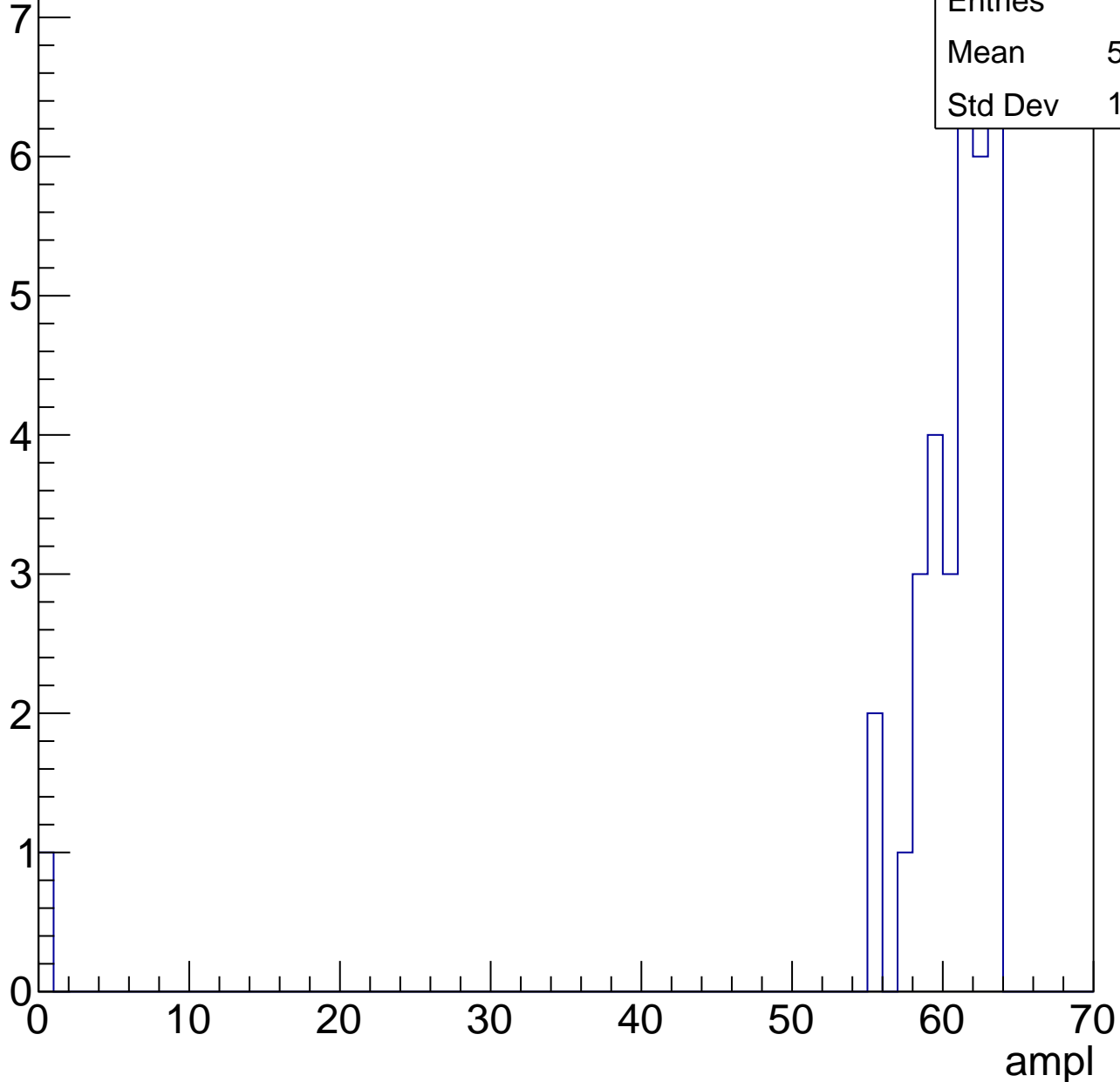
Entries	65
Mean	56.75
Std Dev	3.043

B0L001S, U11-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.74
Std Dev	10.45



B0L001S, U11-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U11-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch99, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	29.42
Std Dev	5.88

Gaus mean : 30.7274

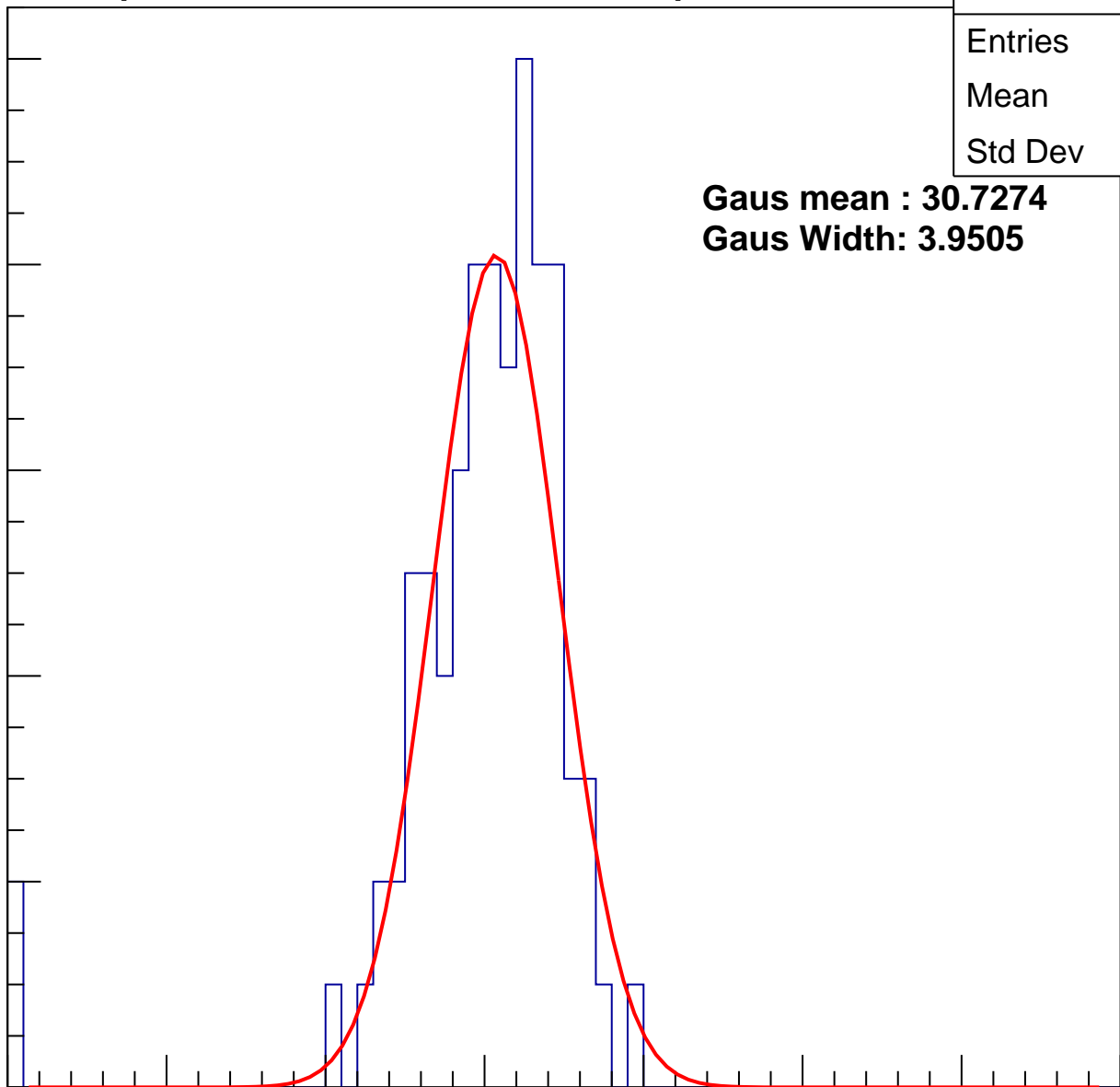
Gaus Width: 3.9505

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



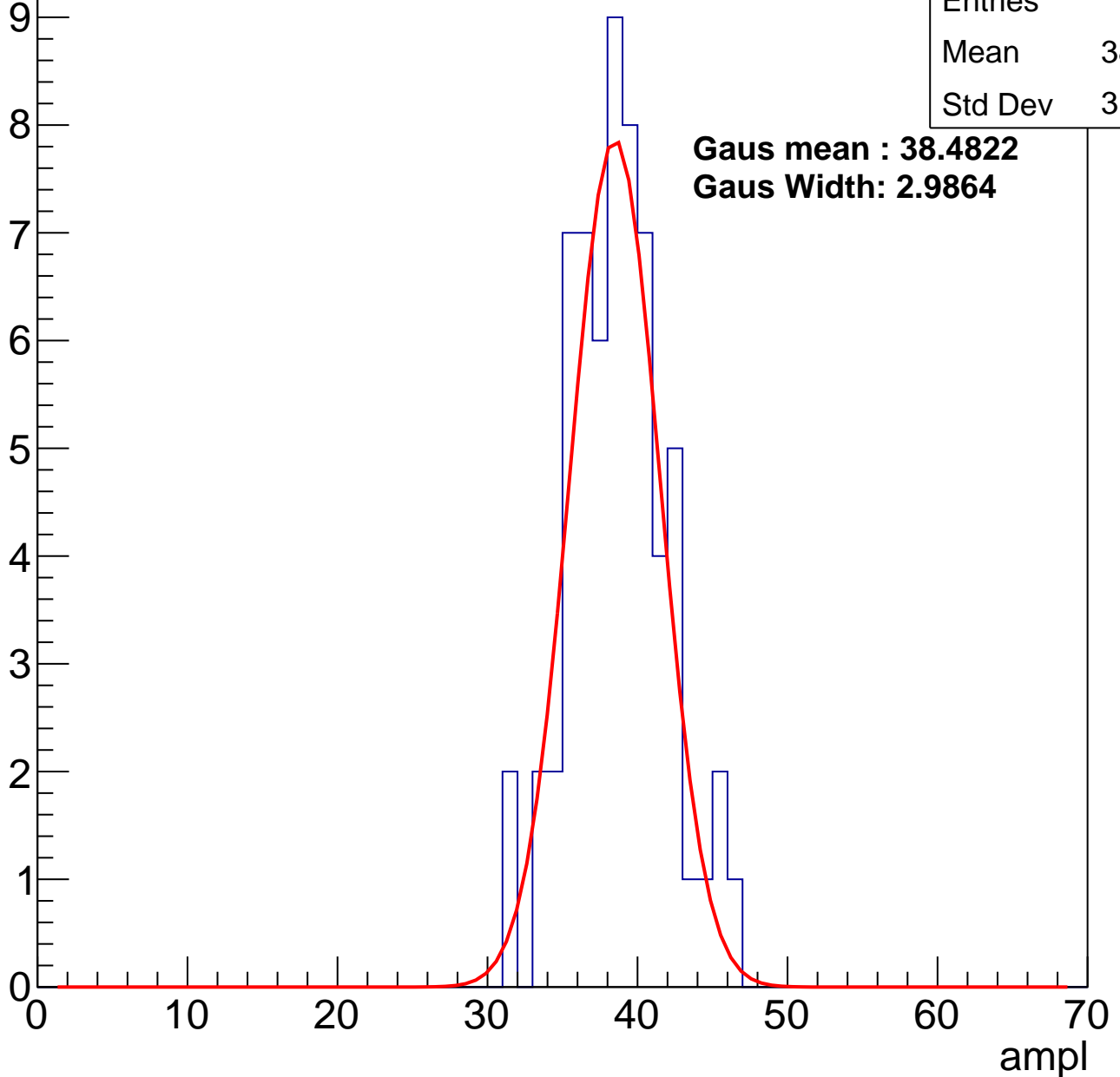
B0L001S, U11-ch99, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	38.22
Std Dev	3.184

Gaus mean : 38.4822
Gaus Width: 2.9864



B0L001S, U11-ch99, adc2

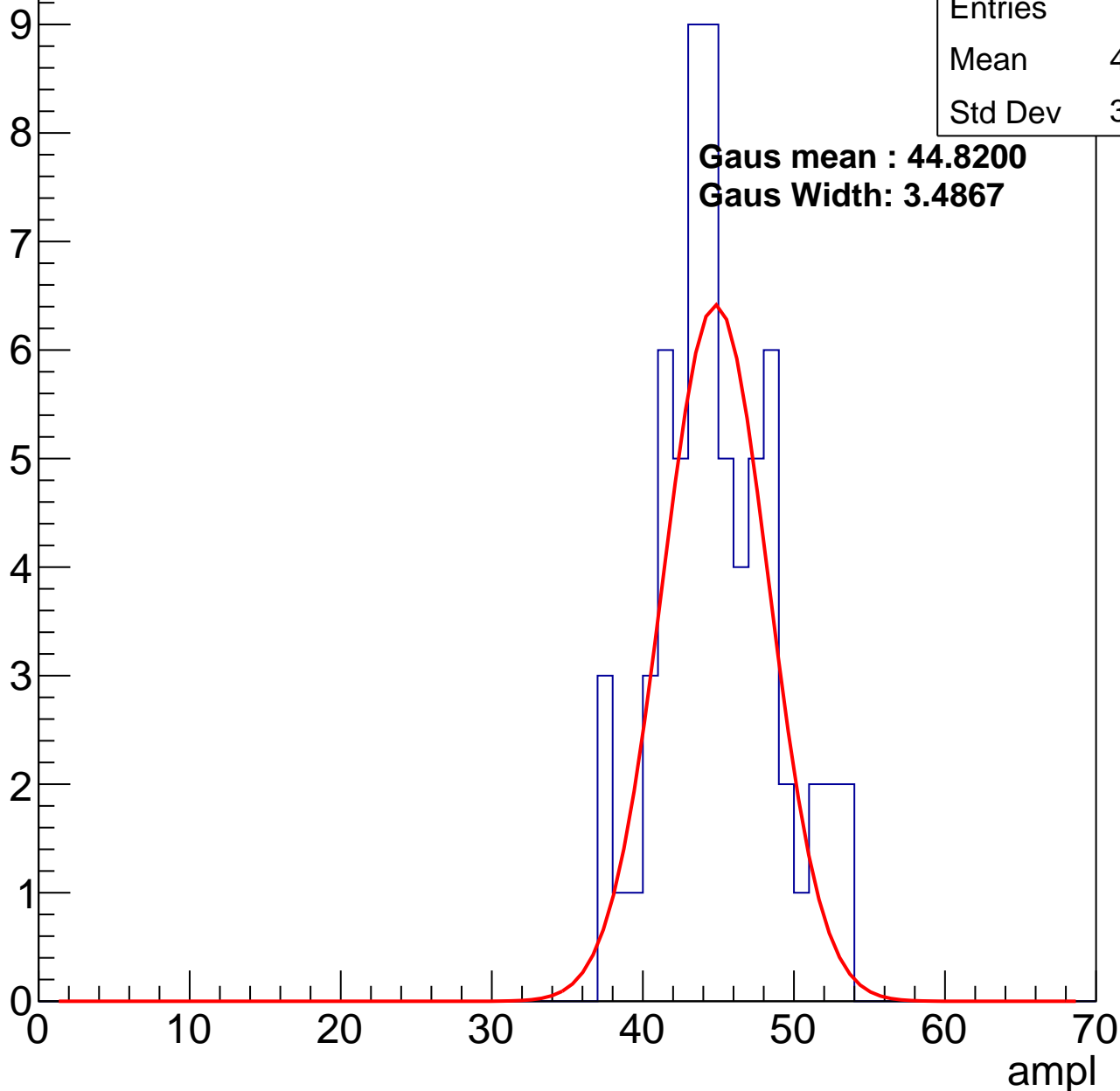
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.53
Std Dev	3.807

Gaus mean : 44.8200

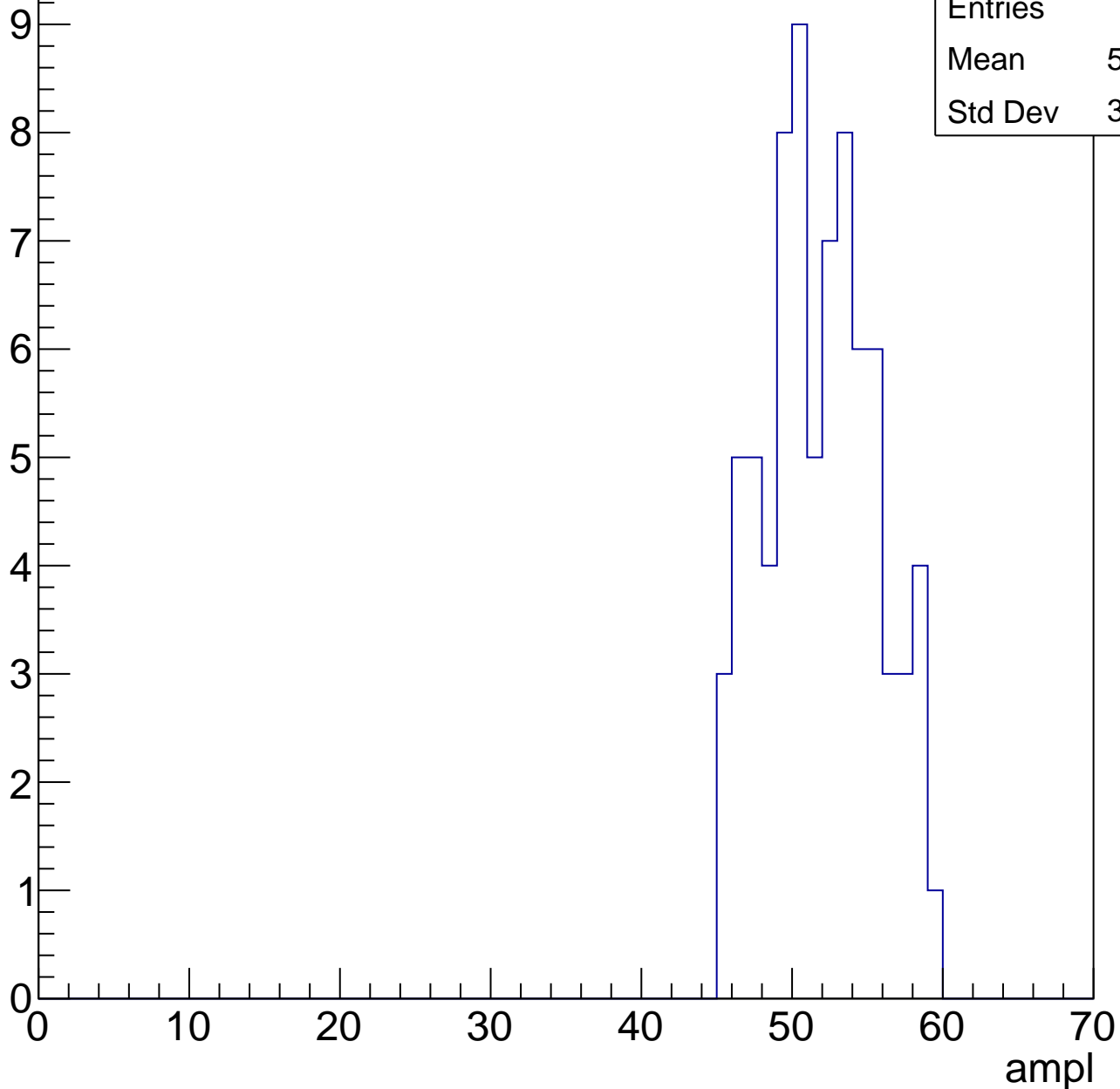
Gaus Width: 3.4867



B0L001S, U11-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



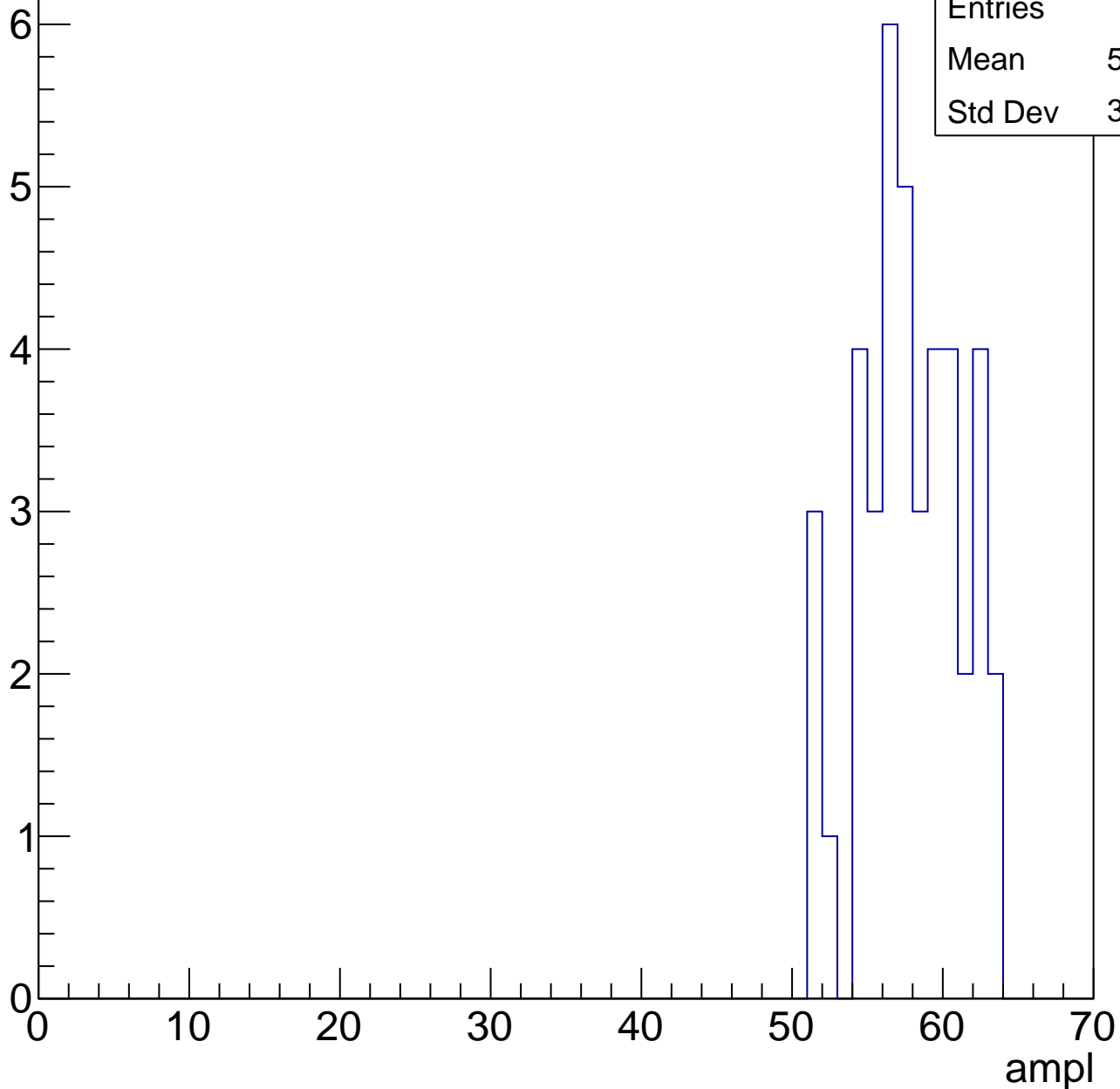
Entries	77
Mean	51.44
Std Dev	3.616

B0L001S, U11-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	57.39
Std Dev	3.275

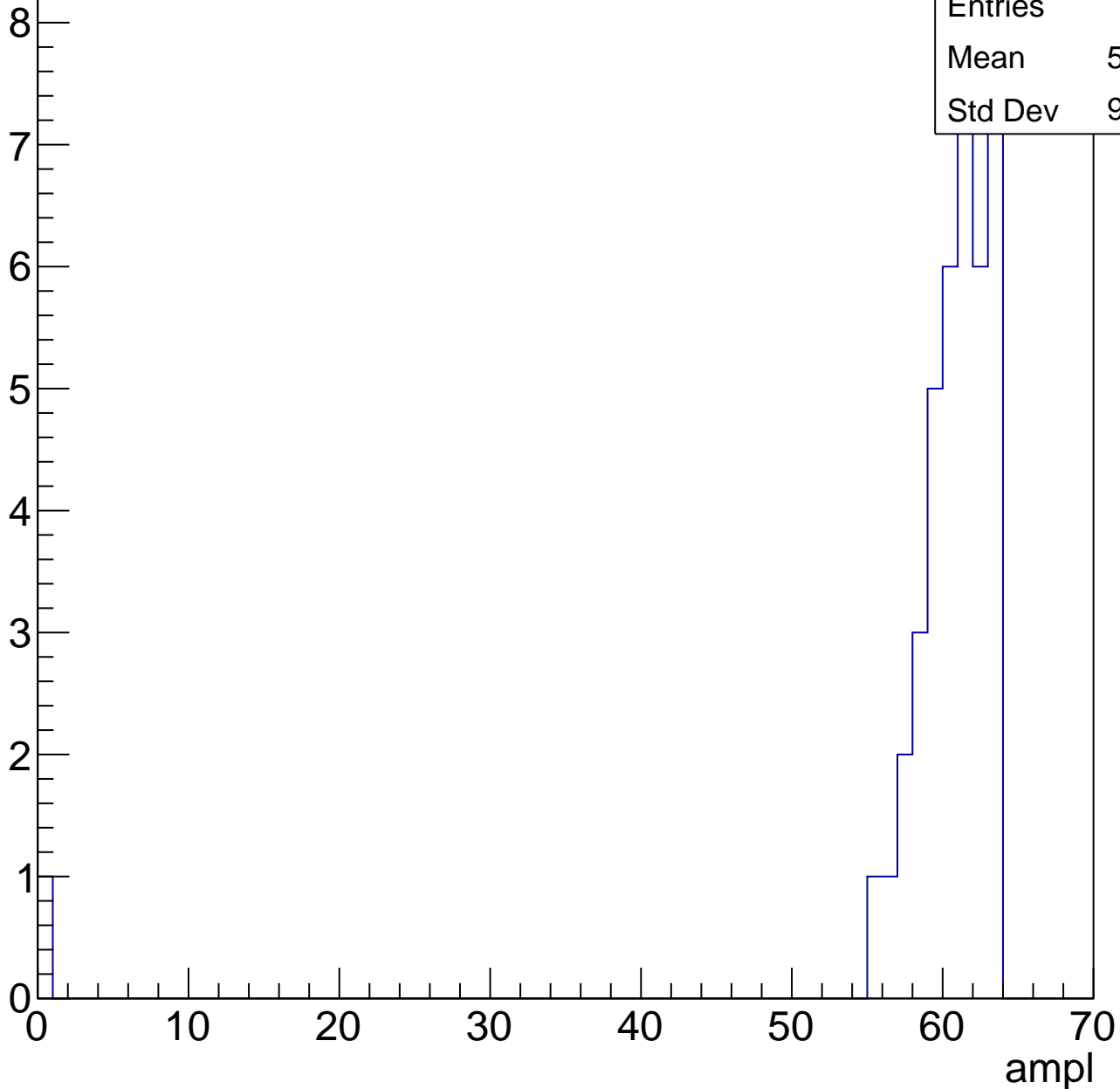


B0L001S, U11-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.98
Std Dev	9.547



B0L001S, U11-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch100, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	30.38
Std Dev	3.558

Gaus mean : 30.6243

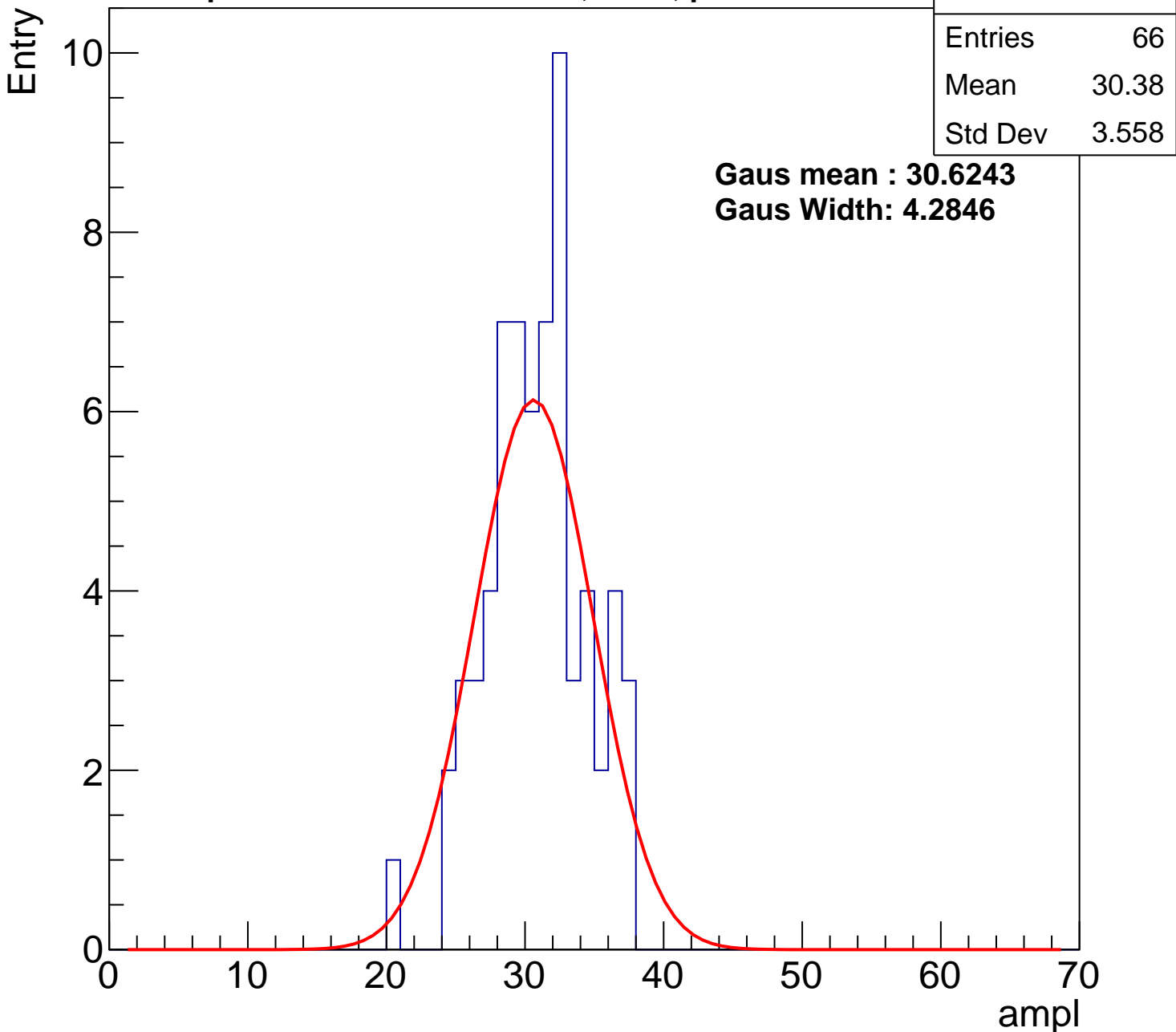
Gaus Width: 4.2846

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch100, adc1

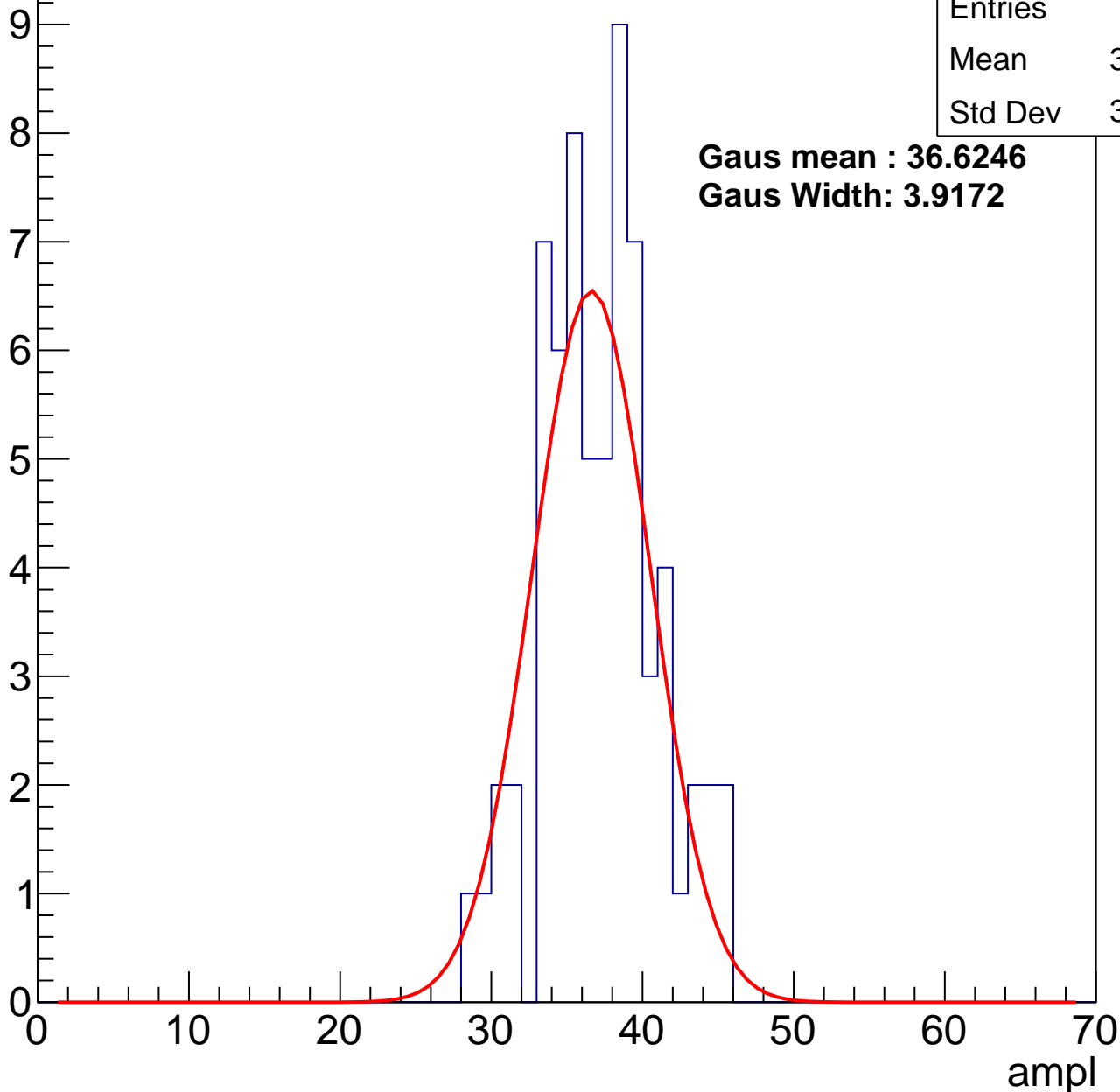
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.78
Std Dev	3.796

Gaus mean : 36.6246

Gaus Width: 3.9172



B0L001S, U11-ch100, adc2

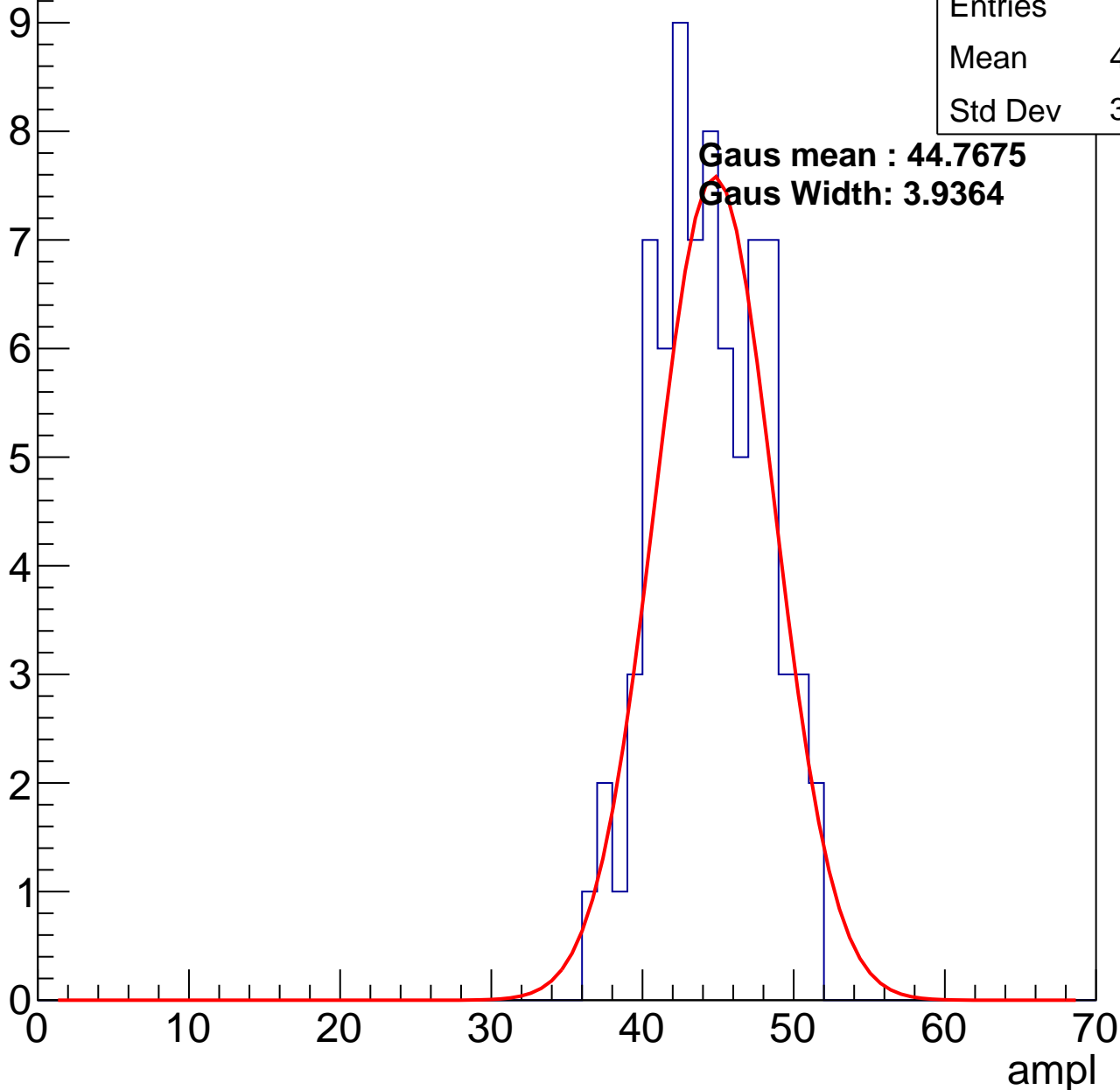
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	43.97
Std Dev	3.538

Gaus mean : 44.7675

Gaus Width: 3.9364

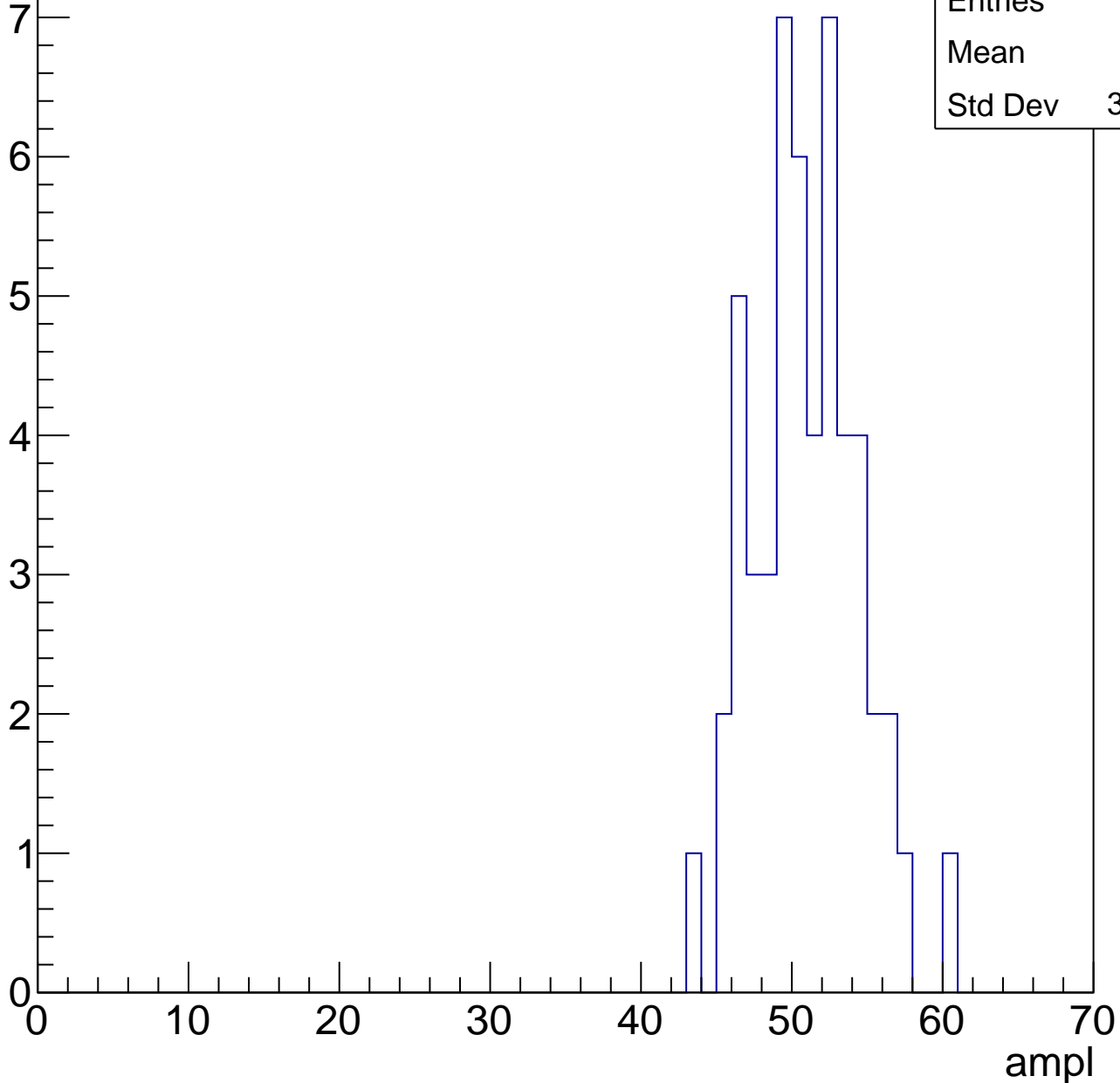


B0L001S, U11-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	50.5
Std Dev	3.439

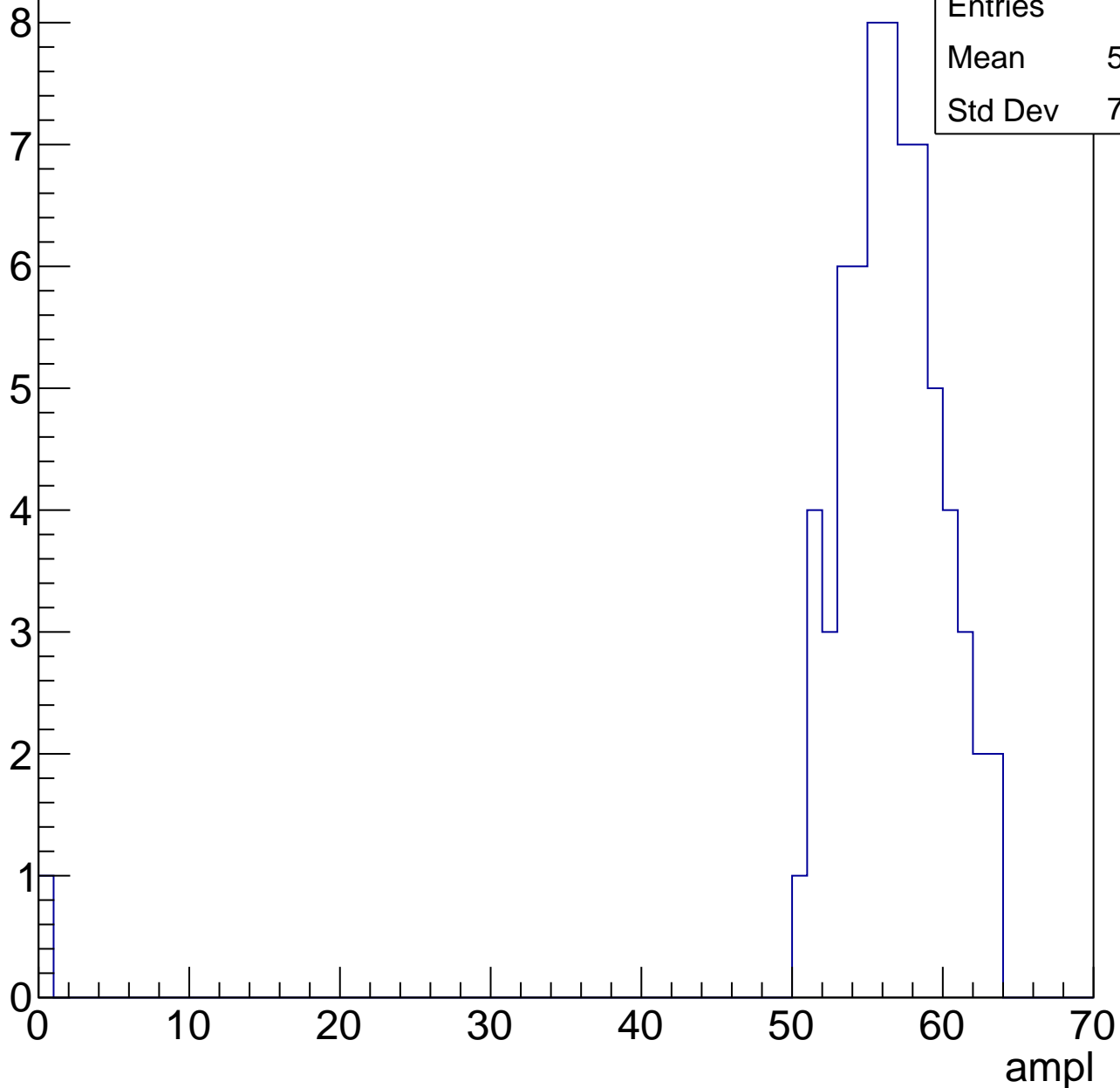


B0L001S, U11-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	55.42
Std Dev	7.504

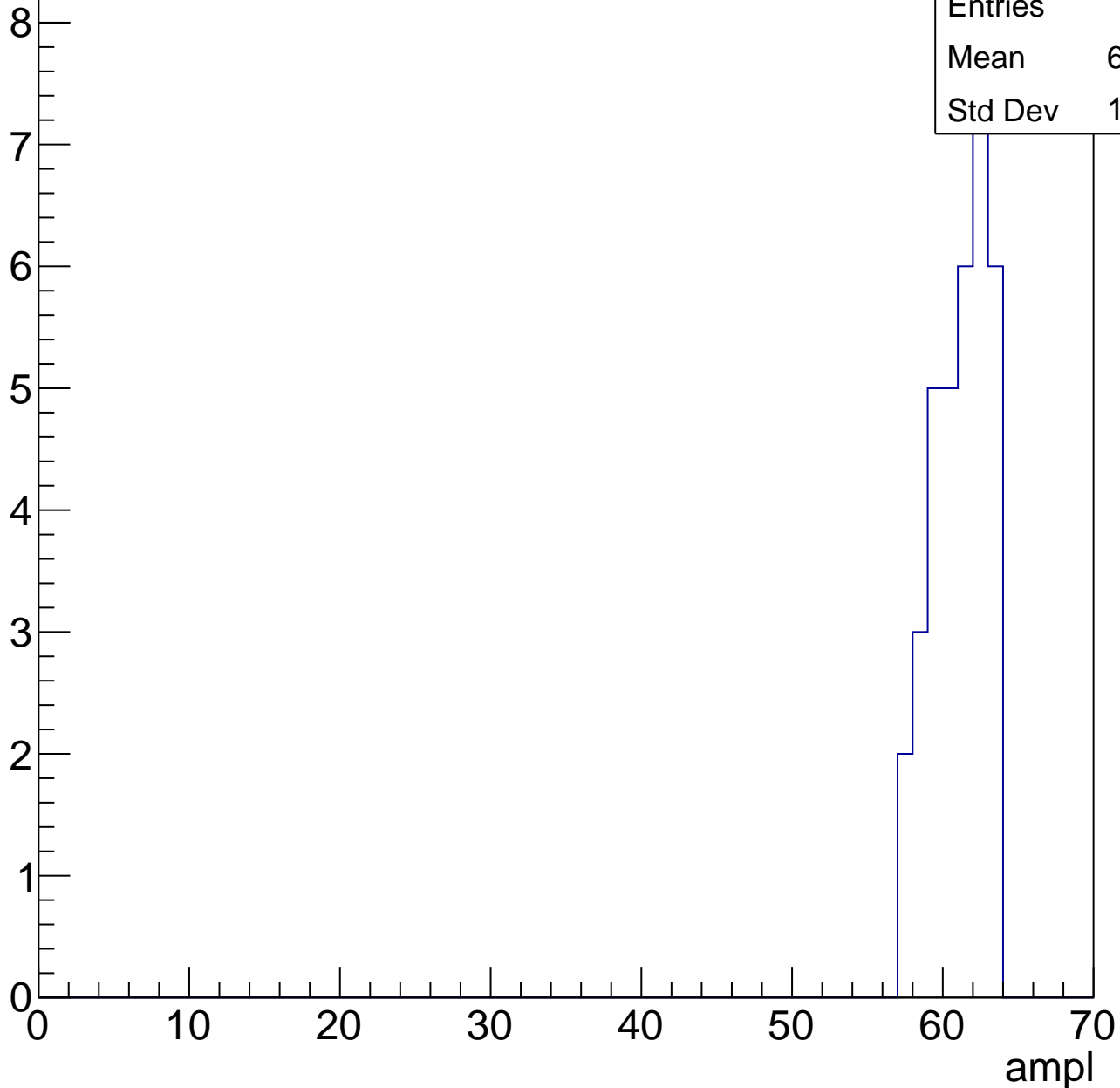


B0L001S, U11-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.66
Std Dev	1.788



B0L001S, U11-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch101, adc0

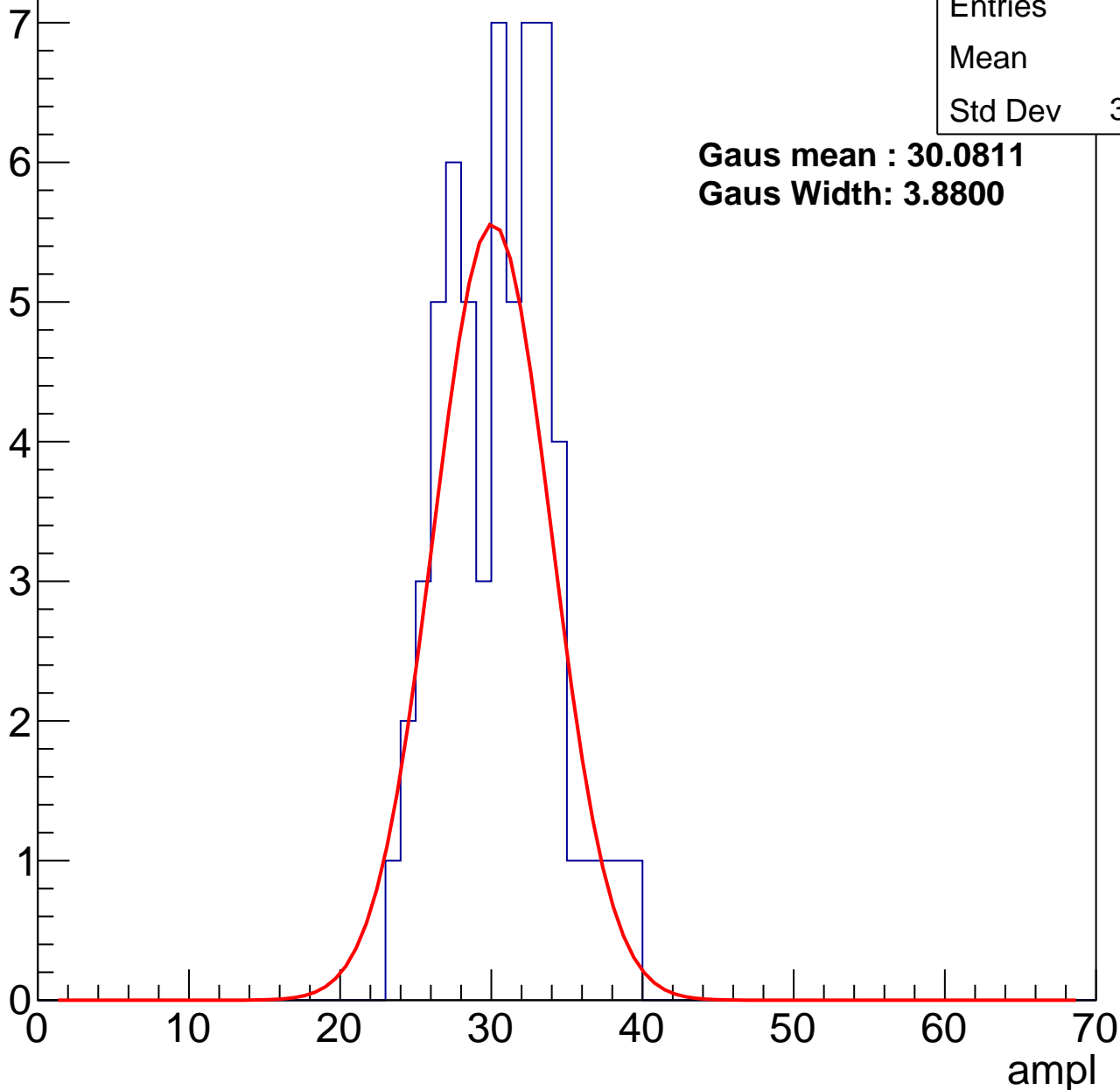
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.1
Std Dev	3.576

Gaus mean : 30.0811

Gaus Width: 3.8800



B0L001S, U11-ch101, adc1

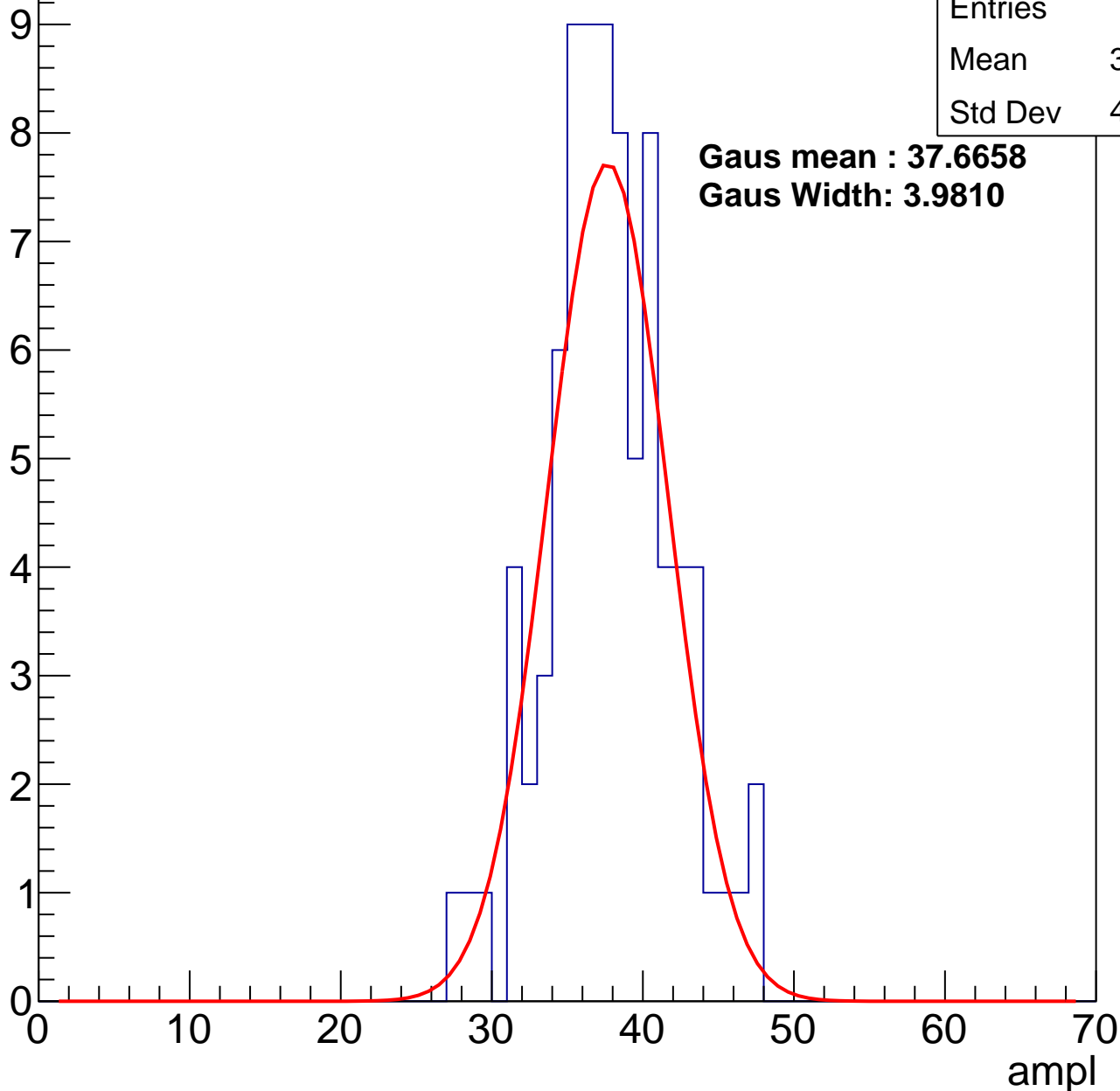
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	37.34
Std Dev	4.075

Gaus mean : 37.6658

Gaus Width: 3.9810



B0L001S, U11-ch101, adc2

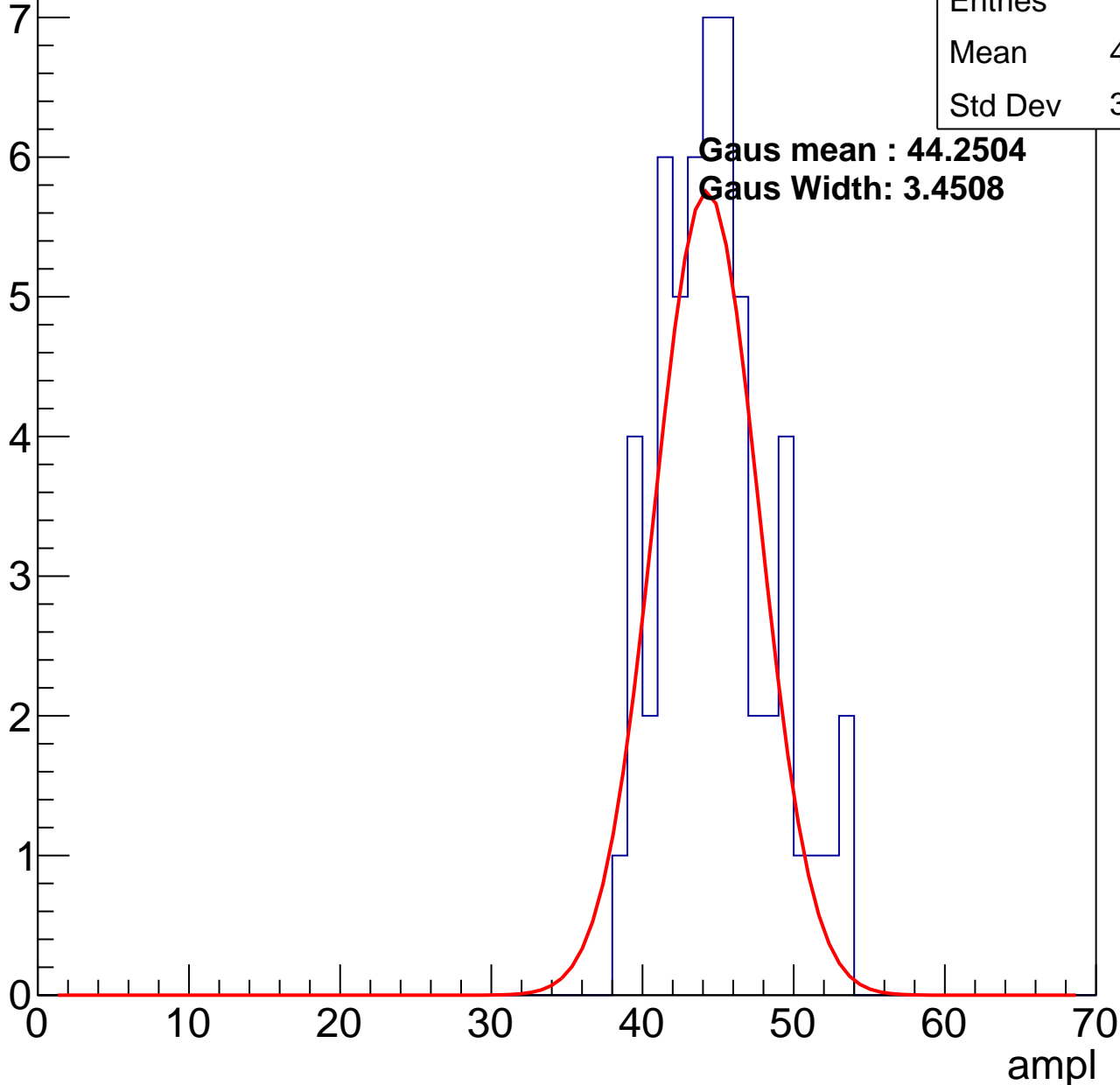
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.39
Std Dev	3.604

Gaus mean : 44.2504

Gaus Width: 3.4508

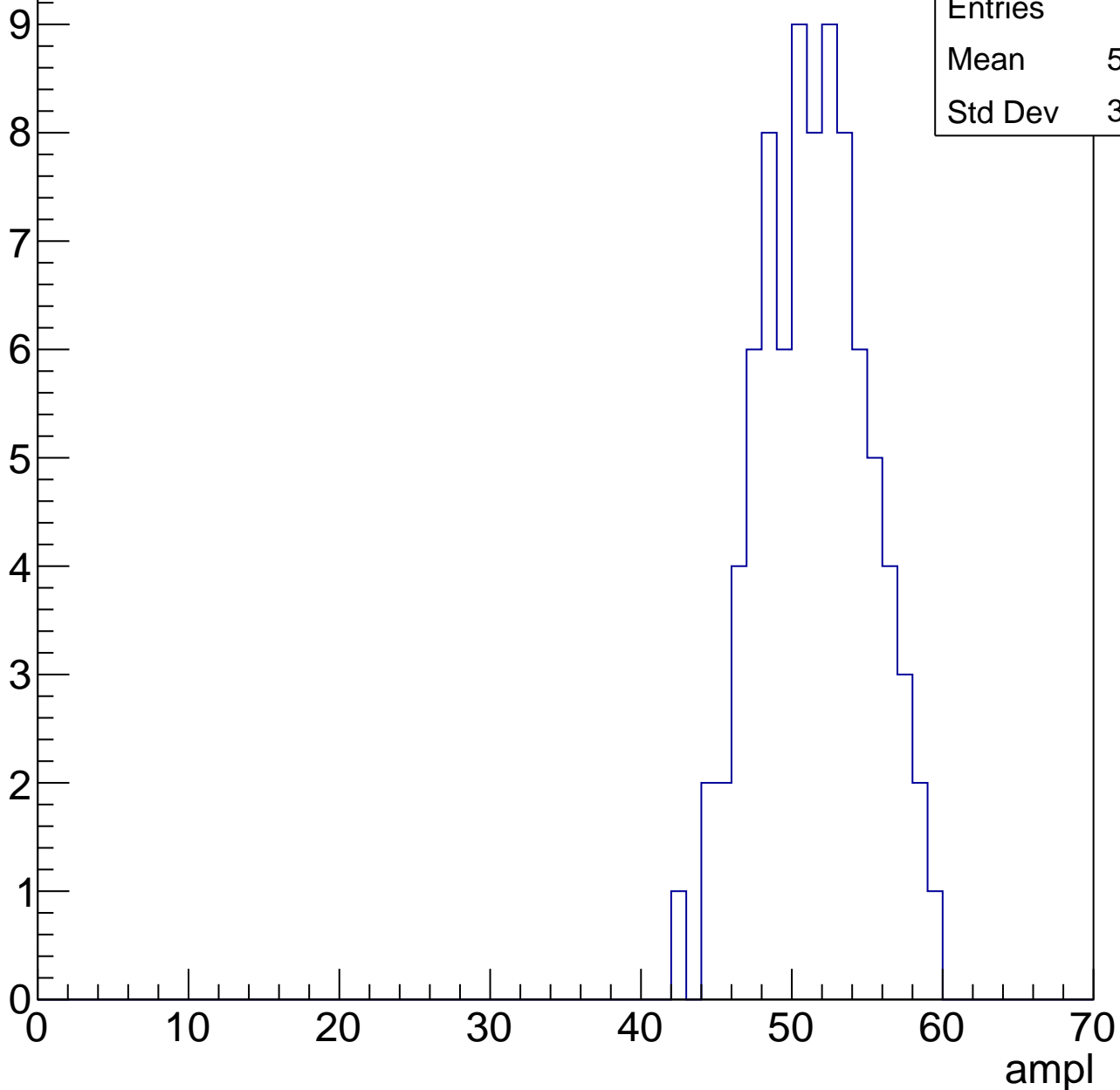


B0L001S, U11-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	50.99
Std Dev	3.614

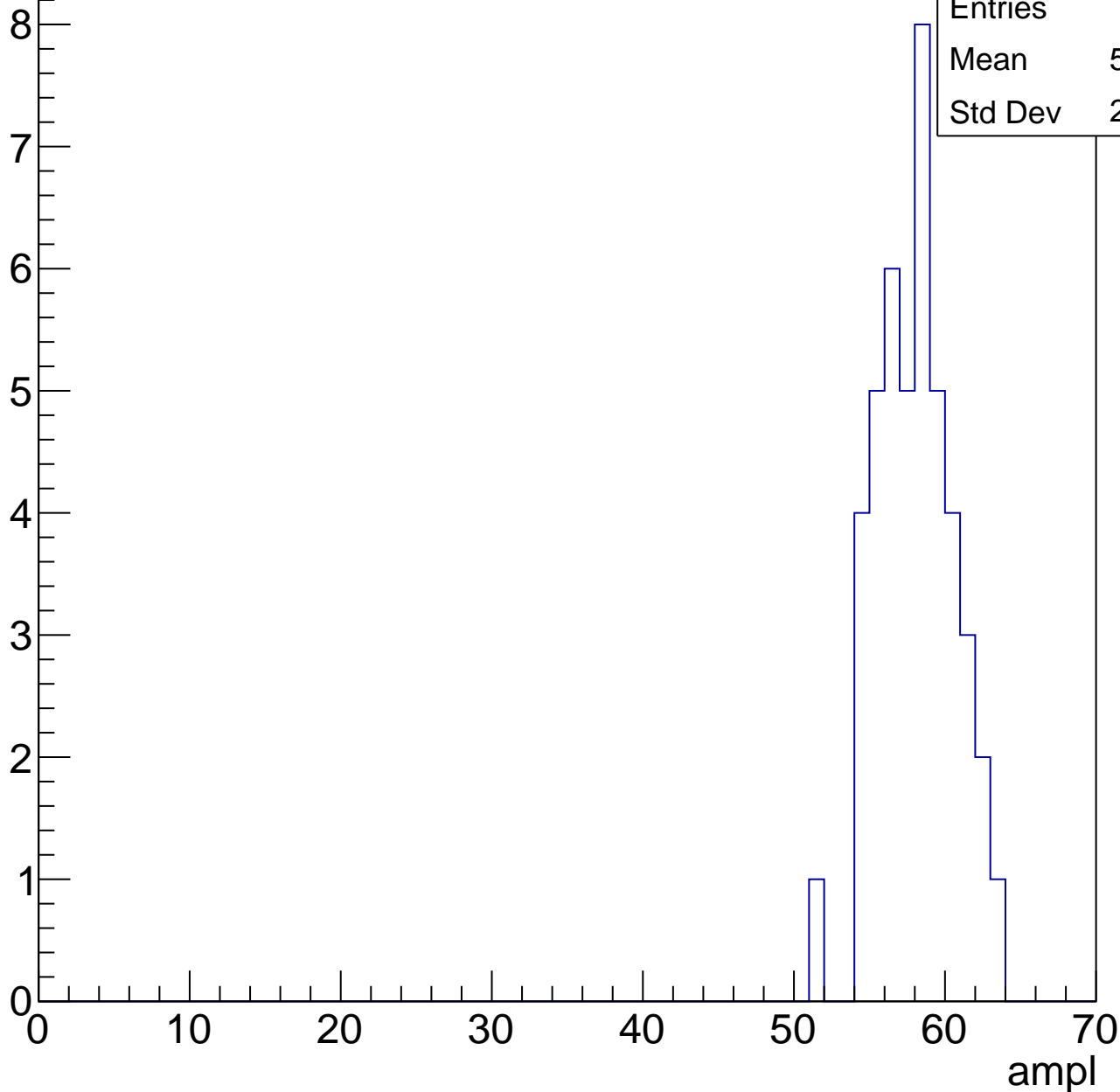


B0L001S, U11-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	57.55
Std Dev	2.536

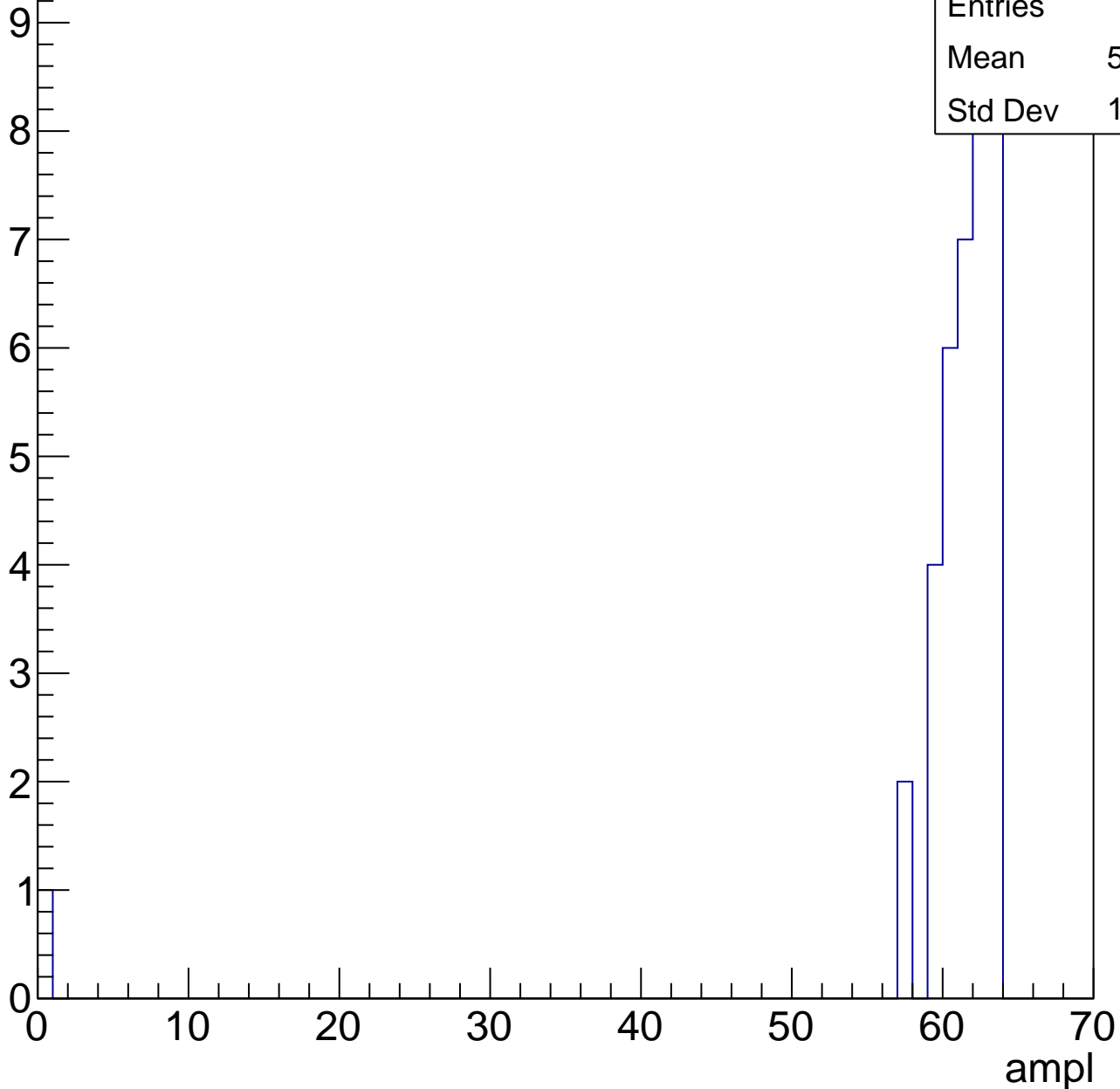


B0L001S, U11-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	59.46
Std Dev	10.04



B0L001S, U11-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch102, adc0

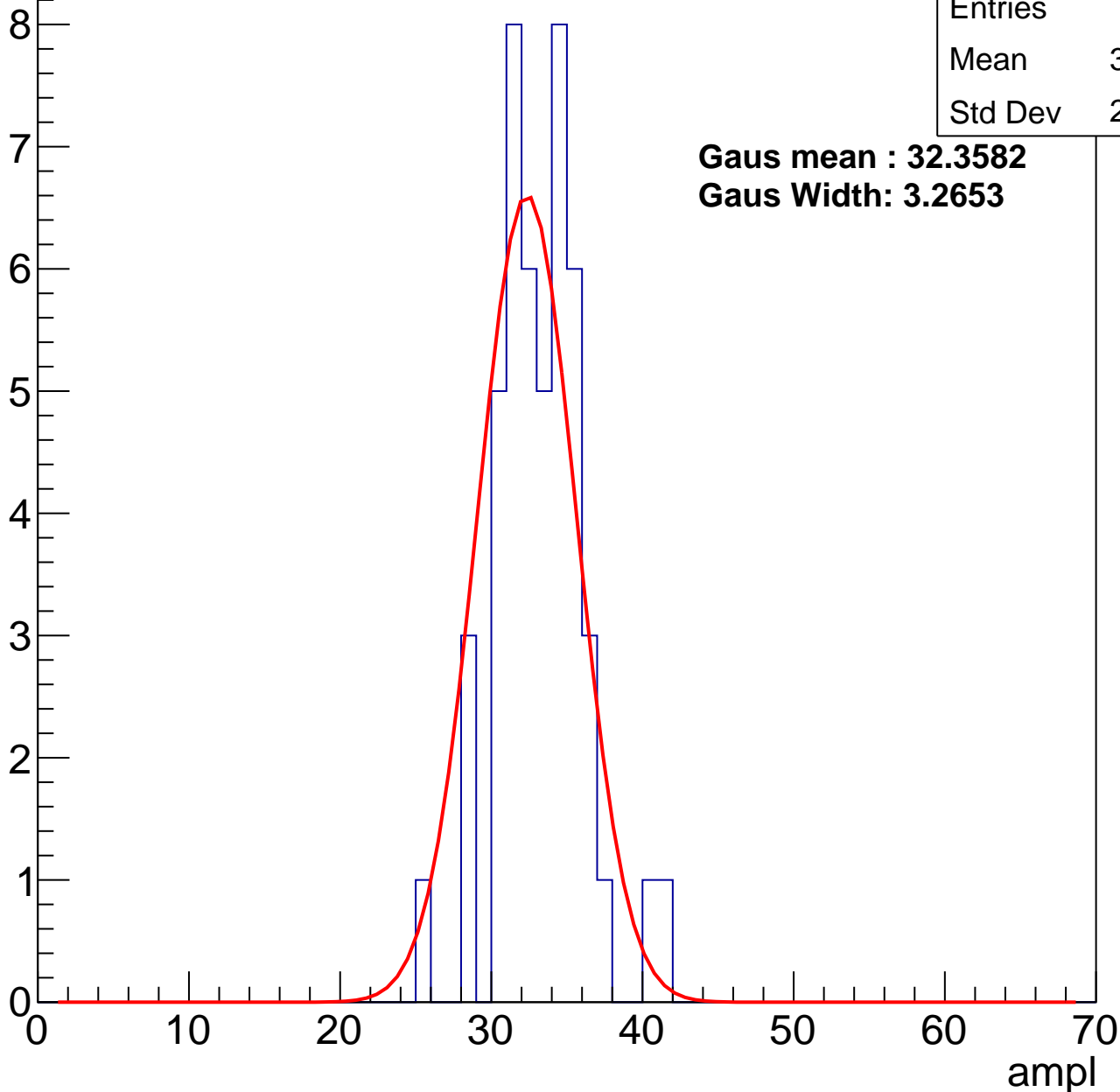
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	32.75
Std Dev	2.912

Gaus mean : 32.3582

Gaus Width: 3.2653



B0L001S, U11-ch102, adc1

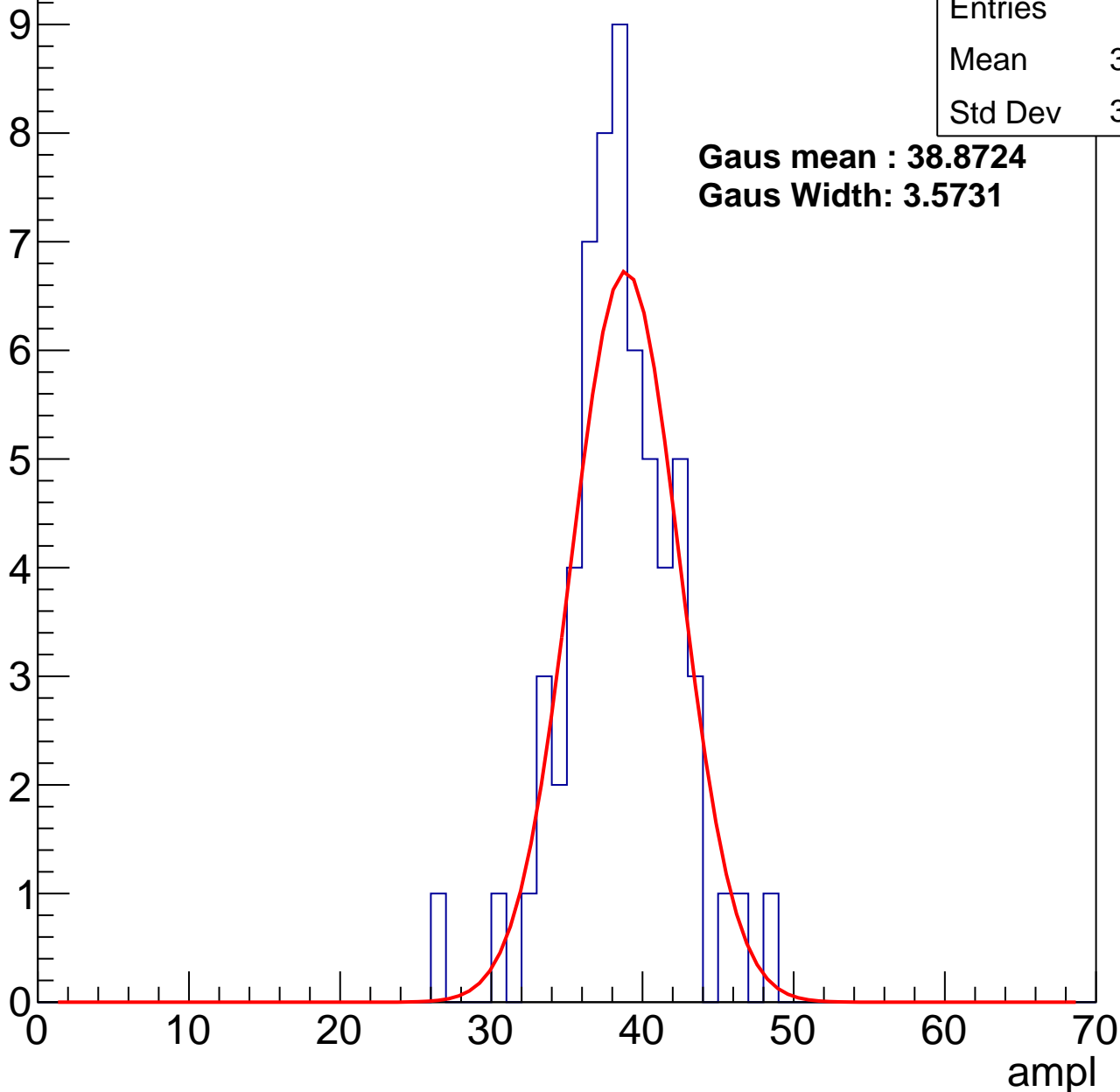
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	38.08
Std Dev	3.708

Gaus mean : 38.8724

Gaus Width: 3.5731



B0L001S, U11-ch102, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

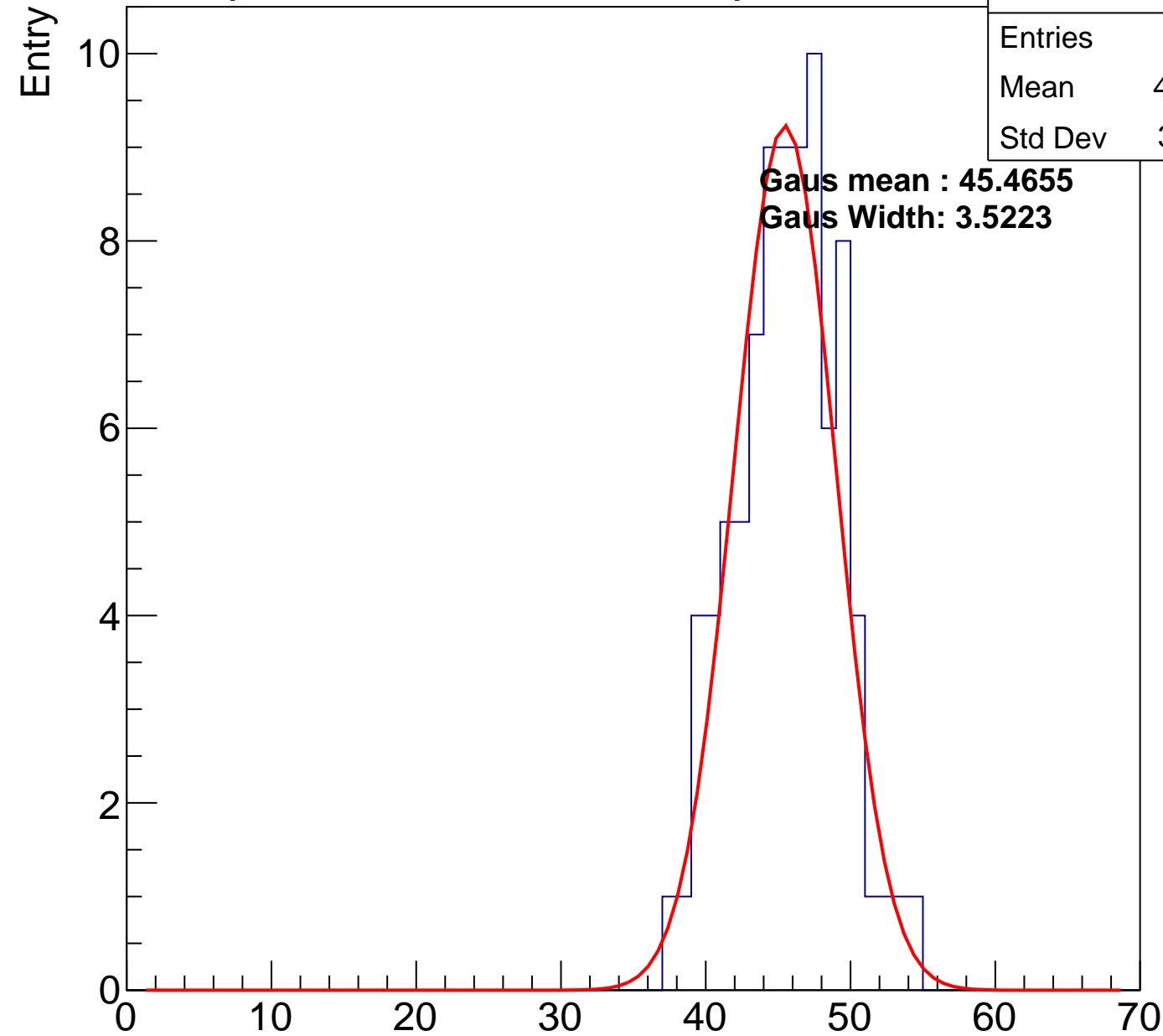
Entries	86
Mean	45.14
Std Dev	3.541

Gaus mean : 45.4655

Gaus Width: 3.5223

10
8
6
4
2
0

ampl

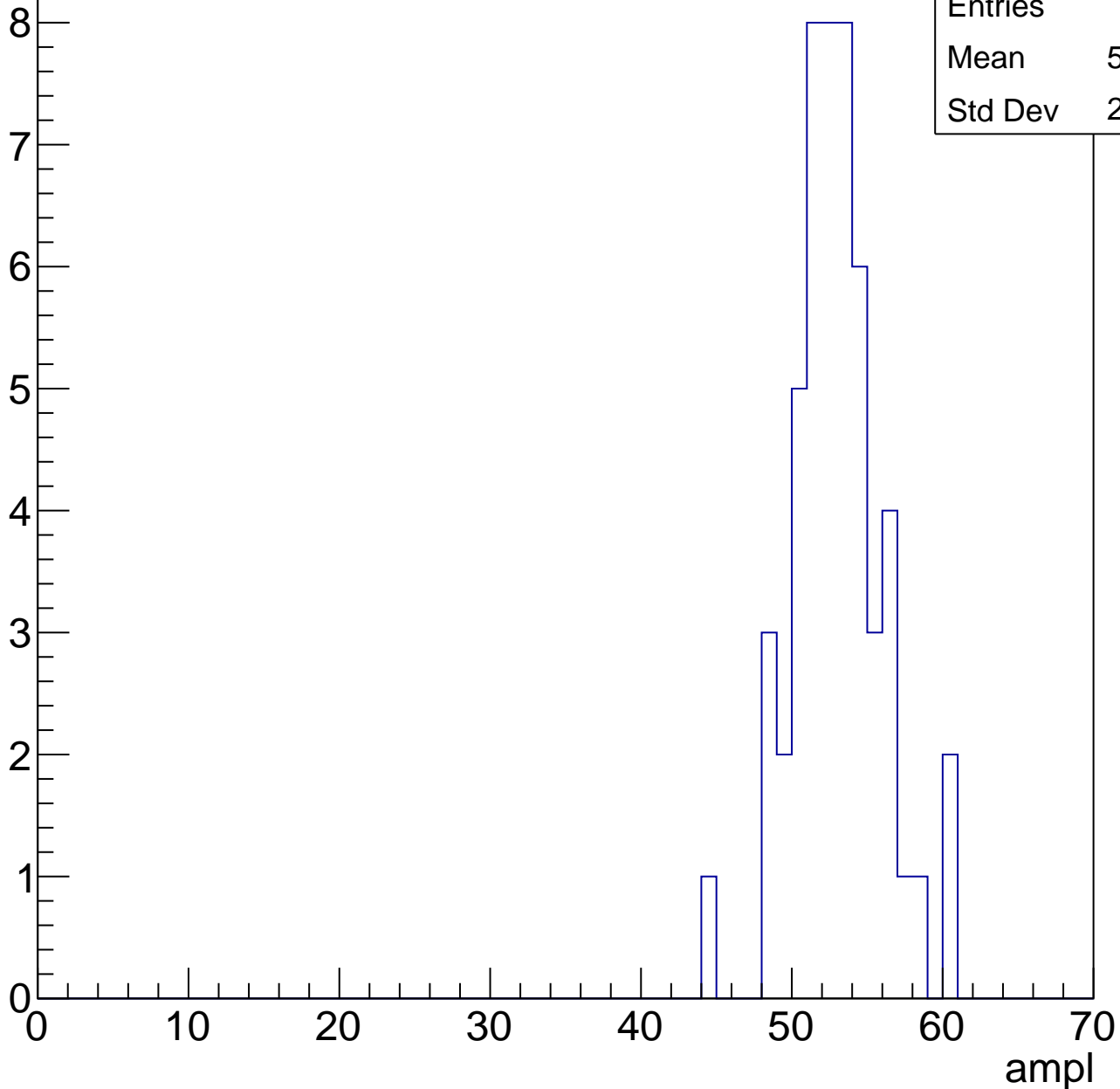


B0L001S, U11-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

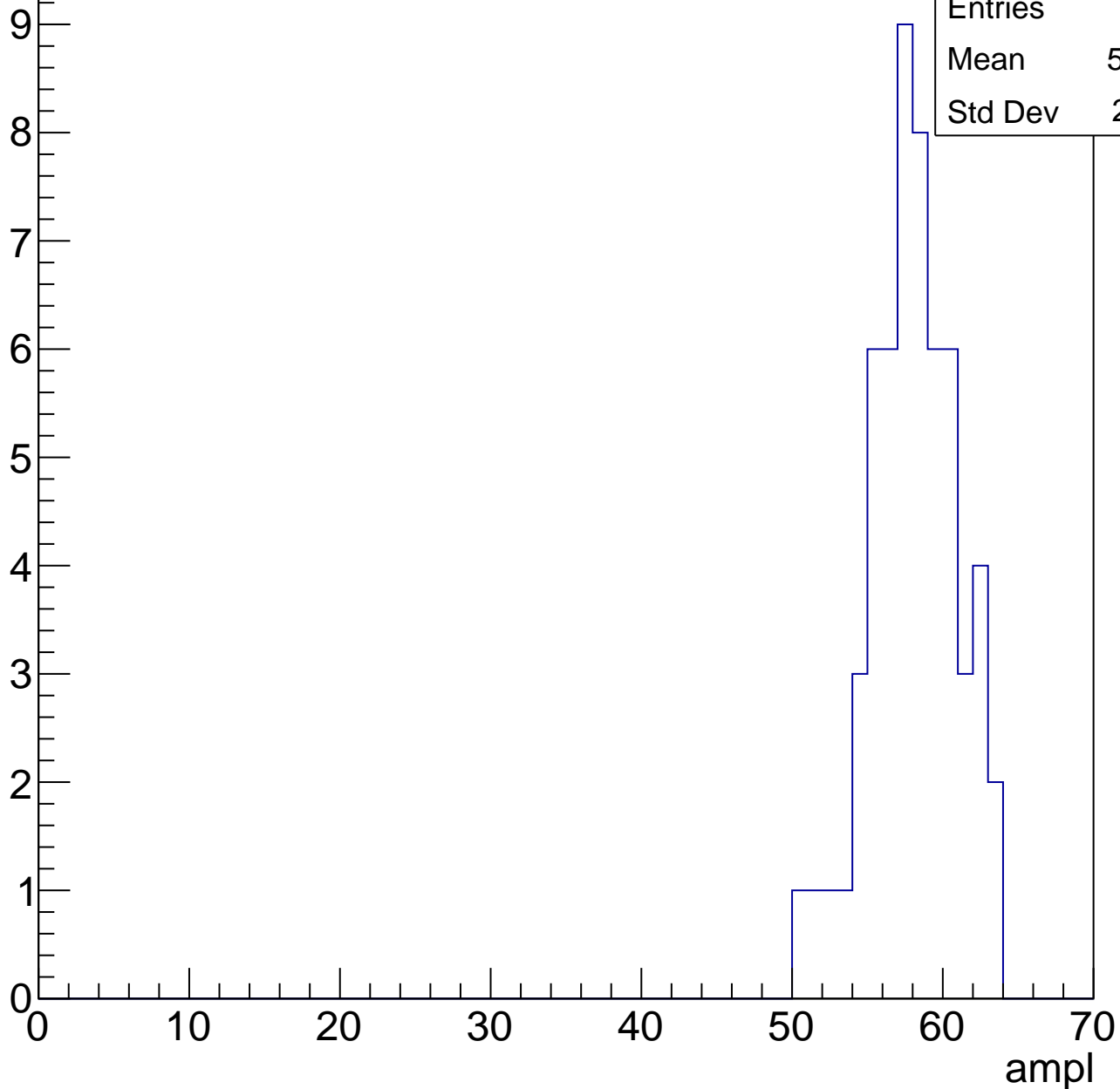
Entries	52
Mean	52.54
Std Dev	2.958



B0L001S, U11-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



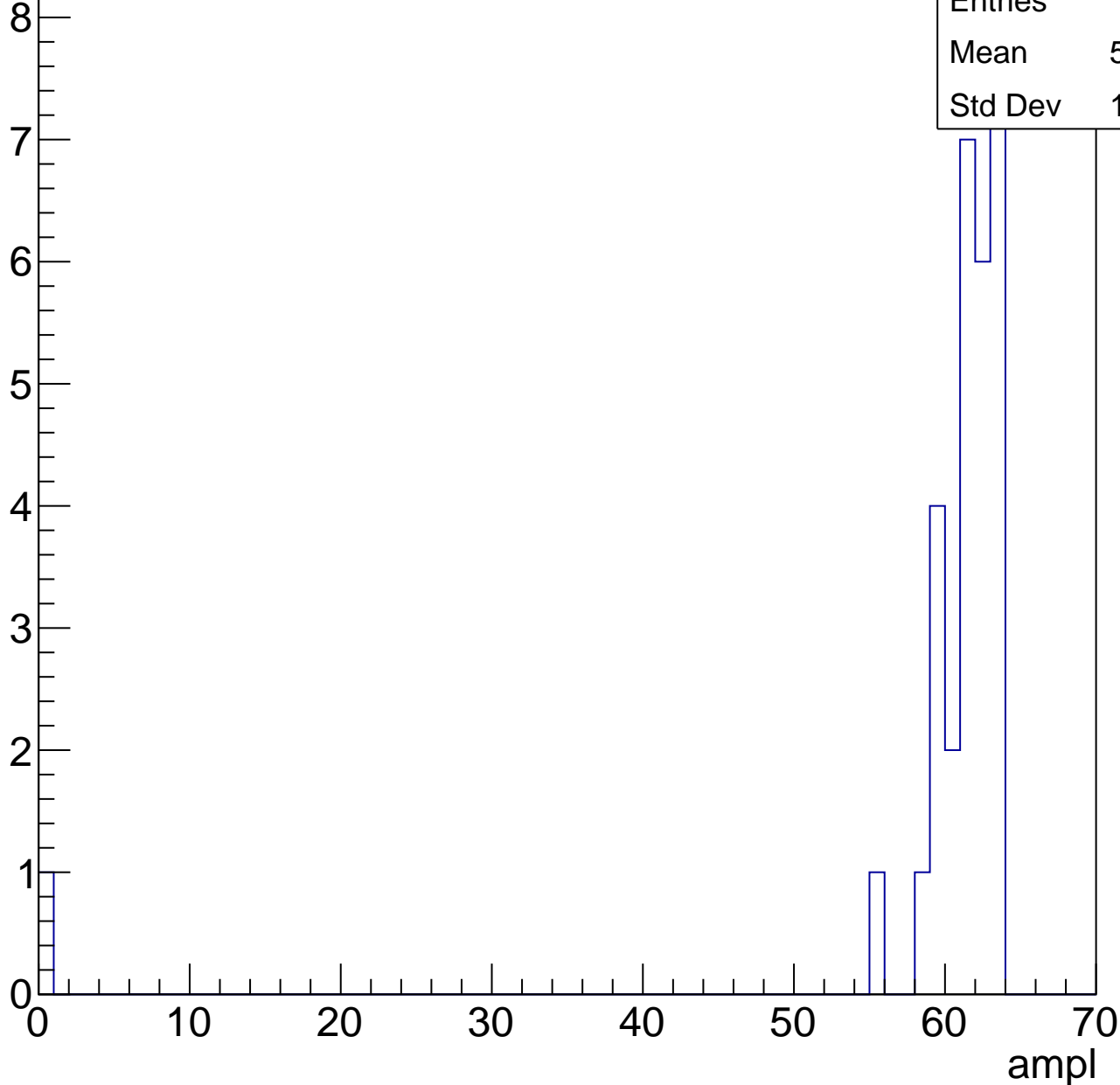
Entries	57
Mean	57.58
Std Dev	2.871

B0L001S, U11-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.07
Std Dev	11.12



B0L001S, U11-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch103, adc0

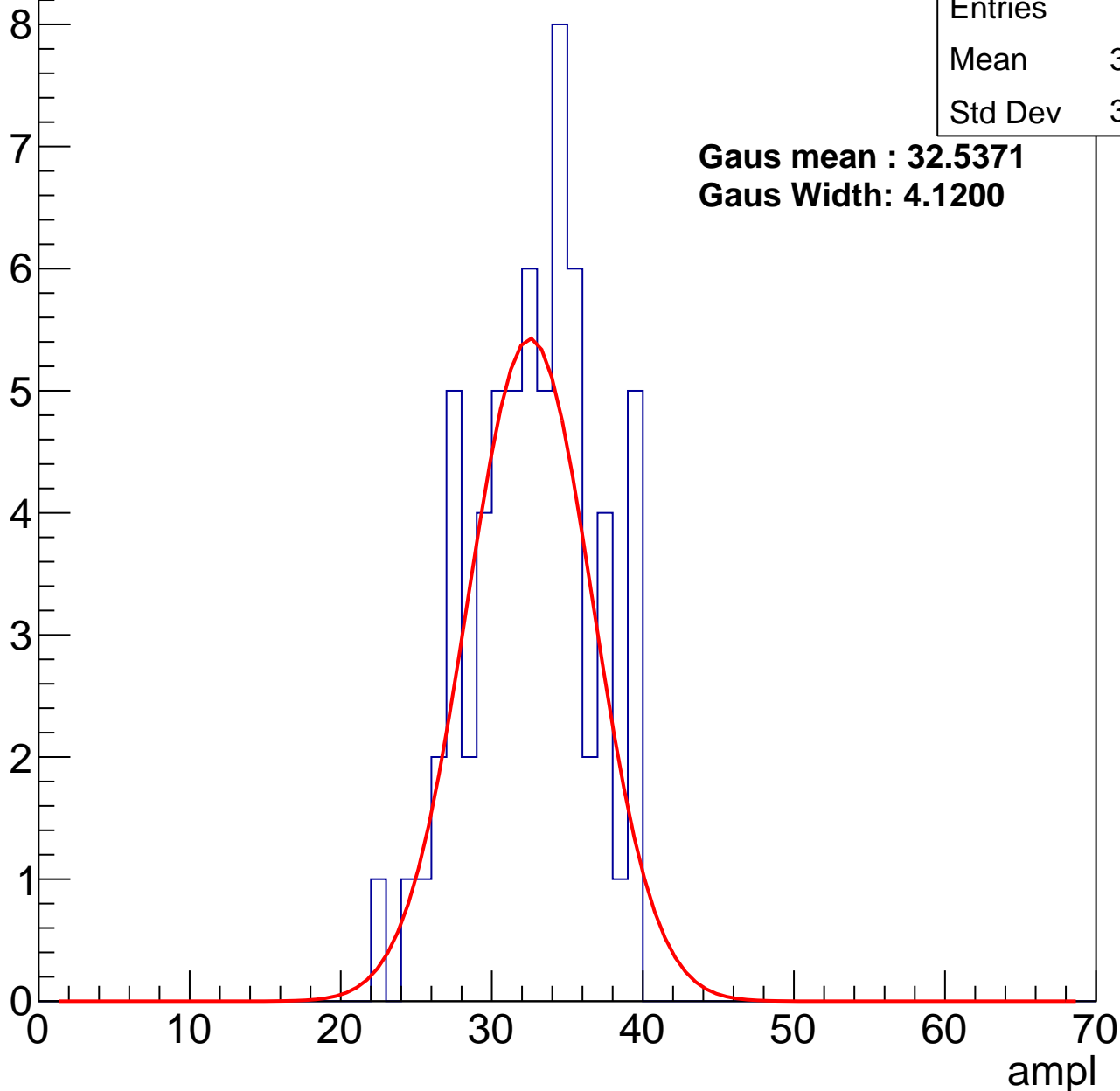
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	32.17
Std Dev	3.994

Gaus mean : 32.5371

Gaus Width: 4.1200



B0L001S, U11-ch103, adc1

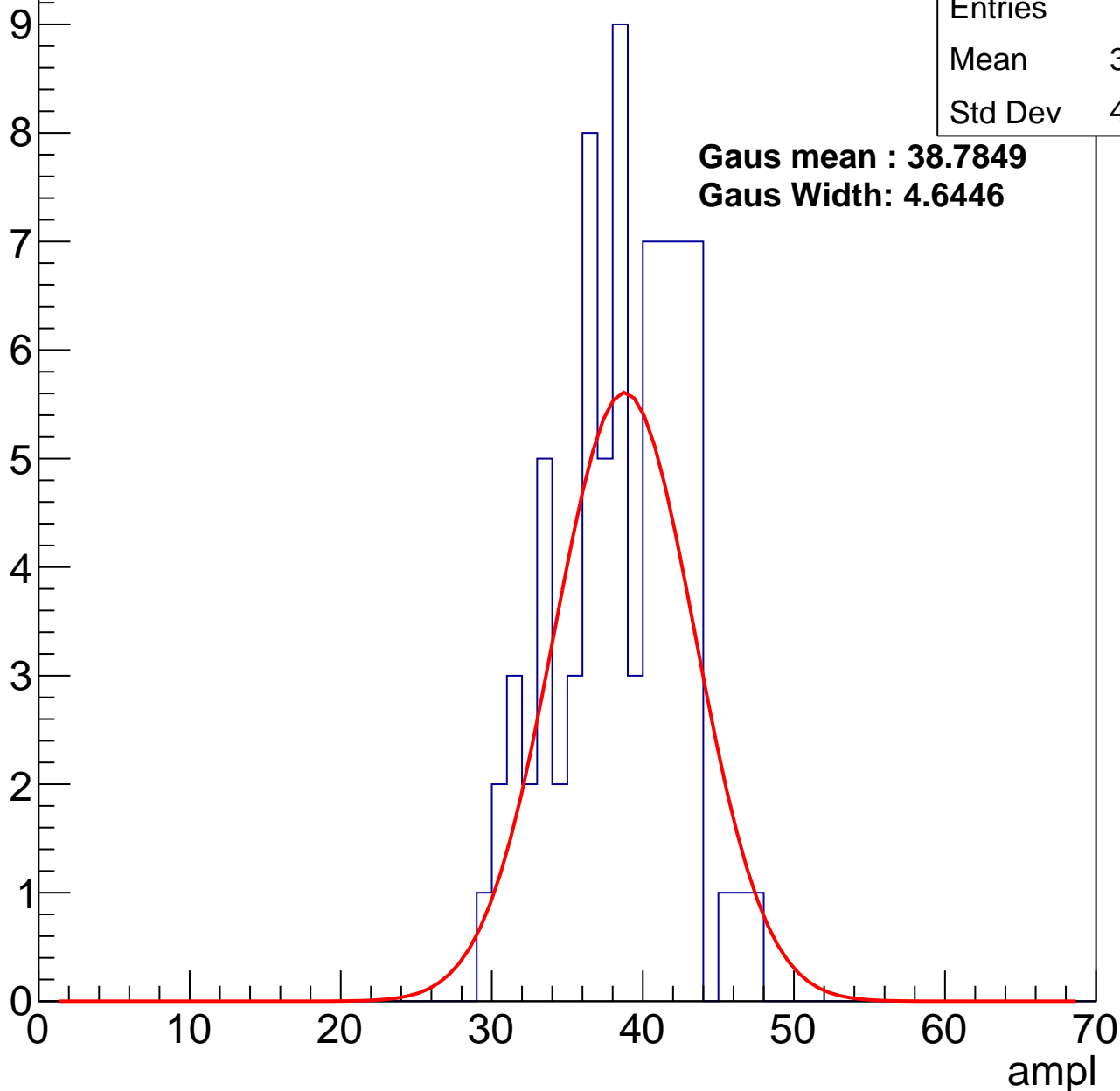
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.05
Std Dev	4.067

Gaus mean : 38.7849

Gaus Width: 4.6446



B0L001S, U11-ch103, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	54
Mean	45.04
Std Dev	3.322

Gaus mean : 45.4973

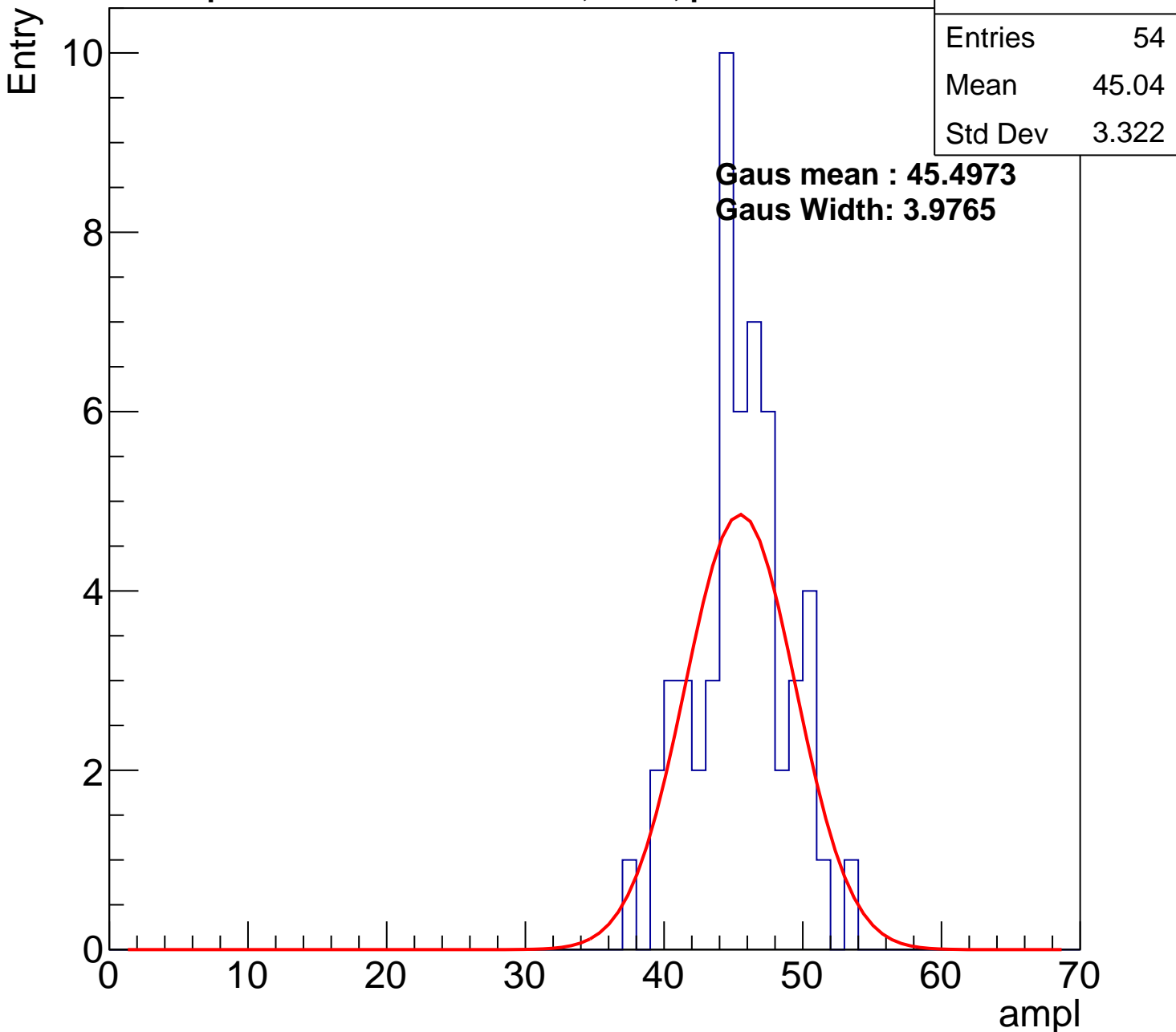
Gaus Width: 3.9765

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

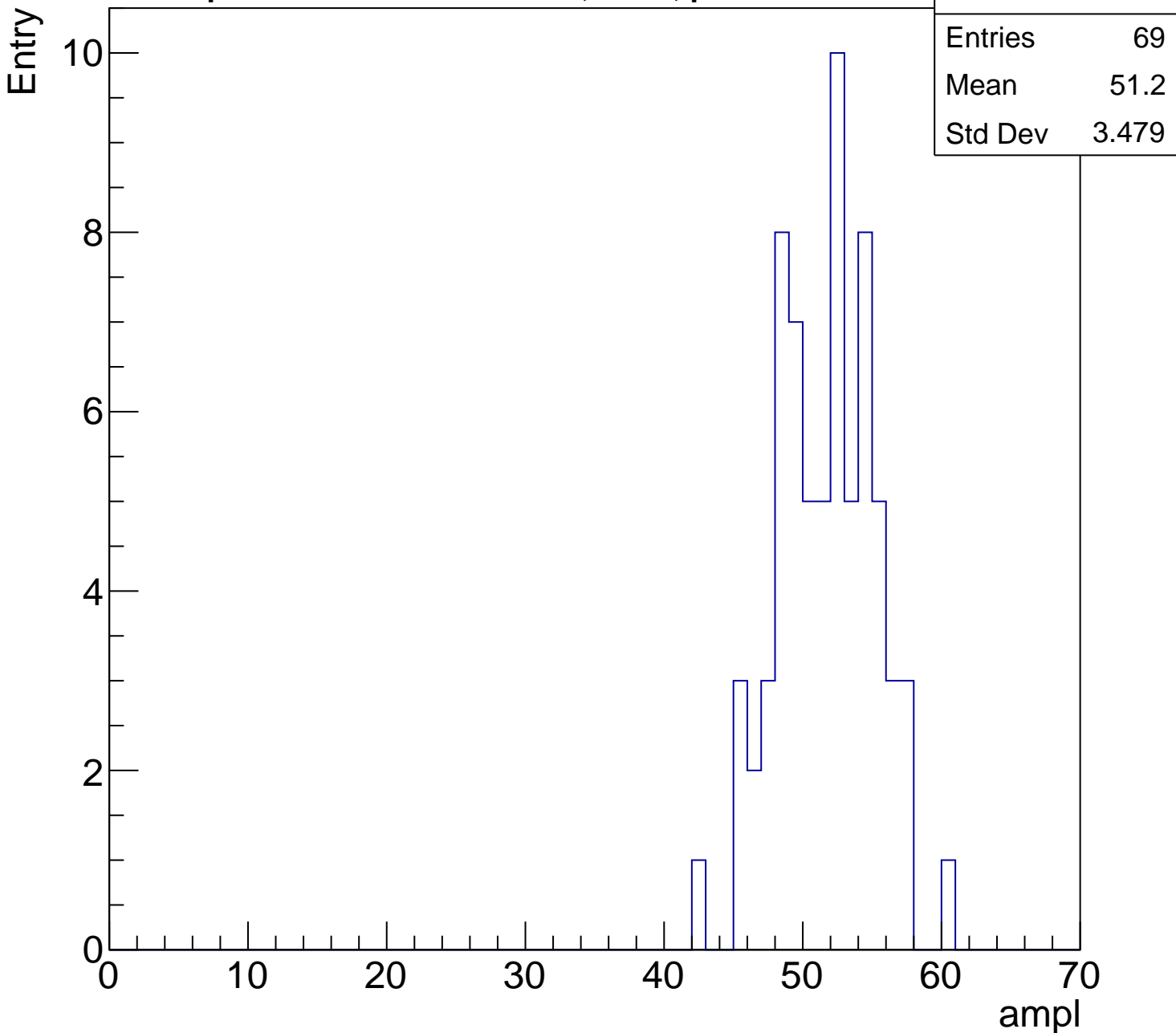
Entries	69
Mean	51.2
Std Dev	3.479

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

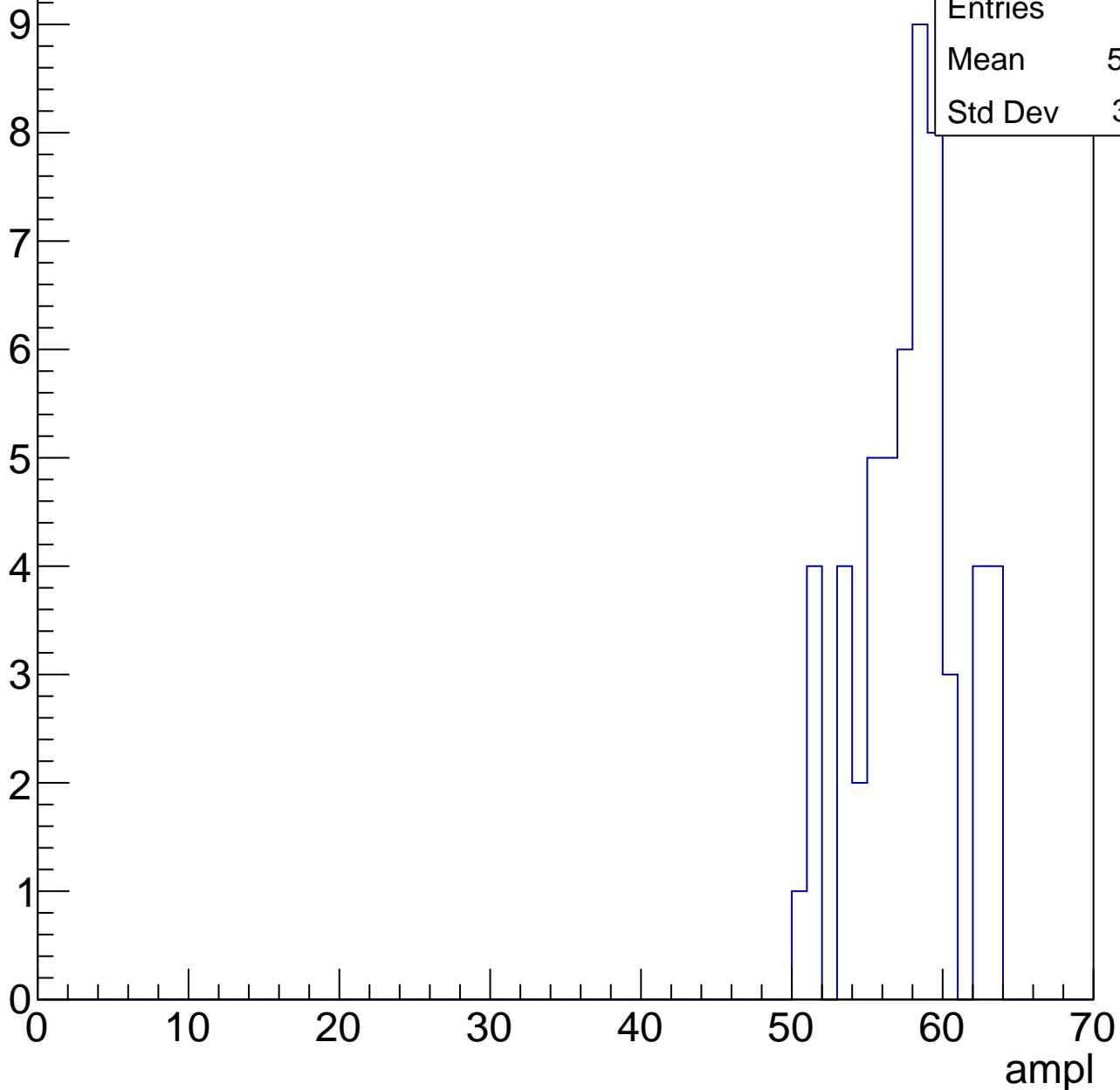


B0L001S, U11-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.18
Std Dev	3.331

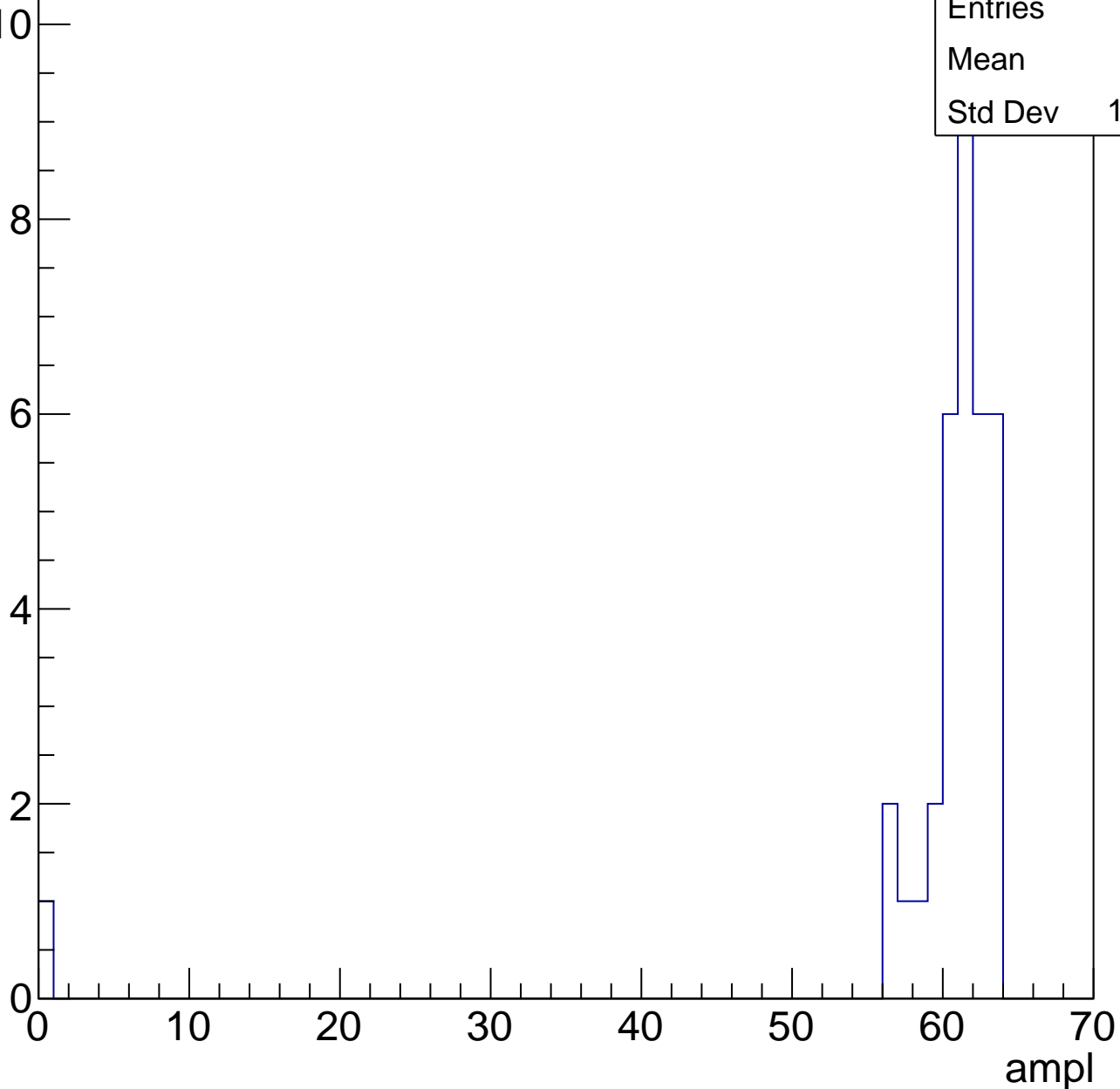


B0L001S, U11-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	59
Std Dev	10.28



B0L001S, U11-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch104, adc0

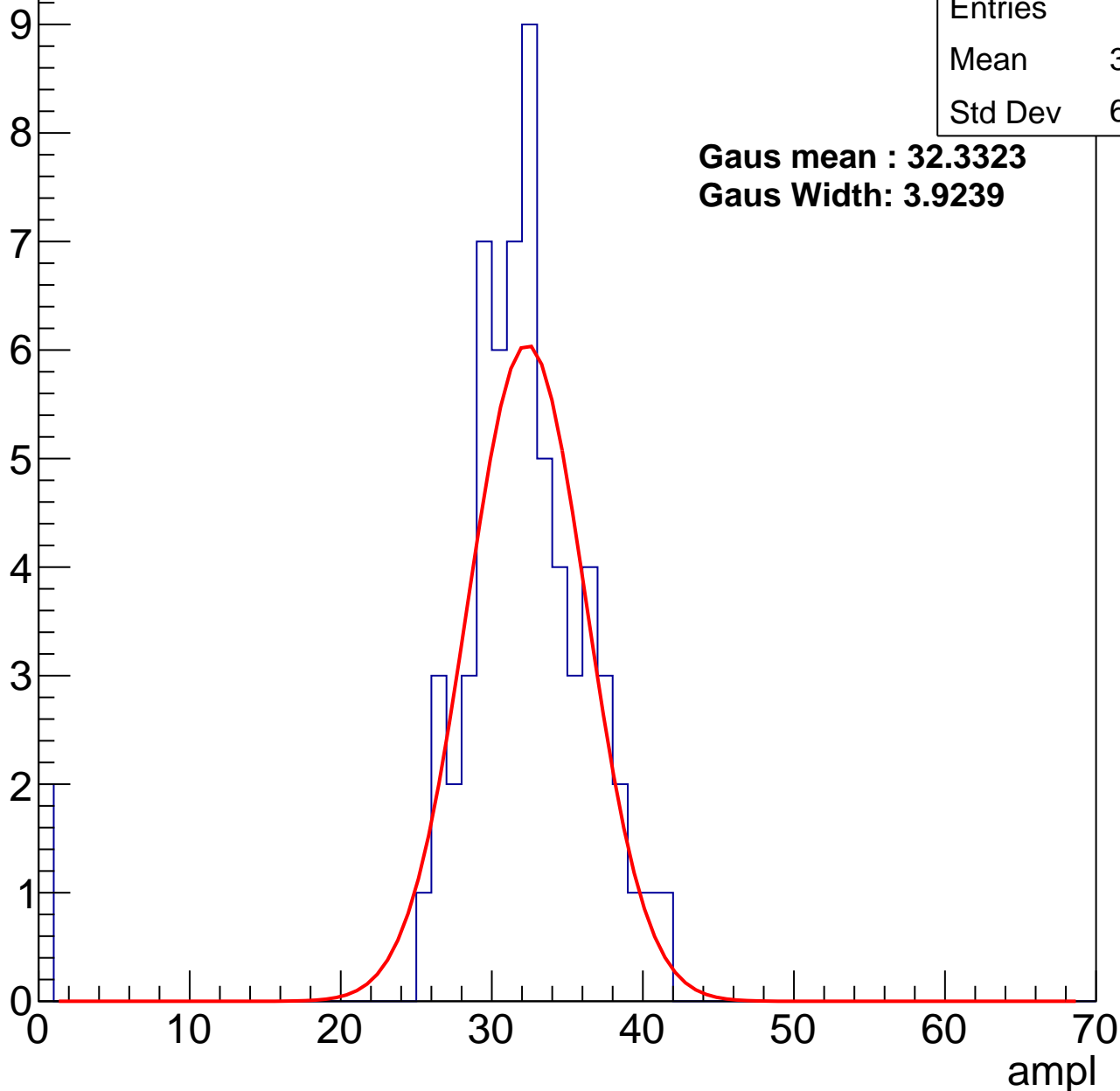
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.03
Std Dev	6.602

Gaus mean : 32.3323

Gaus Width: 3.9239



B0L001S, U11-ch104, adc1

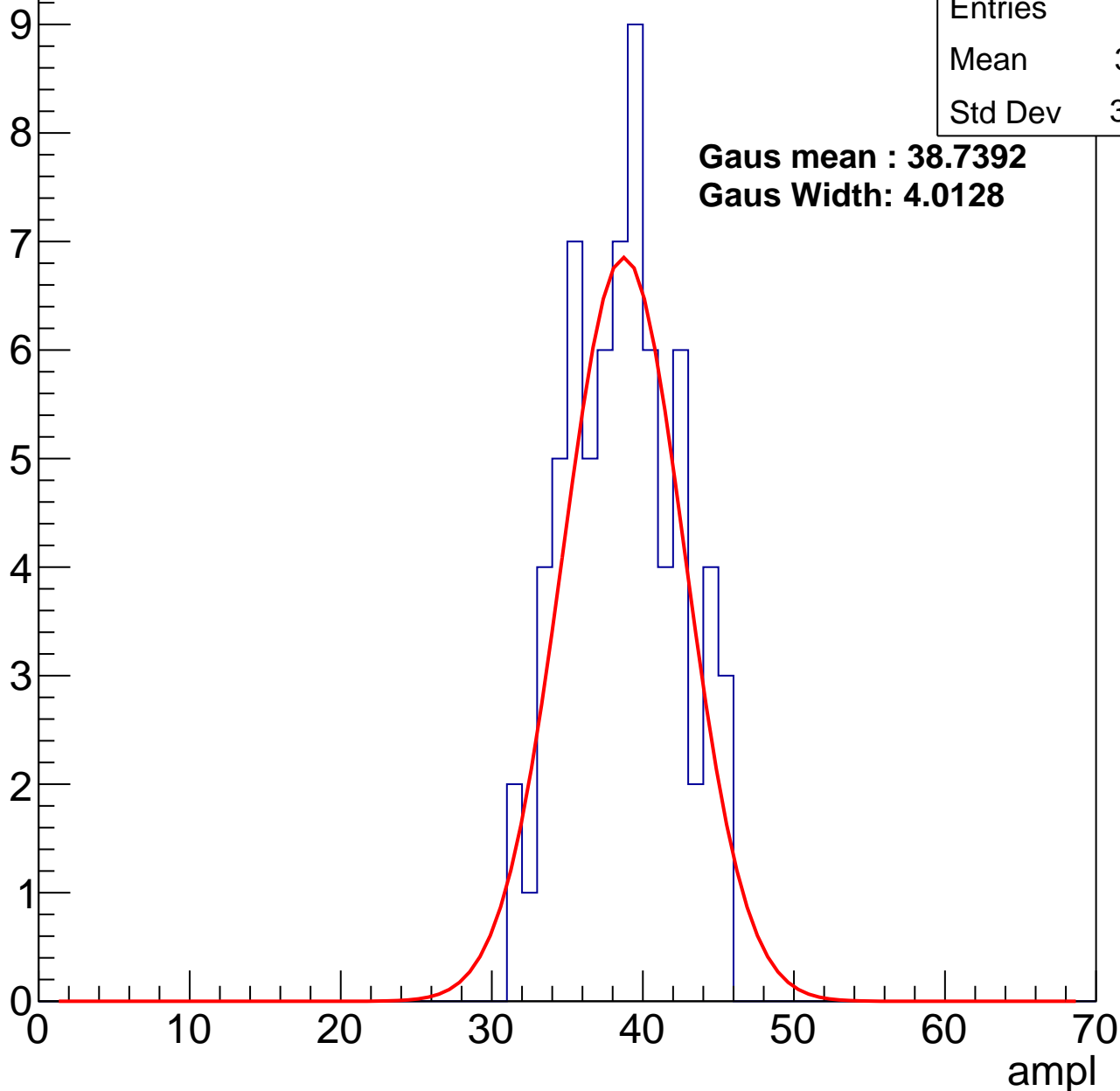
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.21
Std Dev	3.572

Gaus mean : 38.7392

Gaus Width: 4.0128



B0L001S, U11-ch104, adc2

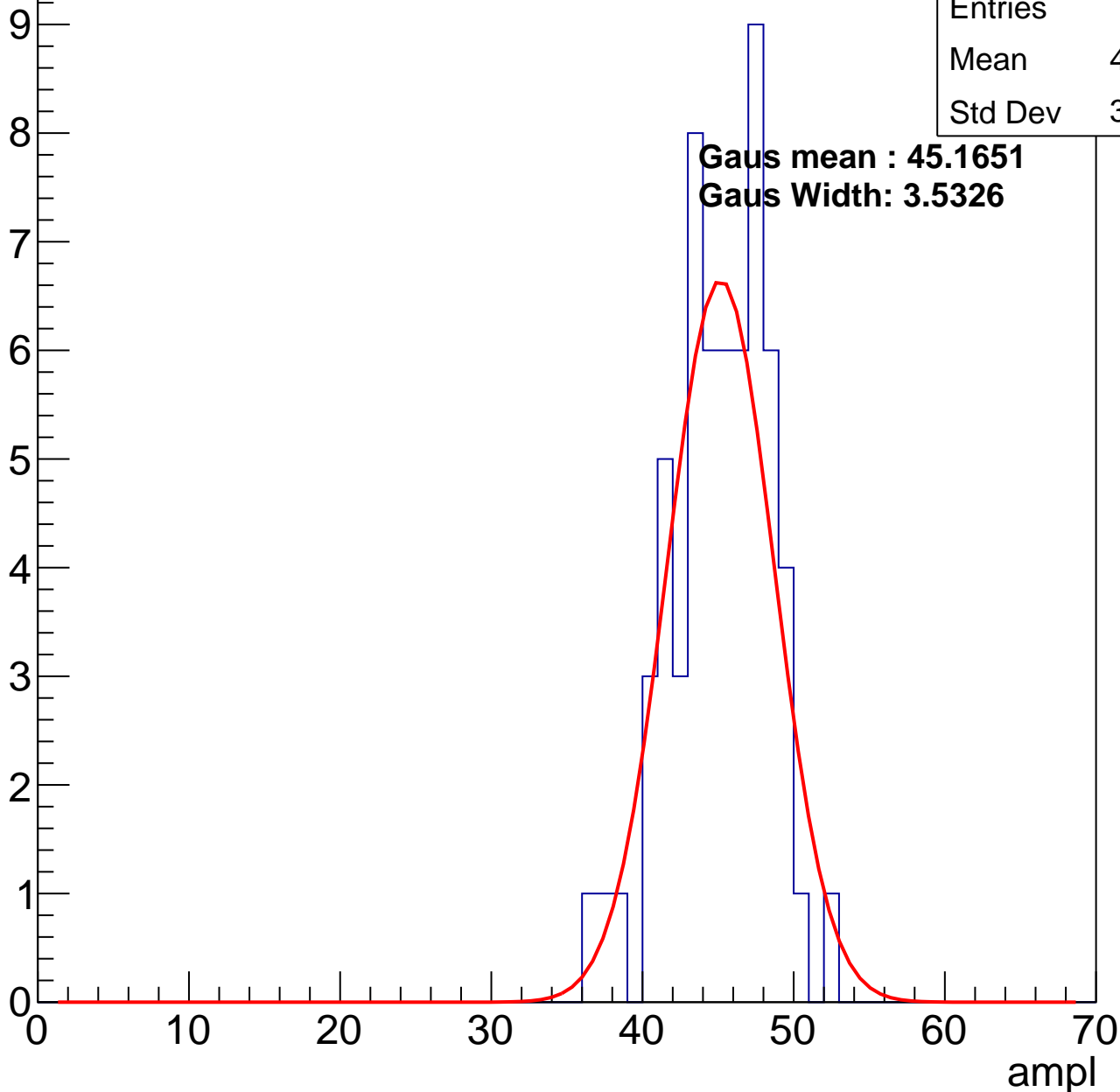
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.67
Std Dev	3.243

Gaus mean : 45.1651

Gaus Width: 3.5326



B0L001S, U11-ch104, adc3

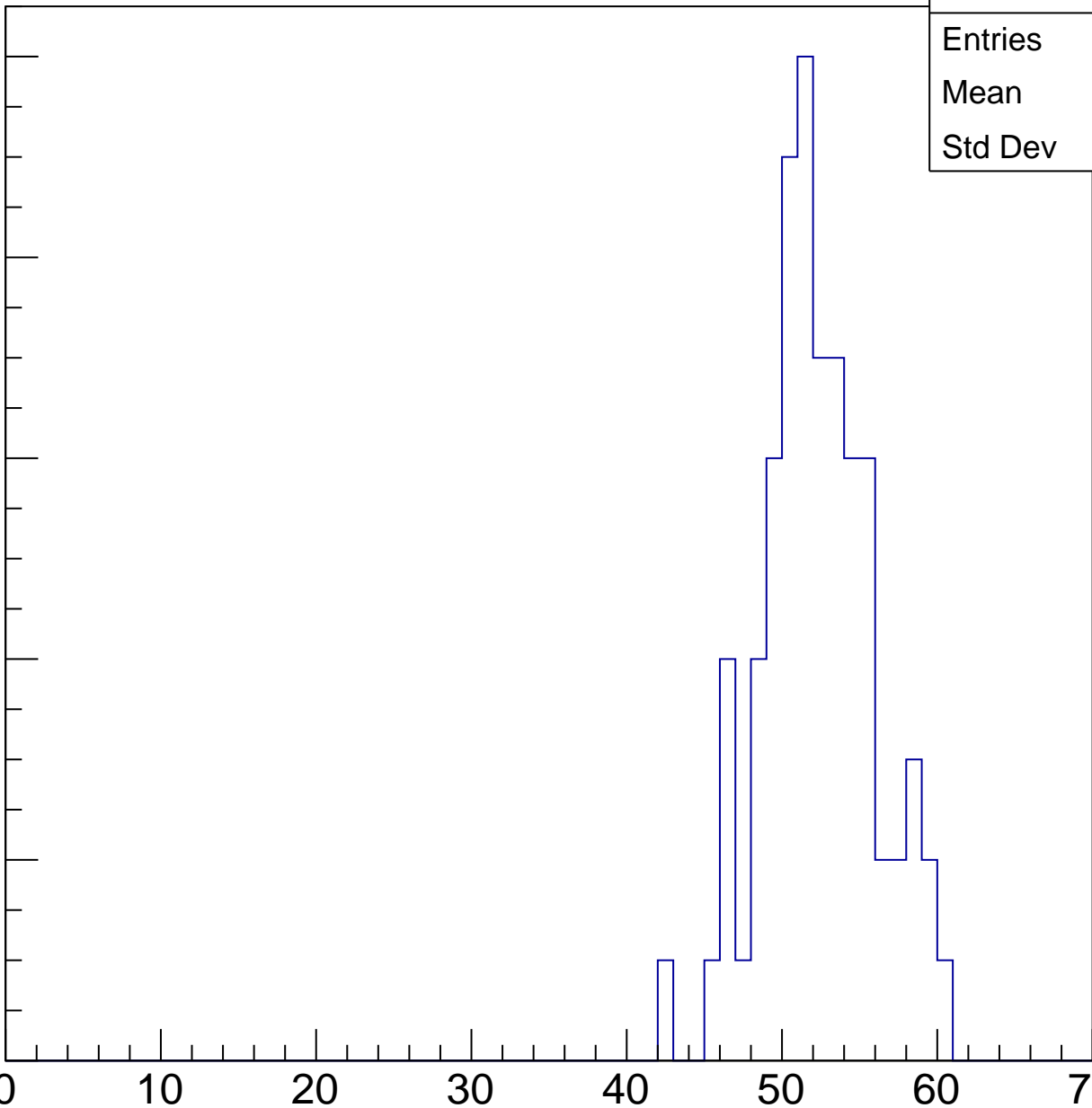
calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	51.82
Std Dev	3.568

Entry

10
8
6
4
2
0

ampl

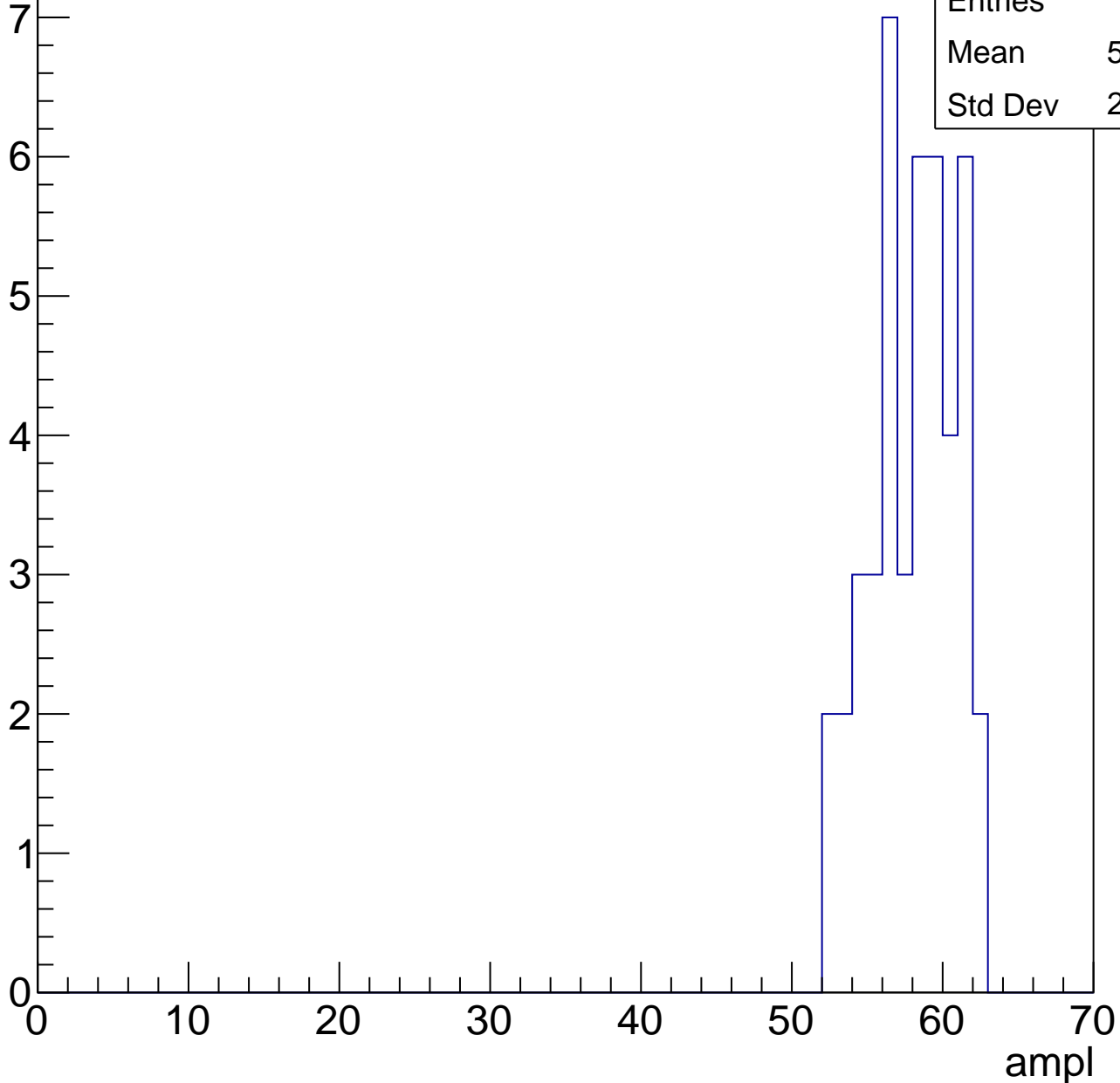


B0L001S, U11-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	57.55
Std Dev	2.726

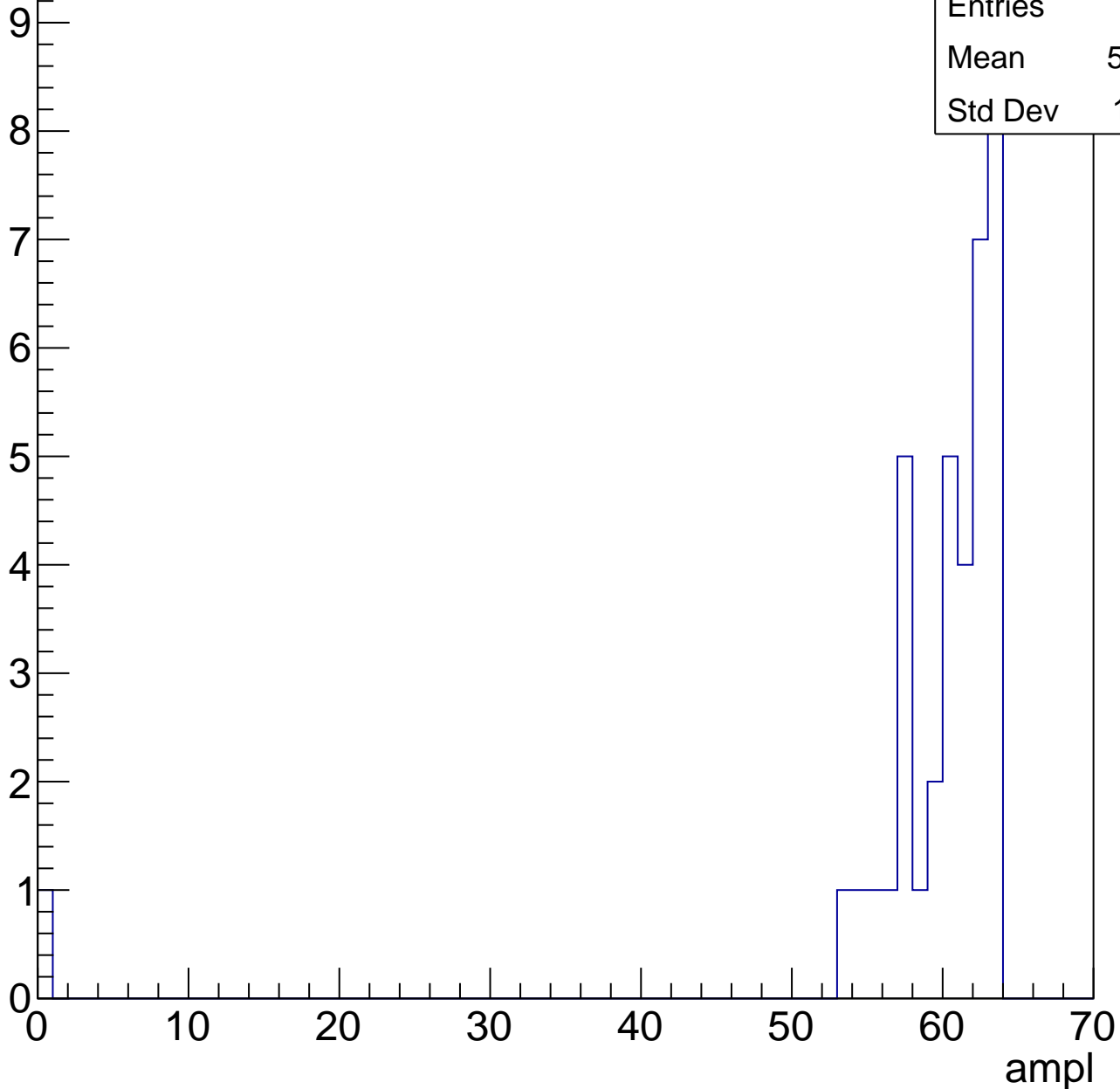


B0L001S, U11-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.53
Std Dev	10.01



B0L001S, U11-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

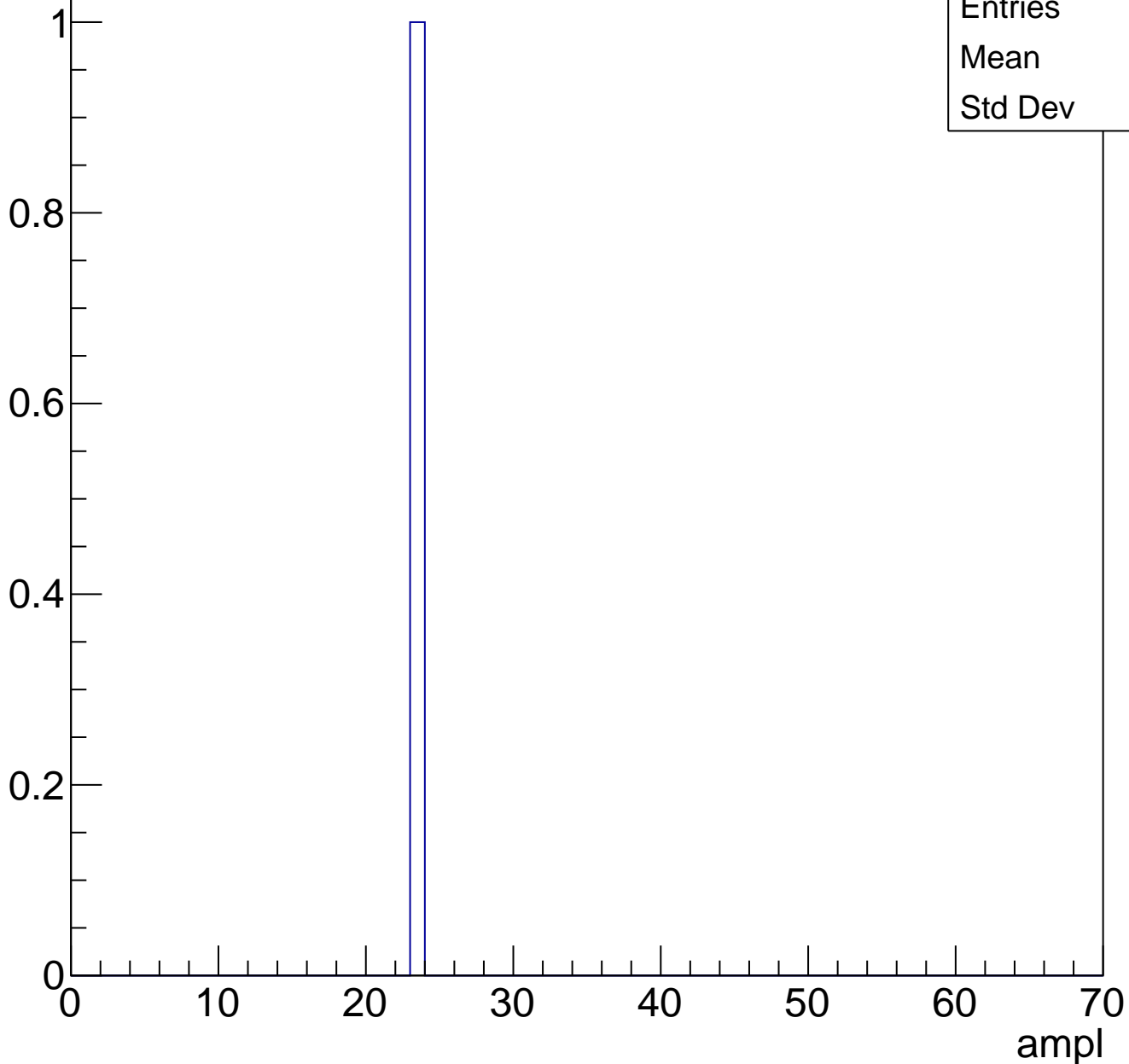
Entry



B0L001S, U11-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U11-ch105, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	31.4
Std Dev	7.331

Gaus mean : 32.7524

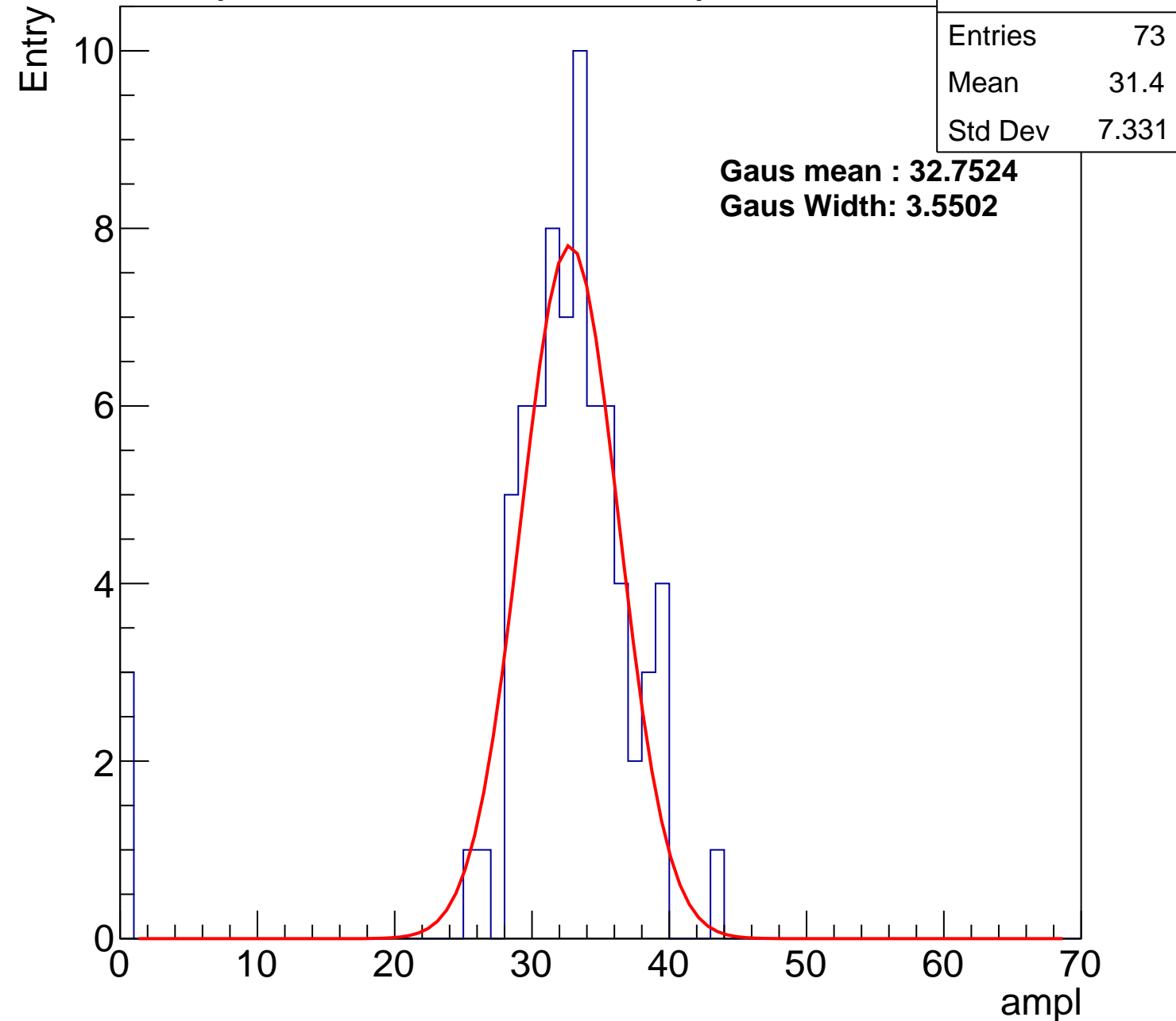
Gaus Width: 3.5502

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch105, adc1

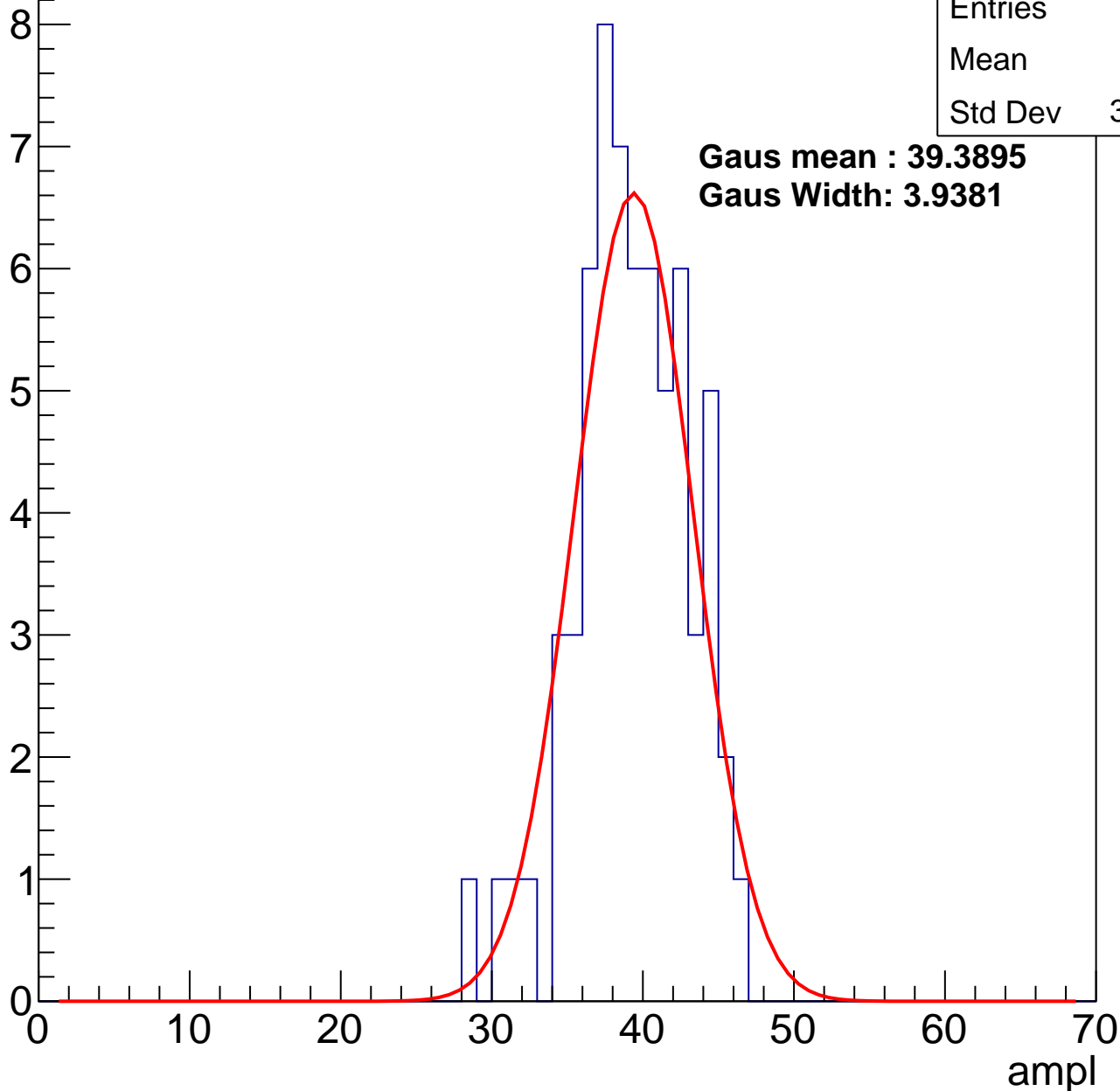
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.8
Std Dev	3.726

Gaus mean : 39.3895

Gaus Width: 3.9381



B0L001S, U11-ch105, adc2

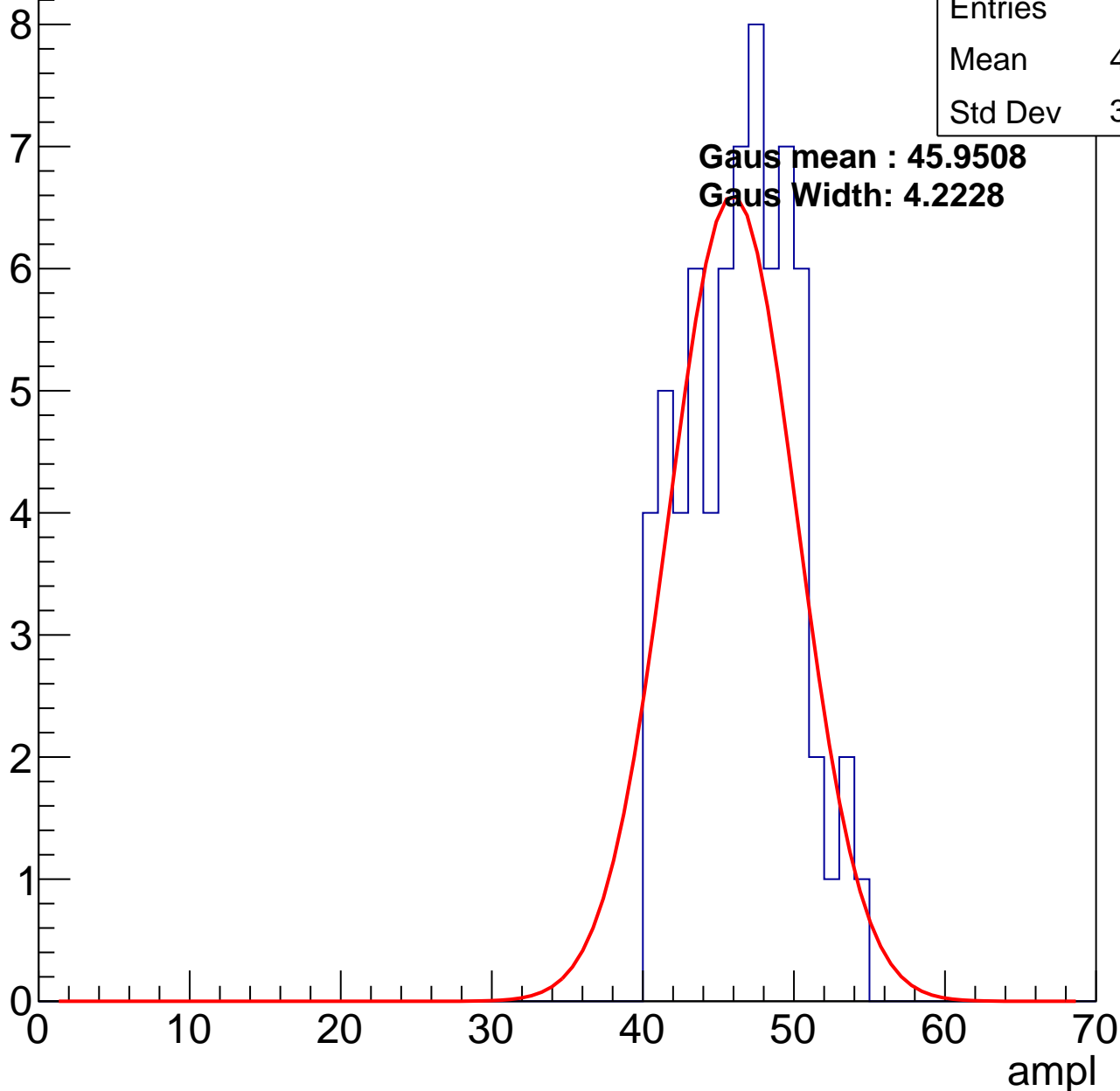
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	46.09
Std Dev	3.505

Gaus mean : 45.9508

Gaus Width: 4.2228

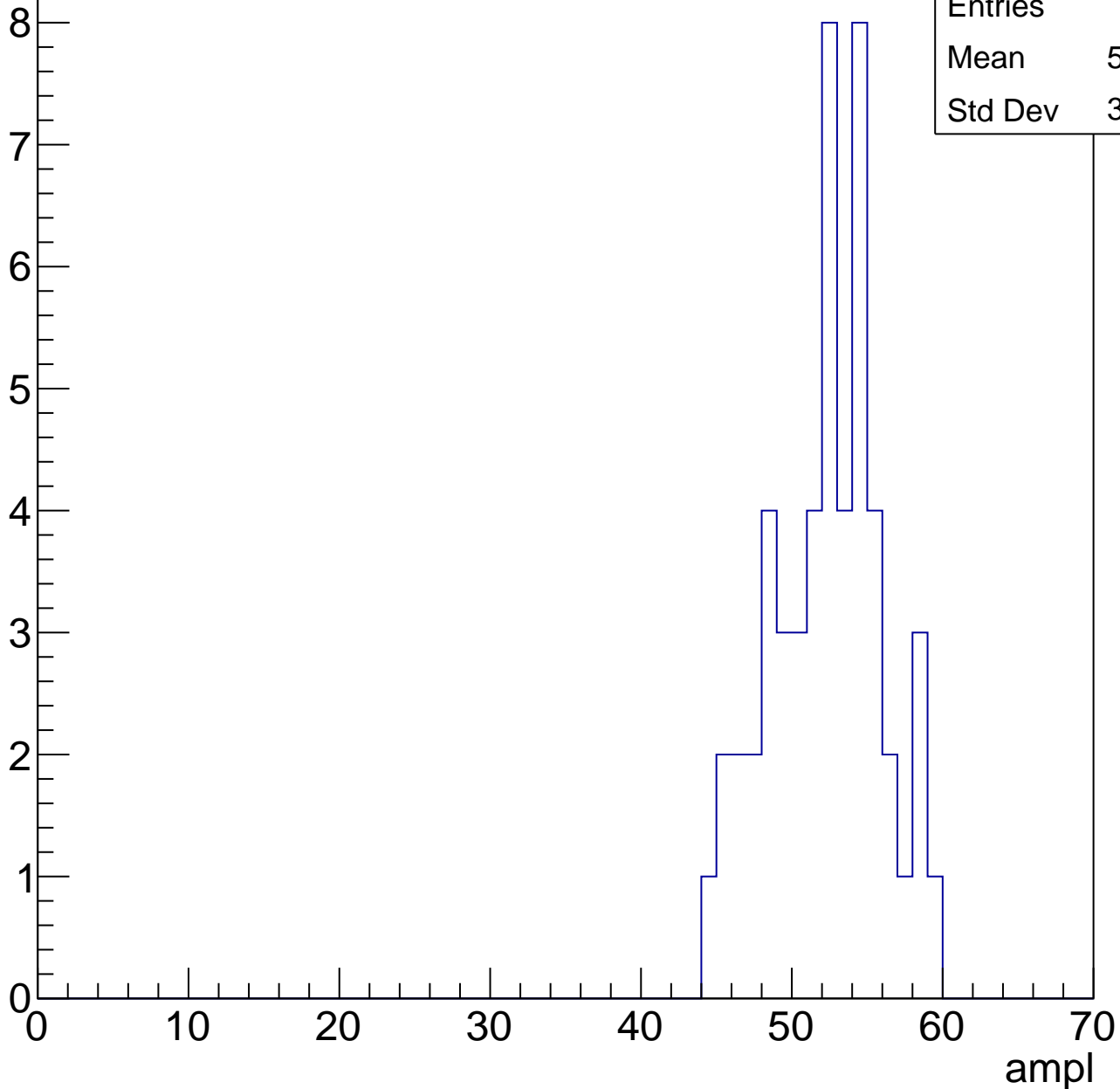


B0L001S, U11-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

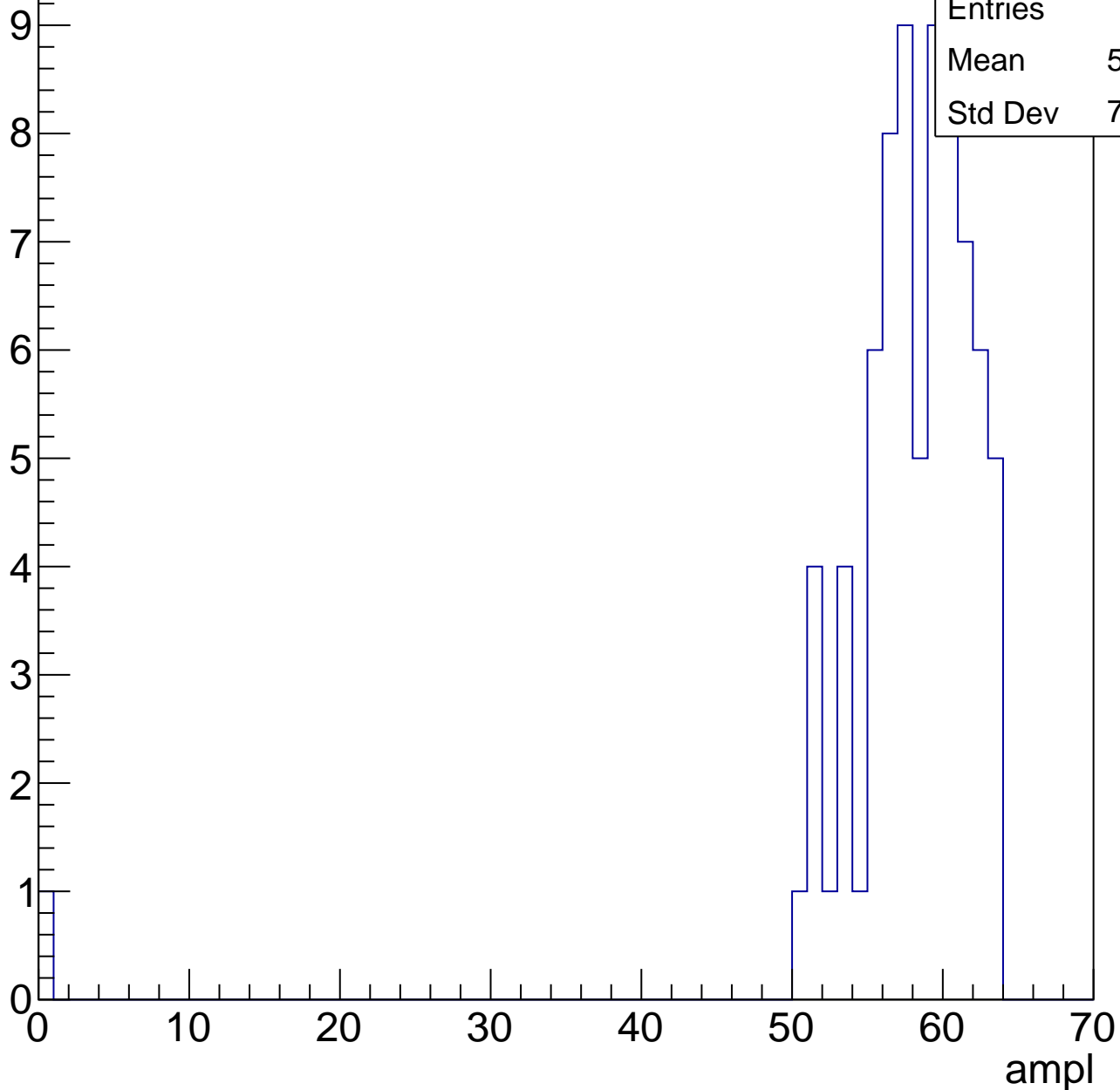
Entries	52
Mean	51.83
Std Dev	3.604



B0L001S, U11-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

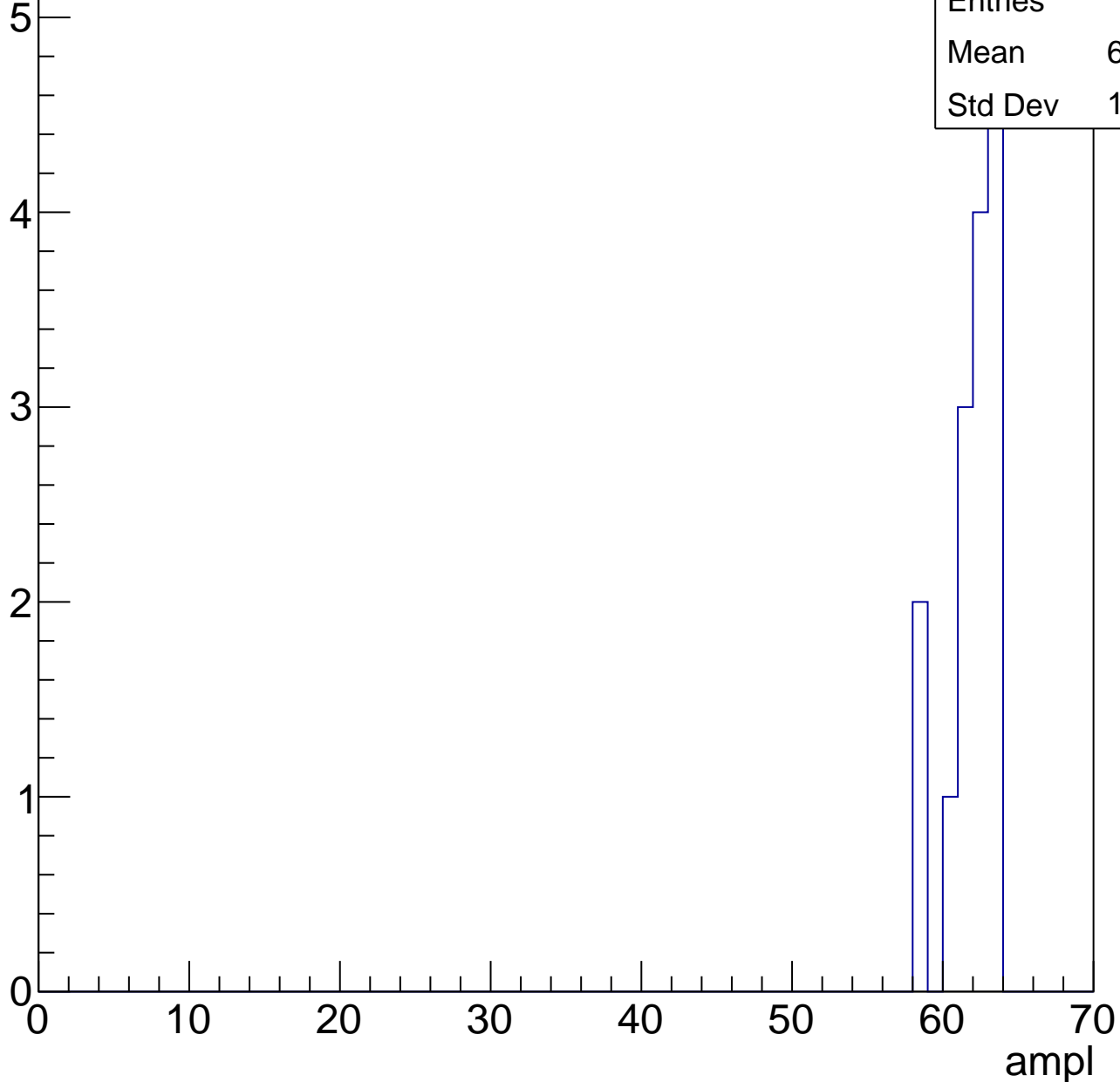


B0L001S, U11-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.47
Std Dev	1.628



B0L001S, U11-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch106, adc0

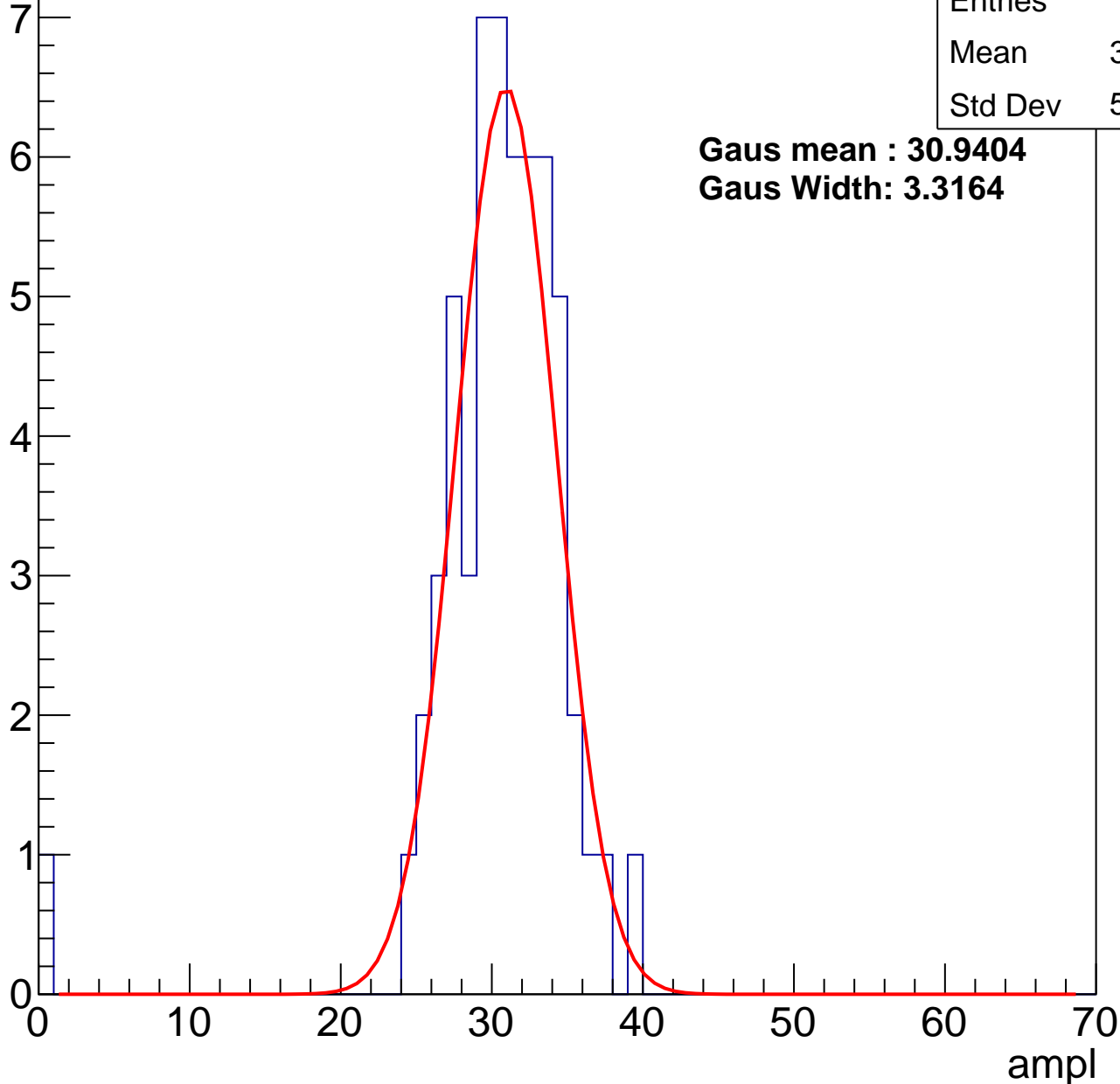
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.04
Std Dev	5.092

Gaus mean : 30.9404

Gaus Width: 3.3164



B0L001S, U11-ch106, adc1

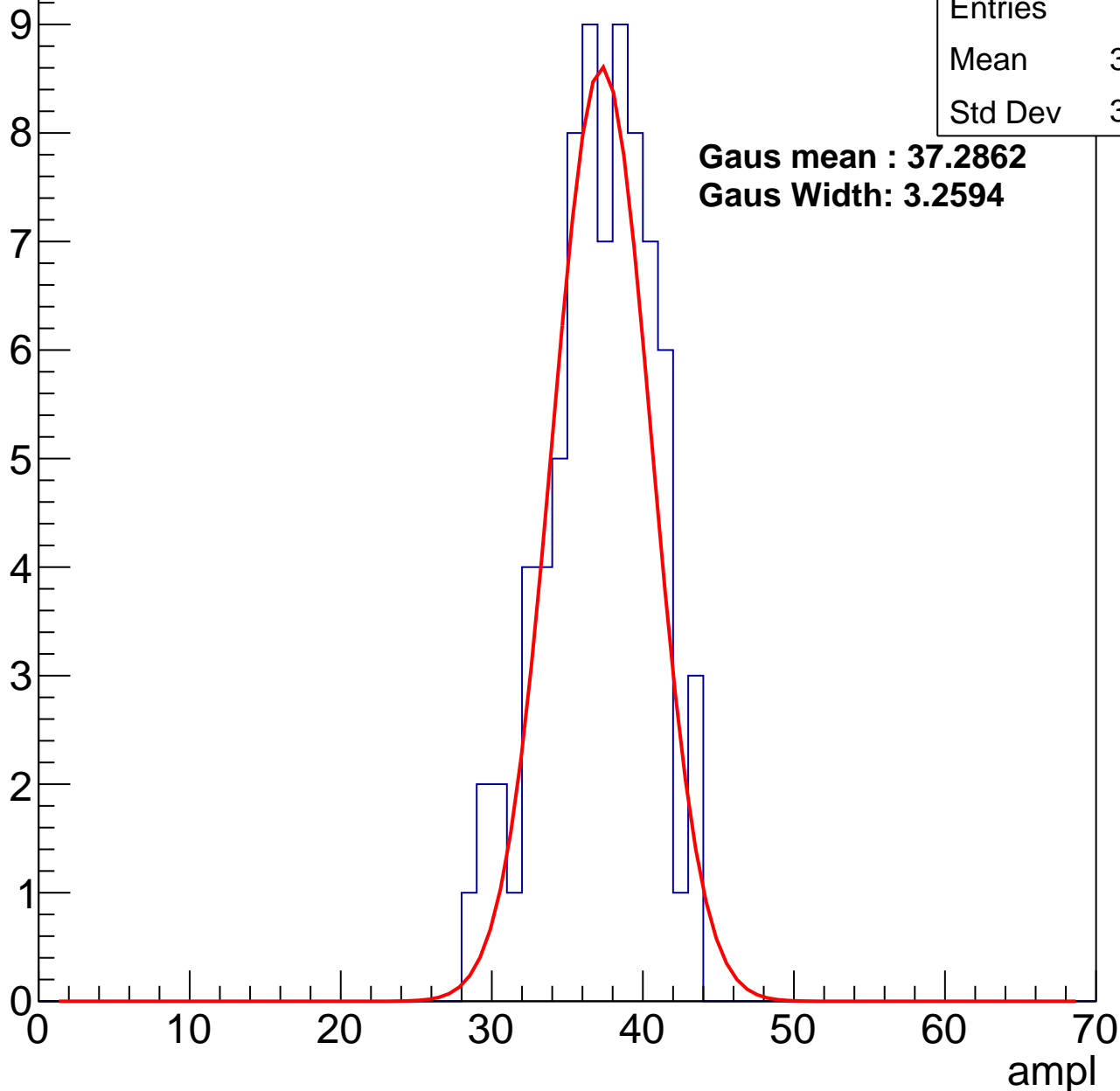
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	36.64
Std Dev	3.449

Gaus mean : 37.2862

Gaus Width: 3.2594



B0L001S, U11-ch106, adc2

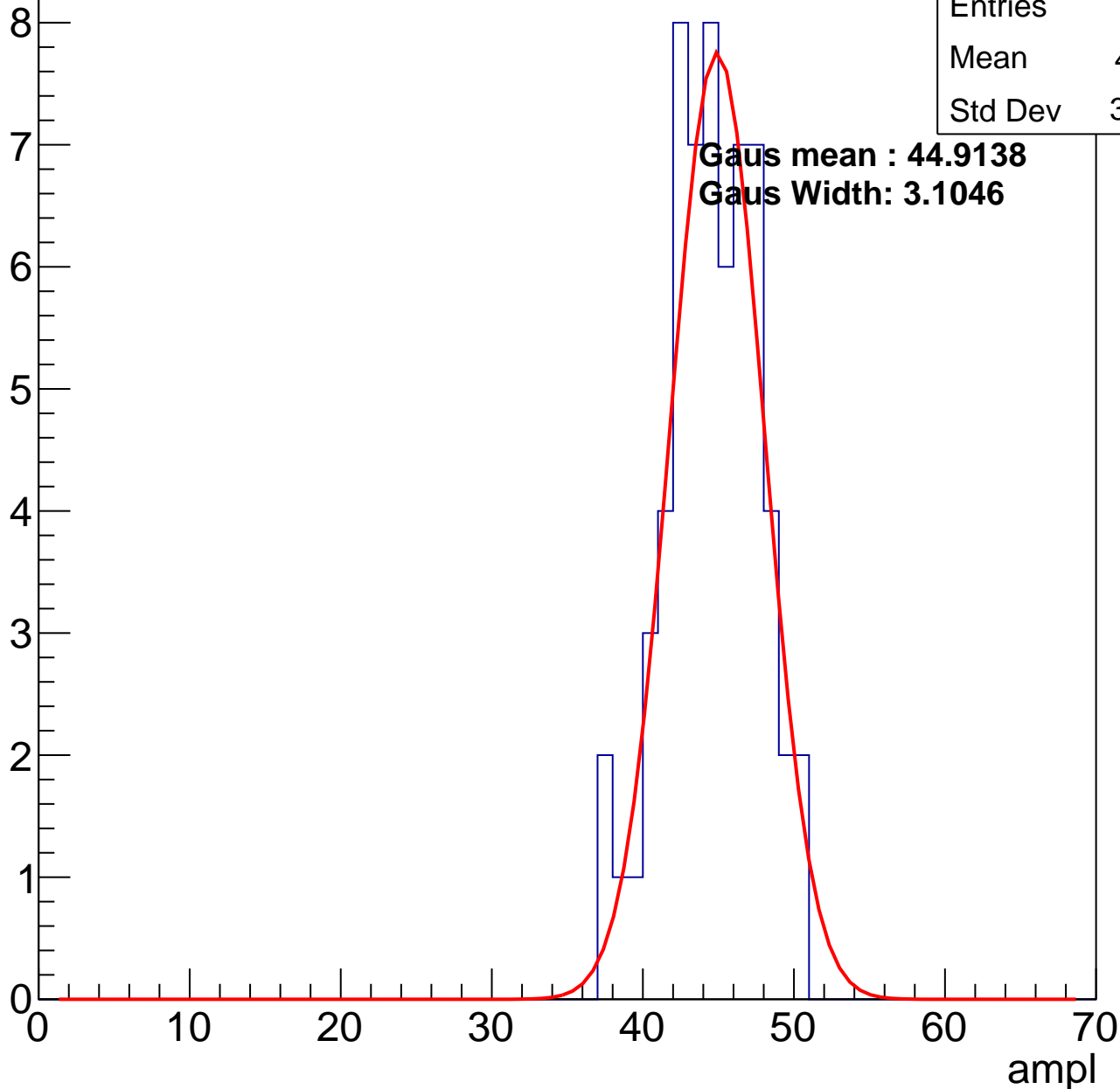
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.11
Std Dev	3.017

Gaus mean : 44.9138

Gaus Width: 3.1046

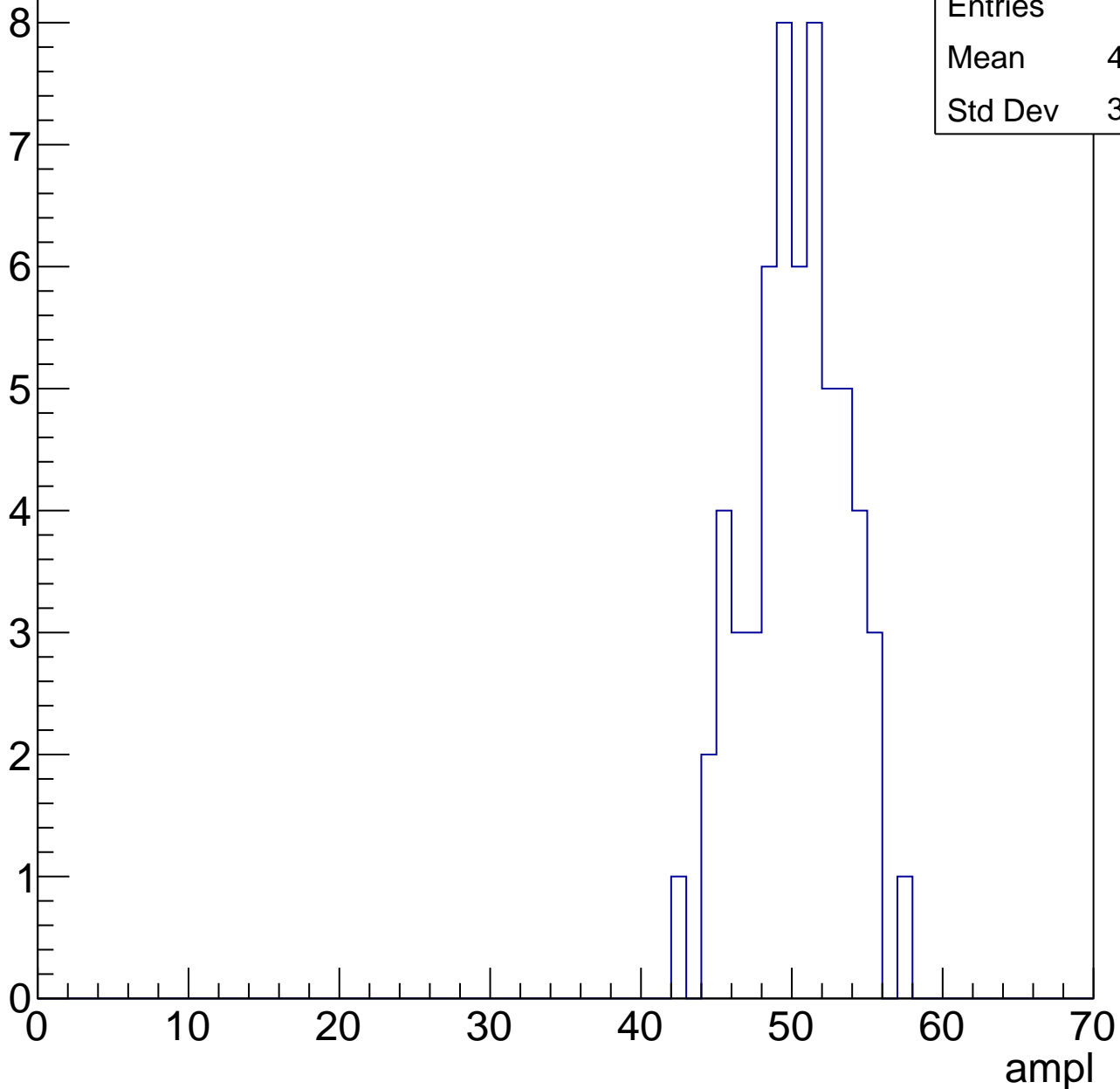


B0L001S, U11-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.83
Std Dev	3.195

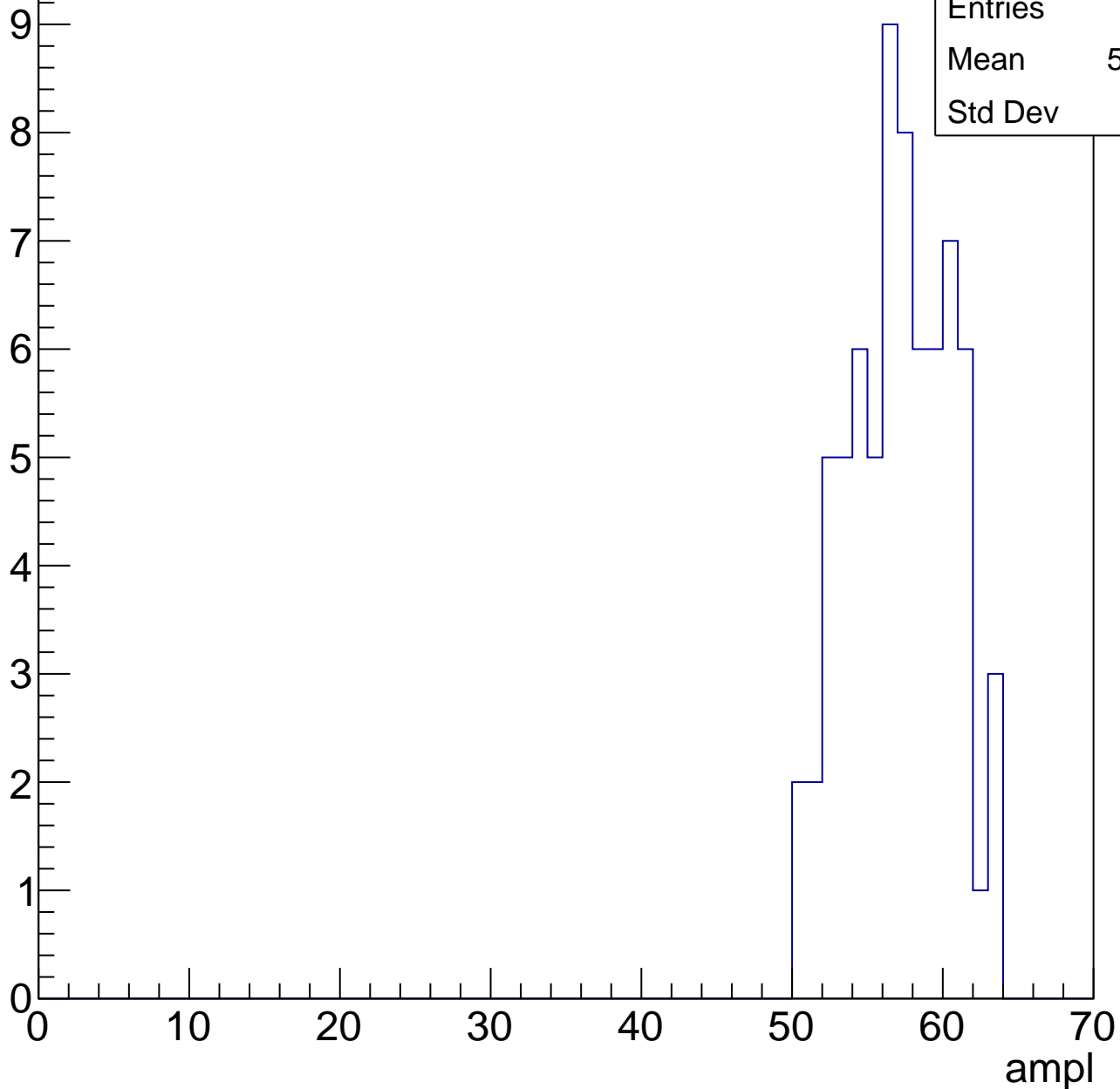


B0L001S, U11-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.69
Std Dev	3.3

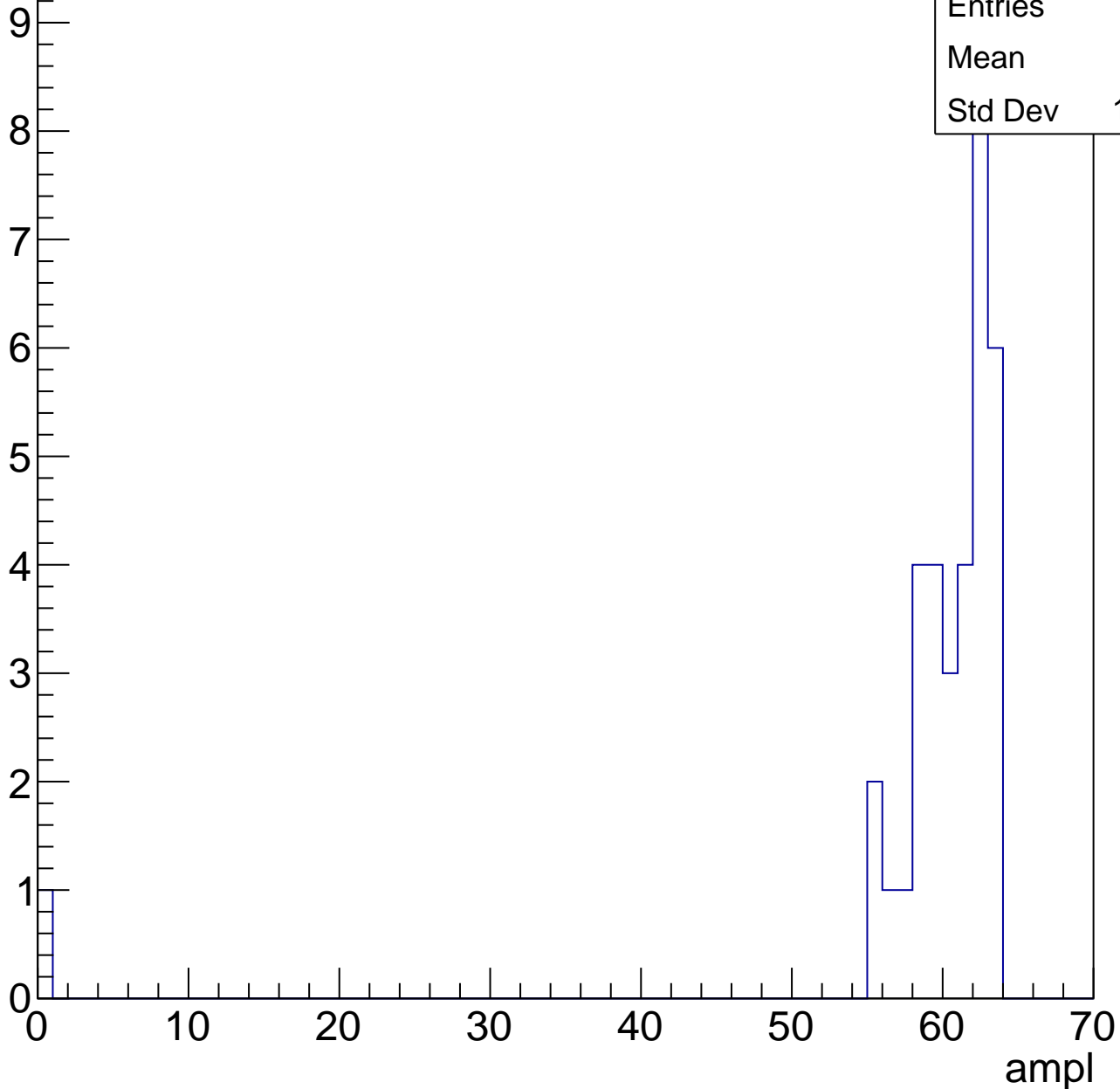


B0L001S, U11-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.6
Std Dev	10.31



B0L001S, U11-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch107, adc0

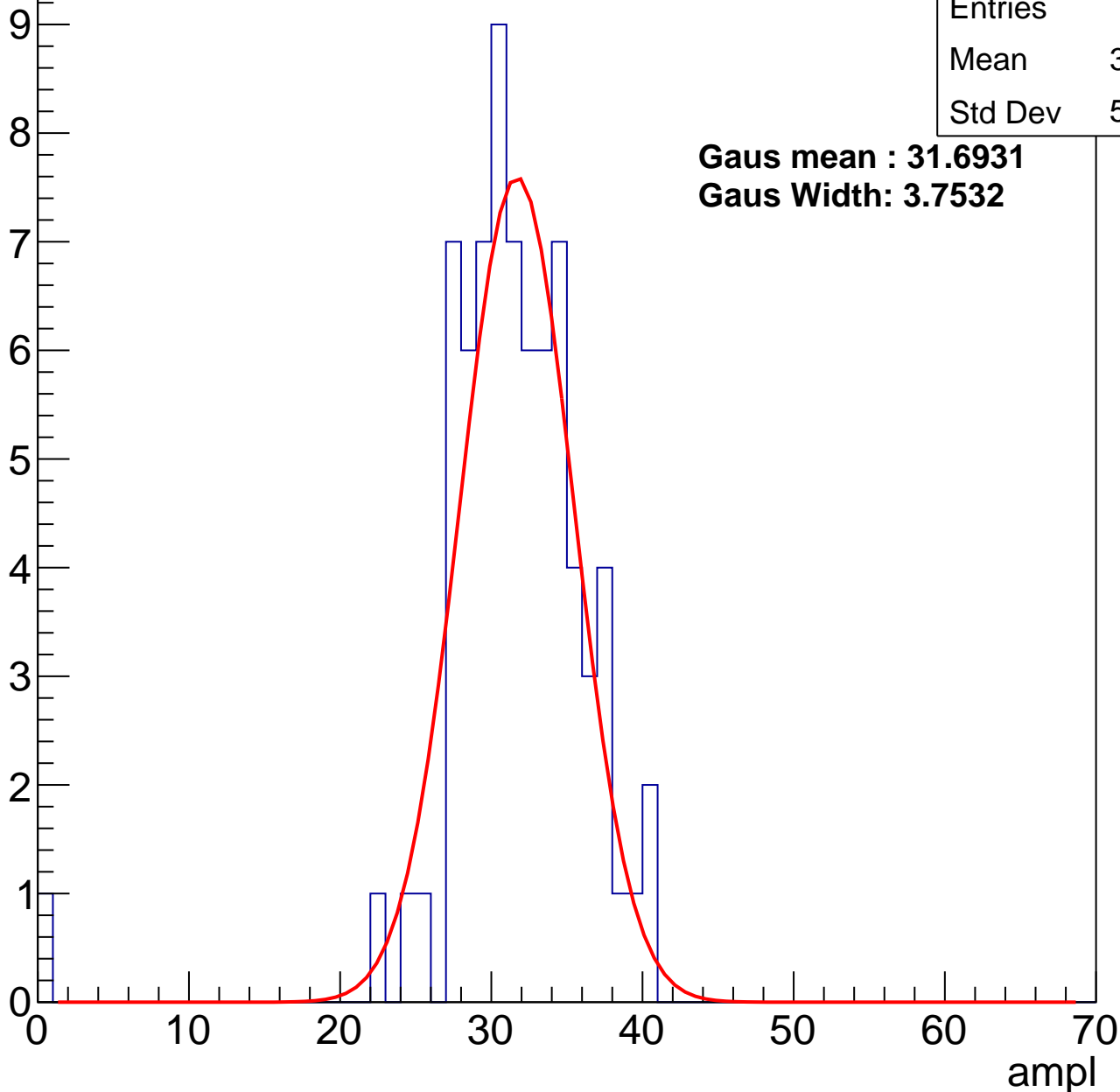
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	31.07
Std Dev	5.174

Gaus mean : 31.6931

Gaus Width: 3.7532



B0L001S, U11-ch107, adc1

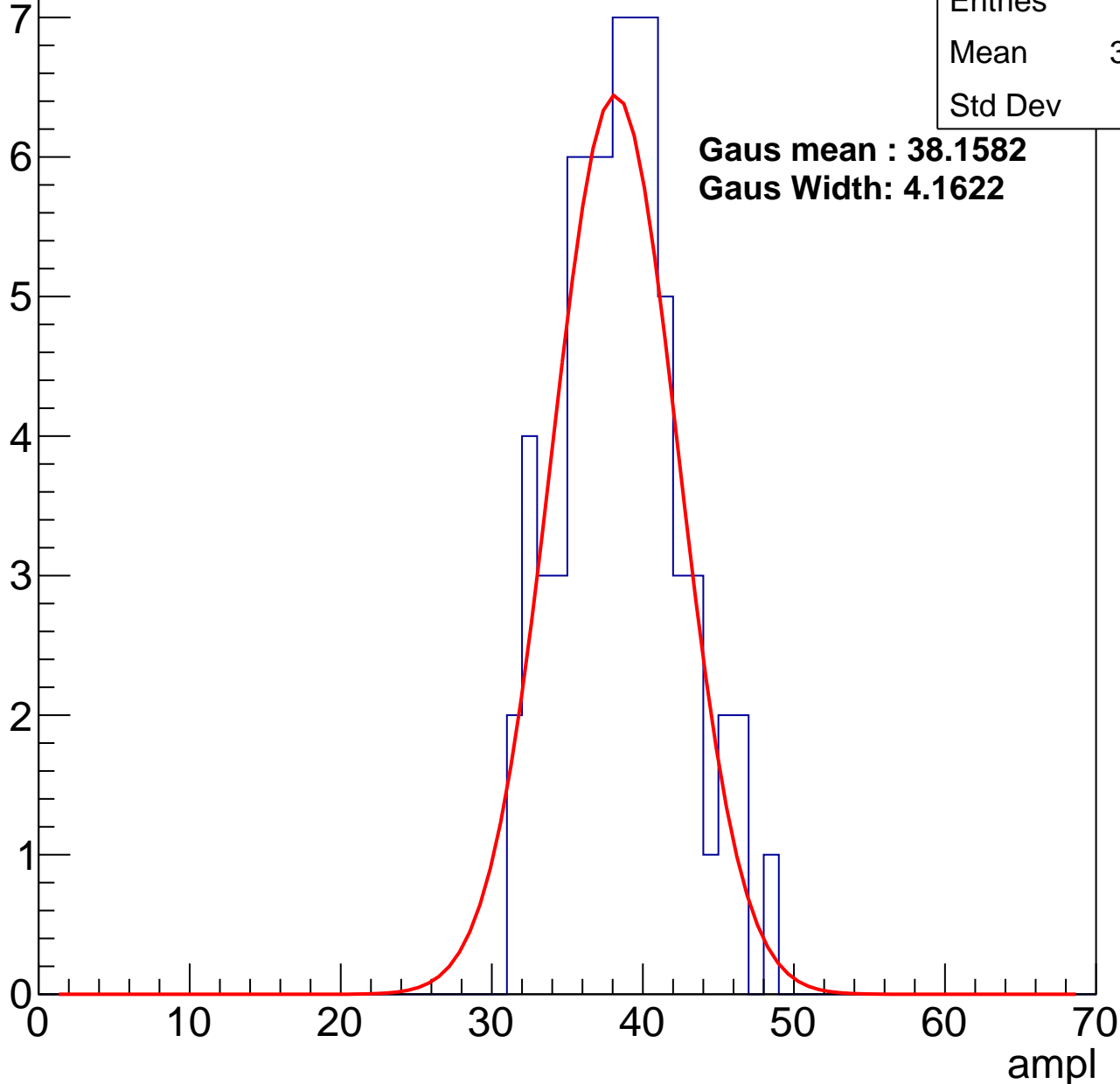
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.12
Std Dev	3.86

Gaus mean : 38.1582

Gaus Width: 4.1622



B0L001S, U11-ch107, adc2

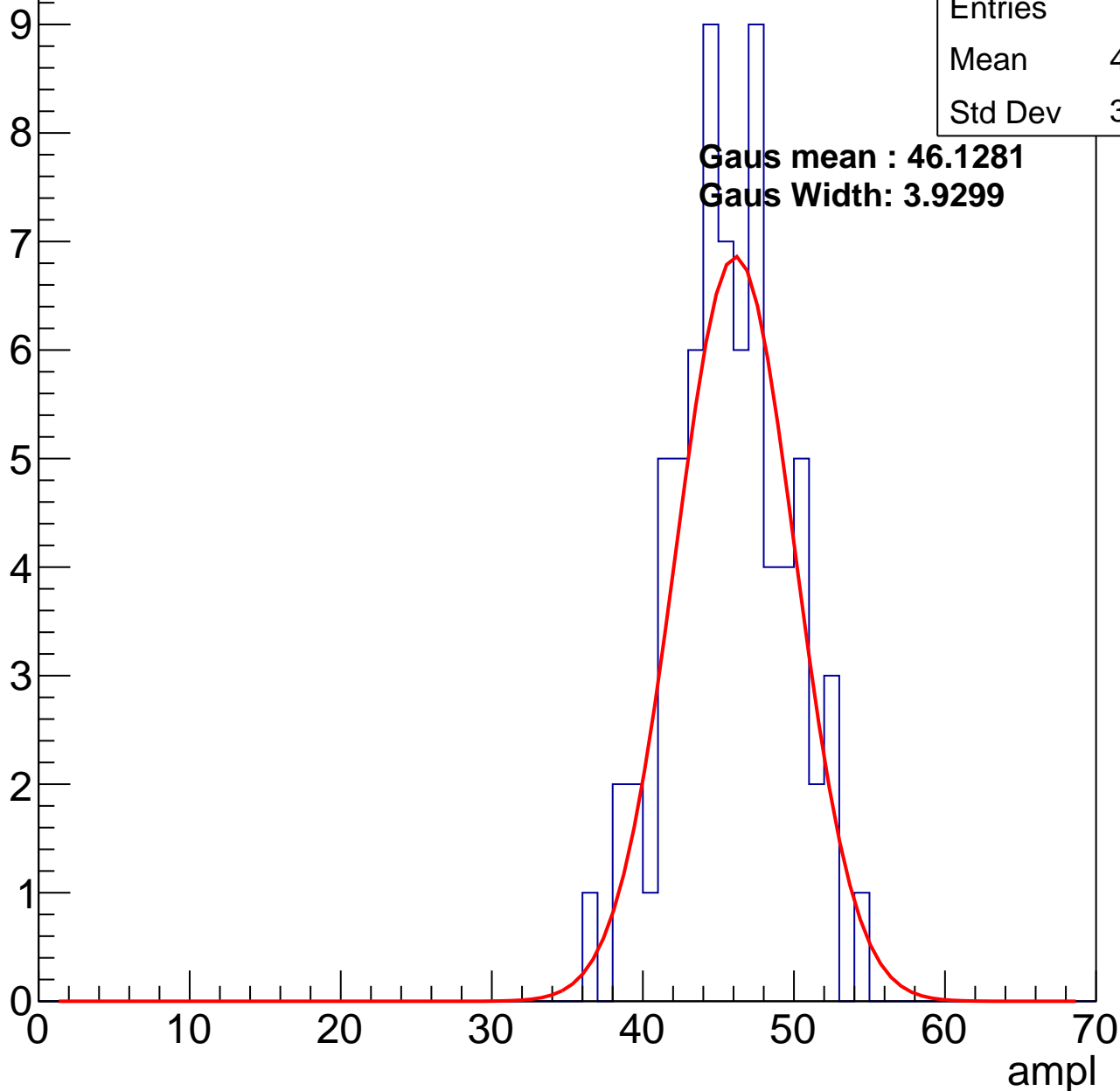
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	45.32
Std Dev	3.715

Gaus mean : 46.1281

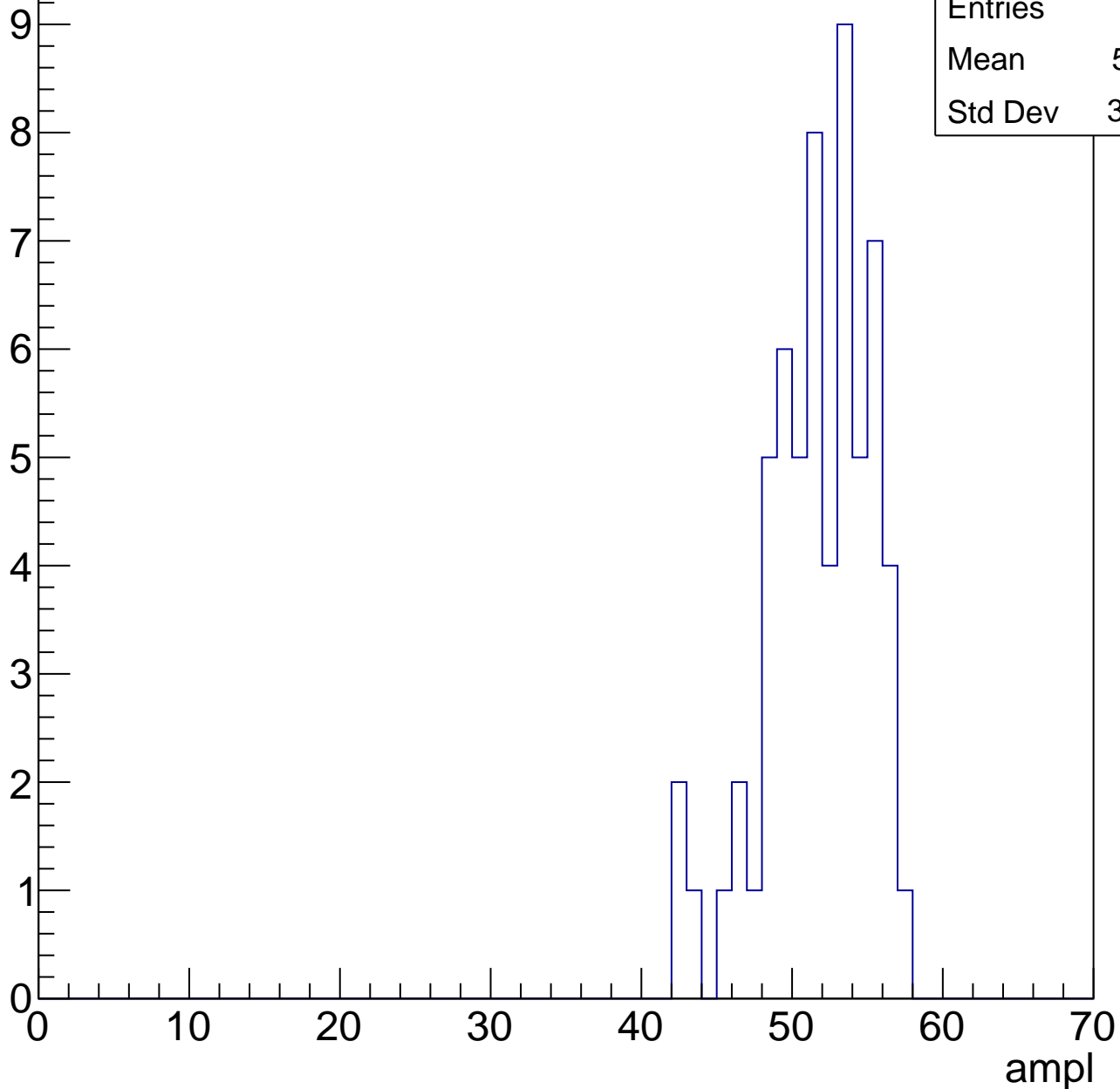
Gaus Width: 3.9299



B0L001S, U11-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



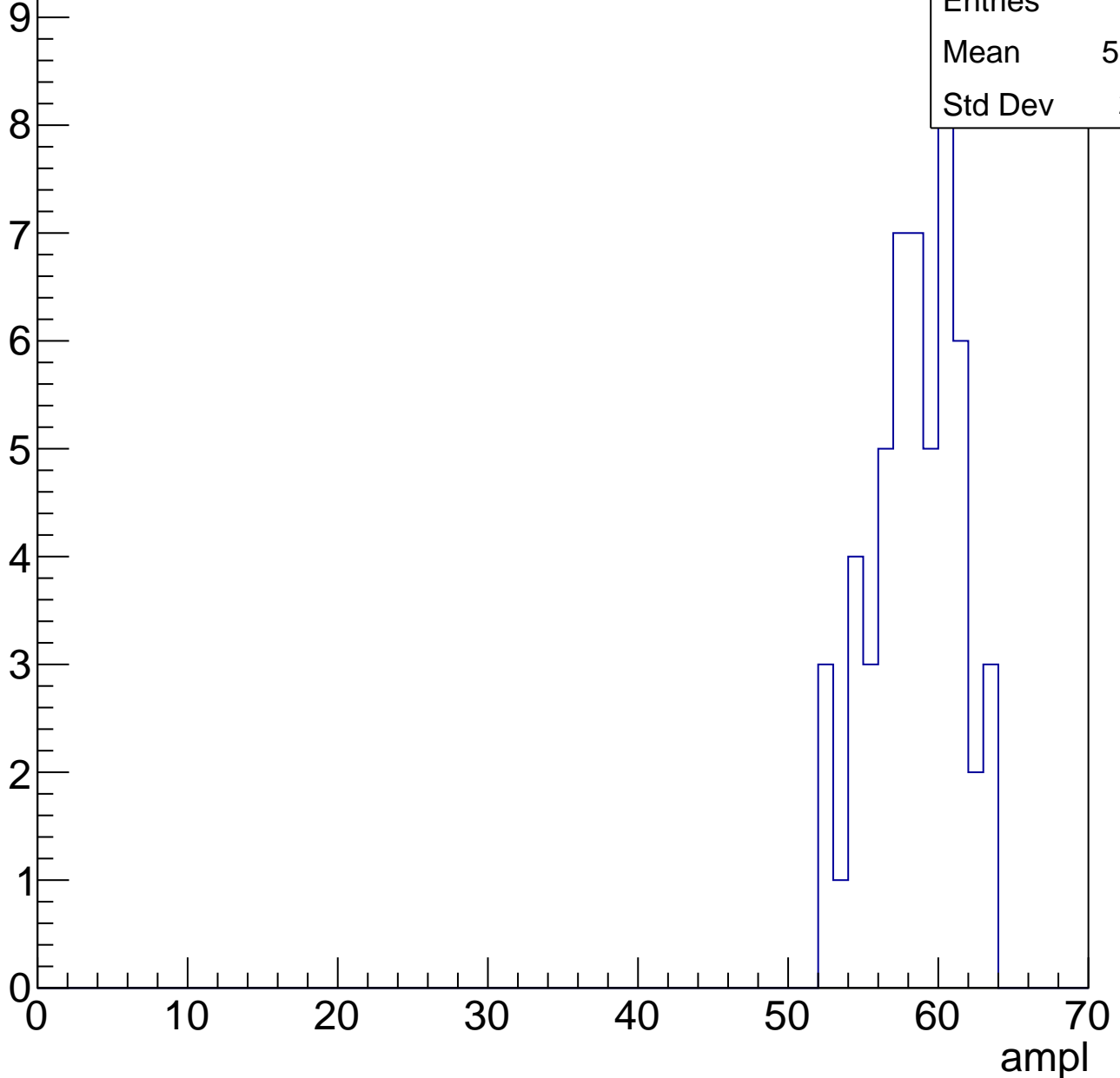
Entries	61
Mean	51.21
Std Dev	3.465

B0L001S, U11-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.98
Std Dev	2.87

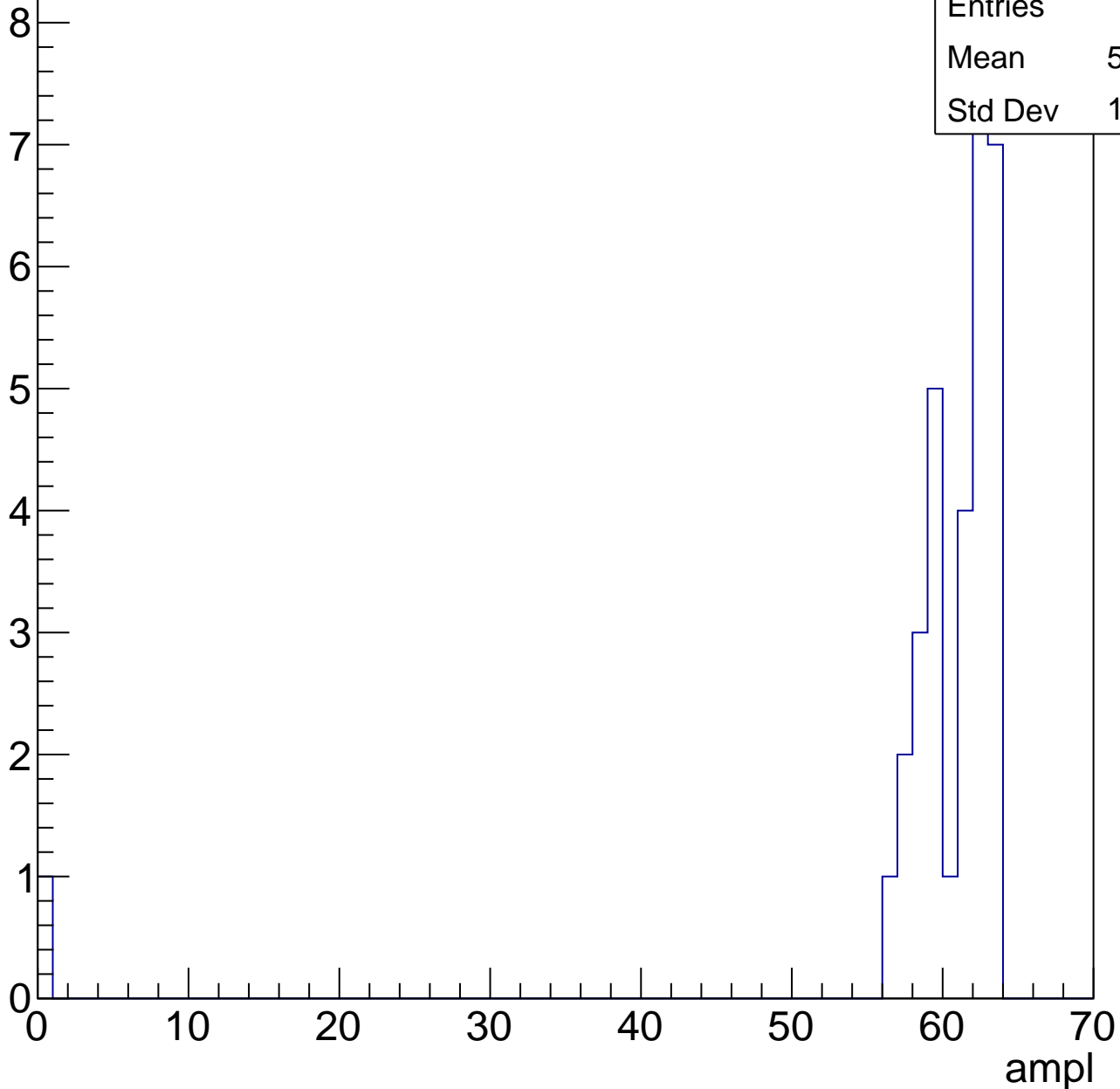


B0L001S, U11-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.75
Std Dev	10.75



B0L001S, U11-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch108, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	31.38
Std Dev	3.388

Gaus mean : 32.1320

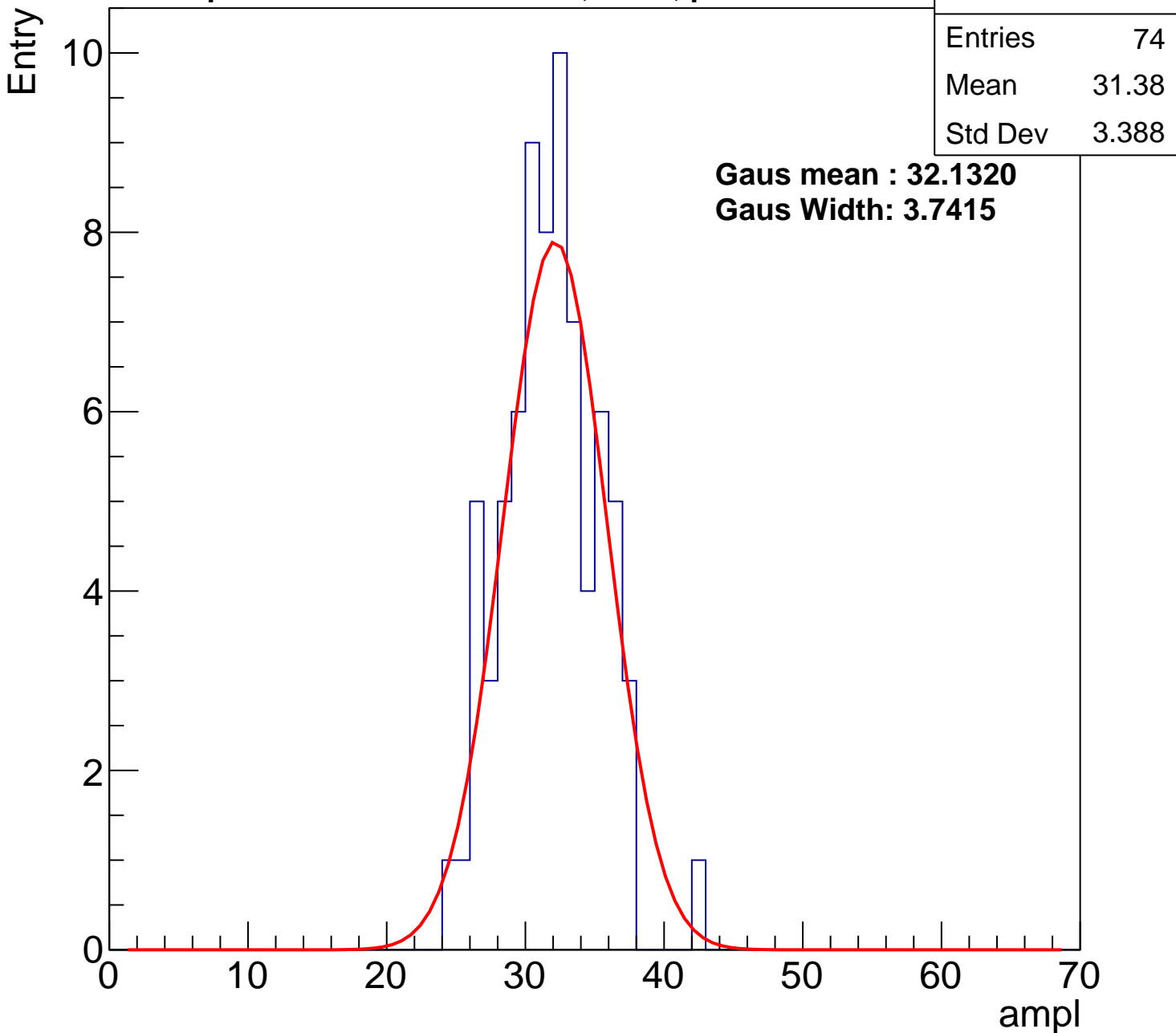
Gaus Width: 3.7415

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch108, adc1

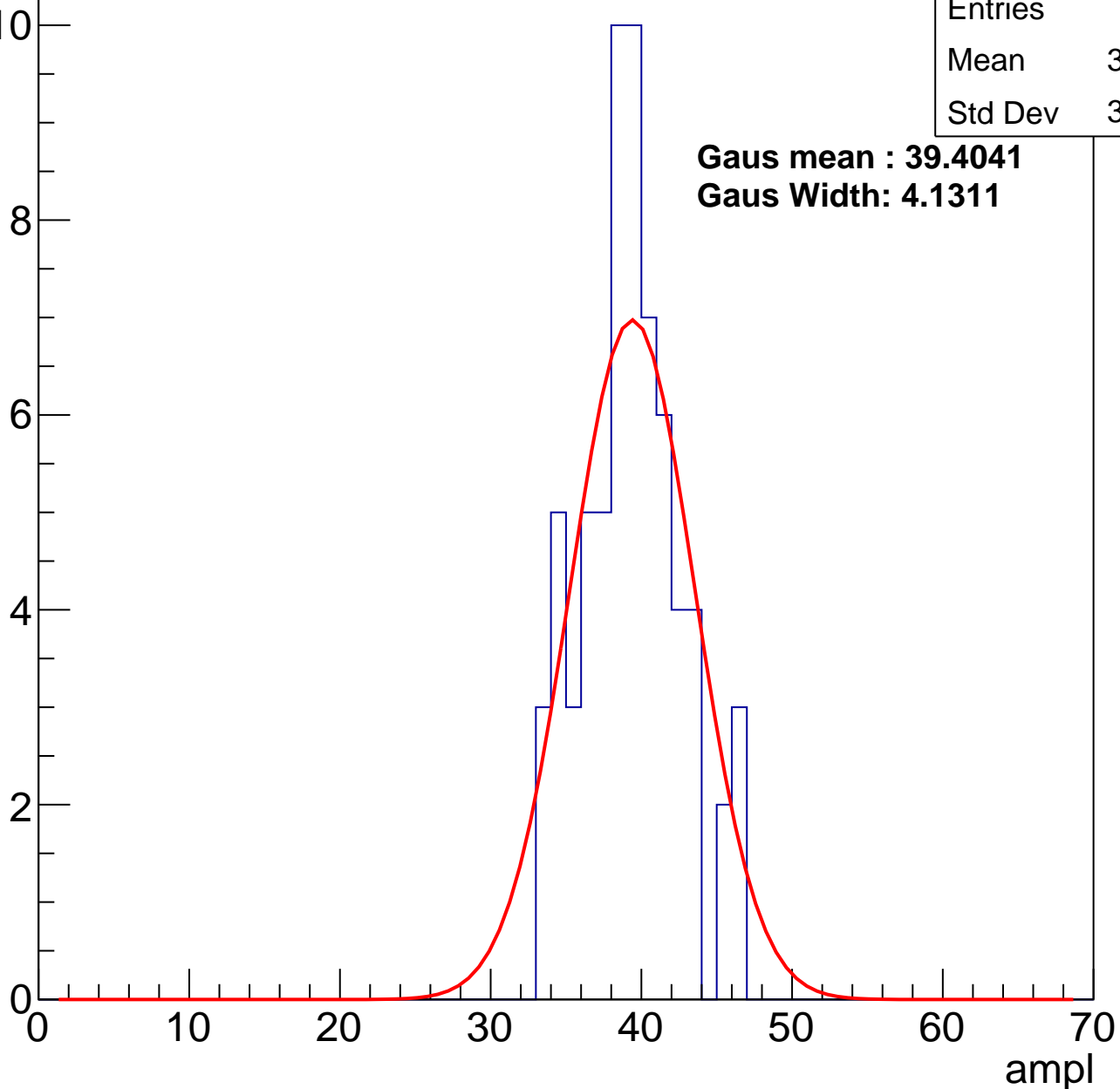
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.85
Std Dev	3.243

Gaus mean : 39.4041

Gaus Width: 4.1311



B0L001S, U11-ch108, adc2

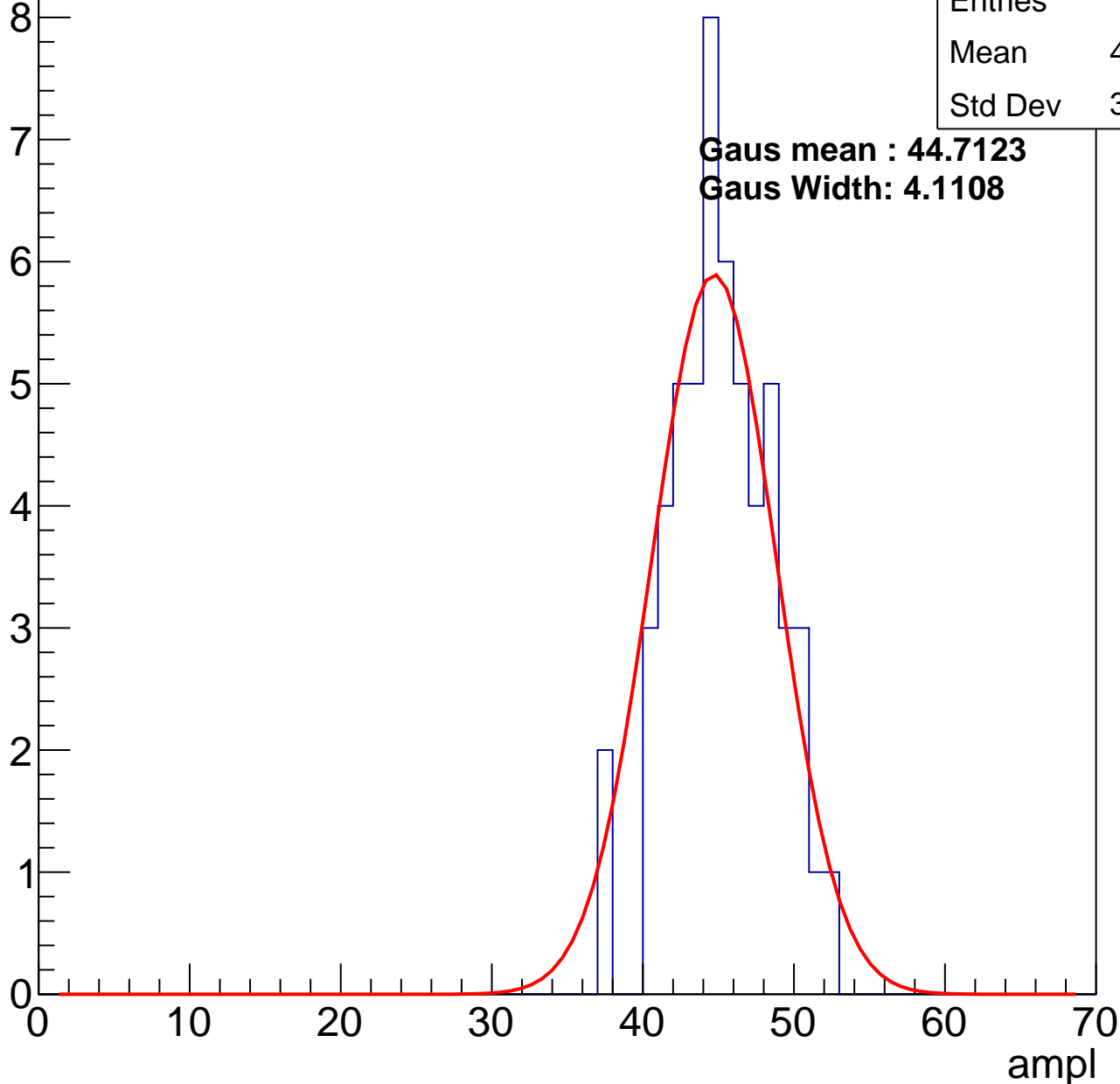
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.78
Std Dev	3.334

Gaus mean : 44.7123

Gaus Width: 4.1108

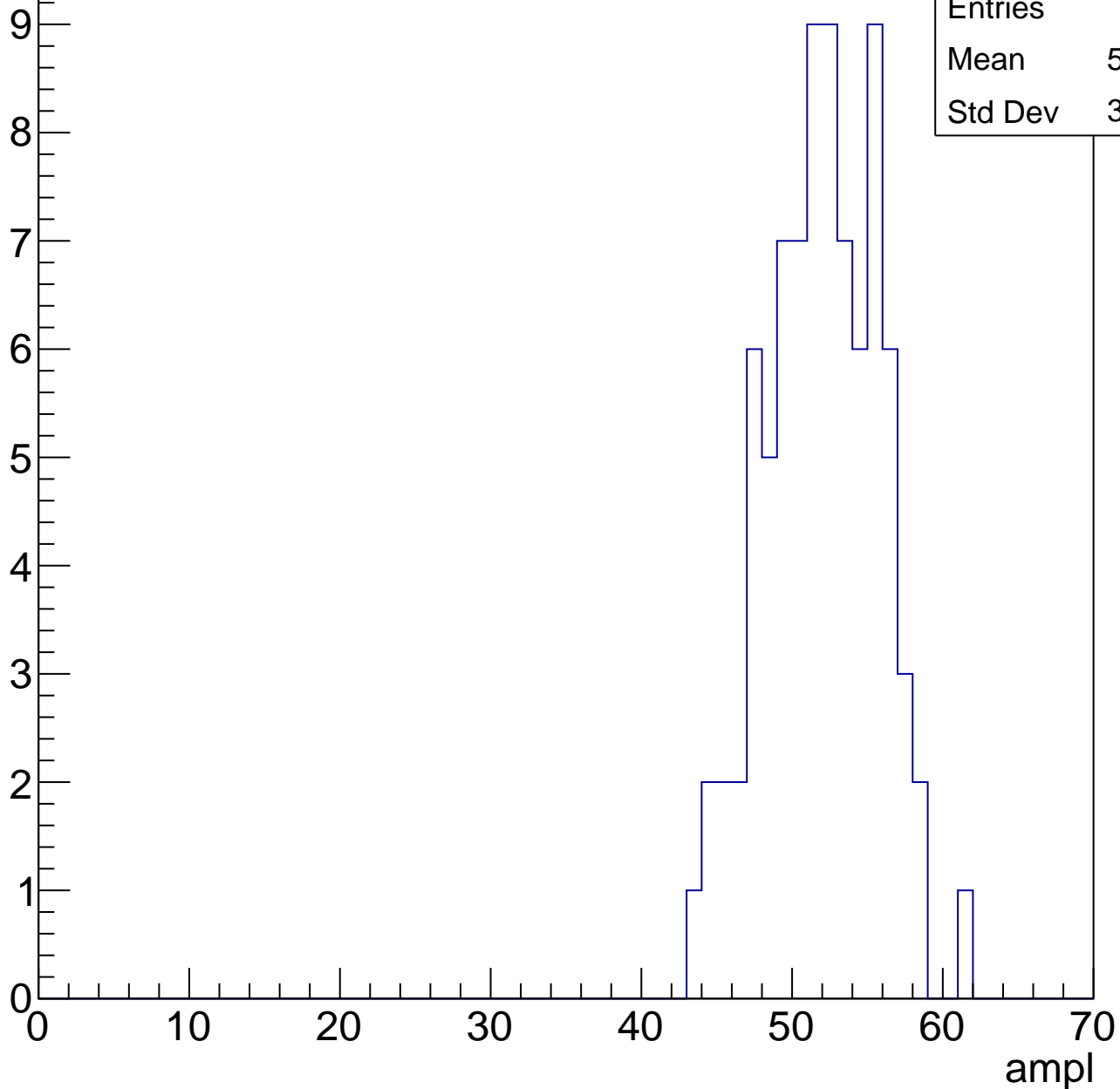


B0L001S, U11-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	51.54
Std Dev	3.663



B0L001S, U11-ch108, adc4

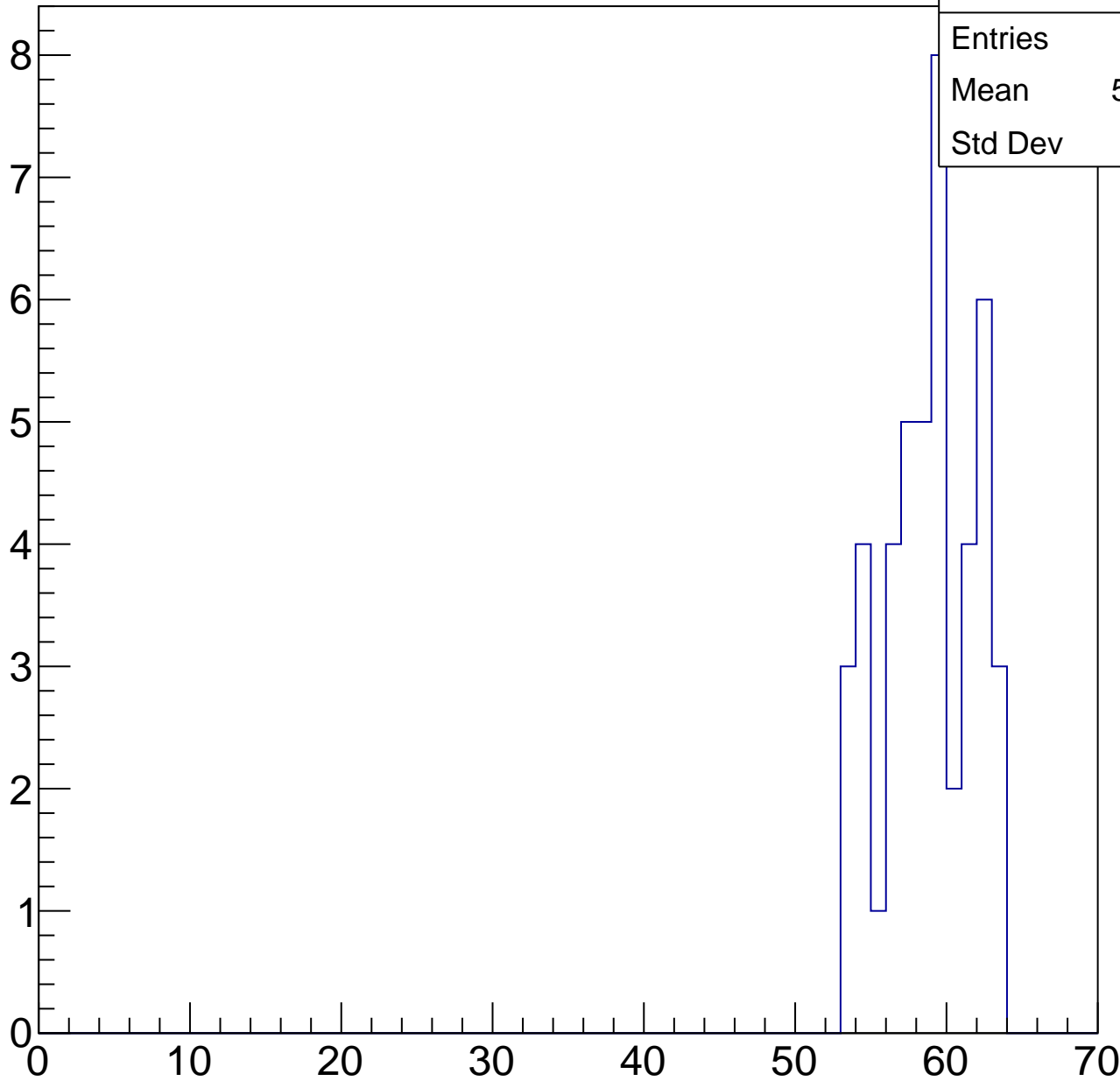
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.36
Std Dev	2.93

ampl



B0L001S, U11-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

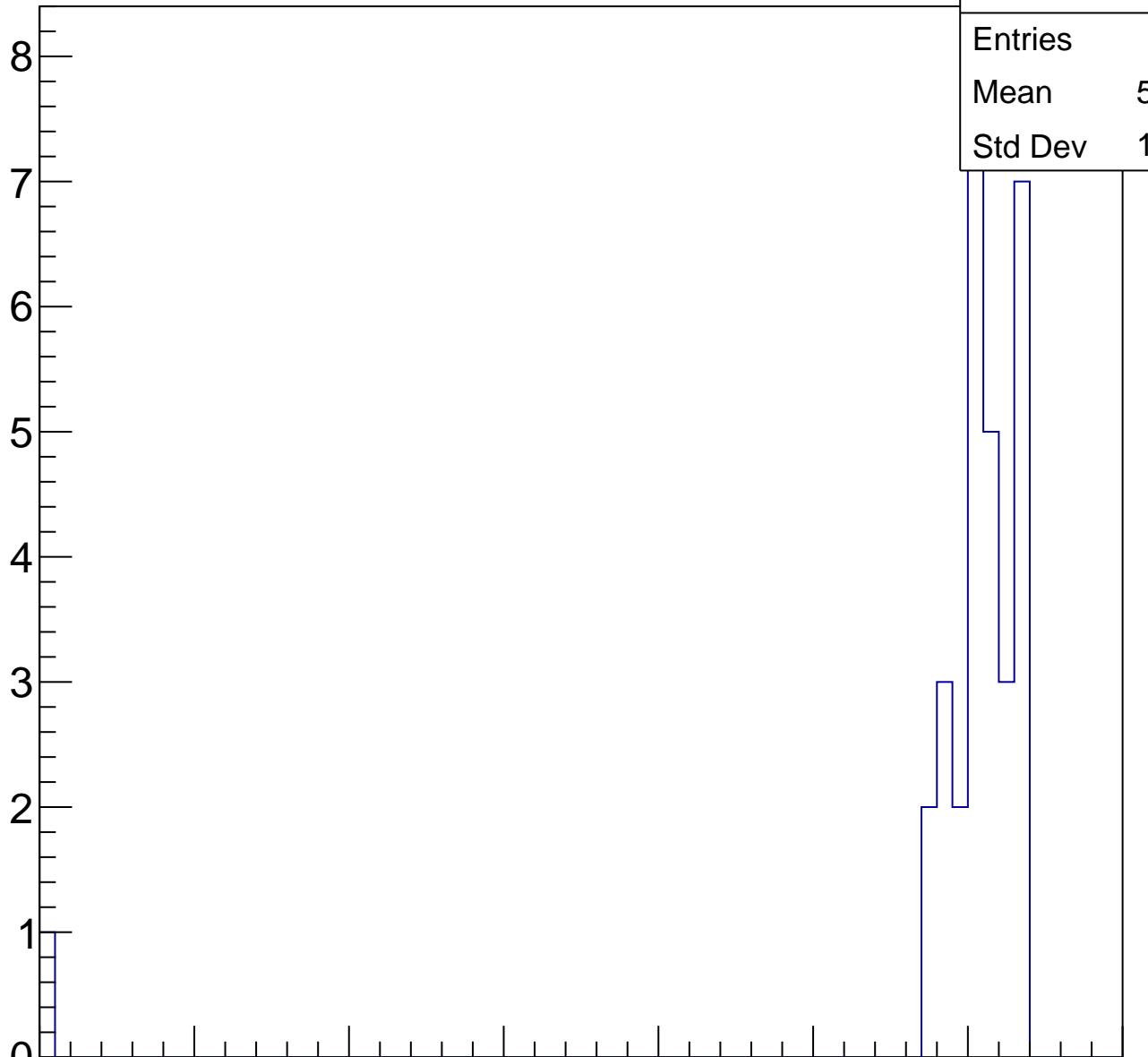
Entry

8
7
6
5
4
3
2
1
0

Entries	31
Mean	58.65
Std Dev	10.86

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch109, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	30.49
Std Dev	3.695

Gaus mean : 31.7450

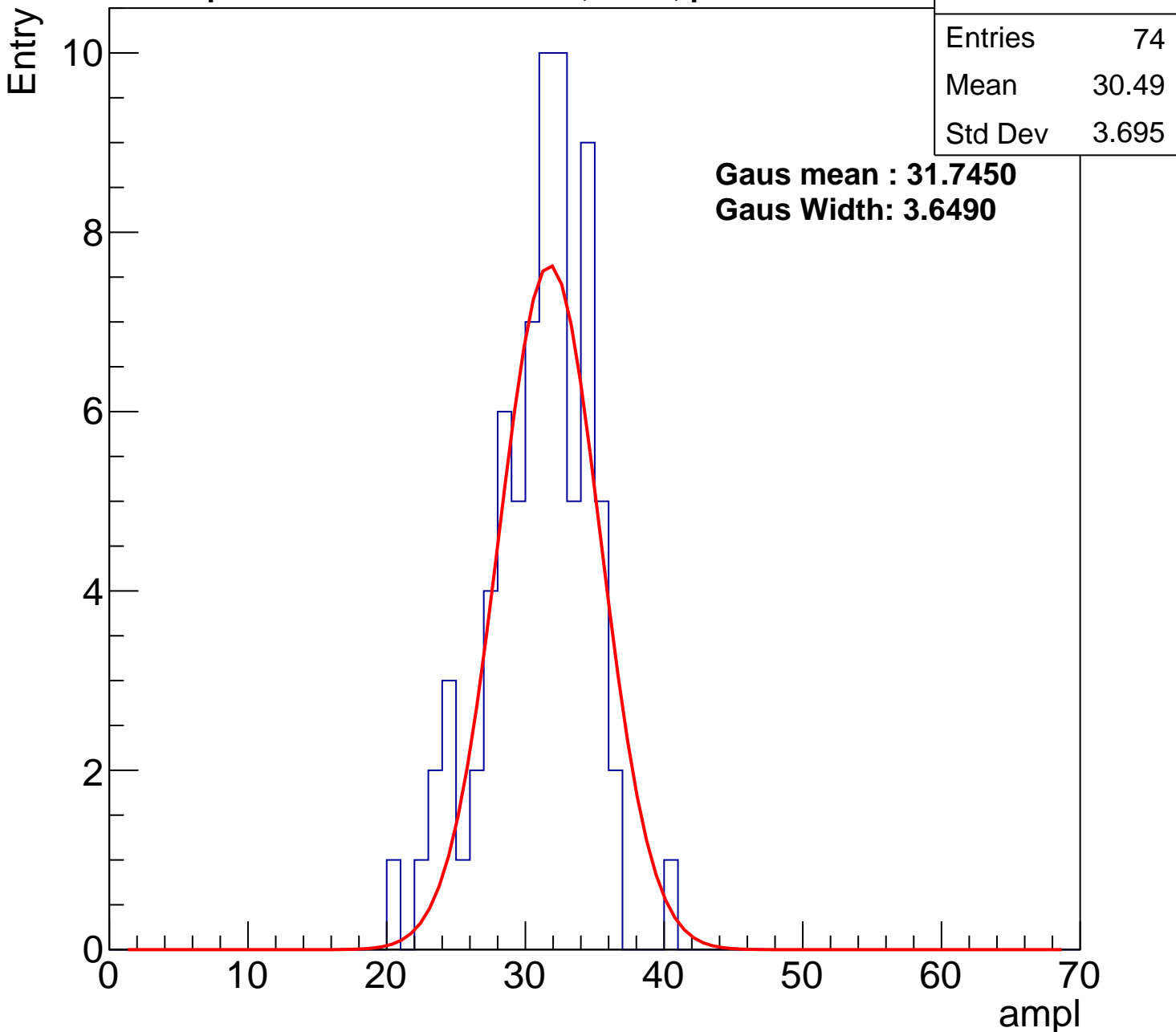
Gaus Width: 3.6490

Entry

10
8
6
4
2
0

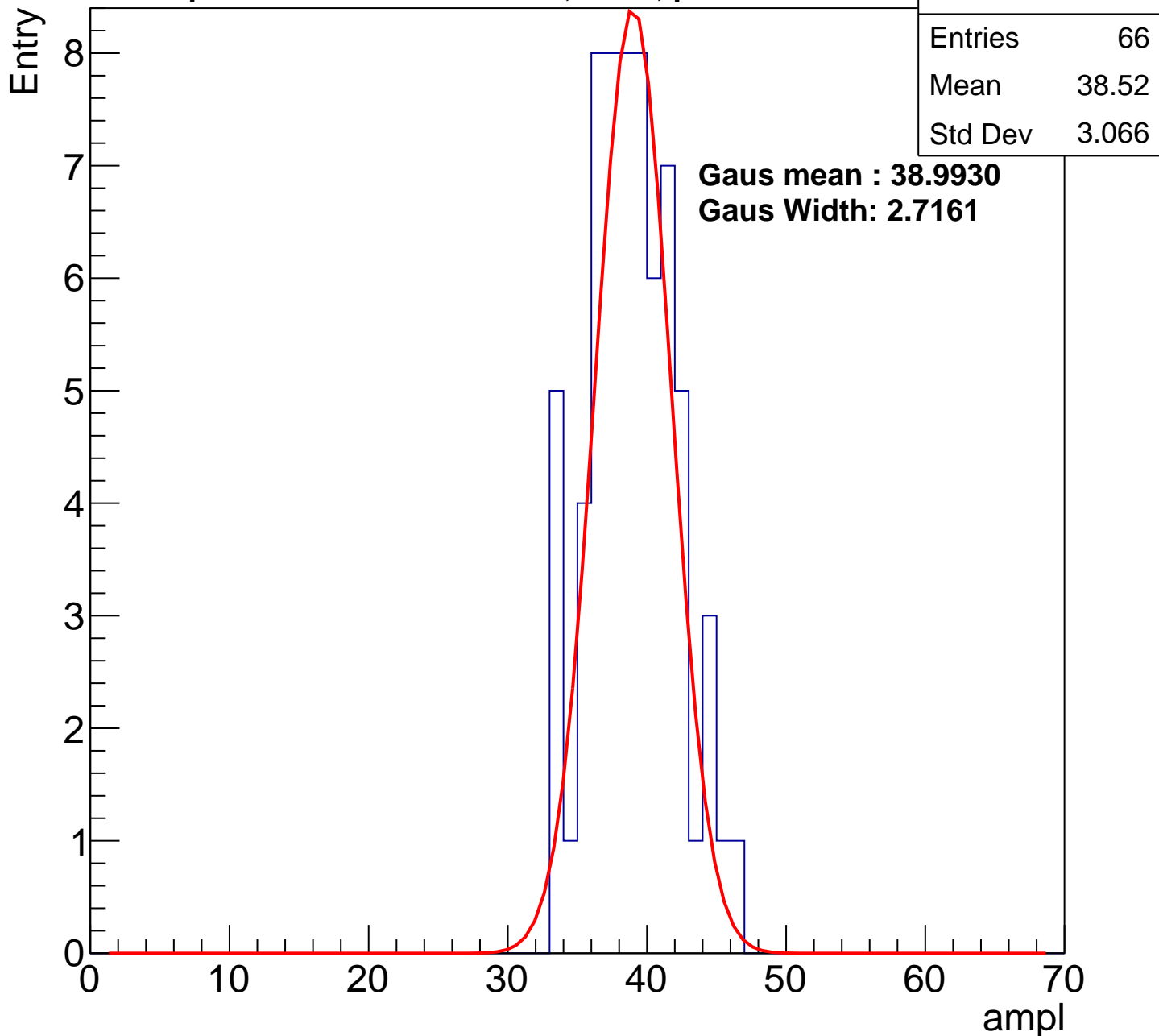
ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch109, adc1

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U11-ch109, adc2

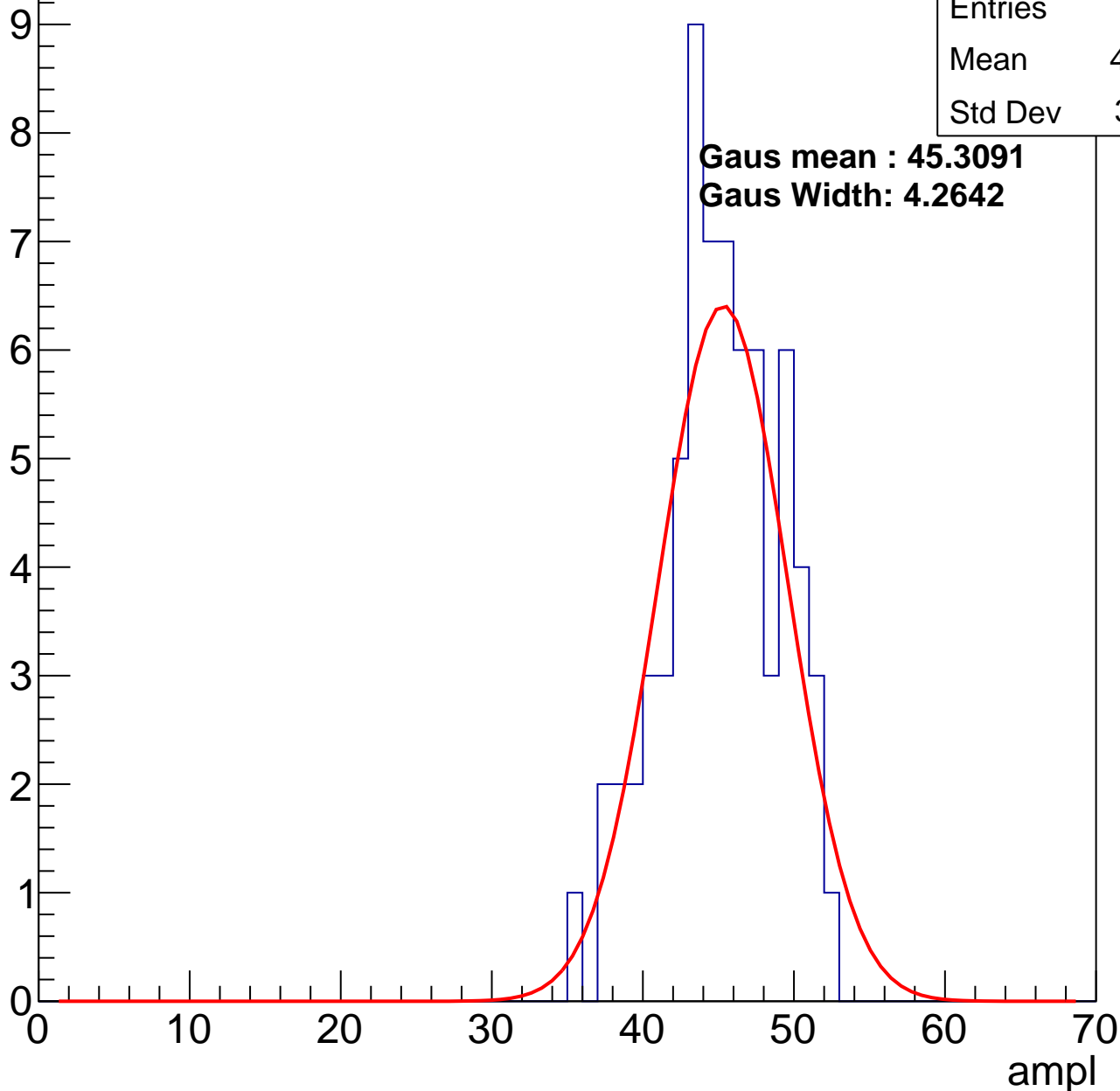
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.67
Std Dev	3.801

Gaus mean : 45.3091

Gaus Width: 4.2642

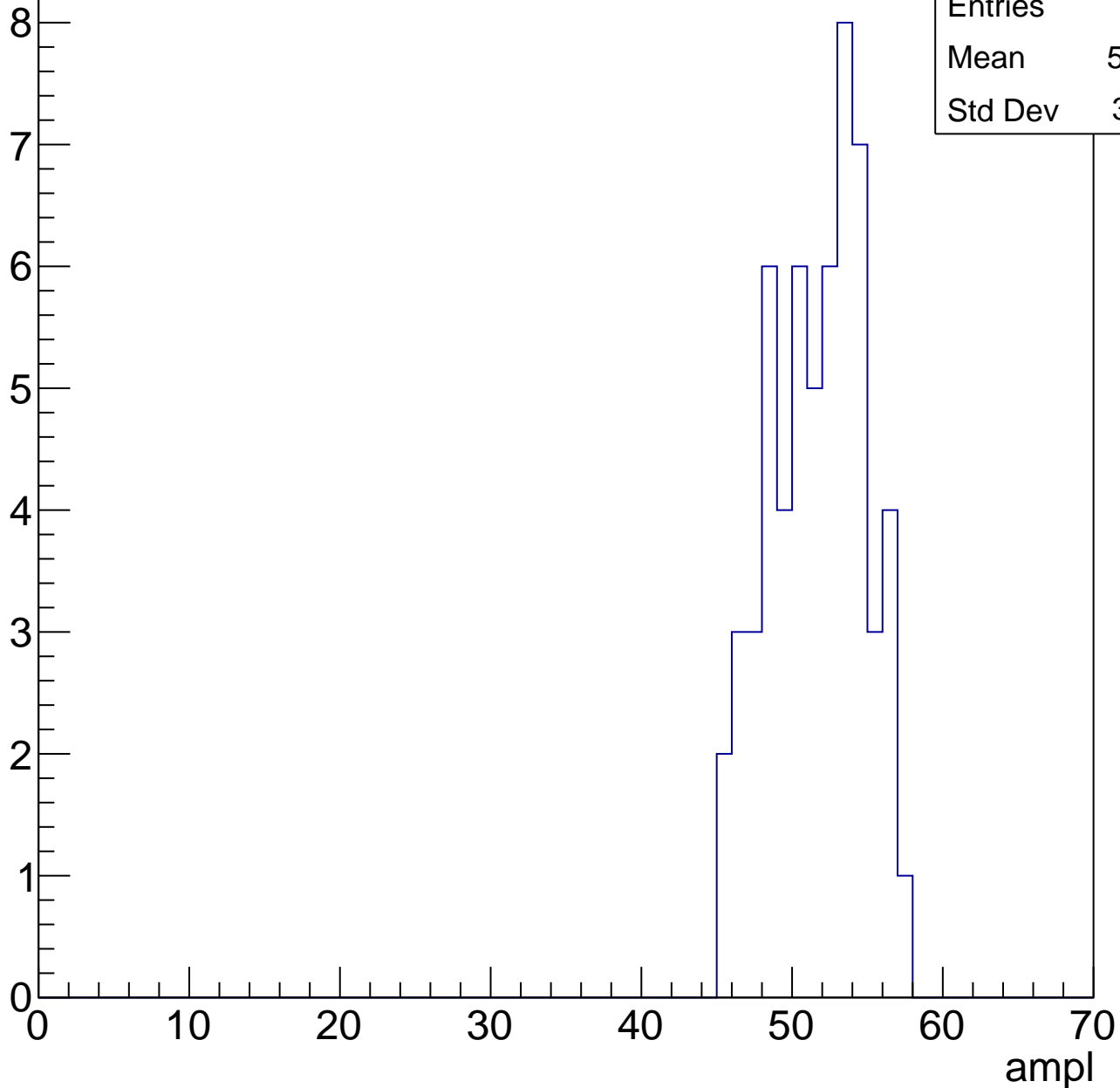


B0L001S, U11-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	51.17
Std Dev	3.091

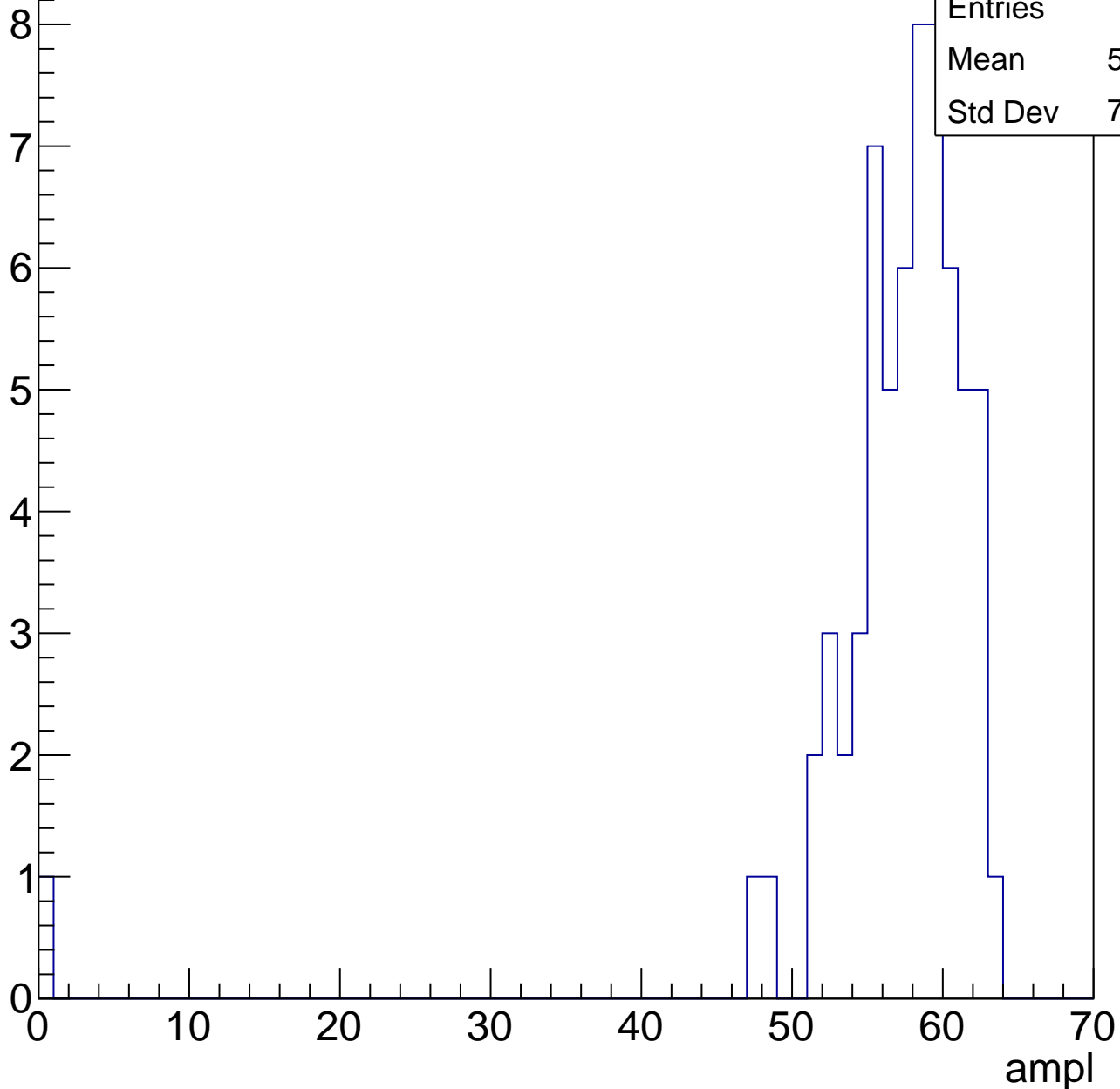


B0L001S, U11-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.28
Std Dev	7.877

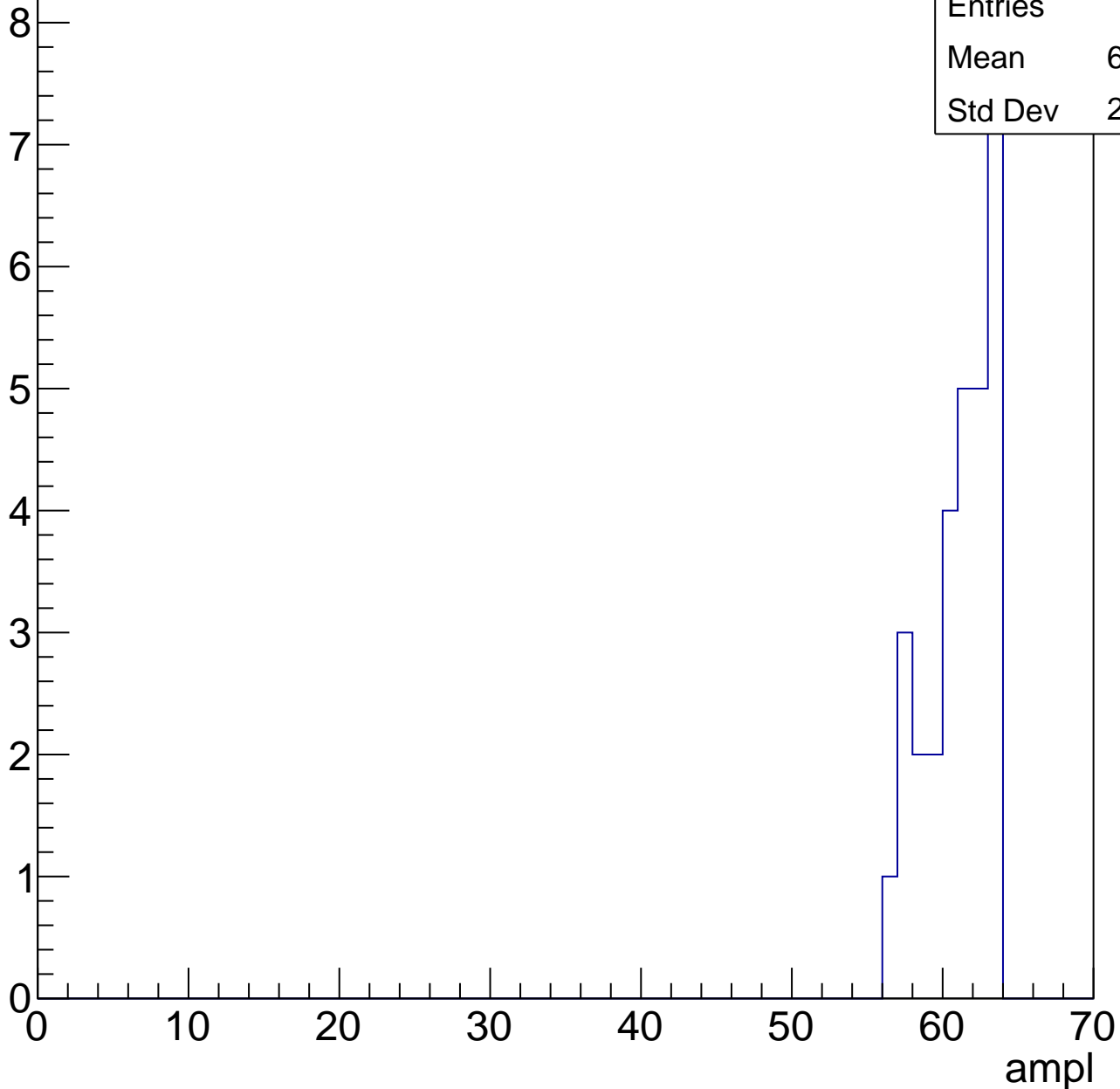


B0L001S, U11-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	60.67
Std Dev	2.134



B0L001S, U11-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch110, adc0

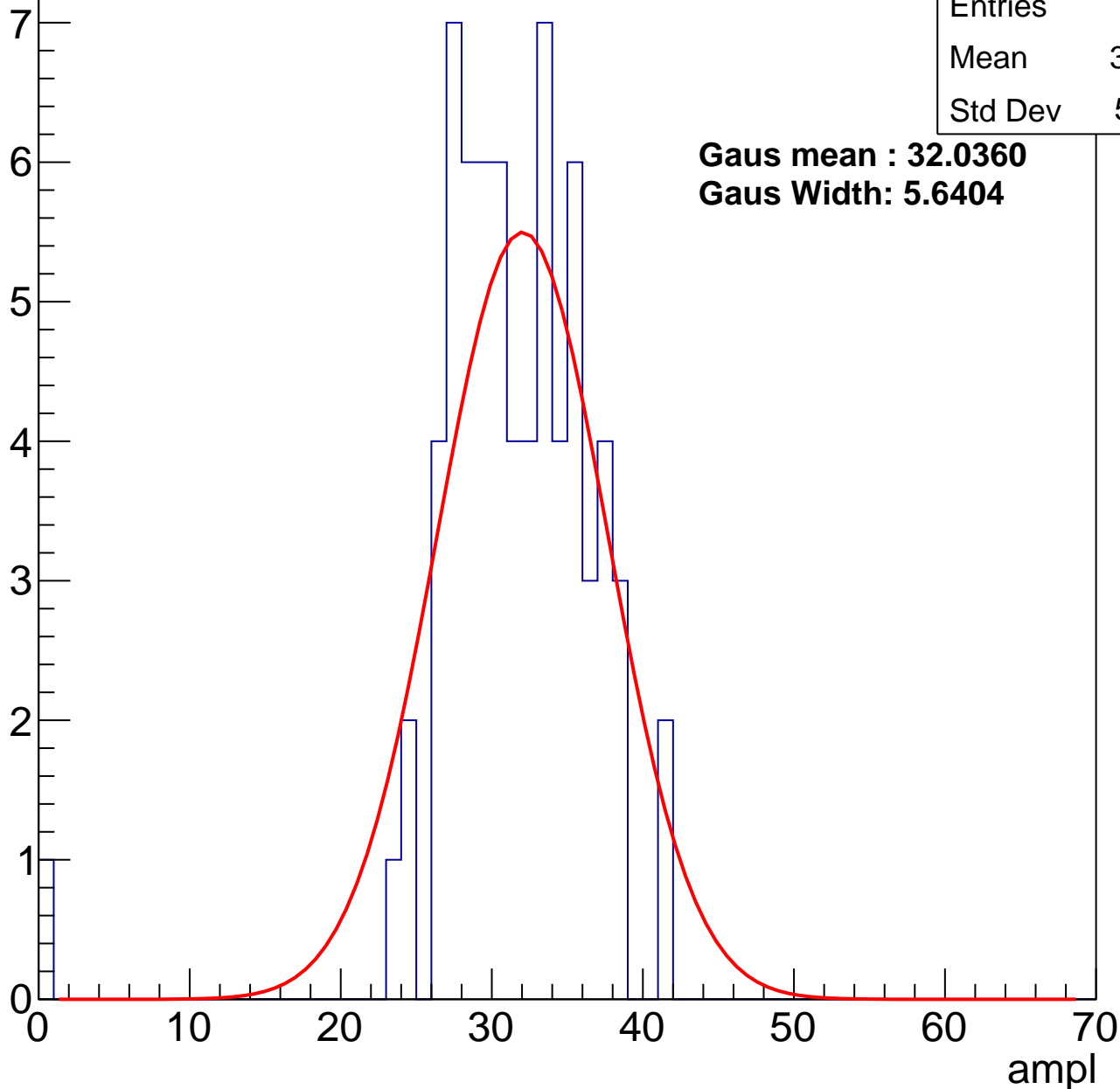
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.96
Std Dev	5.541

Gaus mean : 32.0360

Gaus Width: 5.6404



B0L001S, U11-ch110, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	37.77
Std Dev	3.937

Gaus mean : 37.6501

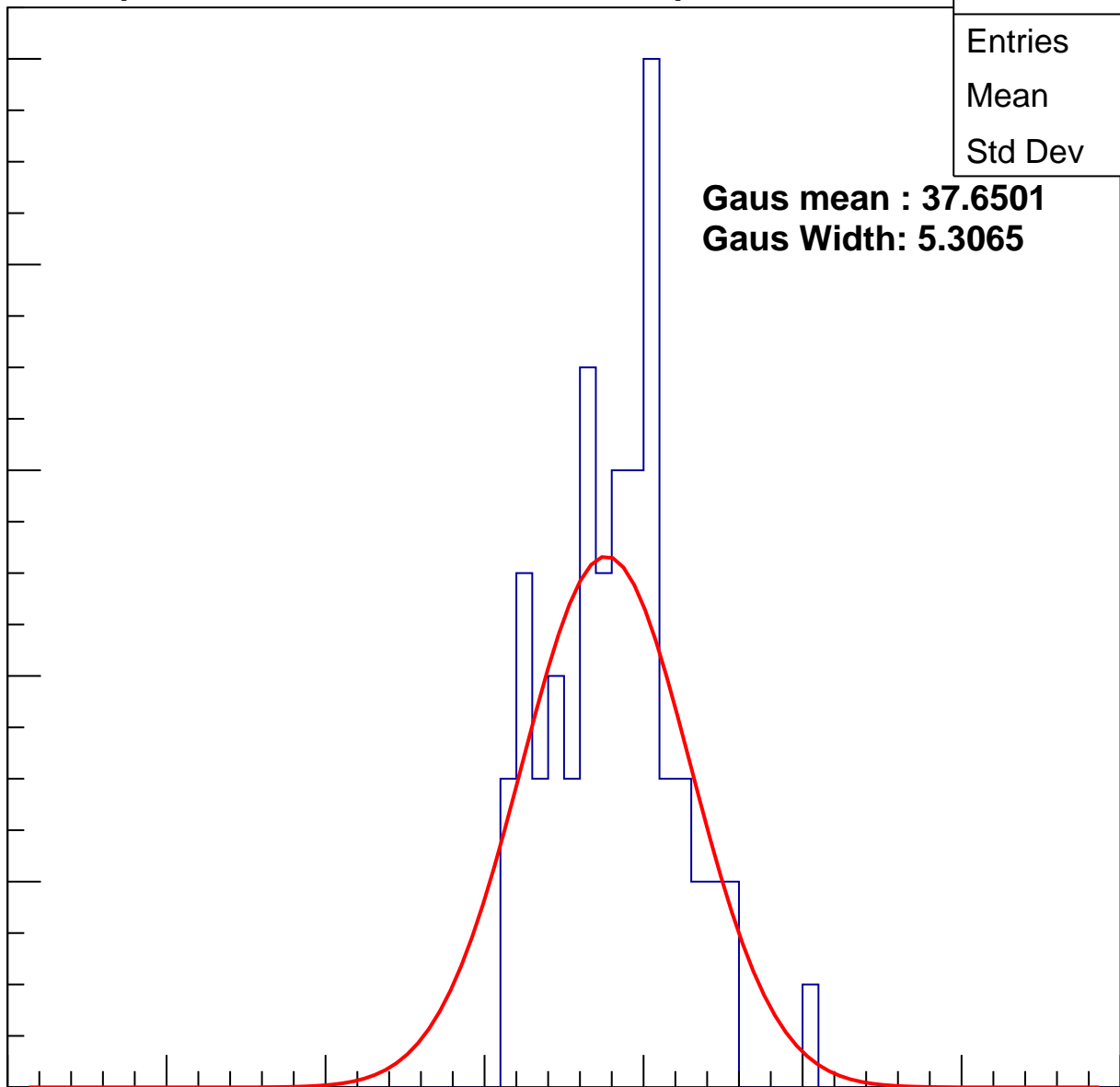
Gaus Width: 5.3065

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch110, adc2

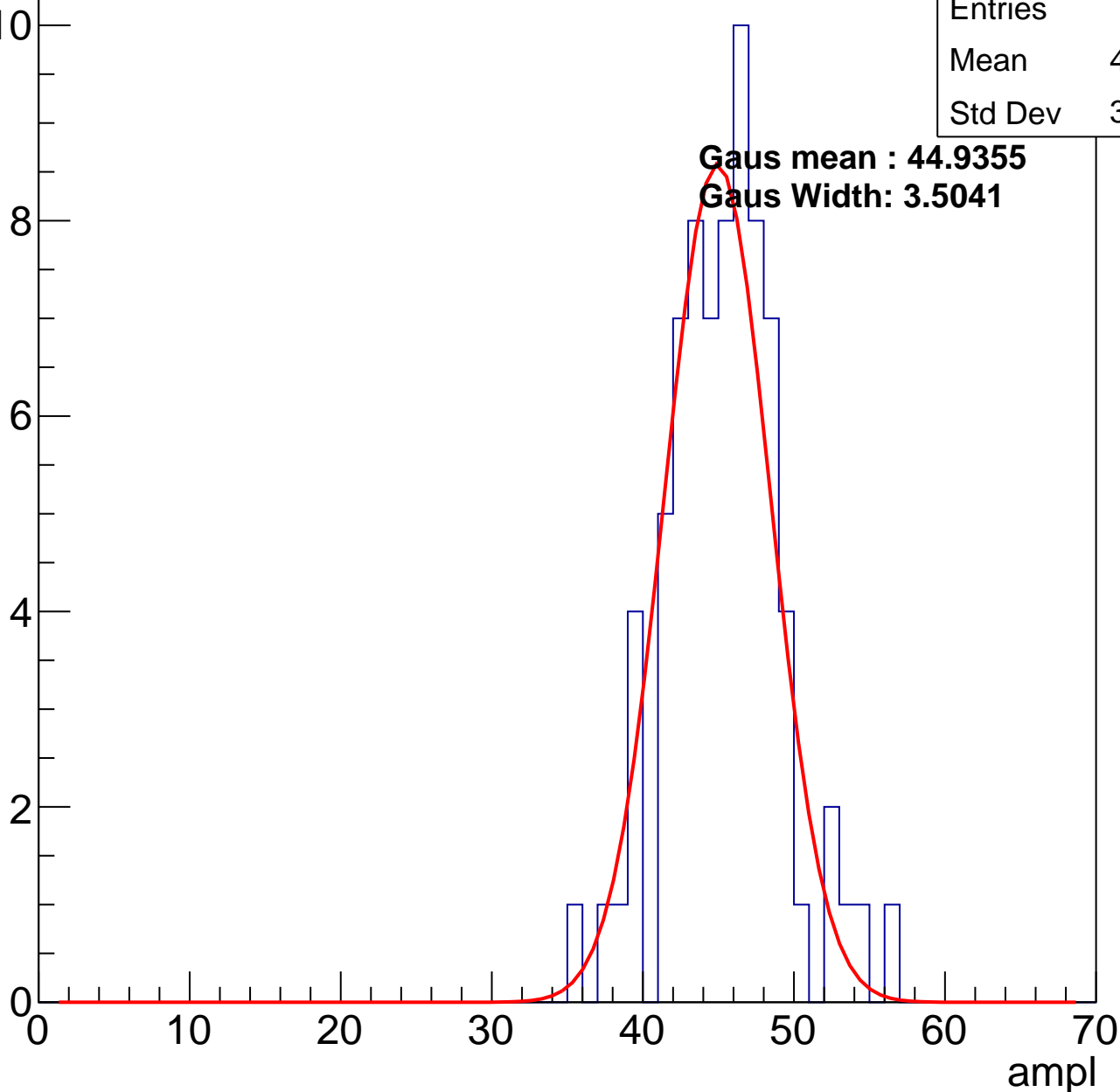
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.96
Std Dev	3.774

Gaus mean : 44.9355

Gaus Width: 3.5041

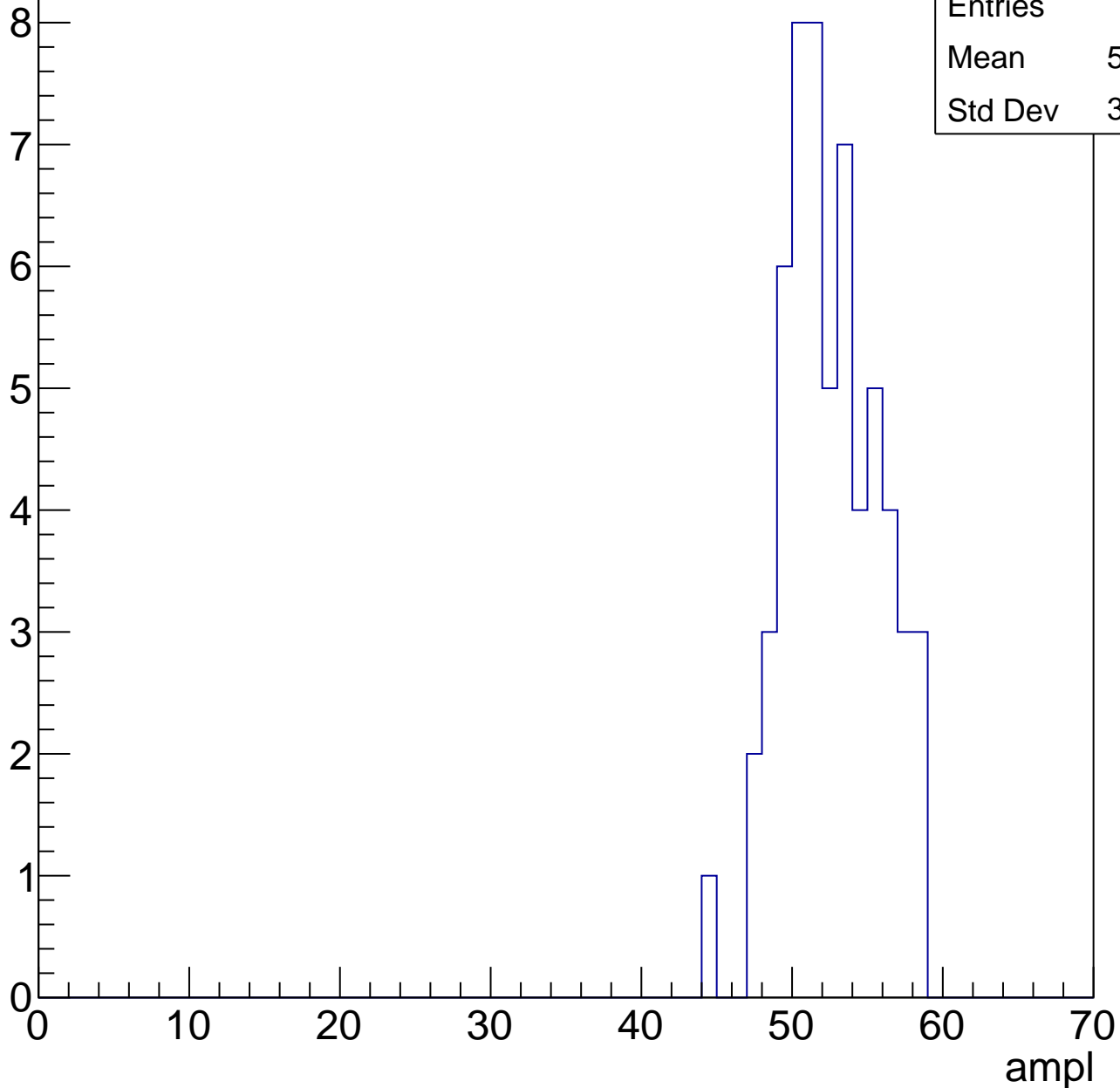


B0L001S, U11-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	52.12
Std Dev	3.109

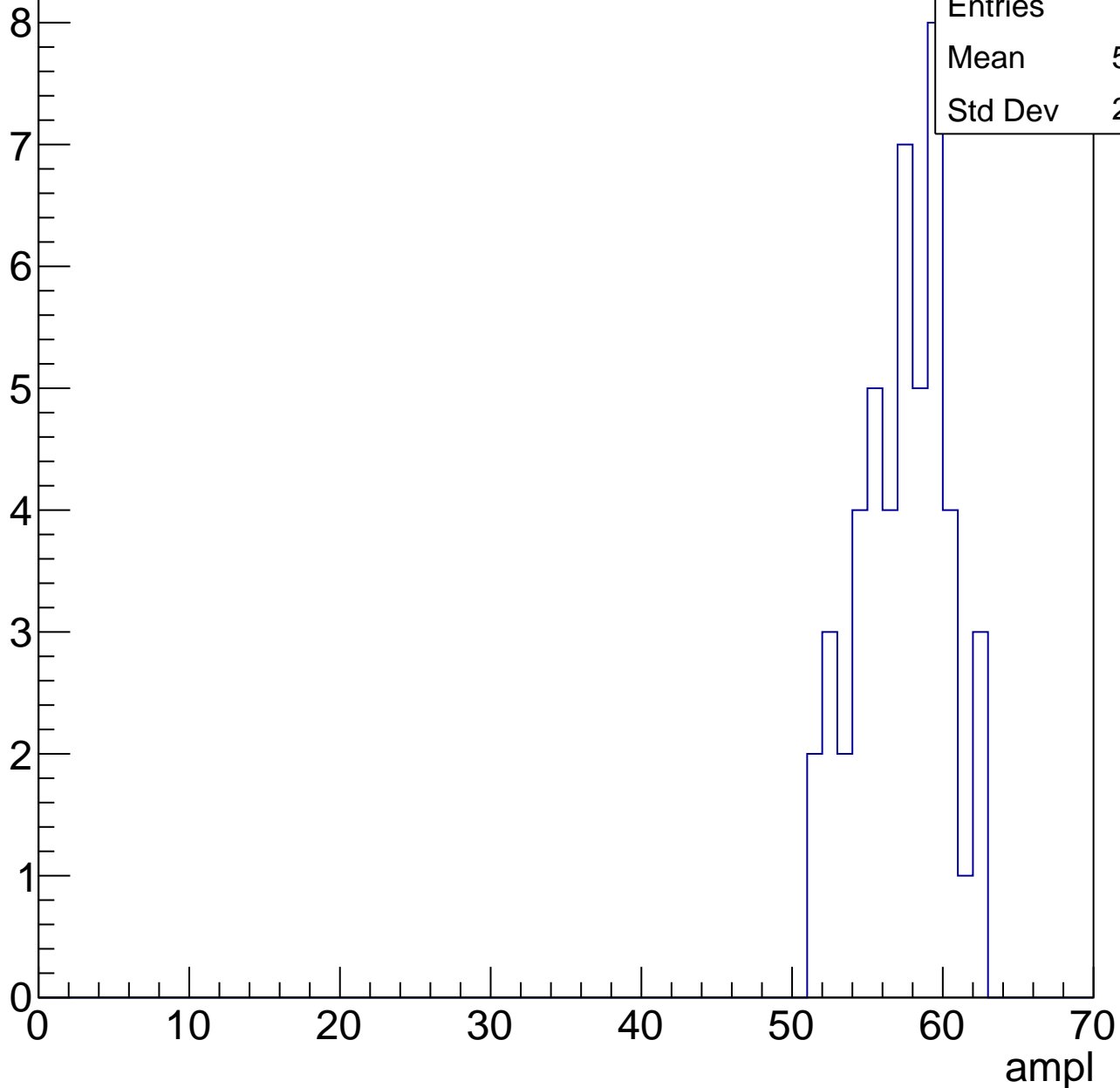


B0L001S, U11-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.81
Std Dev	2.891

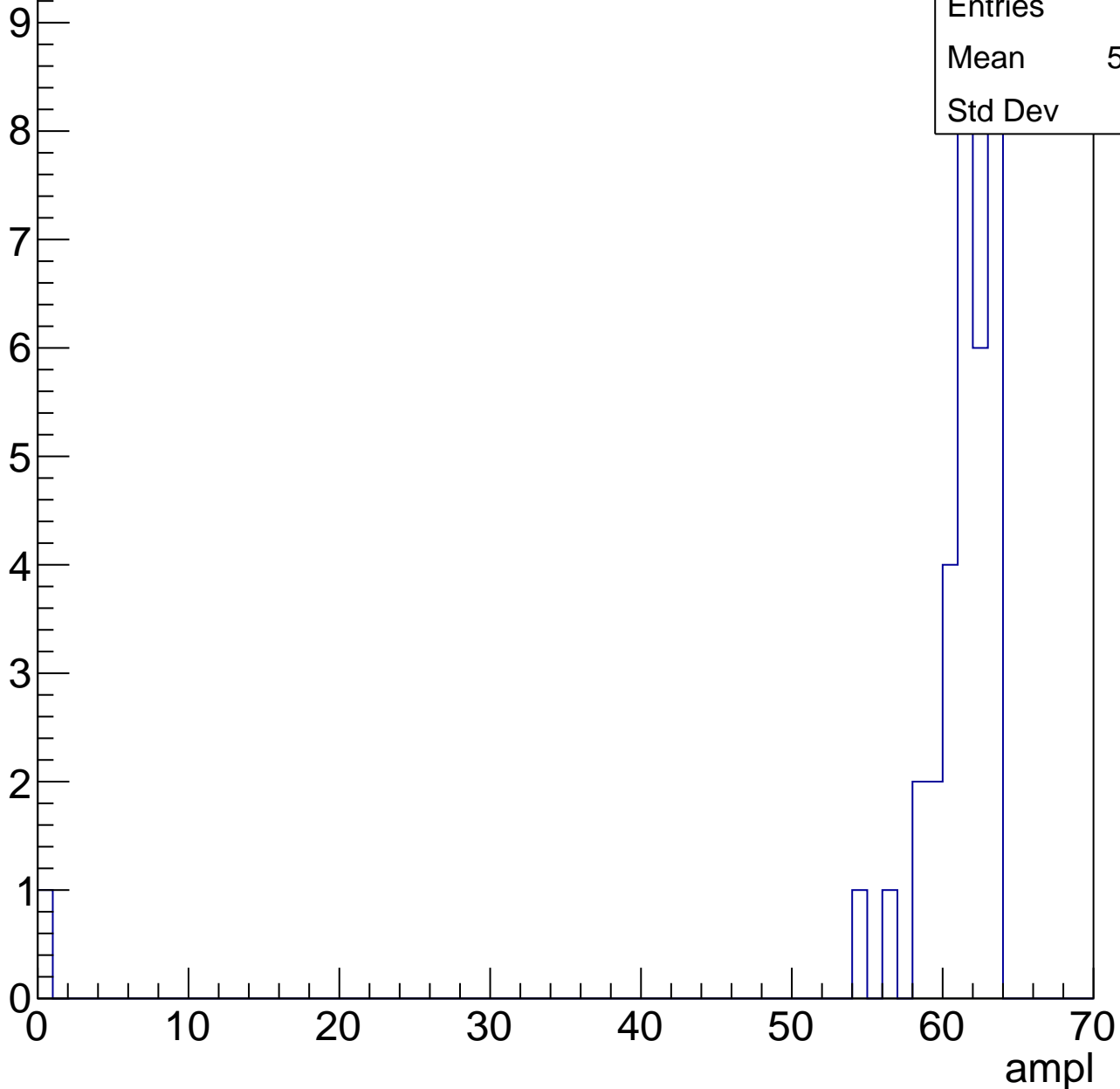


B0L001S, U11-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	59.15
Std Dev	10.5



B0L001S, U11-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

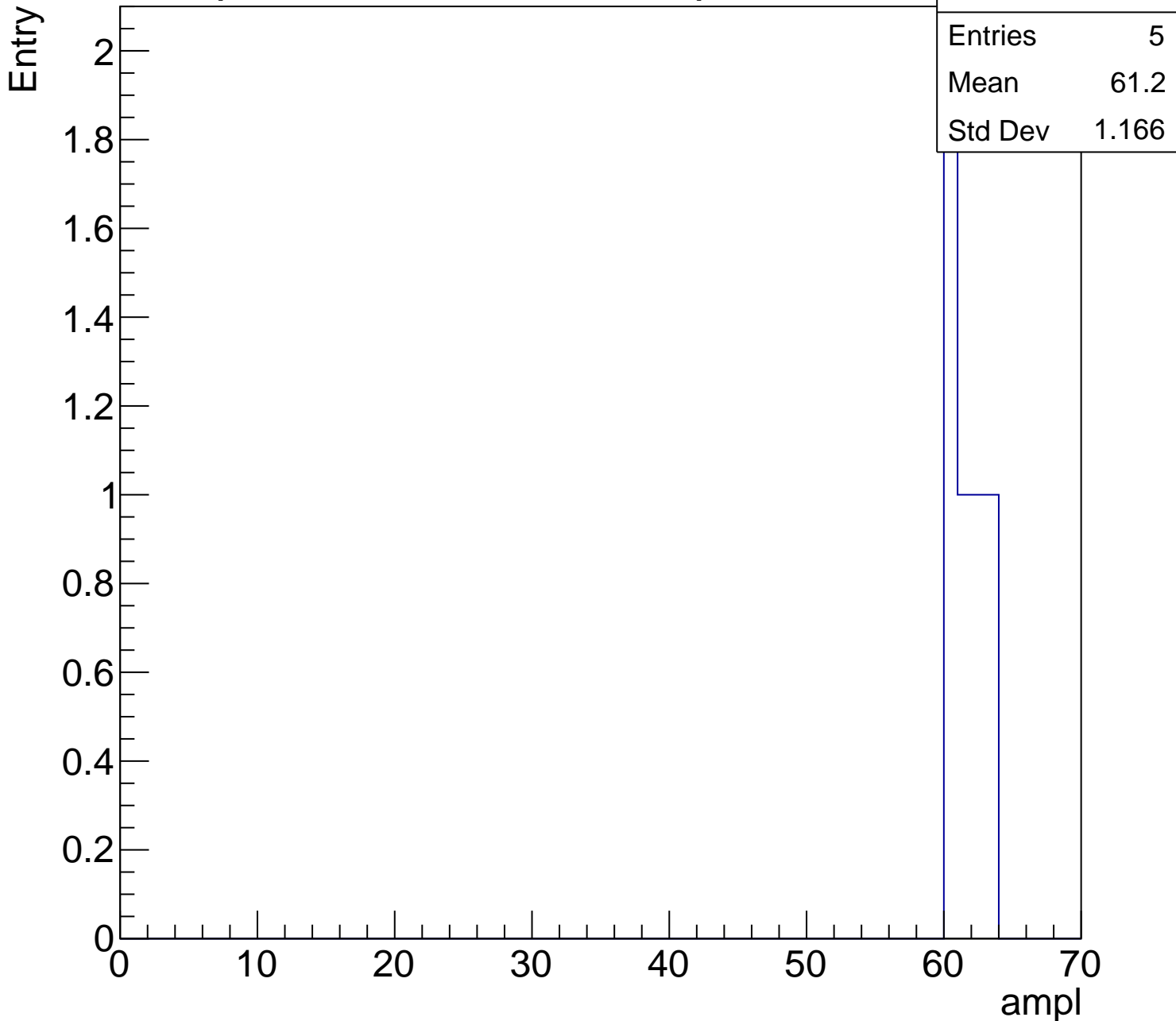
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch111, adc0

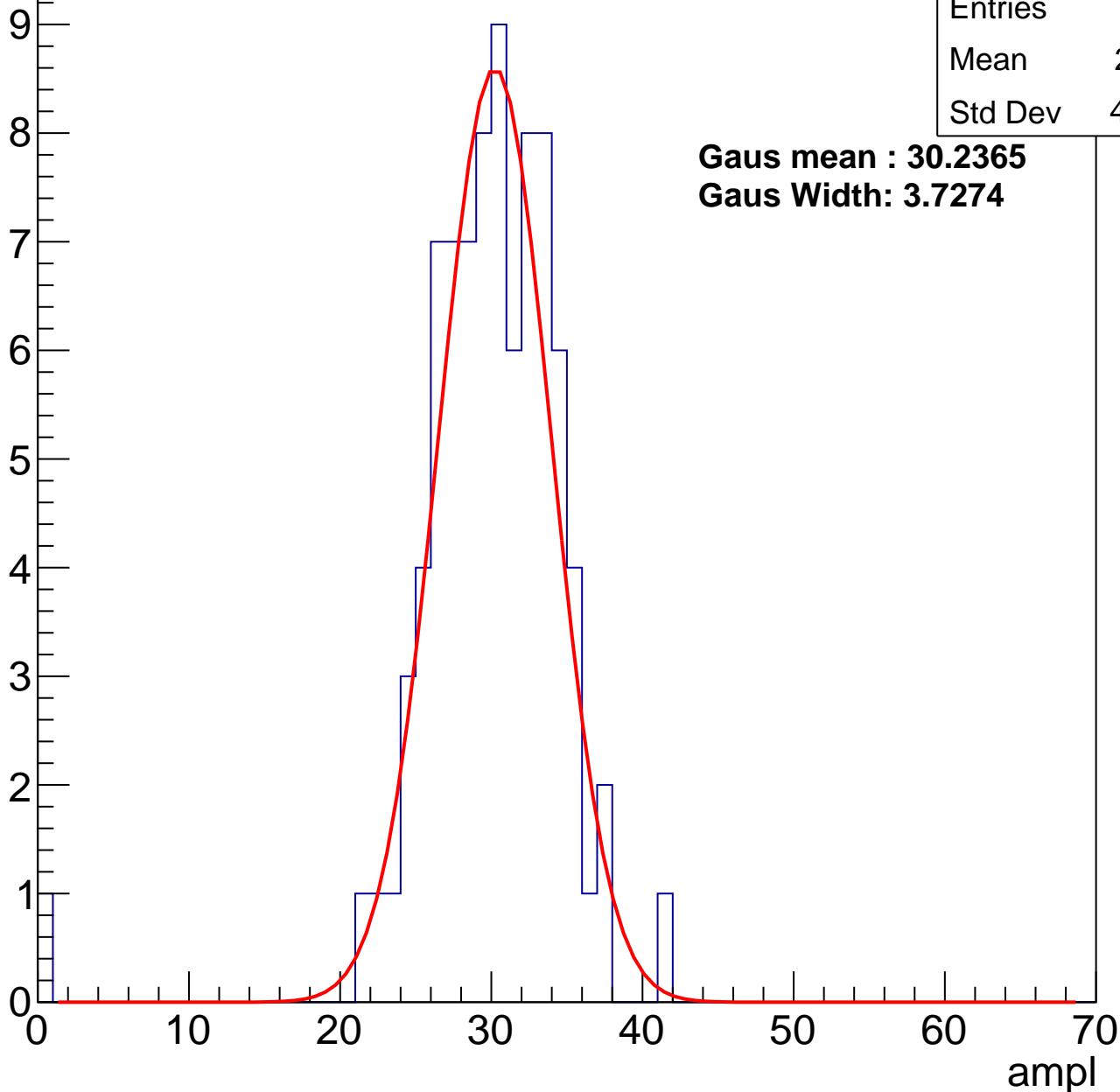
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	29.51
Std Dev	4.915

Gaus mean : 30.2365

Gaus Width: 3.7274



B0L001S, U11-ch111, adc1

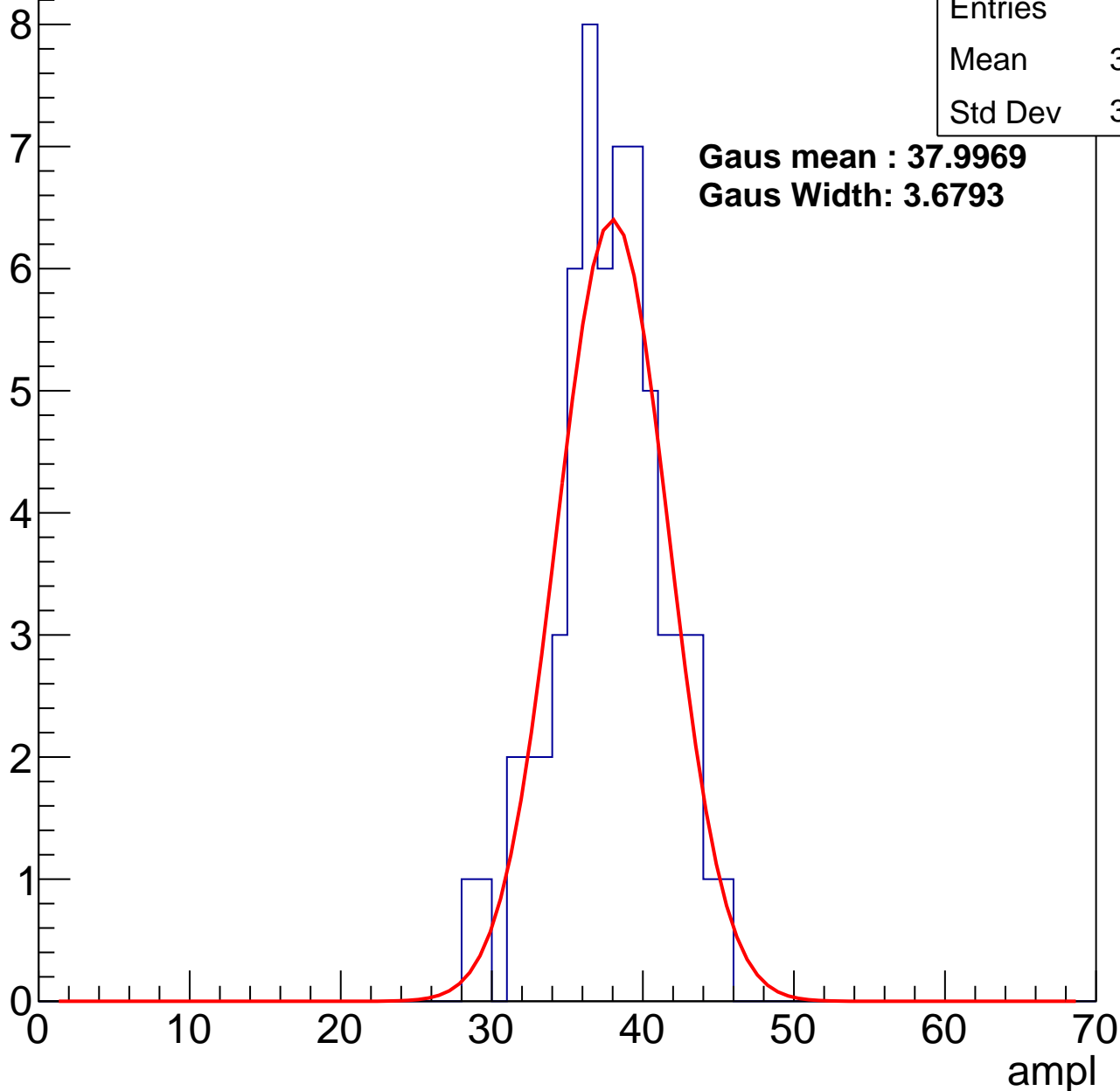
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.33
Std Dev	3.565

Gaus mean : 37.9969

Gaus Width: 3.6793



B0L001S, U11-ch111, adc2

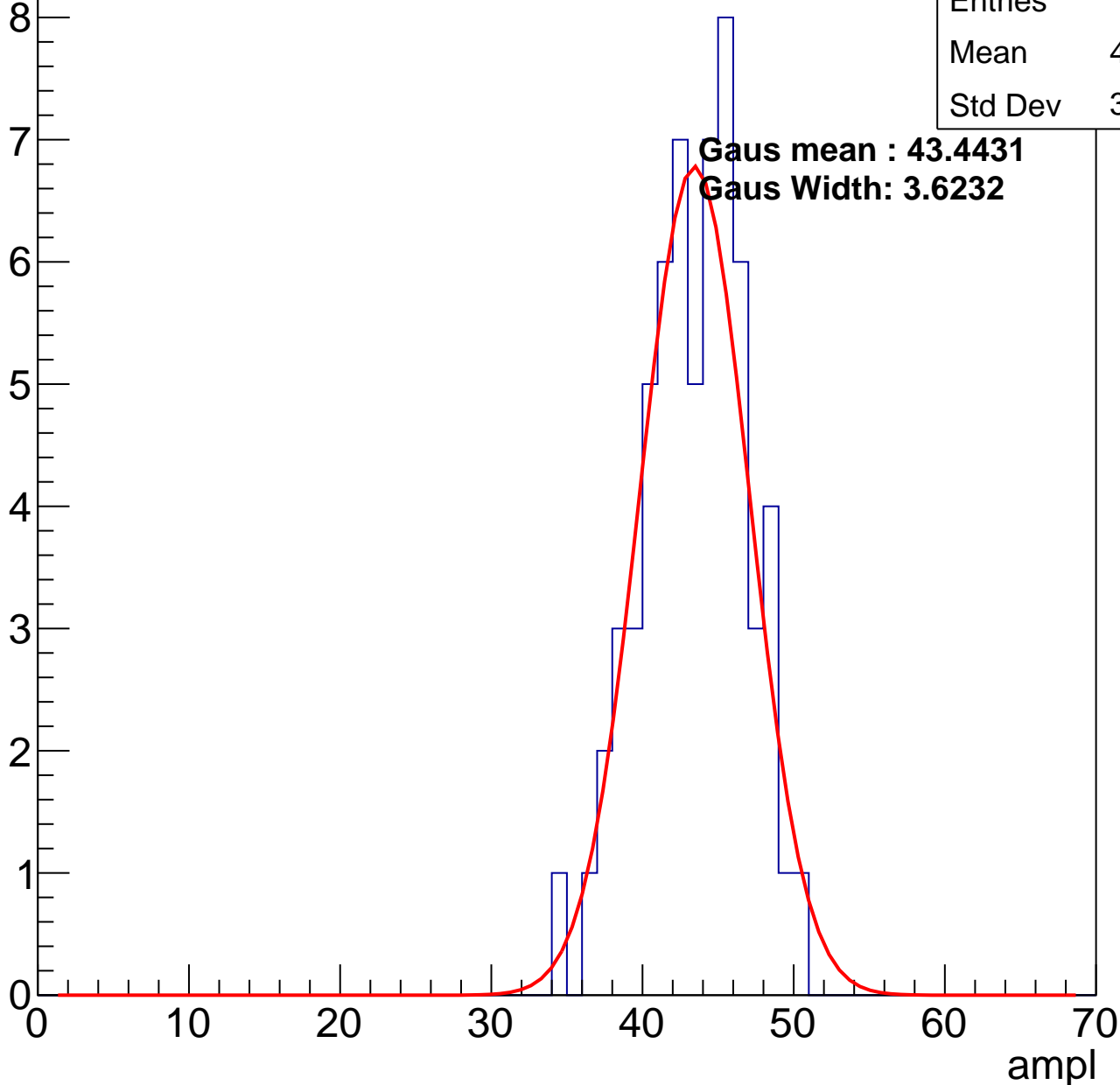
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.95
Std Dev	3.397

Gaus mean : 43.4431

Gaus Width: 3.6232

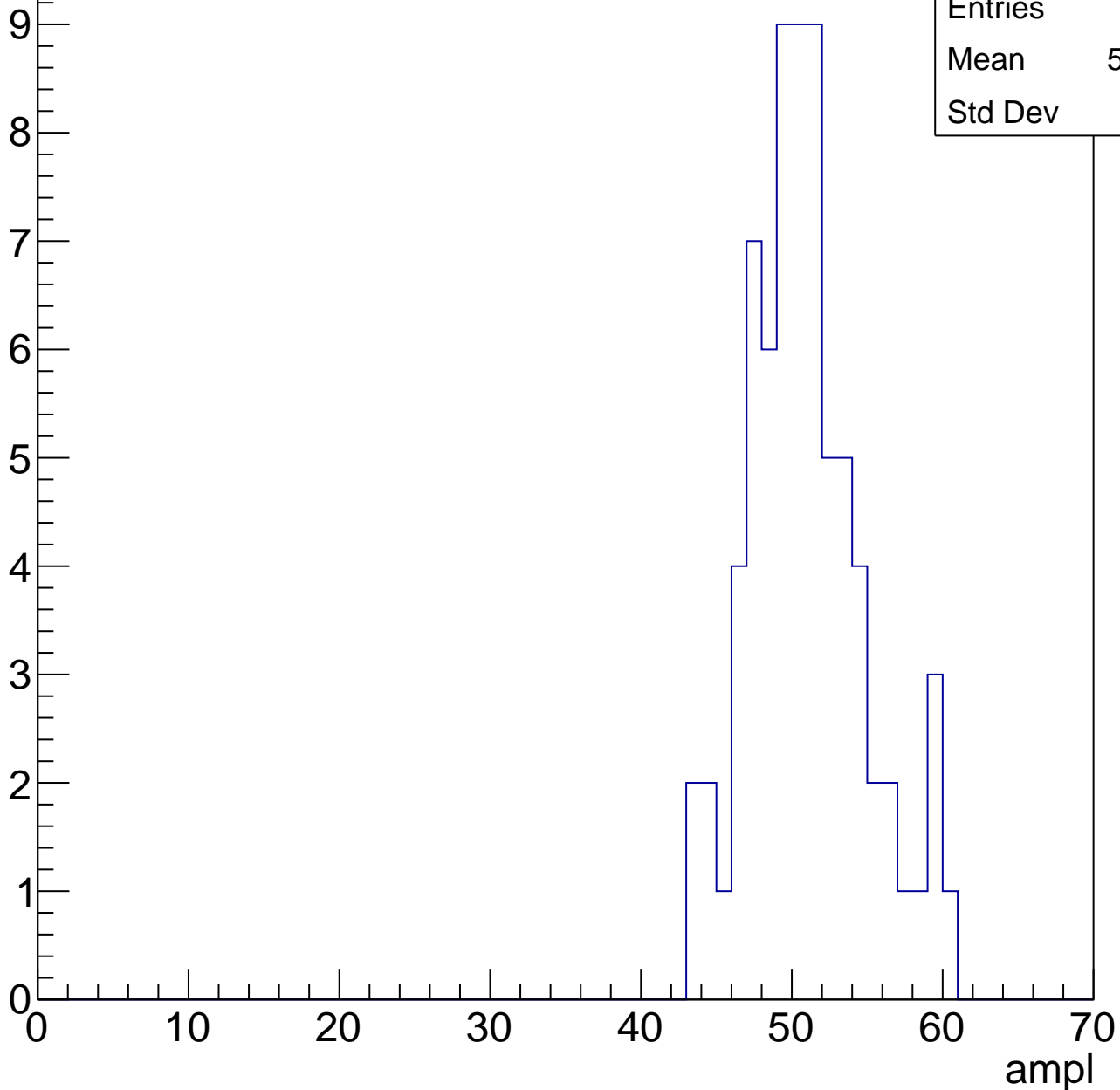


B0L001S, U11-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.48
Std Dev	3.8

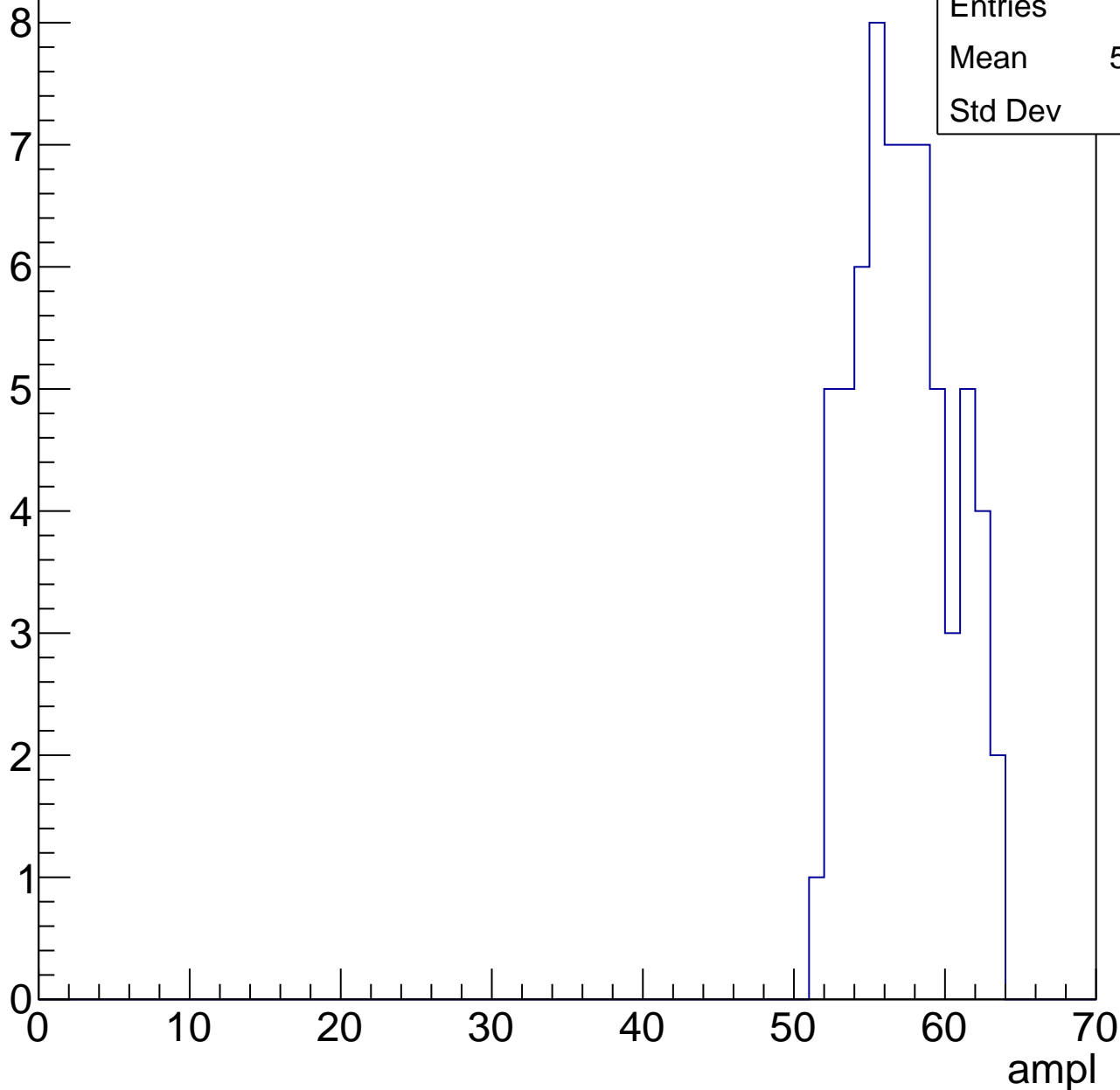


B0L001S, U11-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.78
Std Dev	3.13

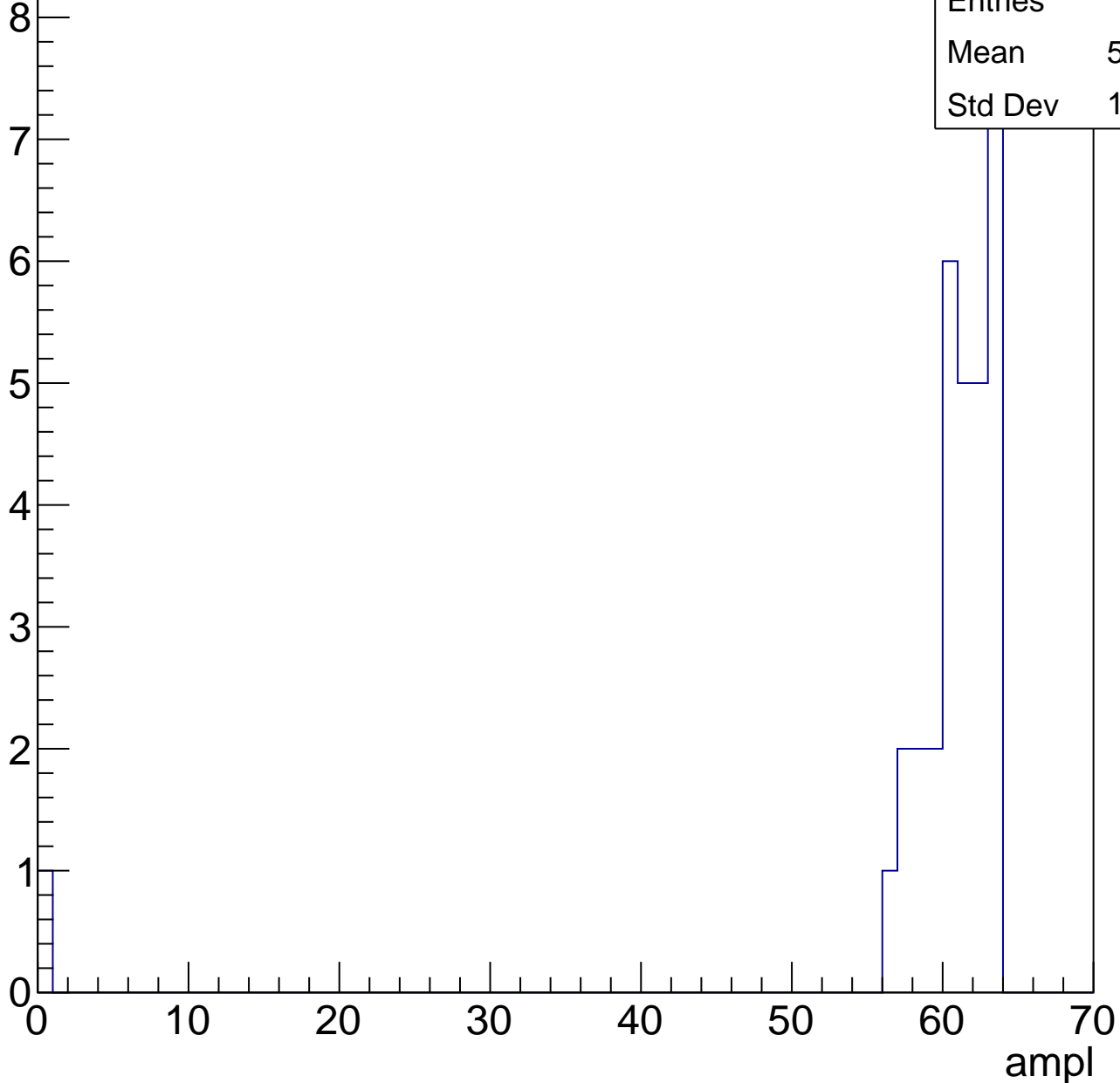


B0L001S, U11-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.84
Std Dev	10.75



B0L001S, U11-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch112, adc0

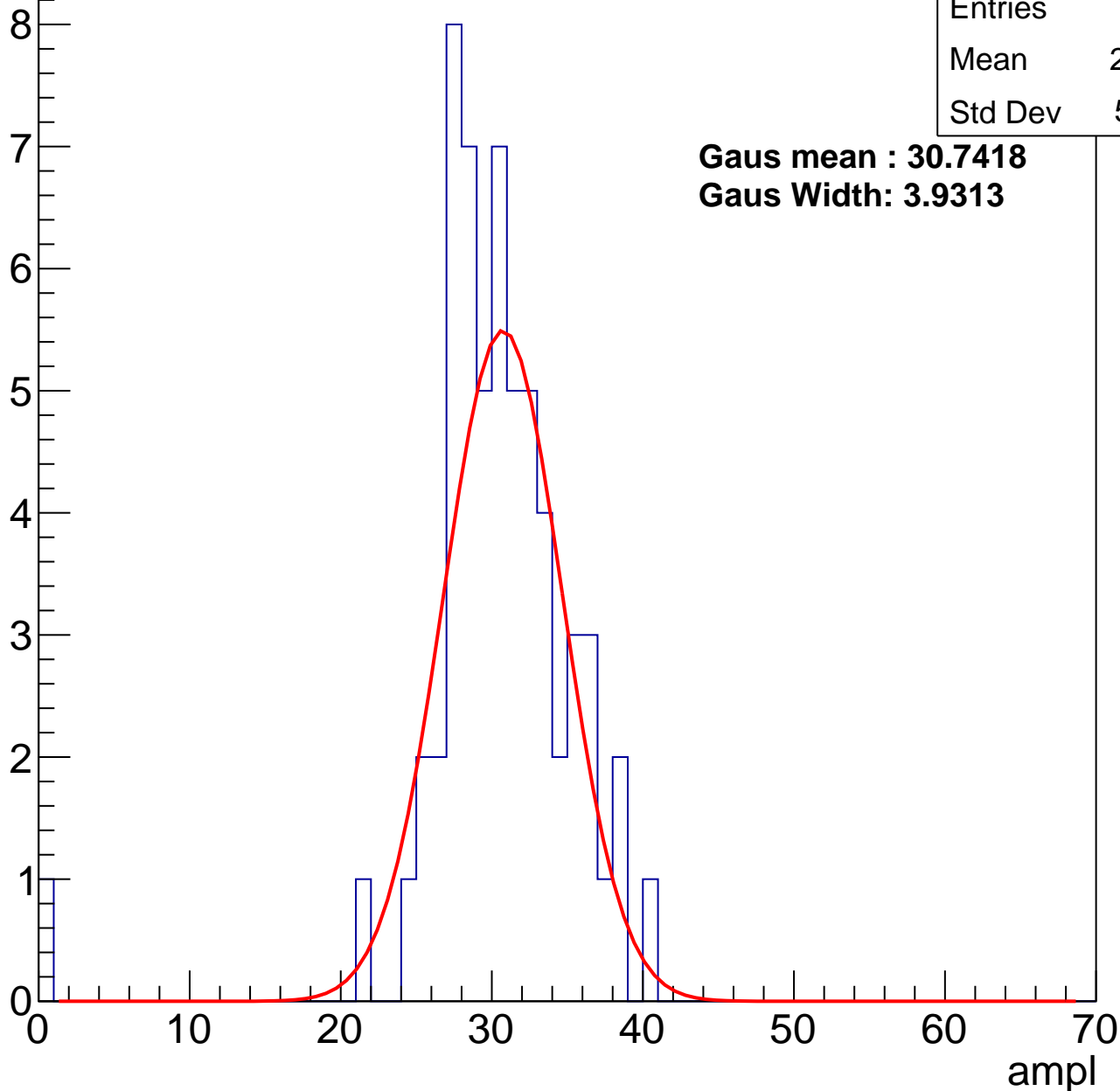
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.92
Std Dev	5.411

Gaus mean : 30.7418

Gaus Width: 3.9313



B0L001S, U11-ch112, adc1

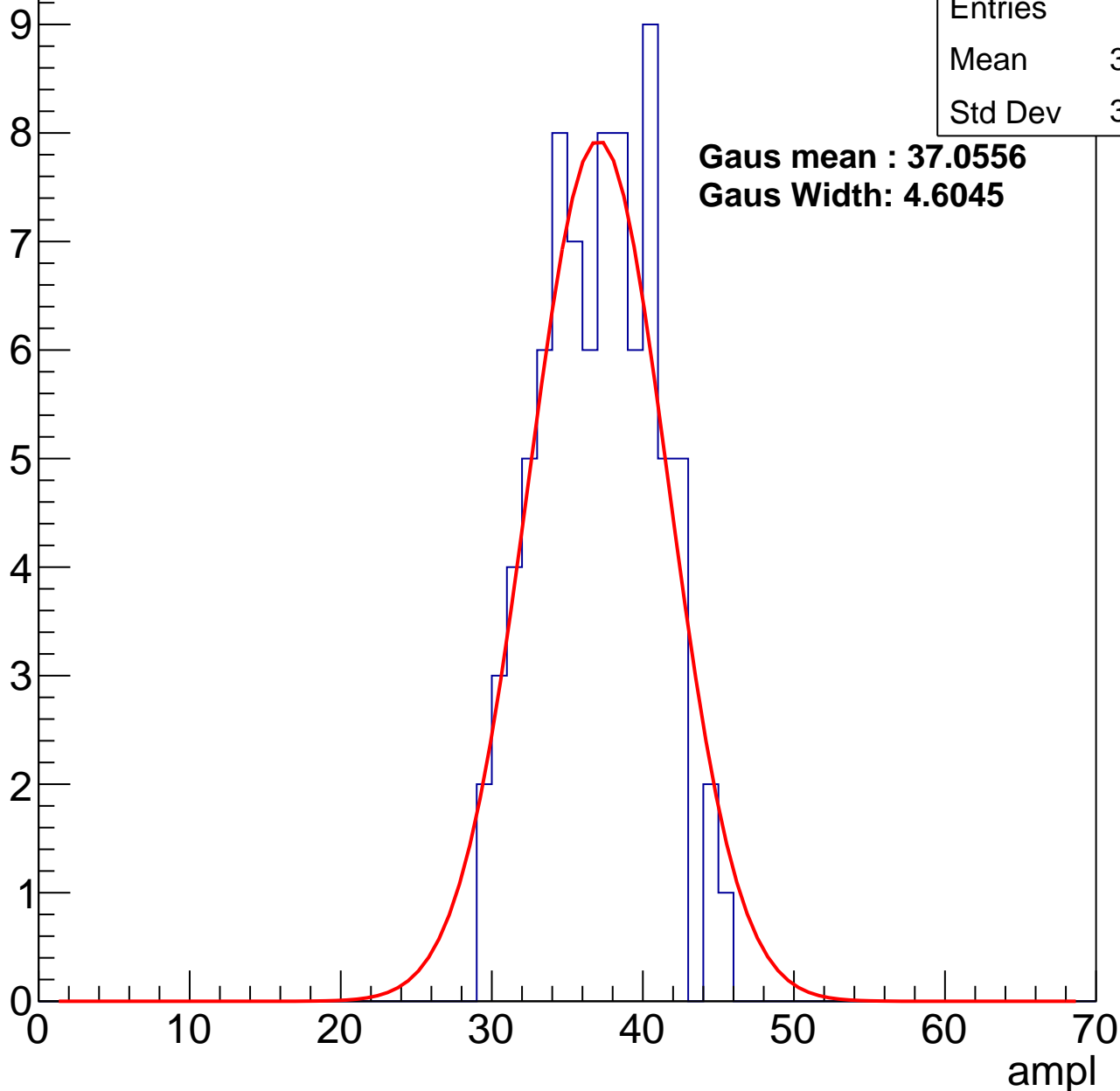
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	36.53
Std Dev	3.769

Gaus mean : 37.0556

Gaus Width: 4.6045



B0L001S, U11-ch112, adc2

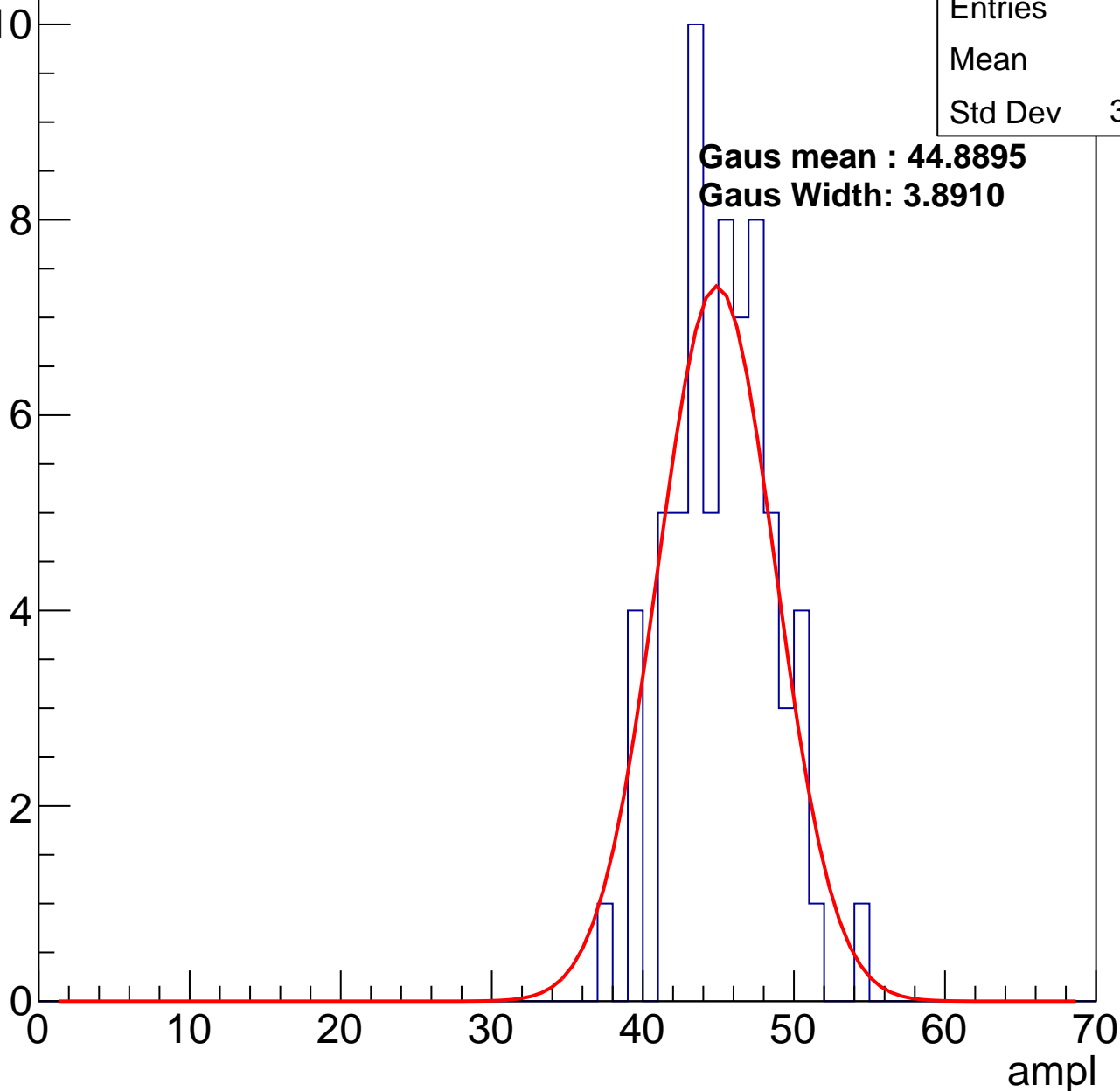
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.9
Std Dev	3.292

Gaus mean : 44.8895

Gaus Width: 3.8910

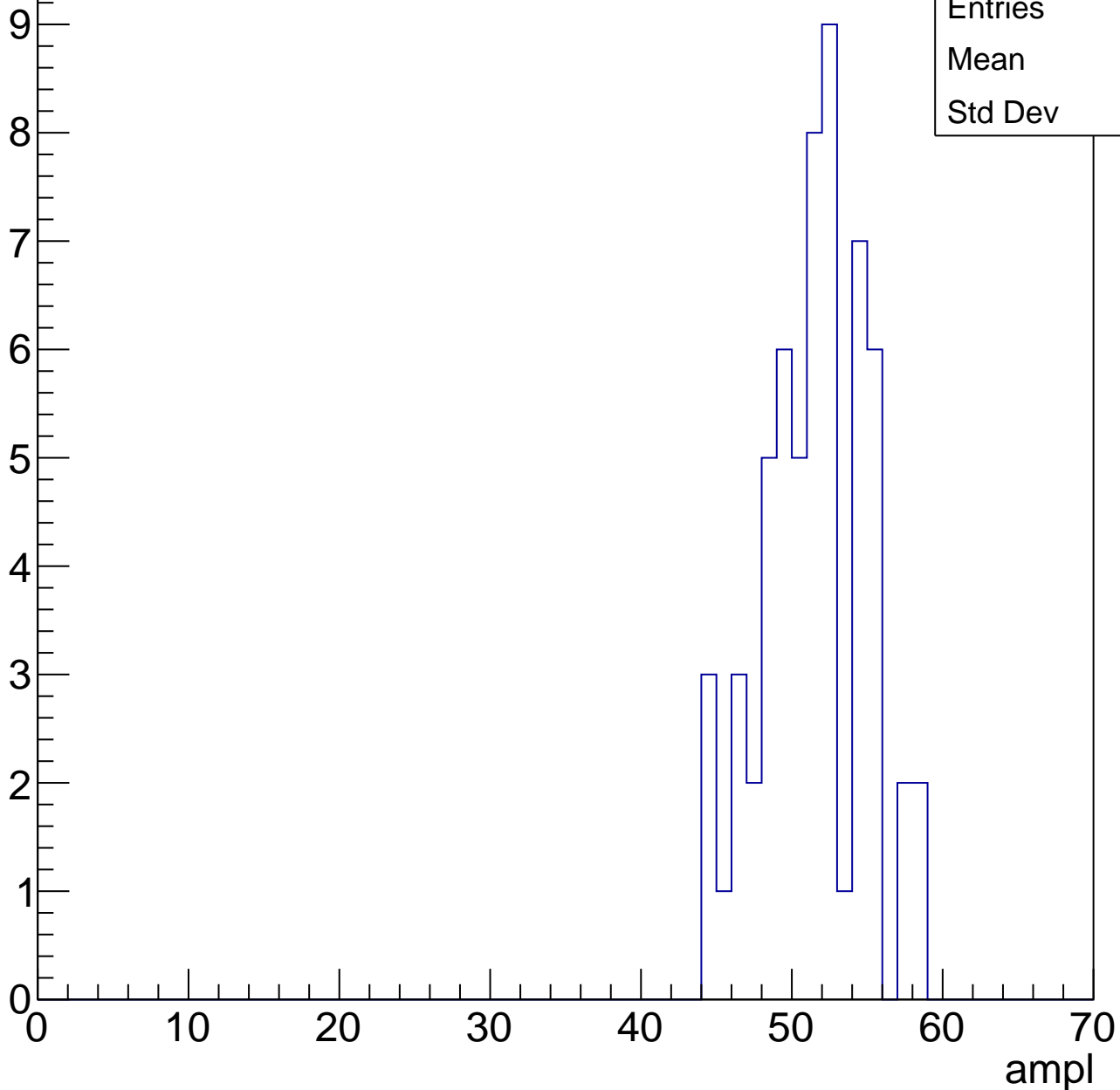


B0L001S, U11-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	51
Std Dev	3.43



B0L001S, U11-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

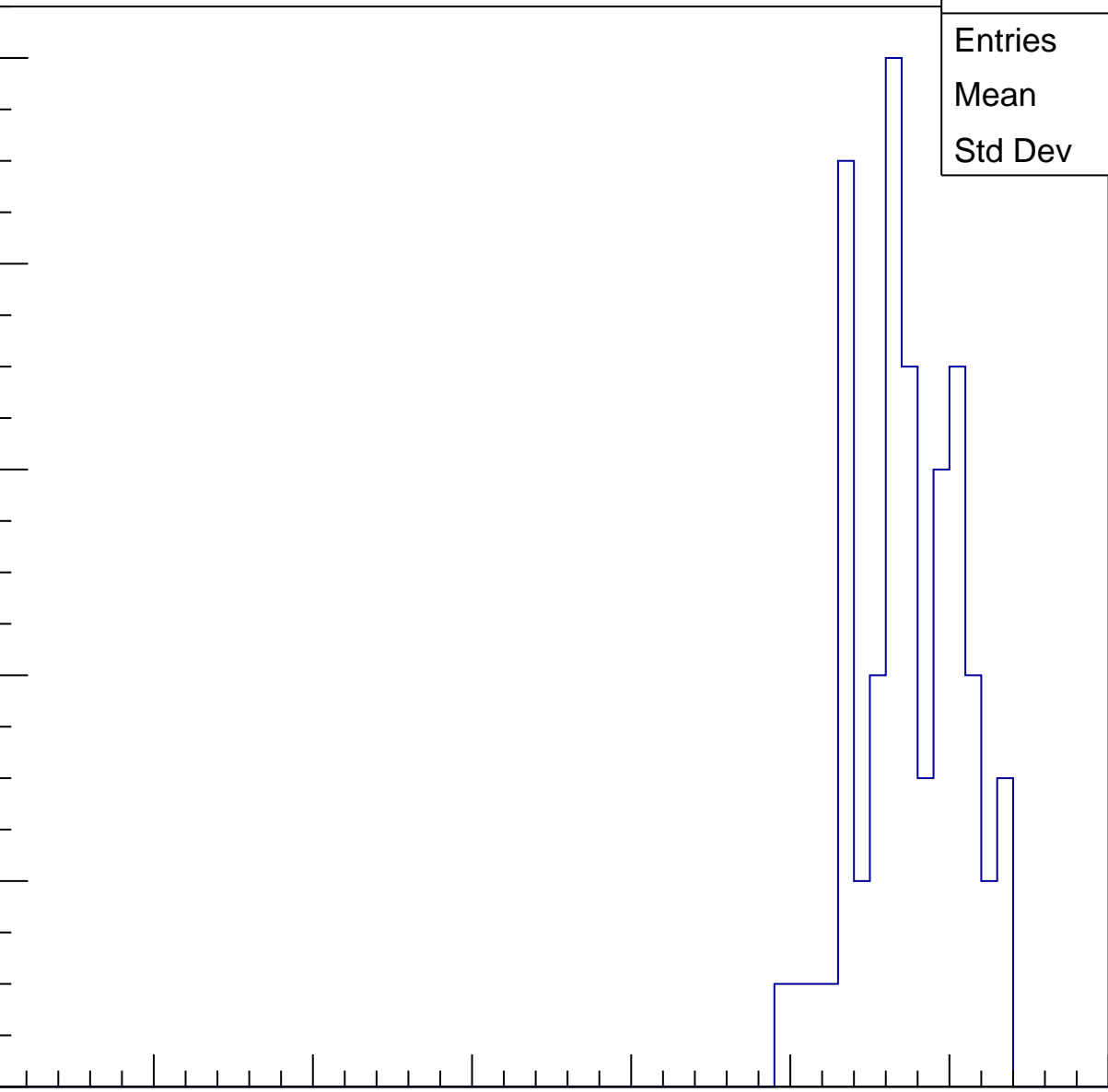
Entries	61
Mean	56.9
Std Dev	3.323

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

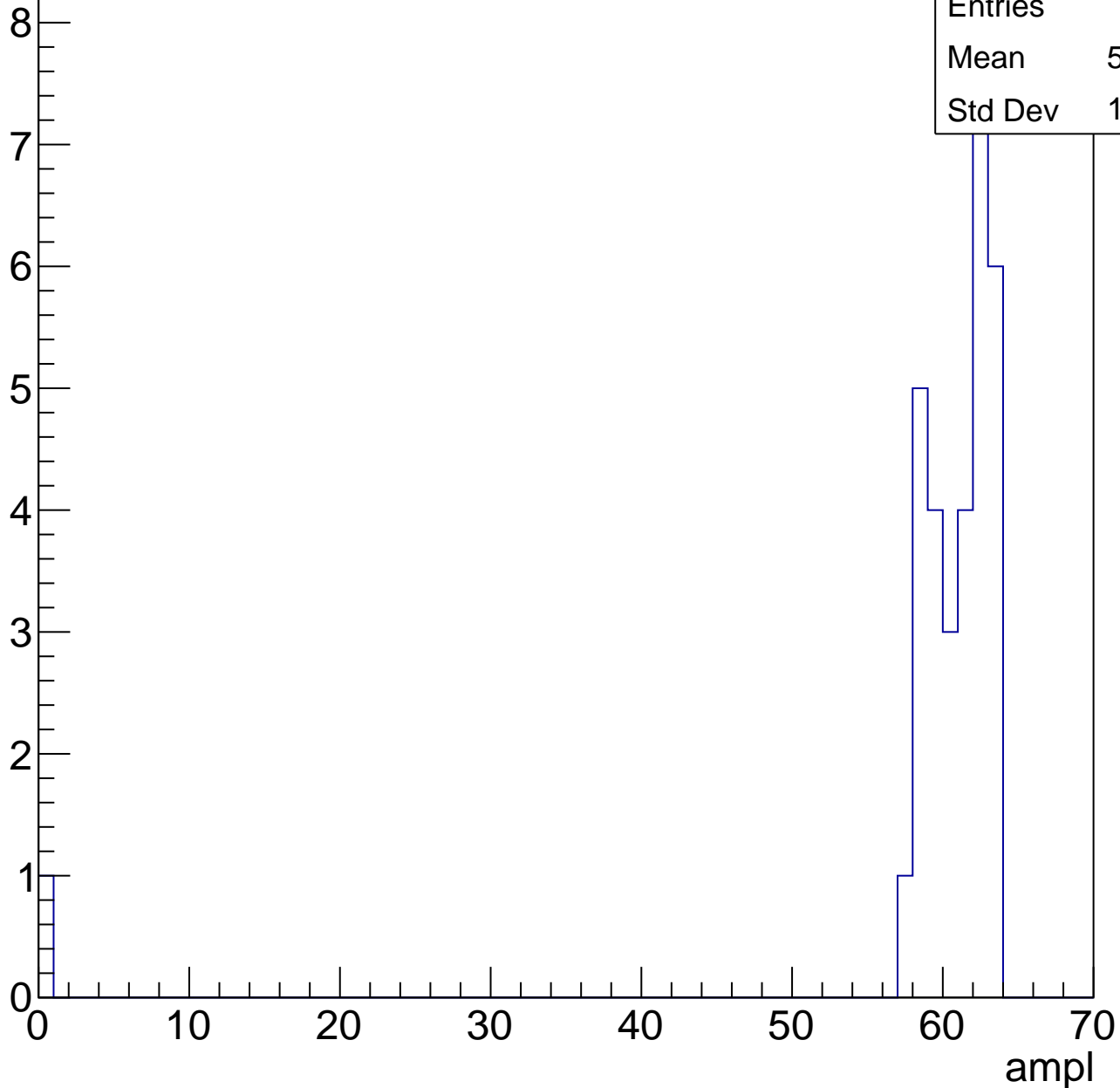


B0L001S, U11-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

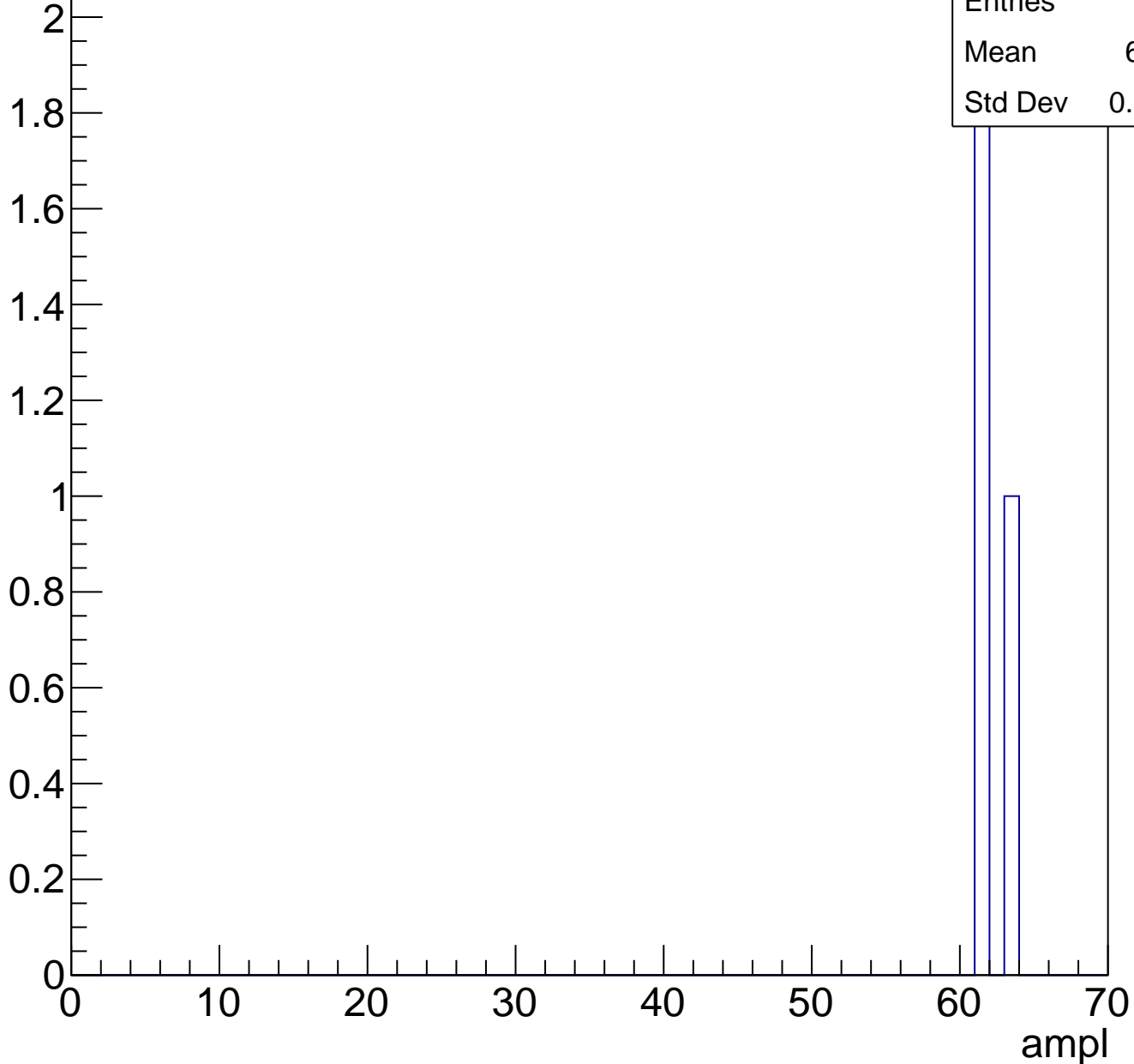
Entries	32
Mean	58.78
Std Dev	10.72



B0L001S, U11-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch113, adc0

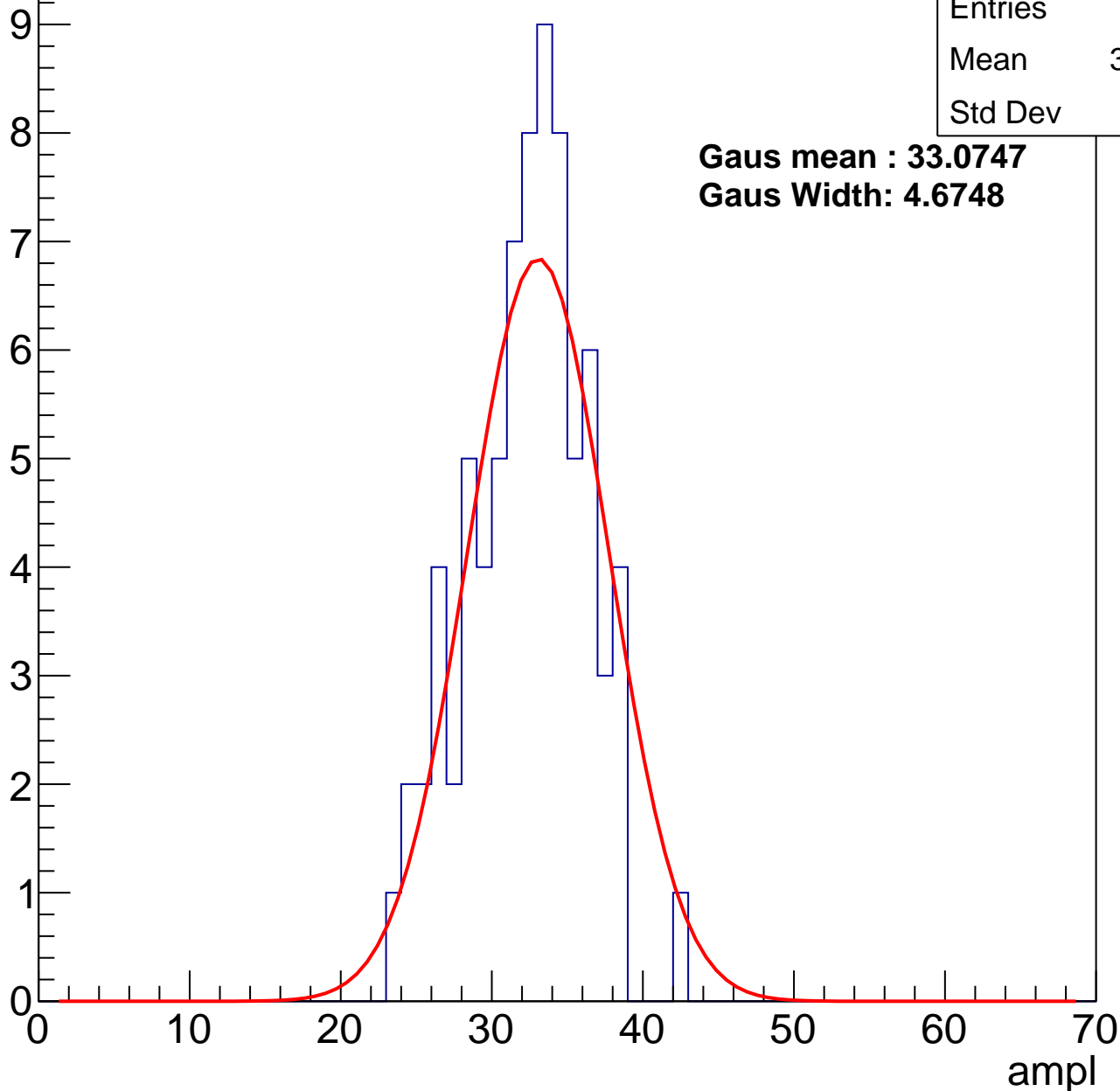
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	31.88
Std Dev	3.88

Gaus mean : 33.0747

Gaus Width: 4.6748



B0L001S, U11-ch113, adc1

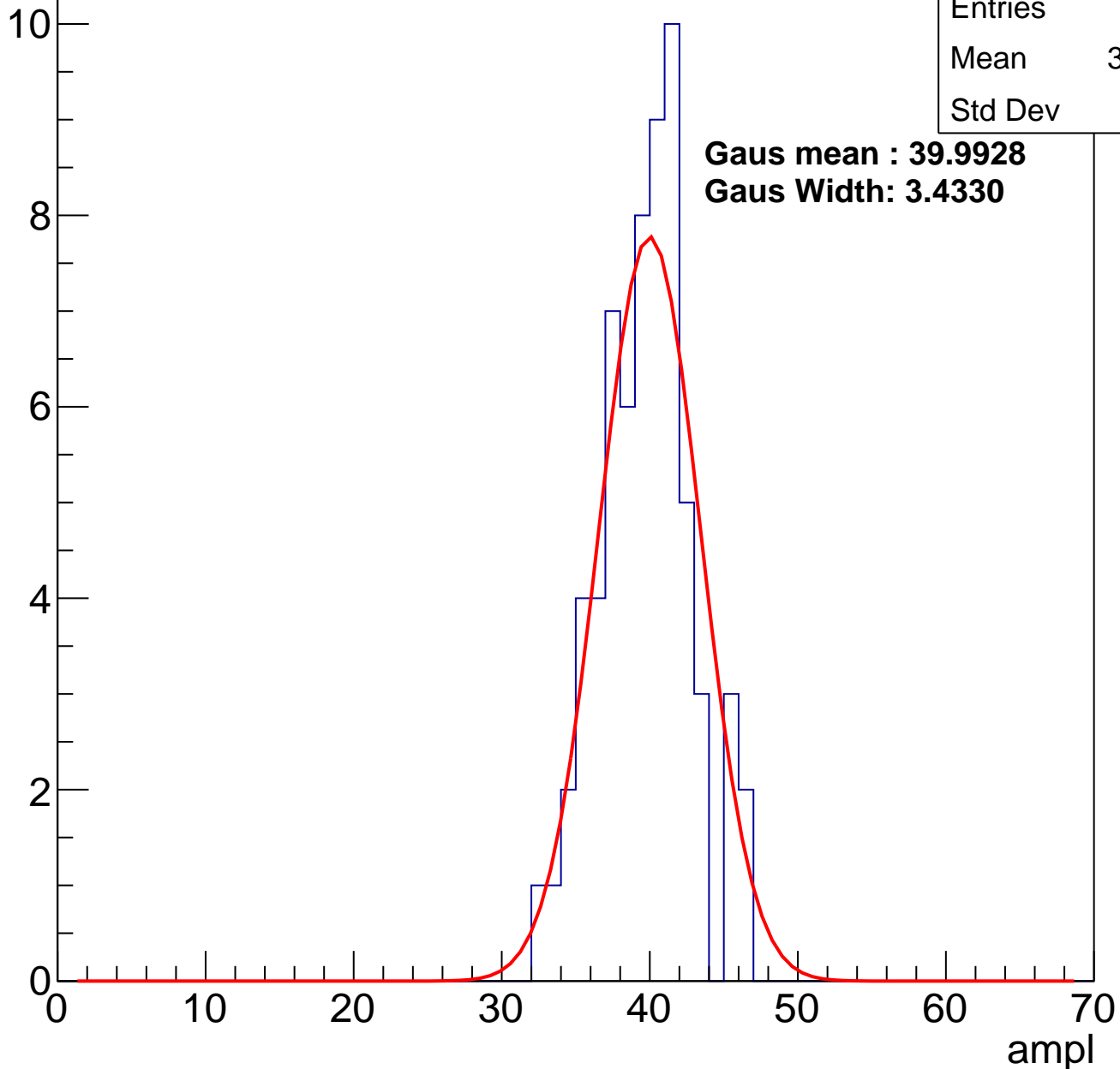
calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	39.26
Std Dev	3.07

Gaus mean : 39.9928

Gaus Width: 3.4330

Entry



B0L001S, U11-ch113, adc2

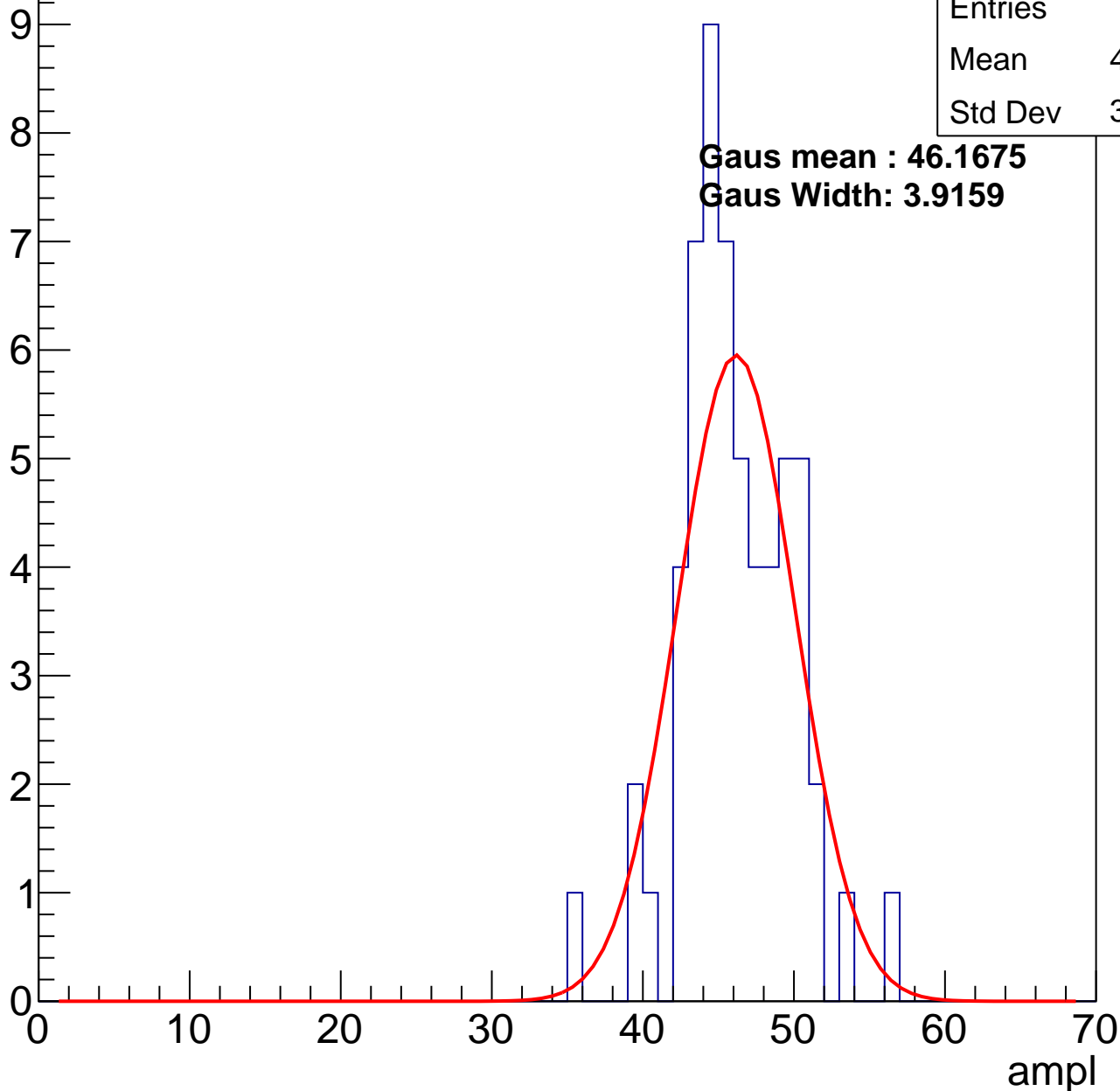
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.67
Std Dev	3.626

Gaus mean : 46.1675

Gaus Width: 3.9159

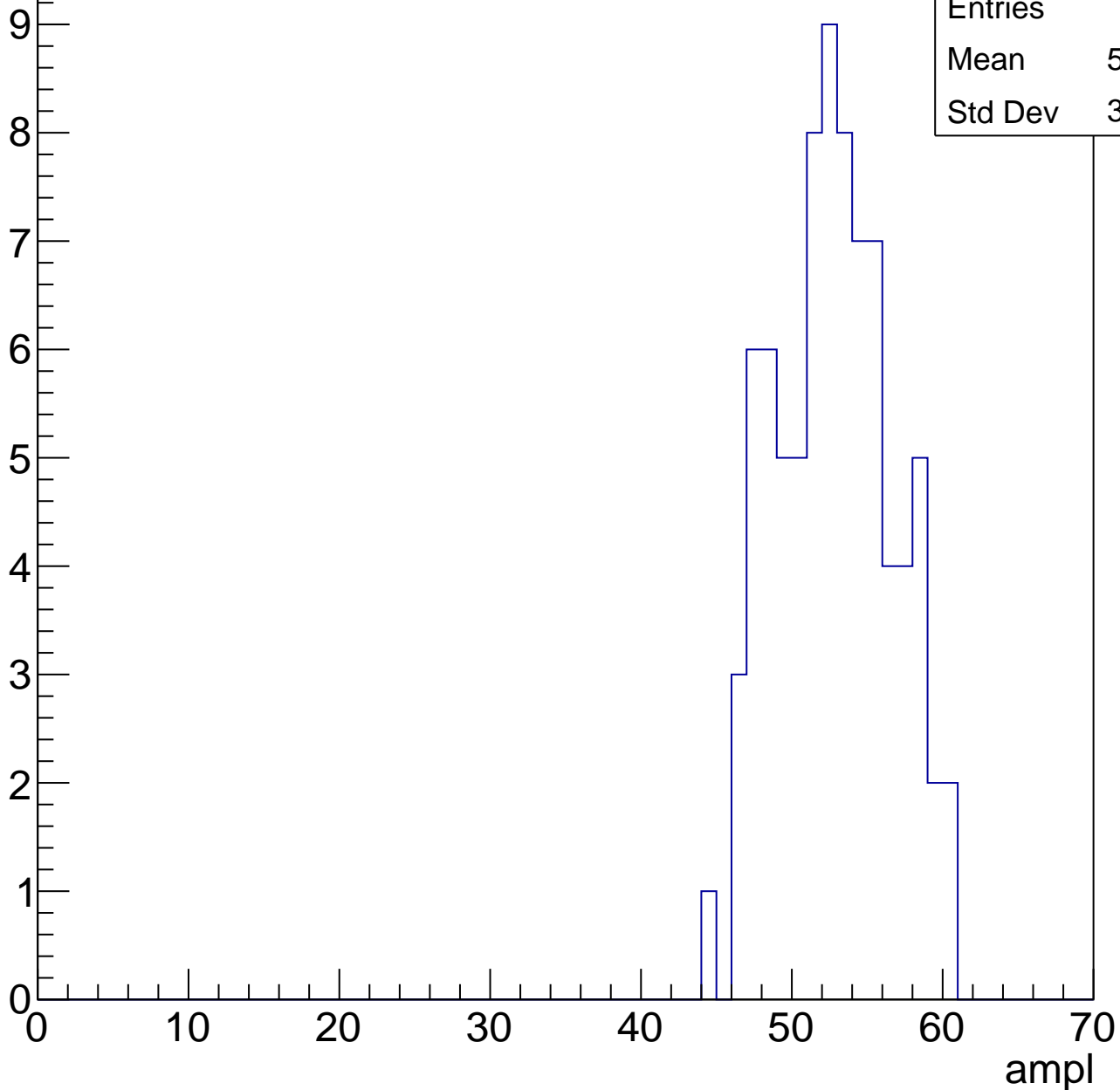


B0L001S, U11-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	52.32
Std Dev	3.748



B0L001S, U11-ch113, adc4

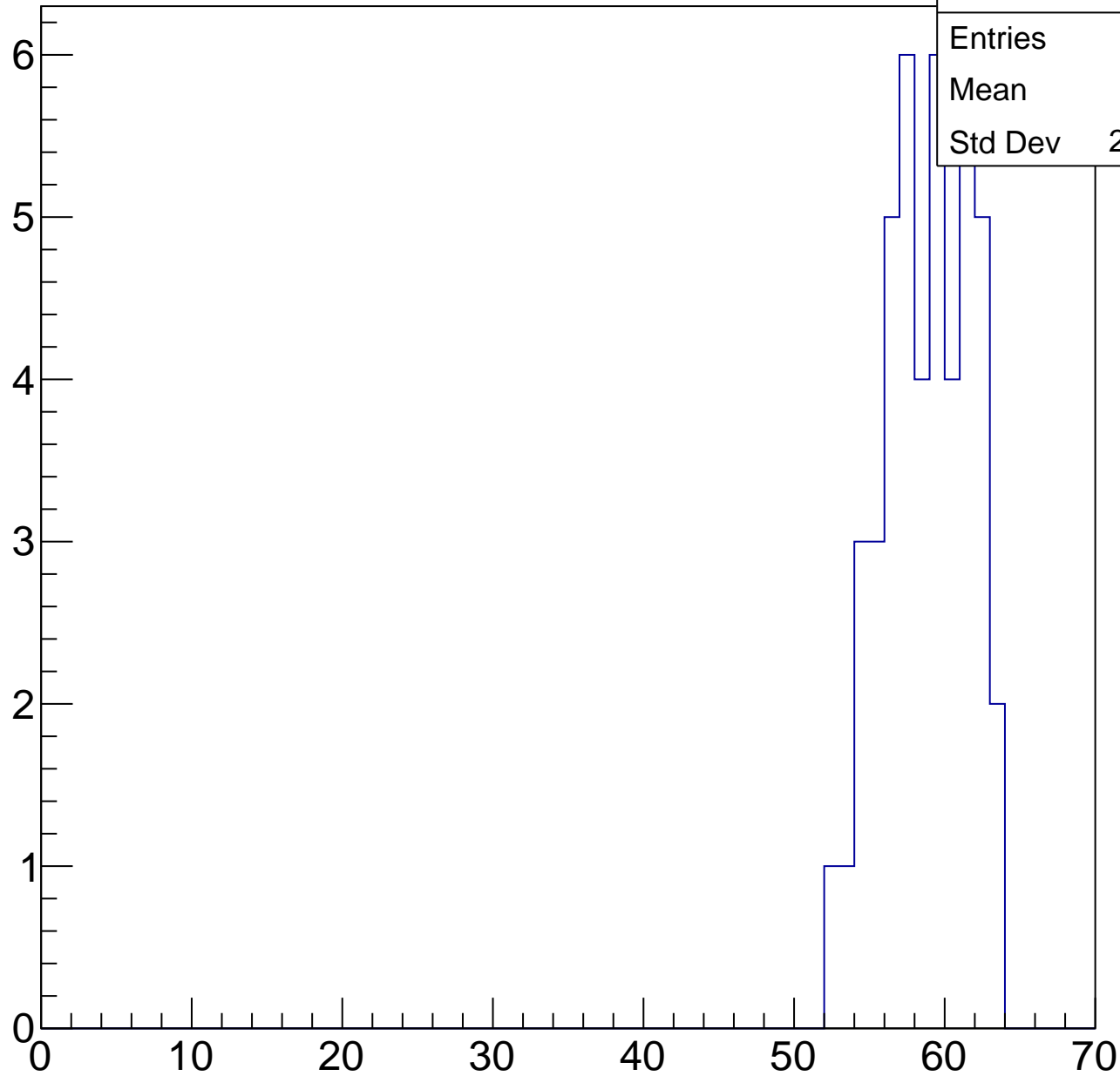
calib_packv5_042523_0143.root, FC#9, port A1

Entry

6
5
4
3
2
1
0

Entries	46
Mean	58.3
Std Dev	2.812

ampl

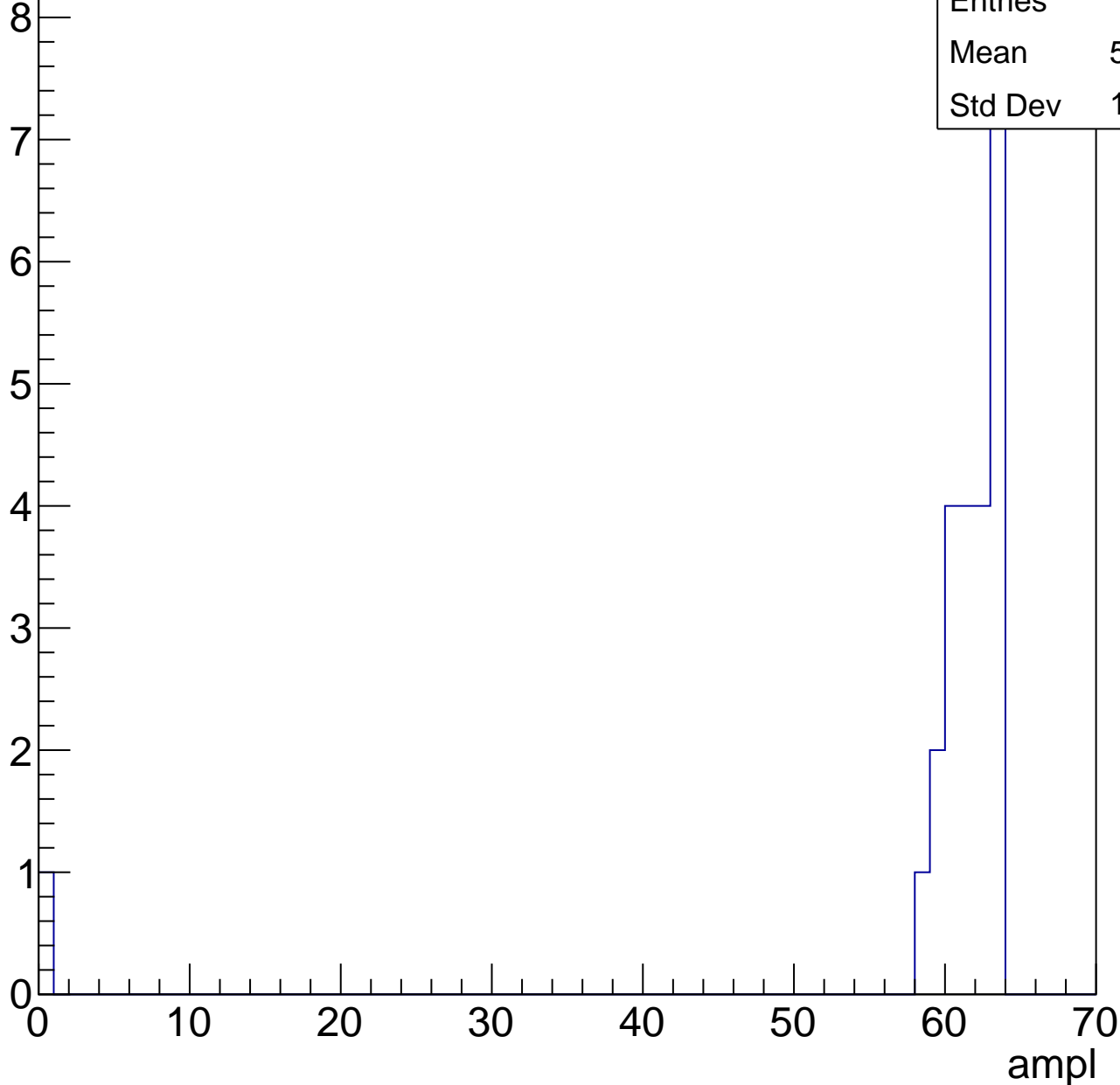


B0L001S, U11-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.83
Std Dev	12.36



B0L001S, U11-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch114, adc0

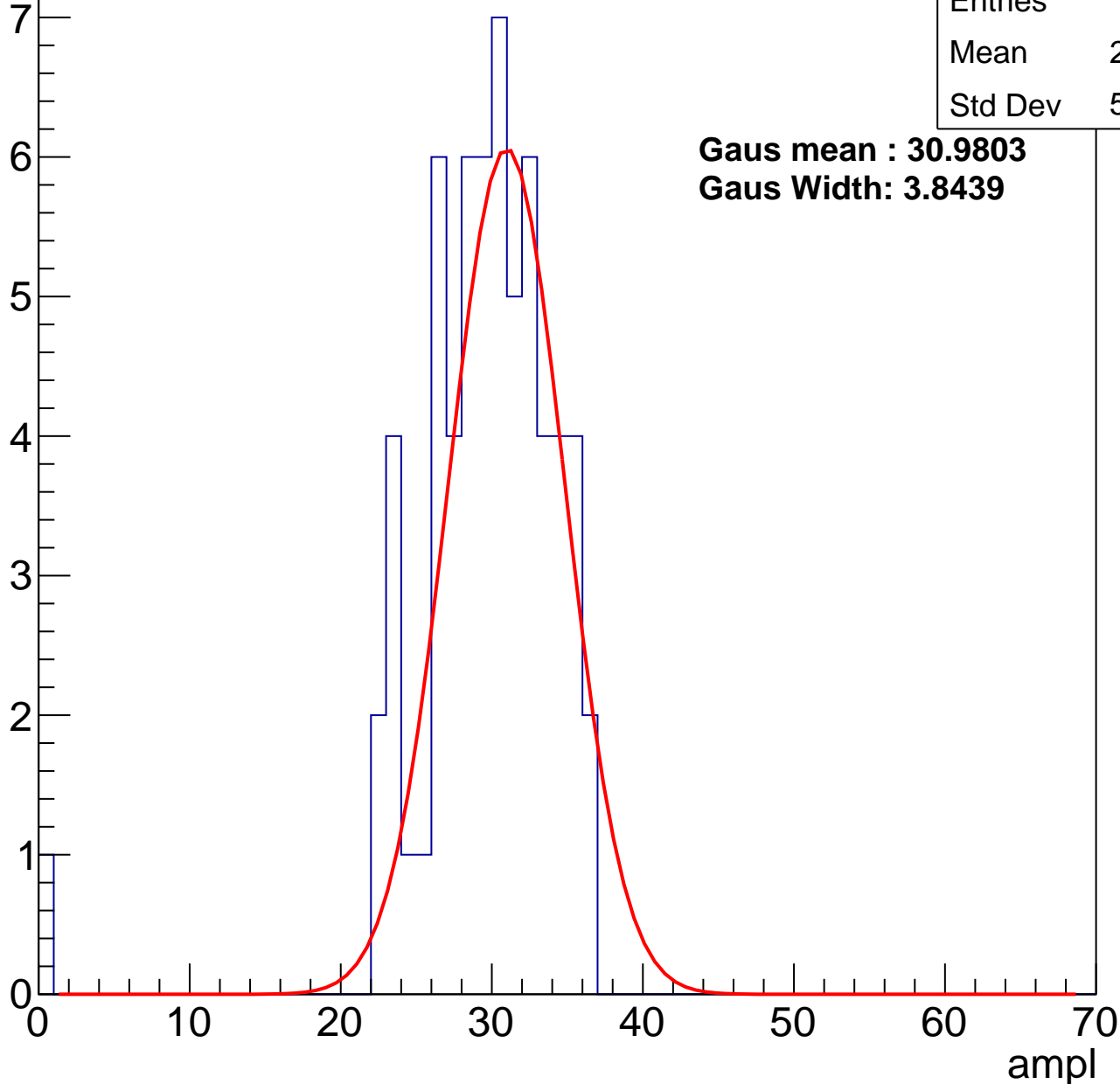
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.02
Std Dev	5.175

Gaus mean : 30.9803

Gaus Width: 3.8439



B0L001S, U11-ch114, adc1

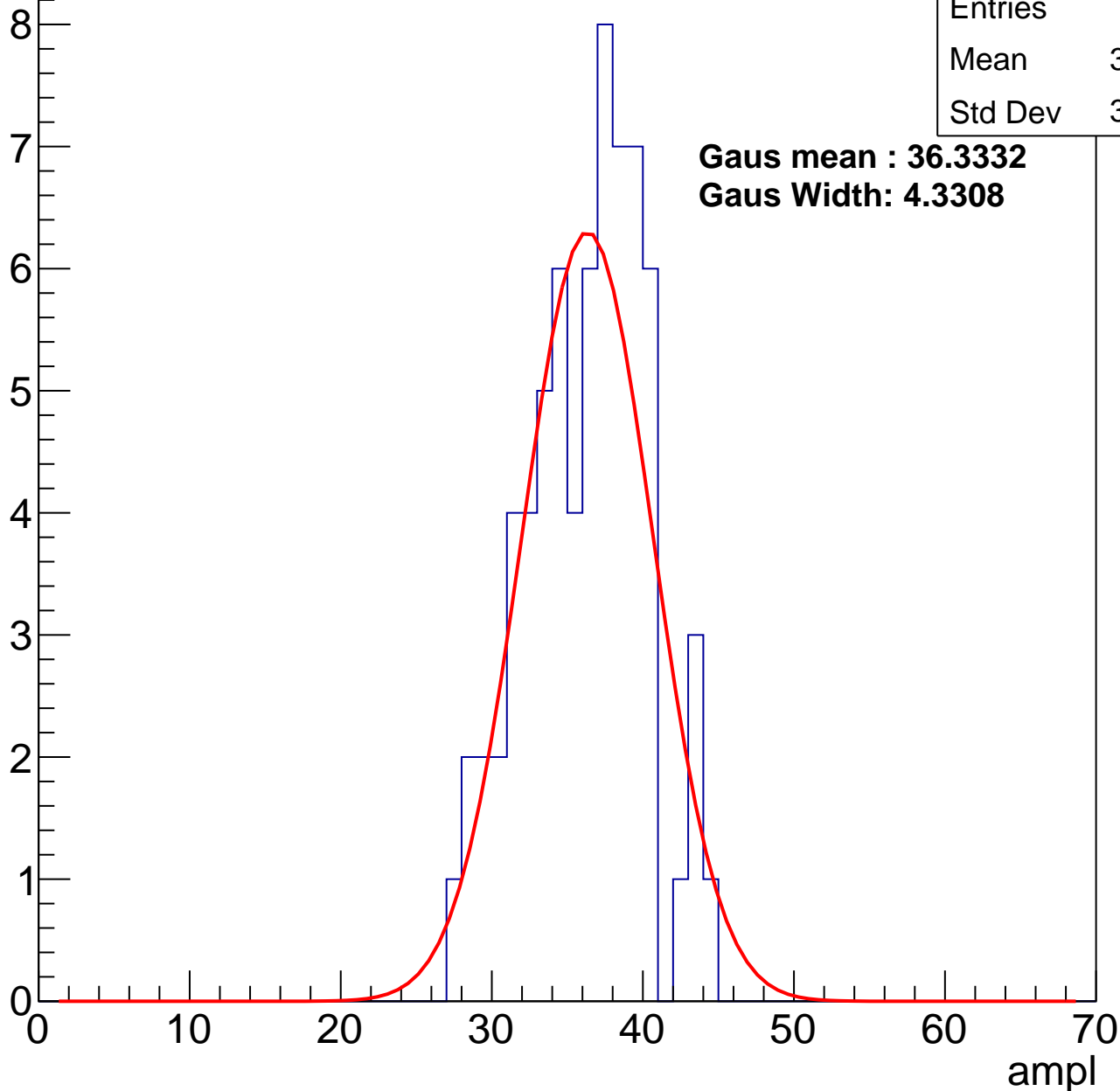
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	35.77
Std Dev	3.909

Gaus mean : 36.3332

Gaus Width: 4.3308



B0L001S, U11-ch114, adc2

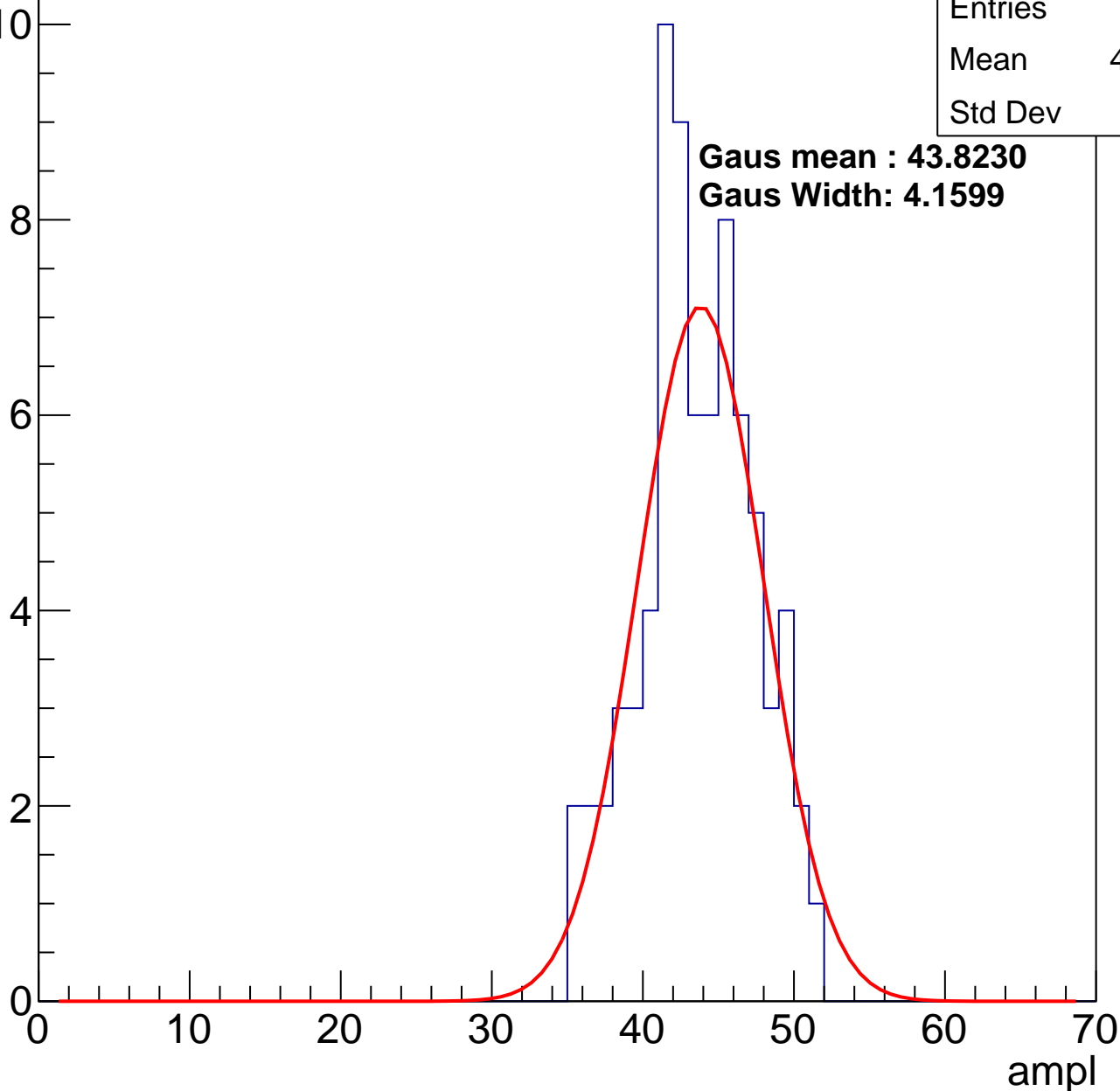
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	43.14
Std Dev	3.73

Gaus mean : 43.8230

Gaus Width: 4.1599

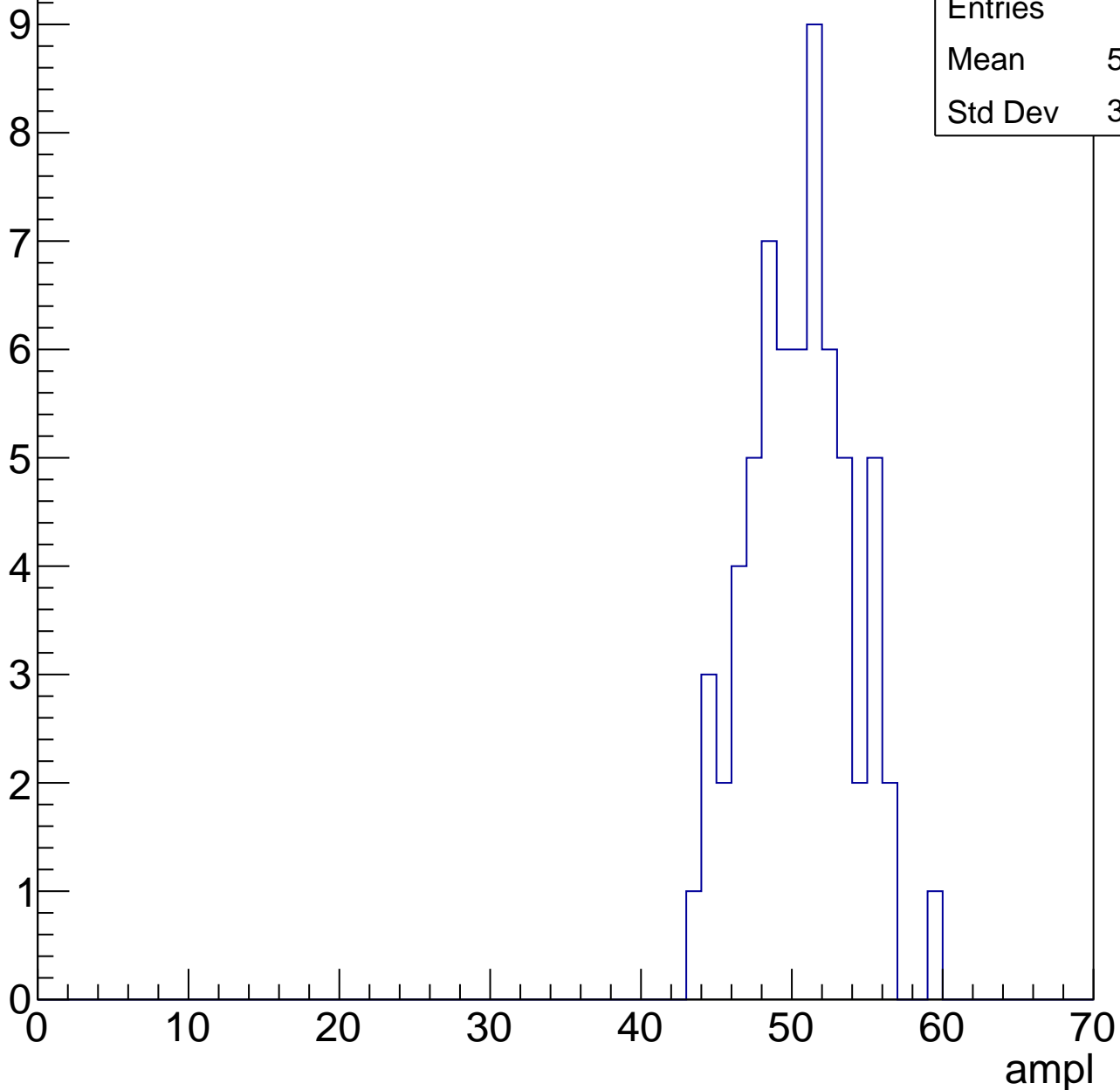


B0L001S, U11-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

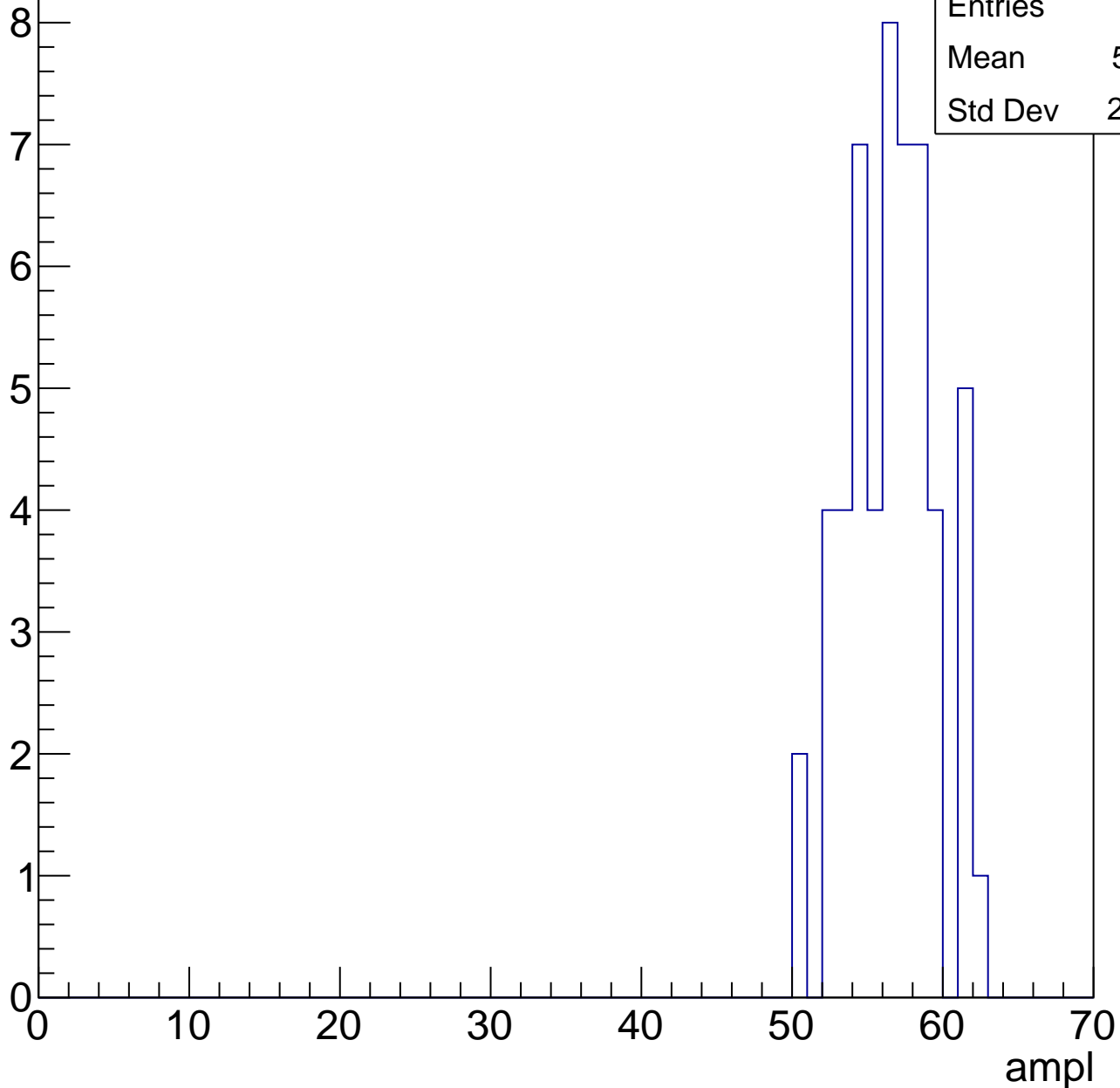
Entries	64
Mean	50.06
Std Dev	3.395



B0L001S, U11-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



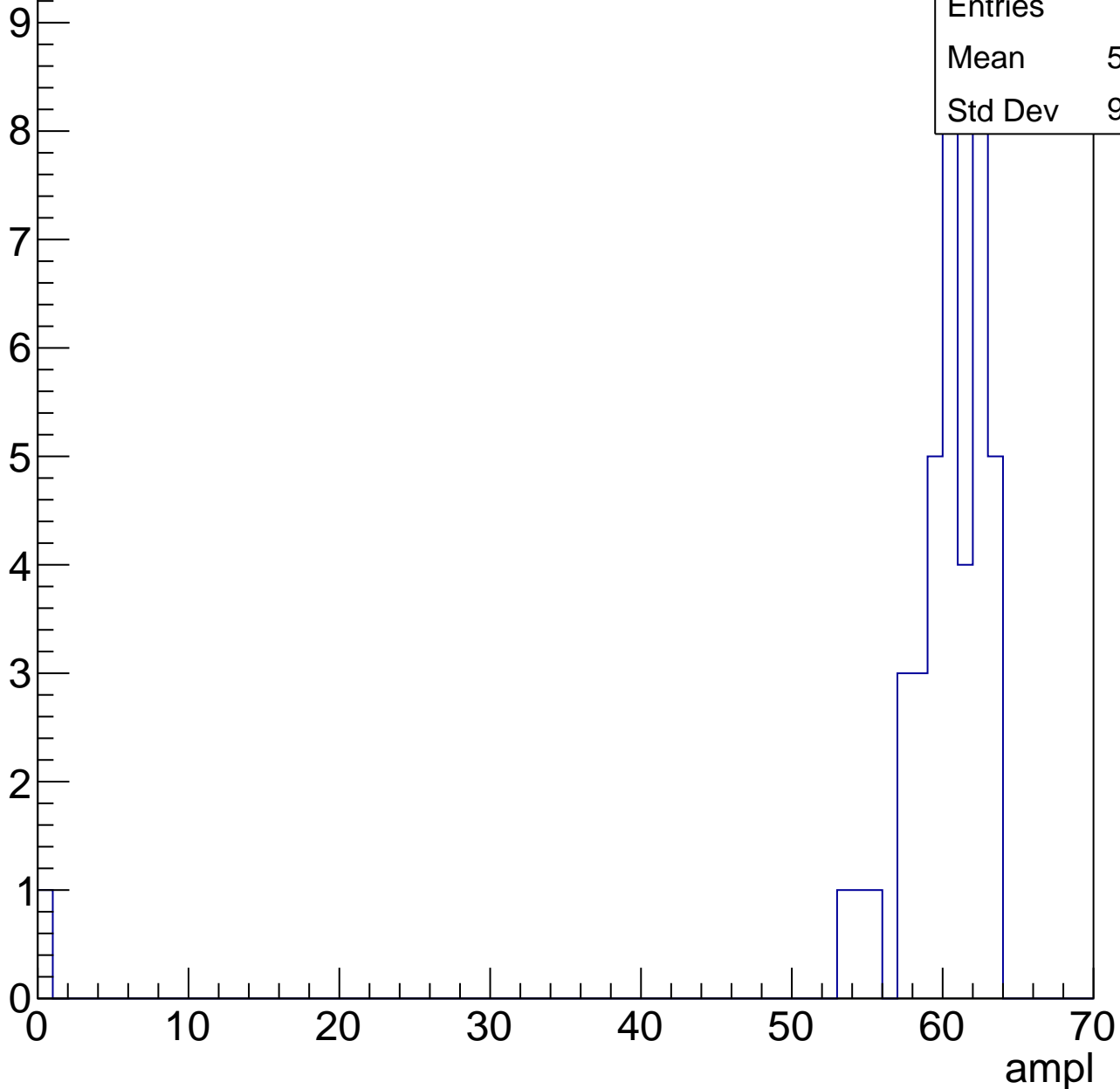
Entries	53
Mean	56.11
Std Dev	2.866

B0L001S, U11-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.46
Std Dev	9.549

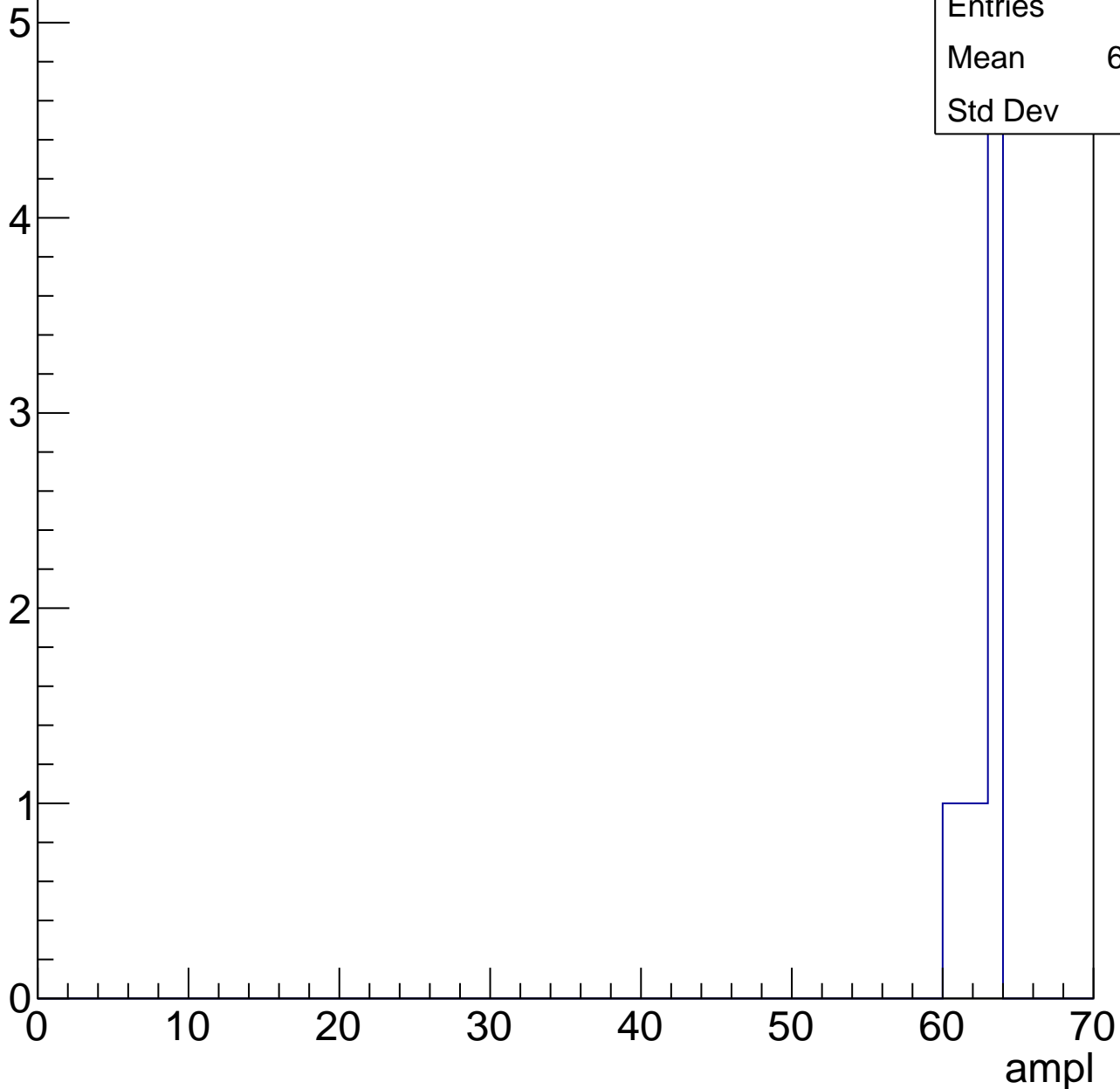


B0L001S, U11-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.25
Std Dev	1.09



B0L001S, U11-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch115, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	30.69
Std Dev	3.254

Gaus mean : 30.7353

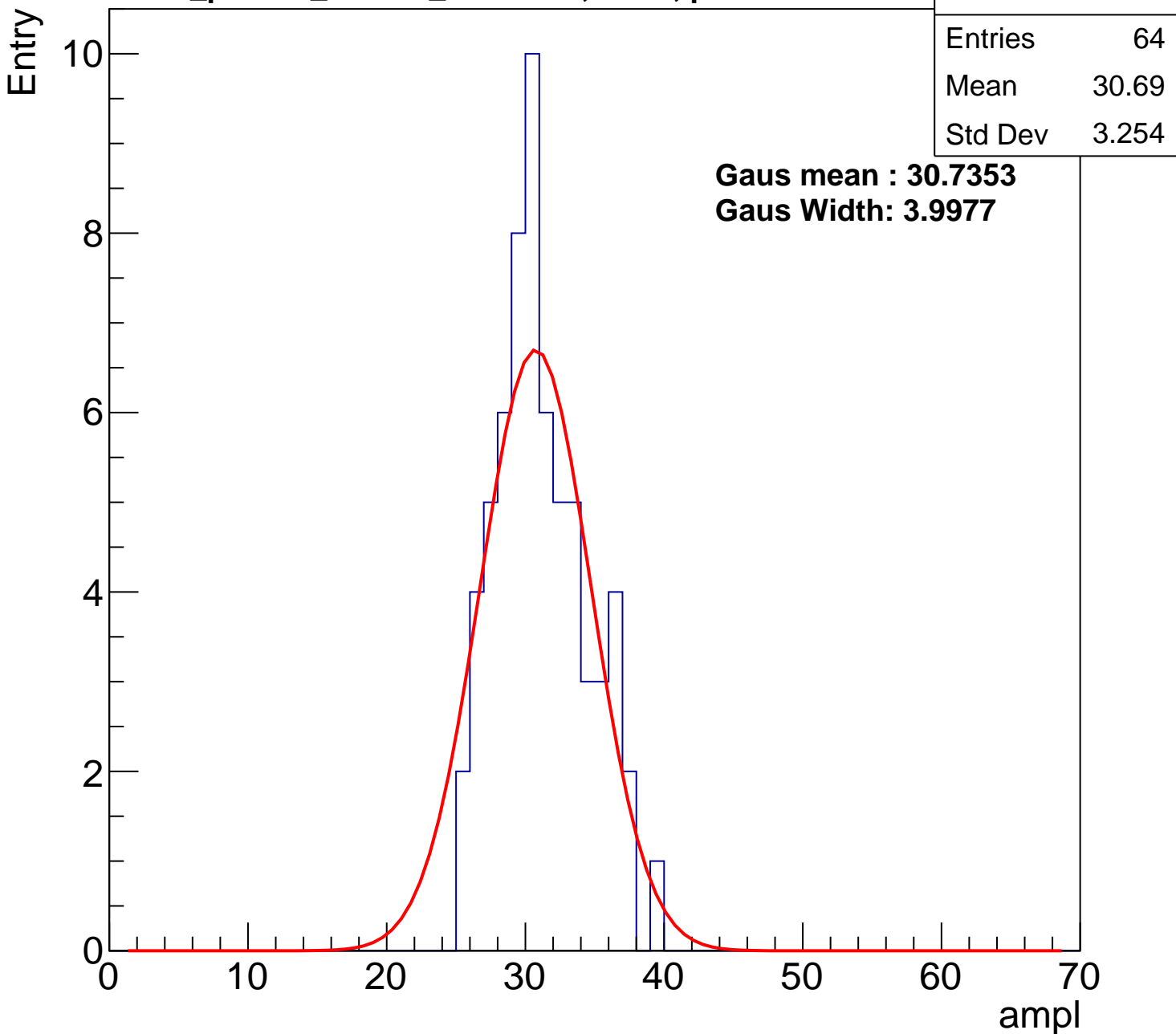
Gaus Width: 3.9977

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch115, adc1

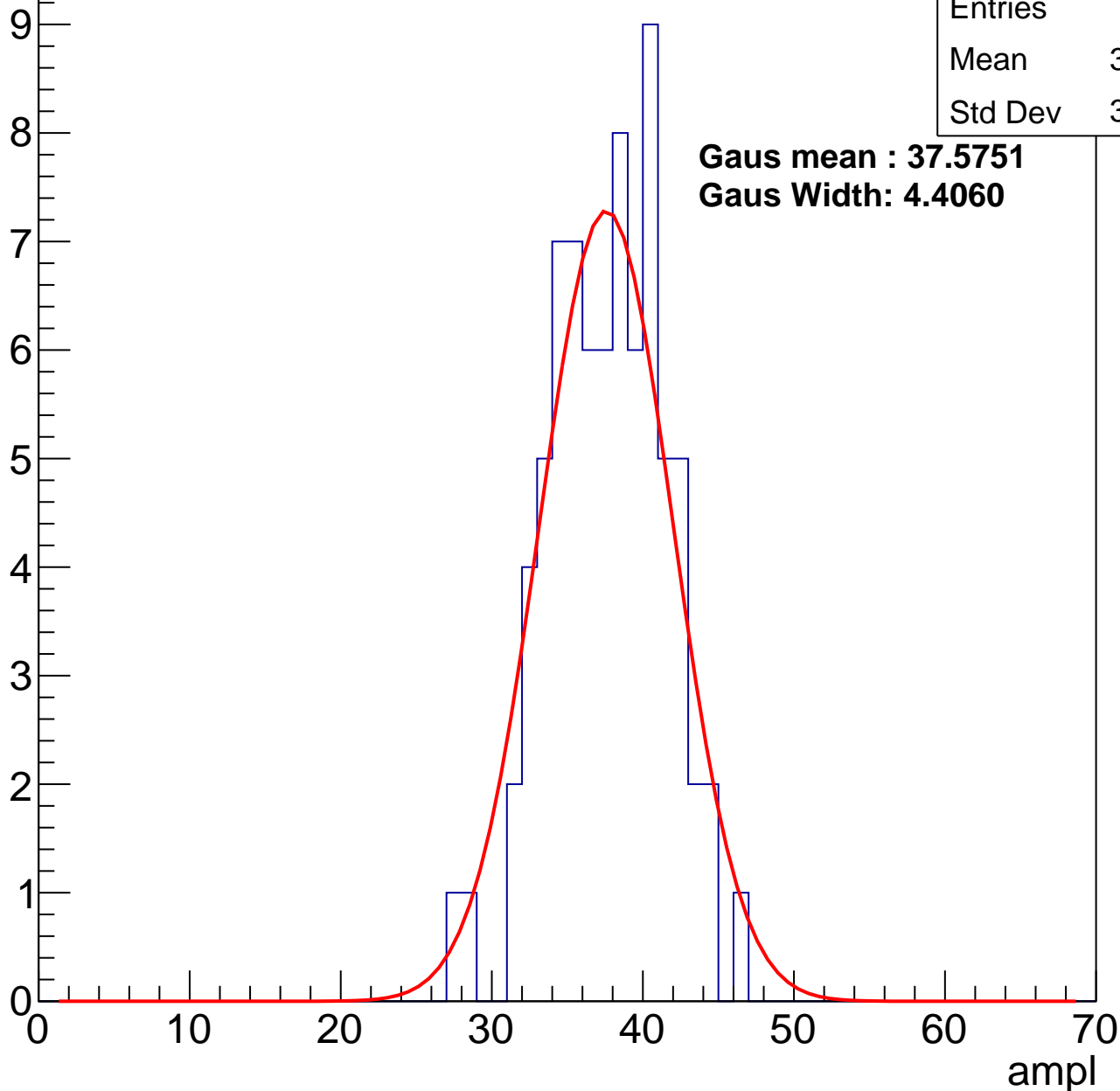
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.19
Std Dev	3.773

Gaus mean : 37.5751

Gaus Width: 4.4060



B0L001S, U11-ch115, adc2

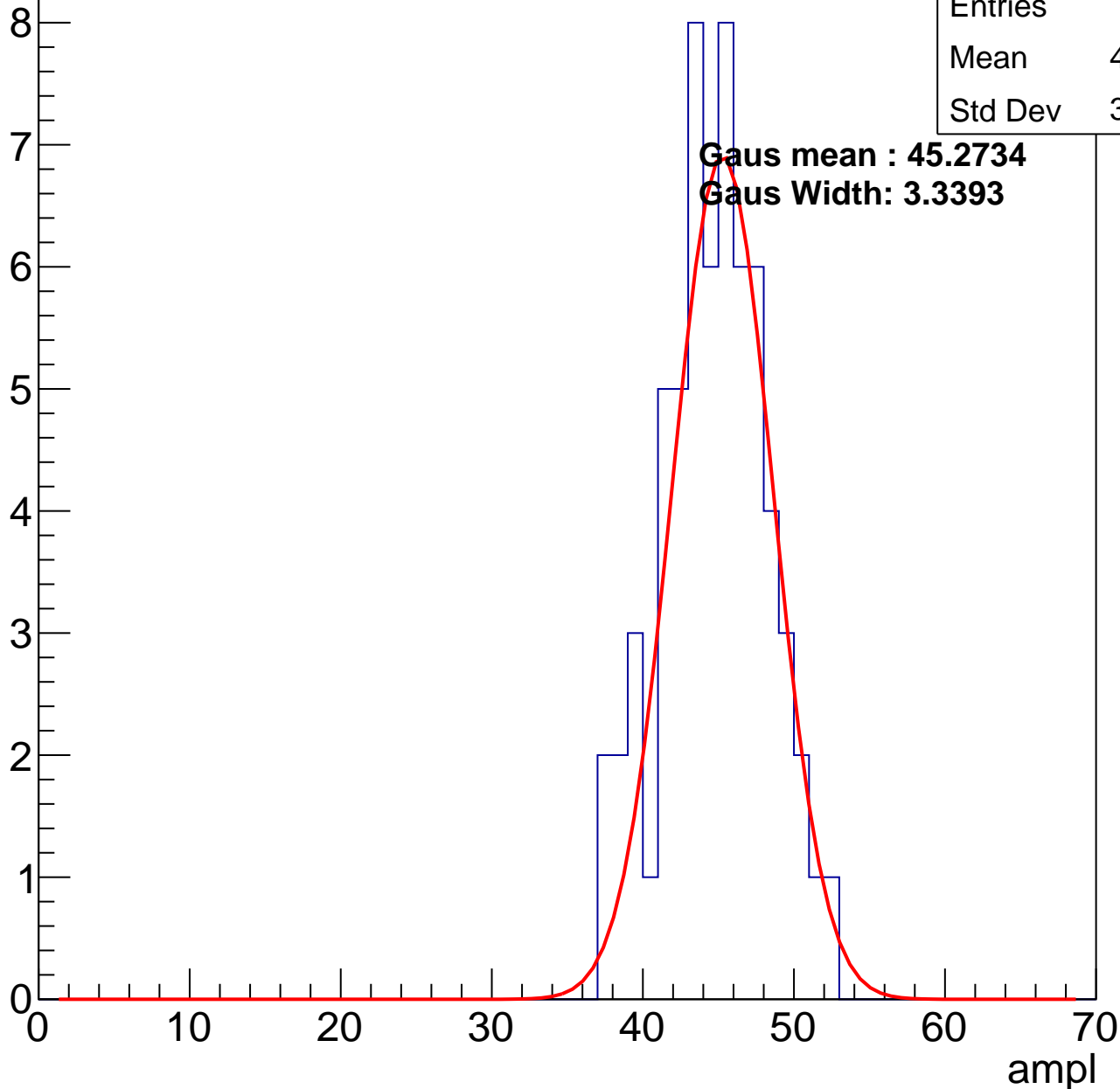
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.29
Std Dev	3.425

Gaus mean : 45.2734

Gaus Width: 3.3393

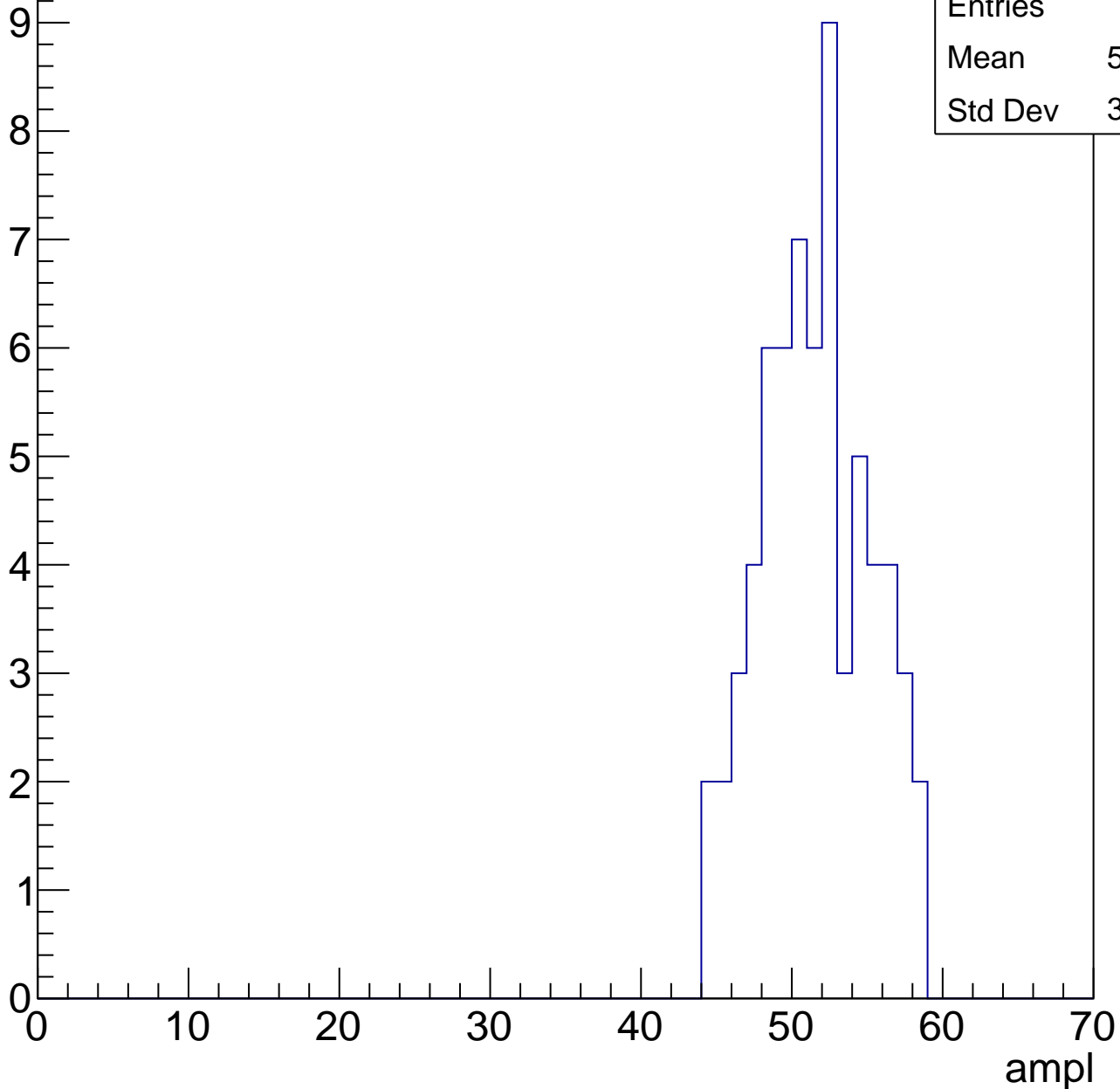


B0L001S, U11-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.06
Std Dev	3.546

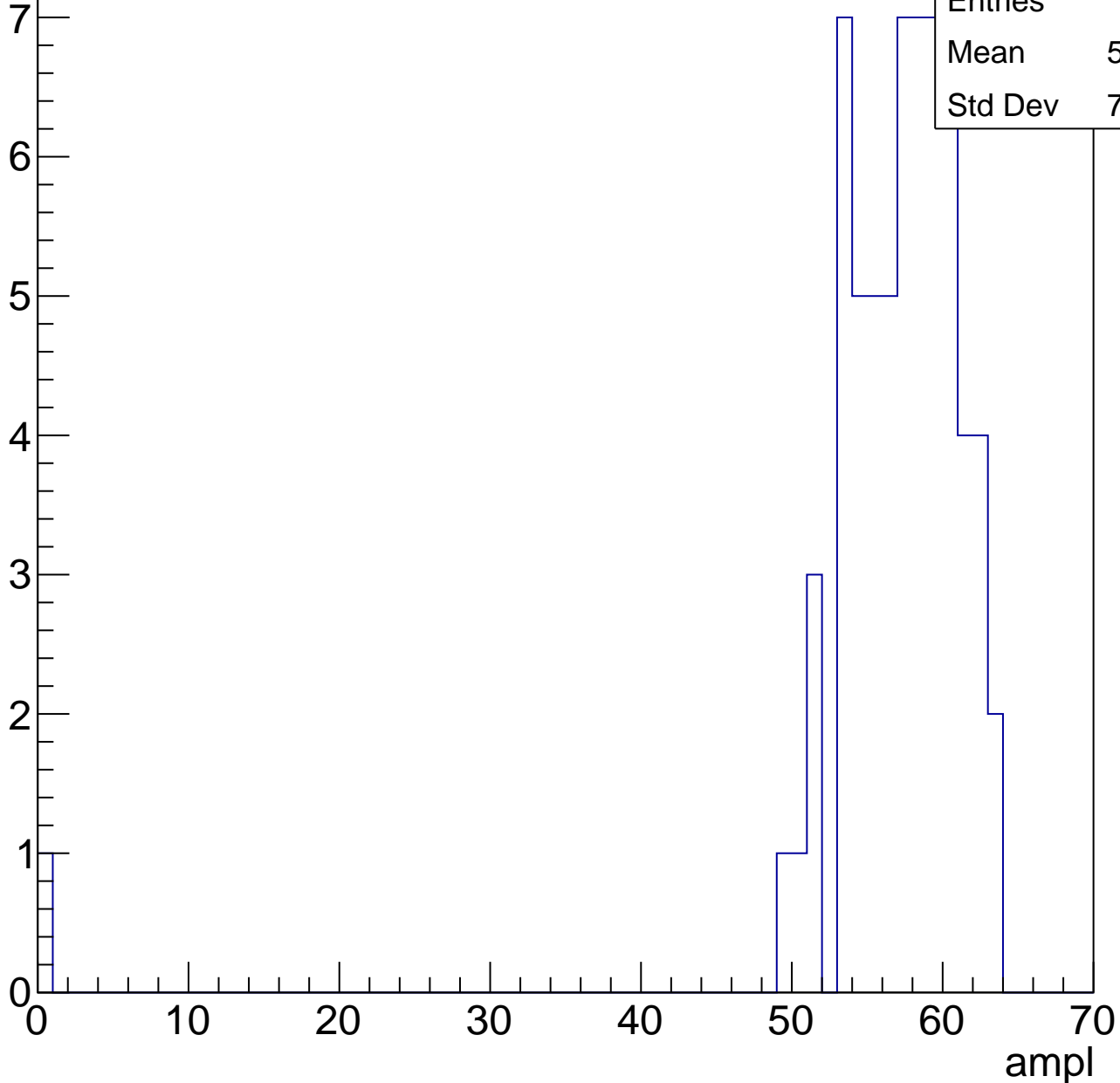


B0L001S, U11-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.12
Std Dev	7.722

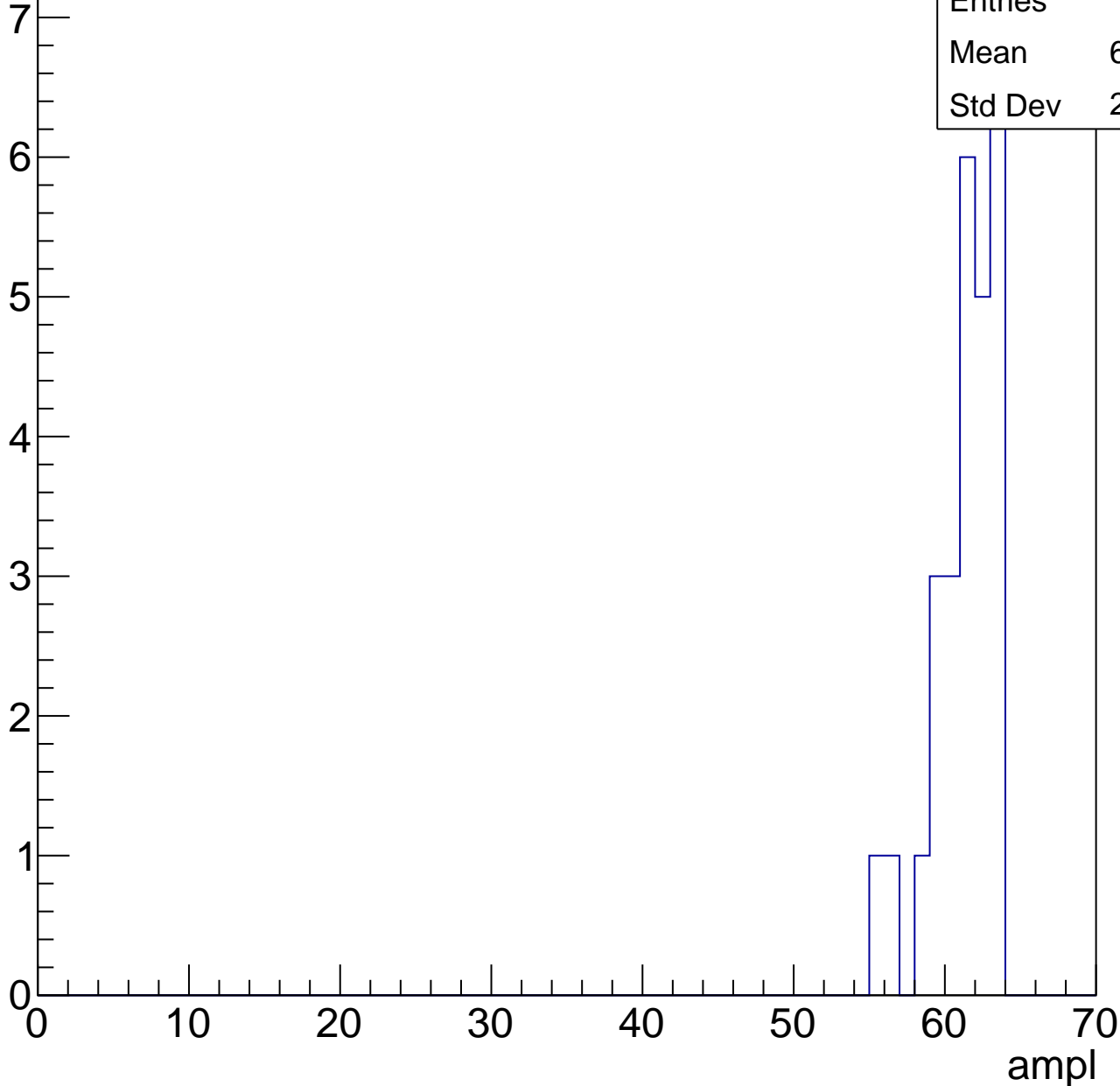


B0L001S, U11-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	60.85
Std Dev	2.085



B0L001S, U11-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

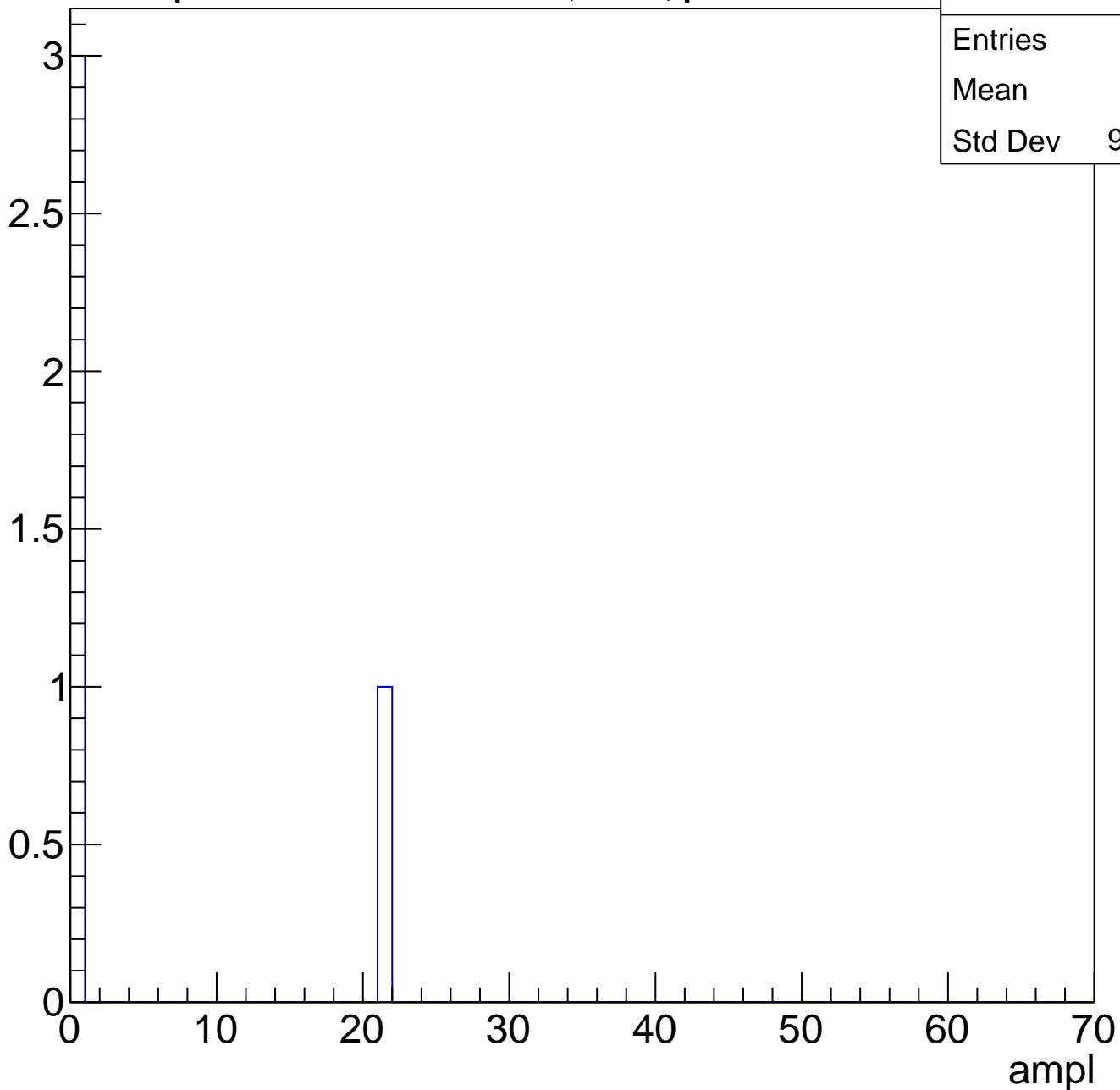
Entry



B0L001S, U11-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B0L001S, U11-ch116, adc0

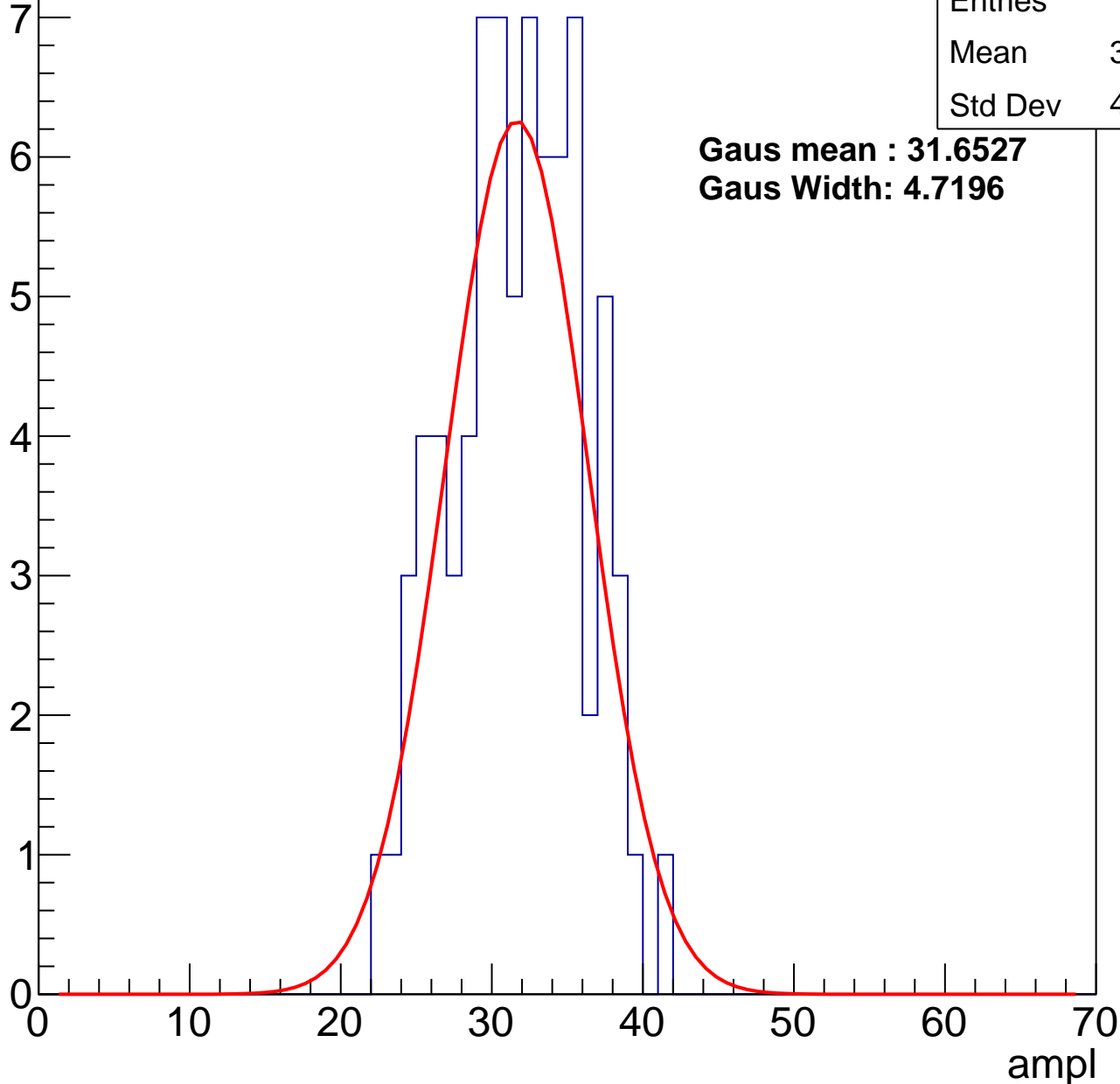
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	31.22
Std Dev	4.248

Gaus mean : 31.6527

Gaus Width: 4.7196



B0L001S, U11-ch116, adc1

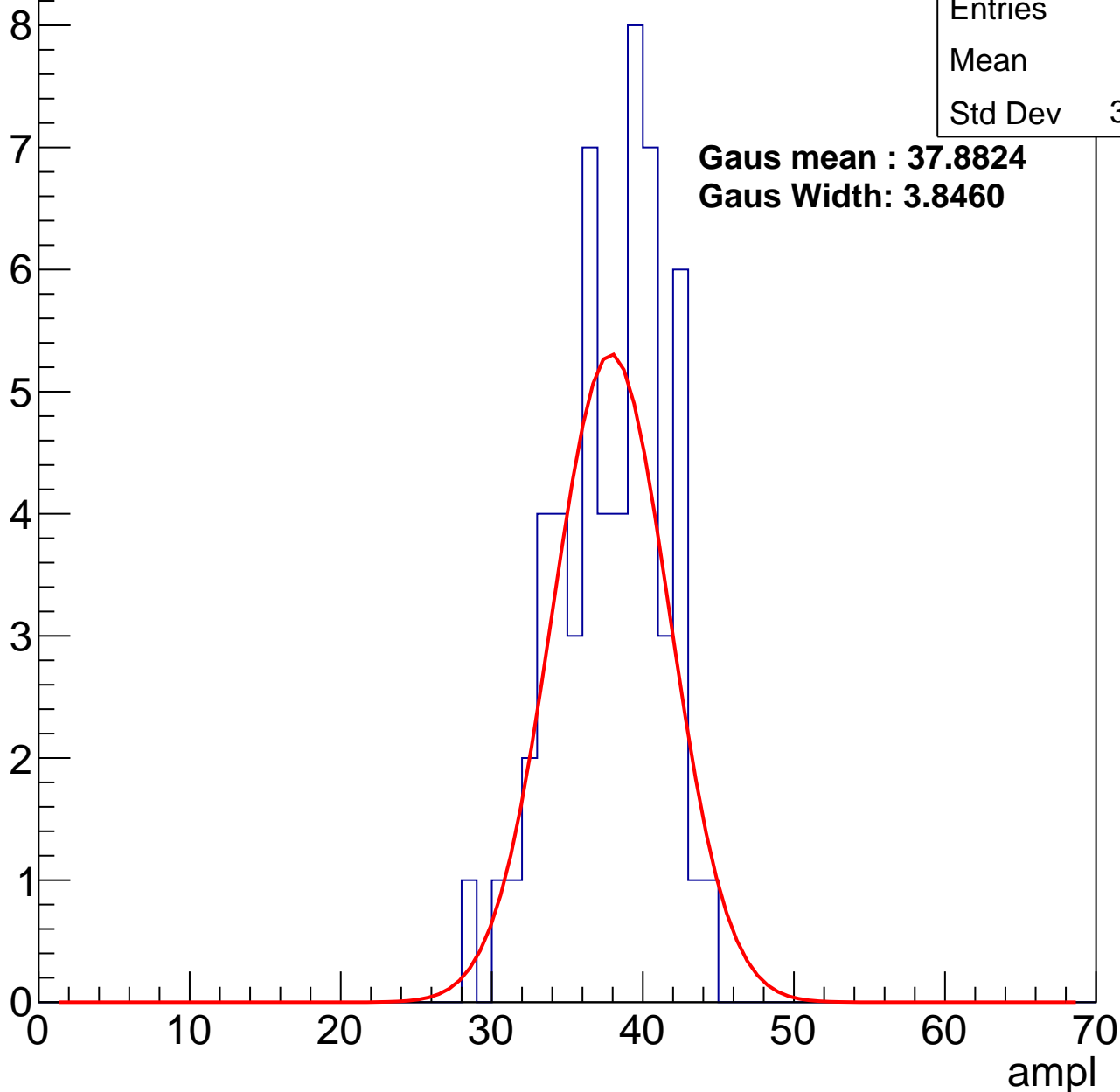
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.4
Std Dev	3.524

Gaus mean : 37.8824

Gaus Width: 3.8460



B0L001S, U11-ch116, adc2

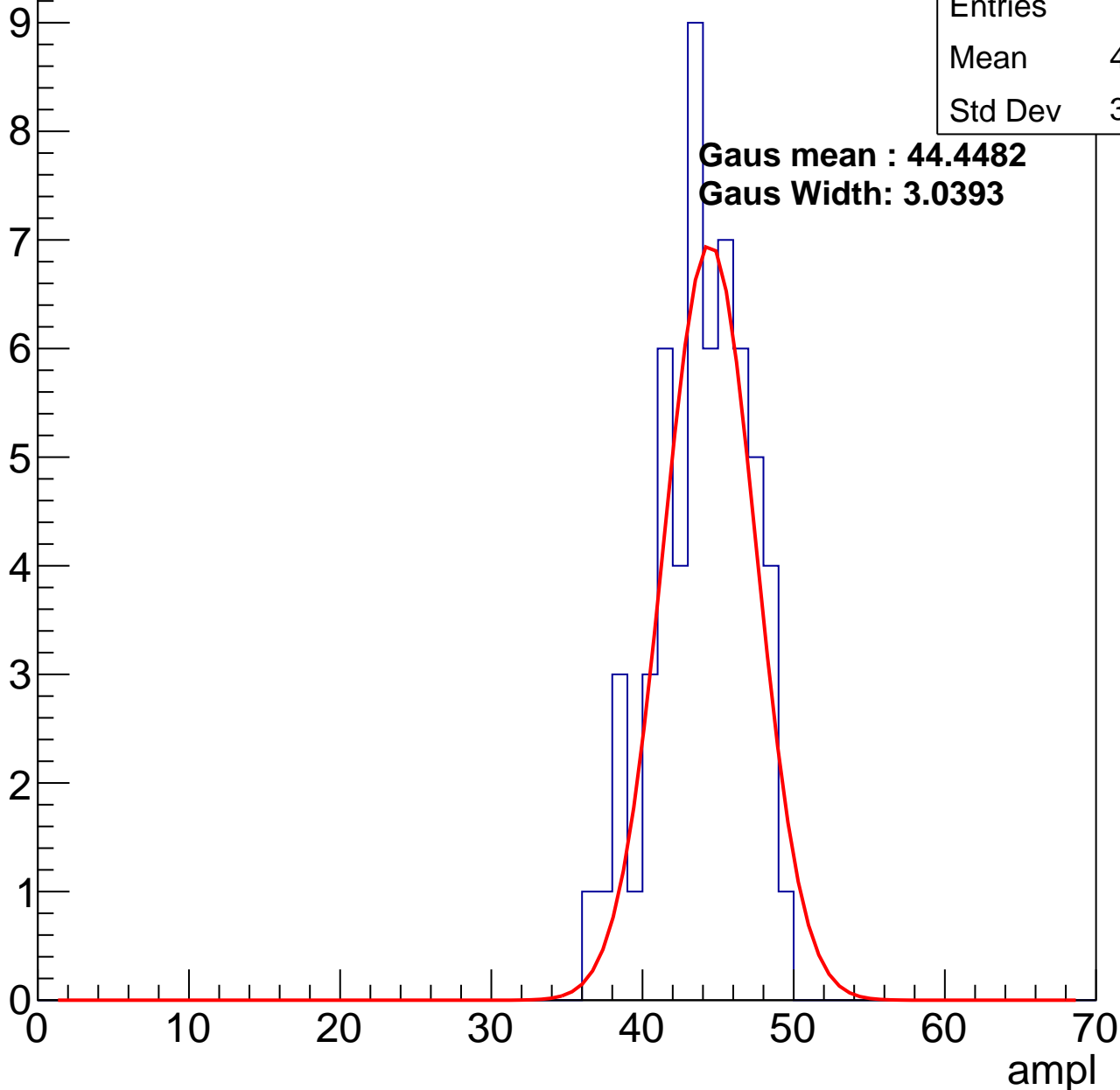
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.47
Std Dev	3.027

Gaus mean : 44.4482

Gaus Width: 3.0393

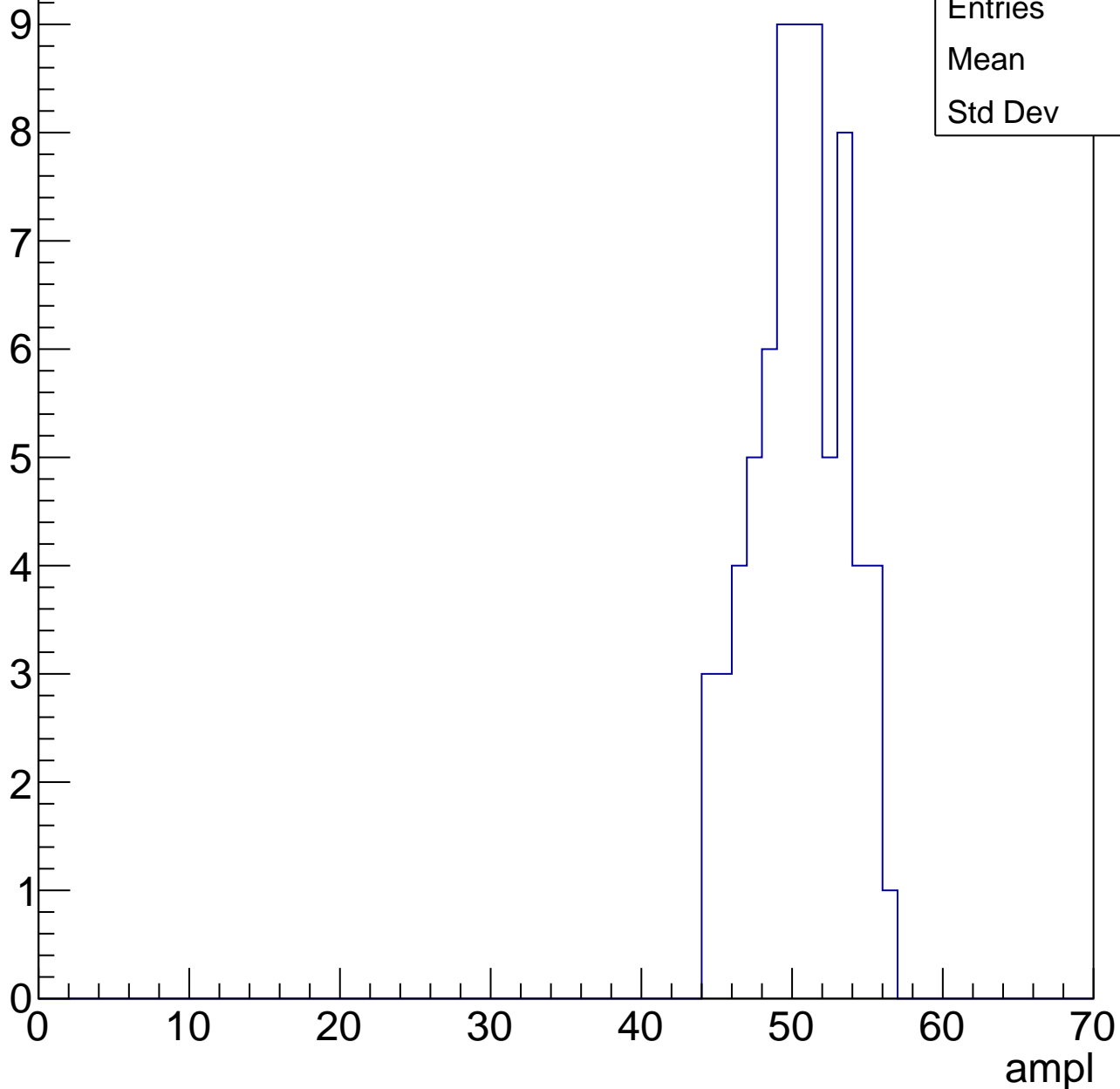


B0L001S, U11-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50
Std Dev	2.99

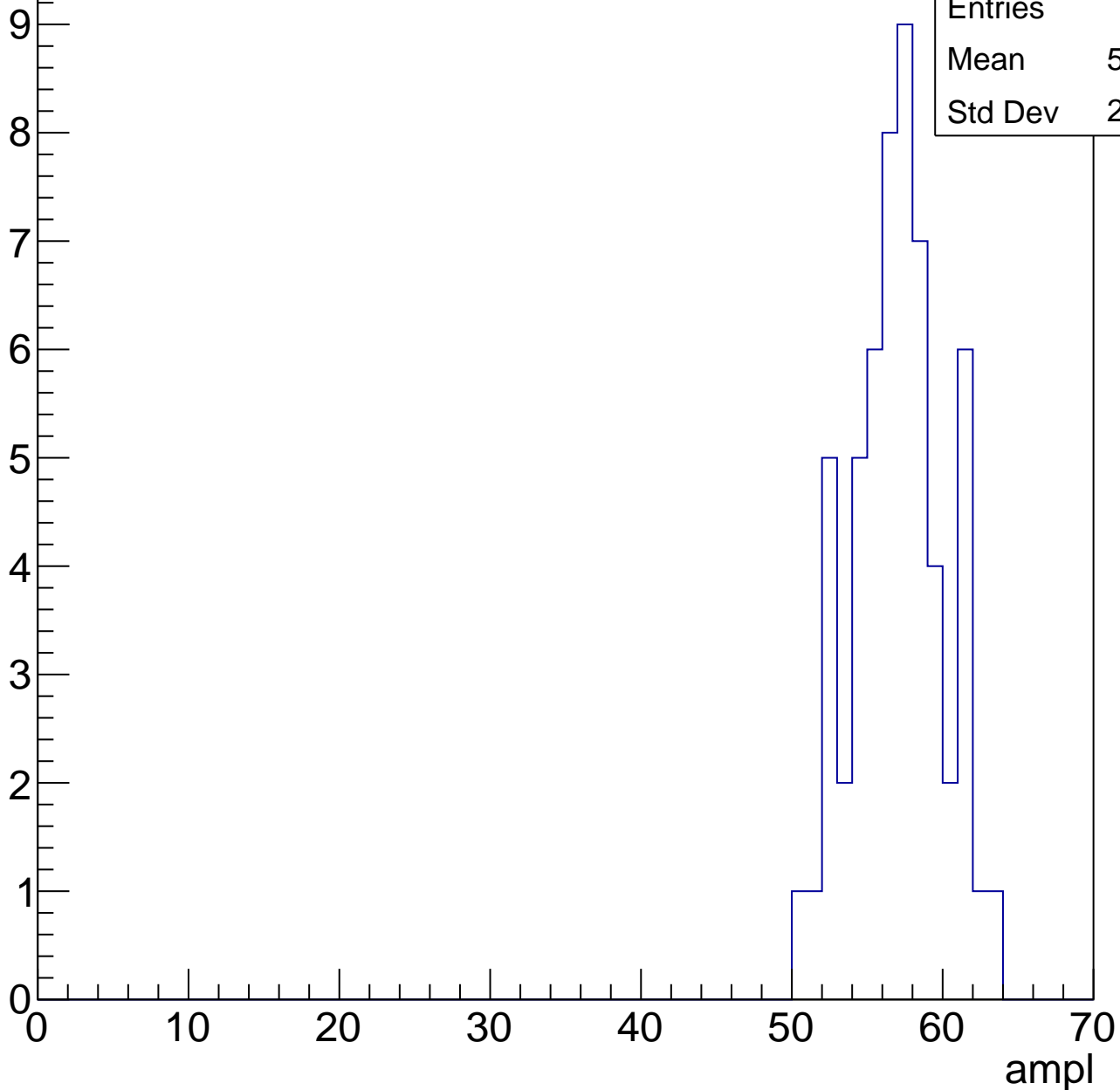


B0L001S, U11-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.57
Std Dev	2.954

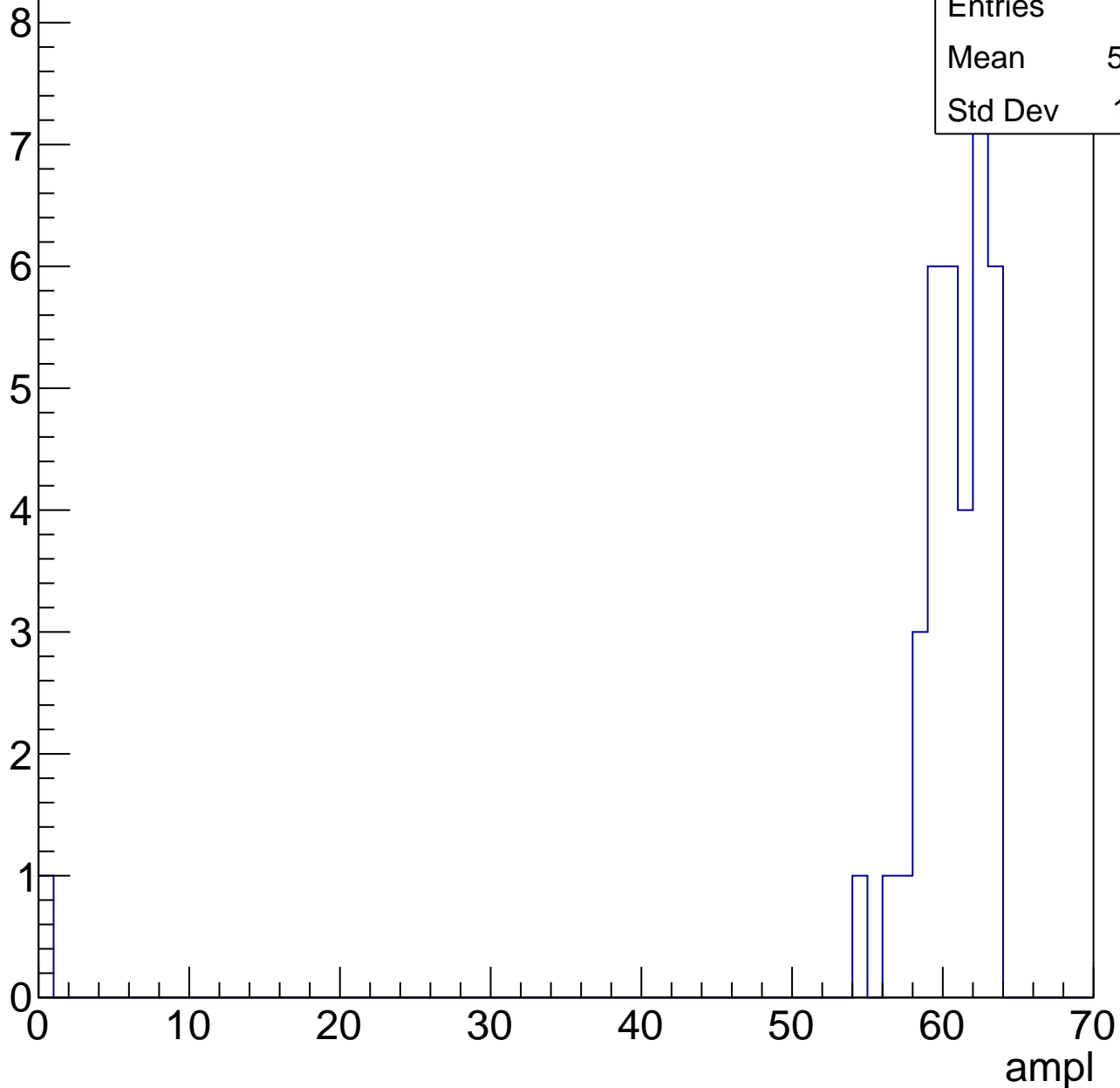


B0L001S, U11-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

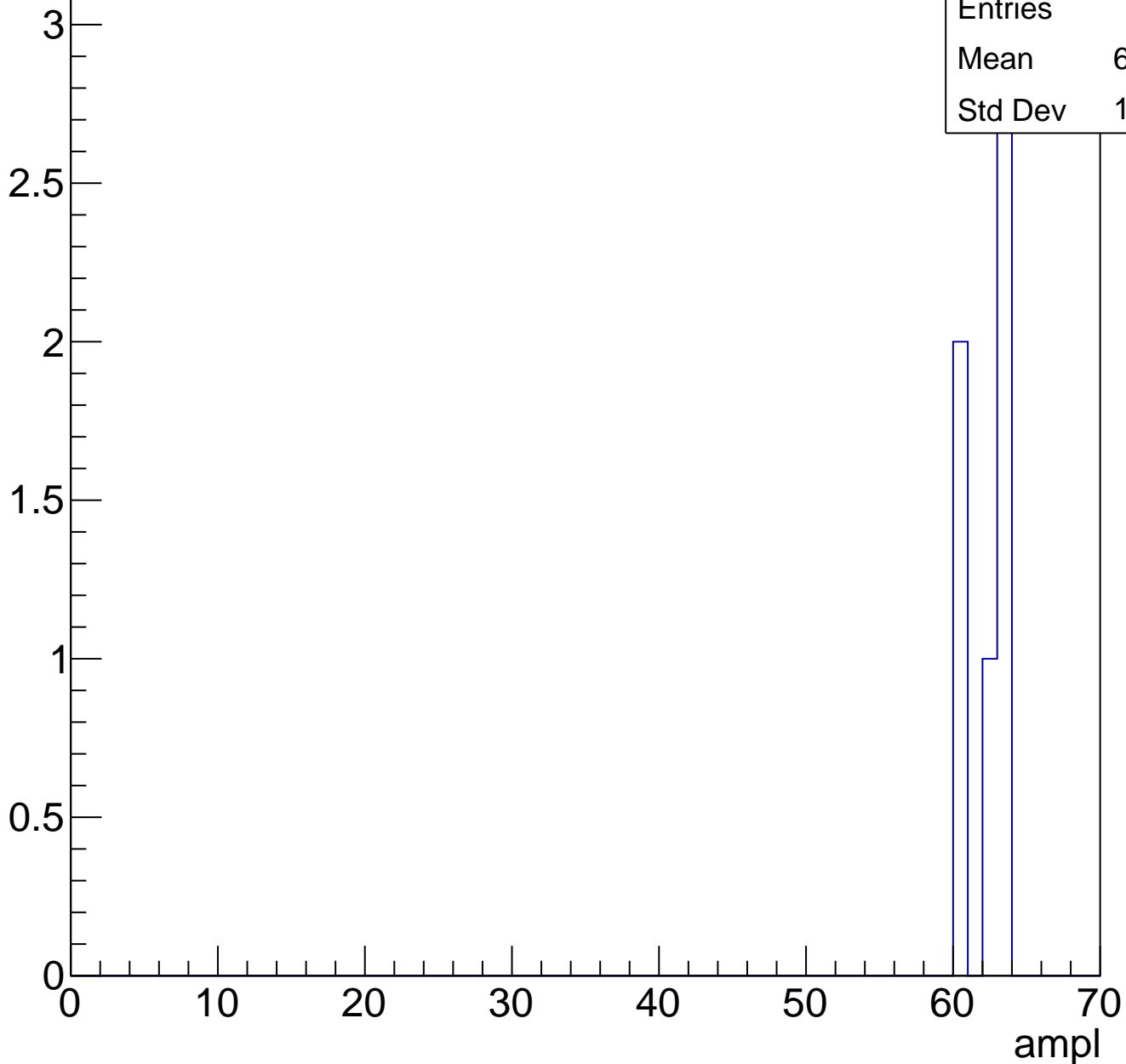
Entries	37
Mean	58.73
Std Dev	10.01



B0L001S, U11-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch117, adc0

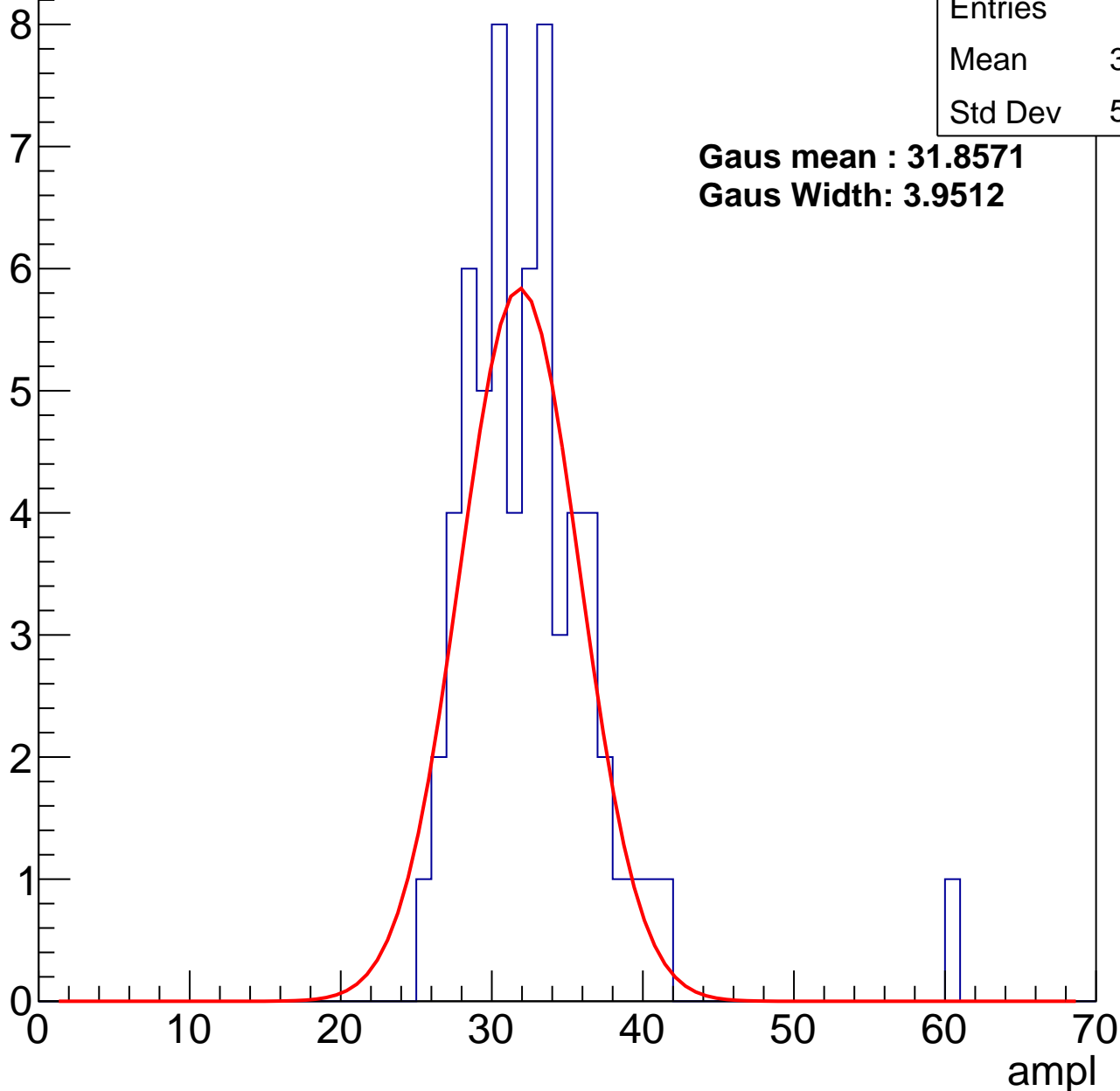
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	32.19
Std Dev	5.048

Gaus mean : 31.8571

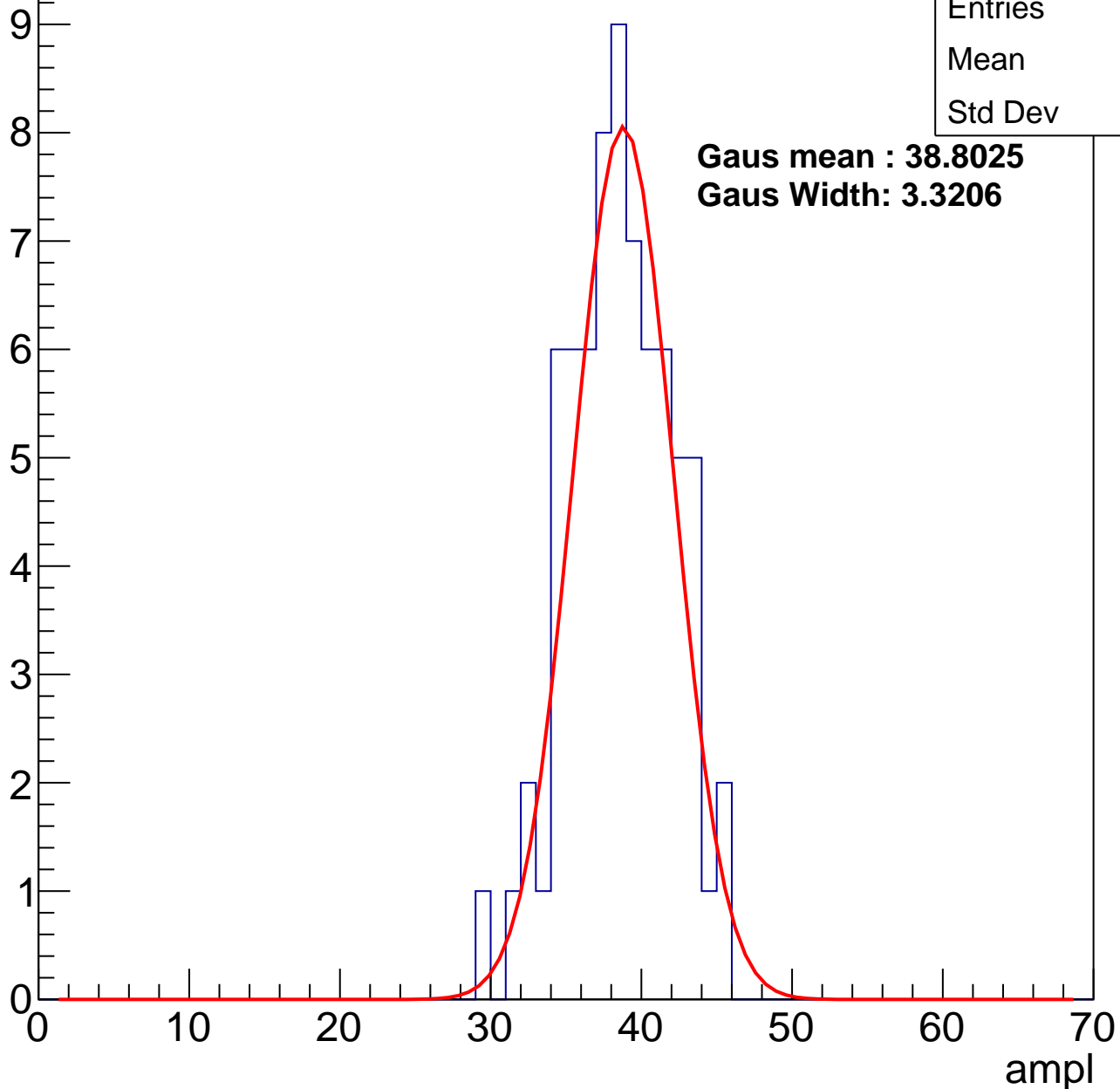
Gaus Width: 3.9512



B0L001S, U11-ch117, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch117, adc2

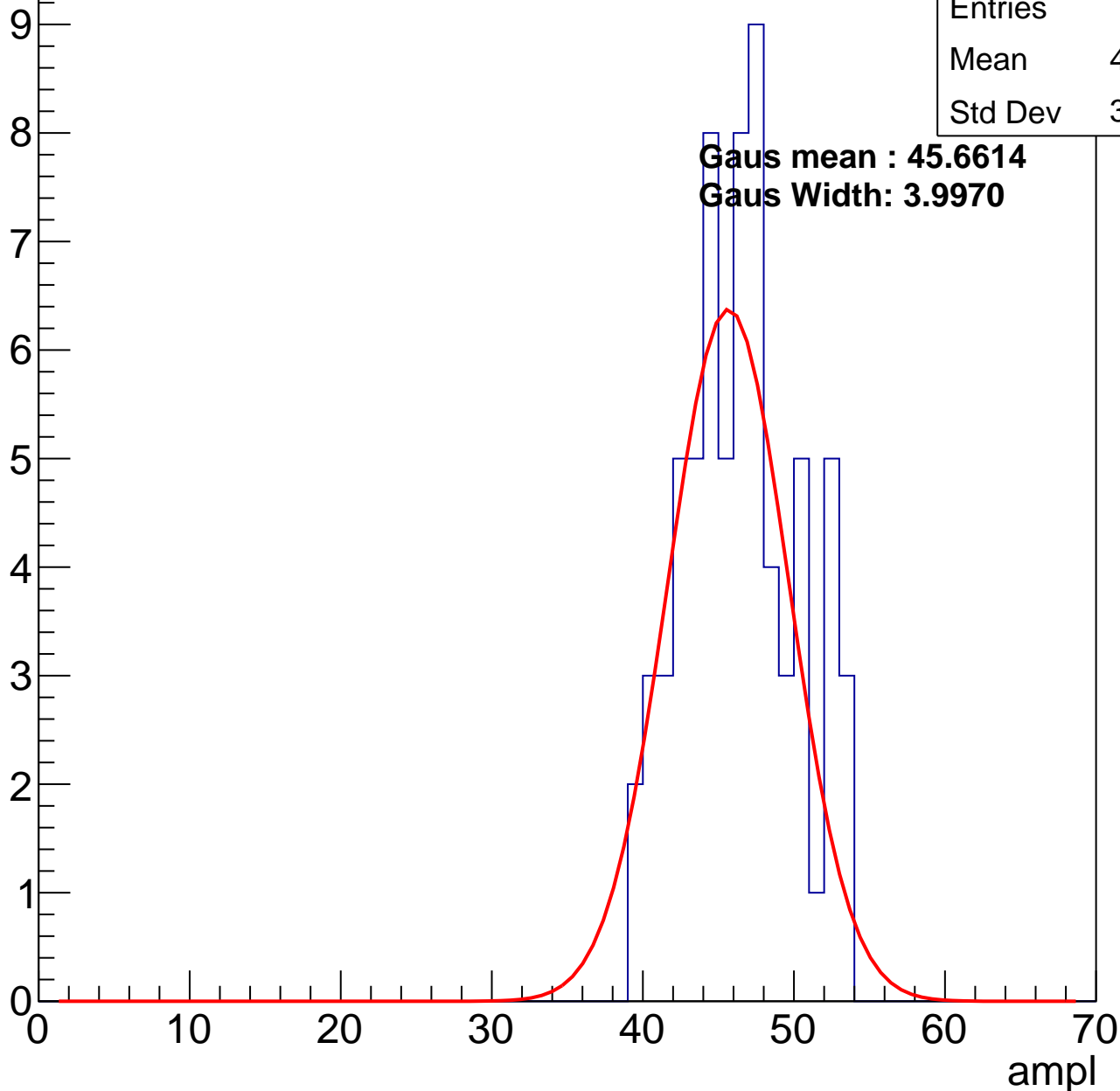
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	45.99
Std Dev	3.665

Gaus mean : 45.6614

Gaus Width: 3.9970

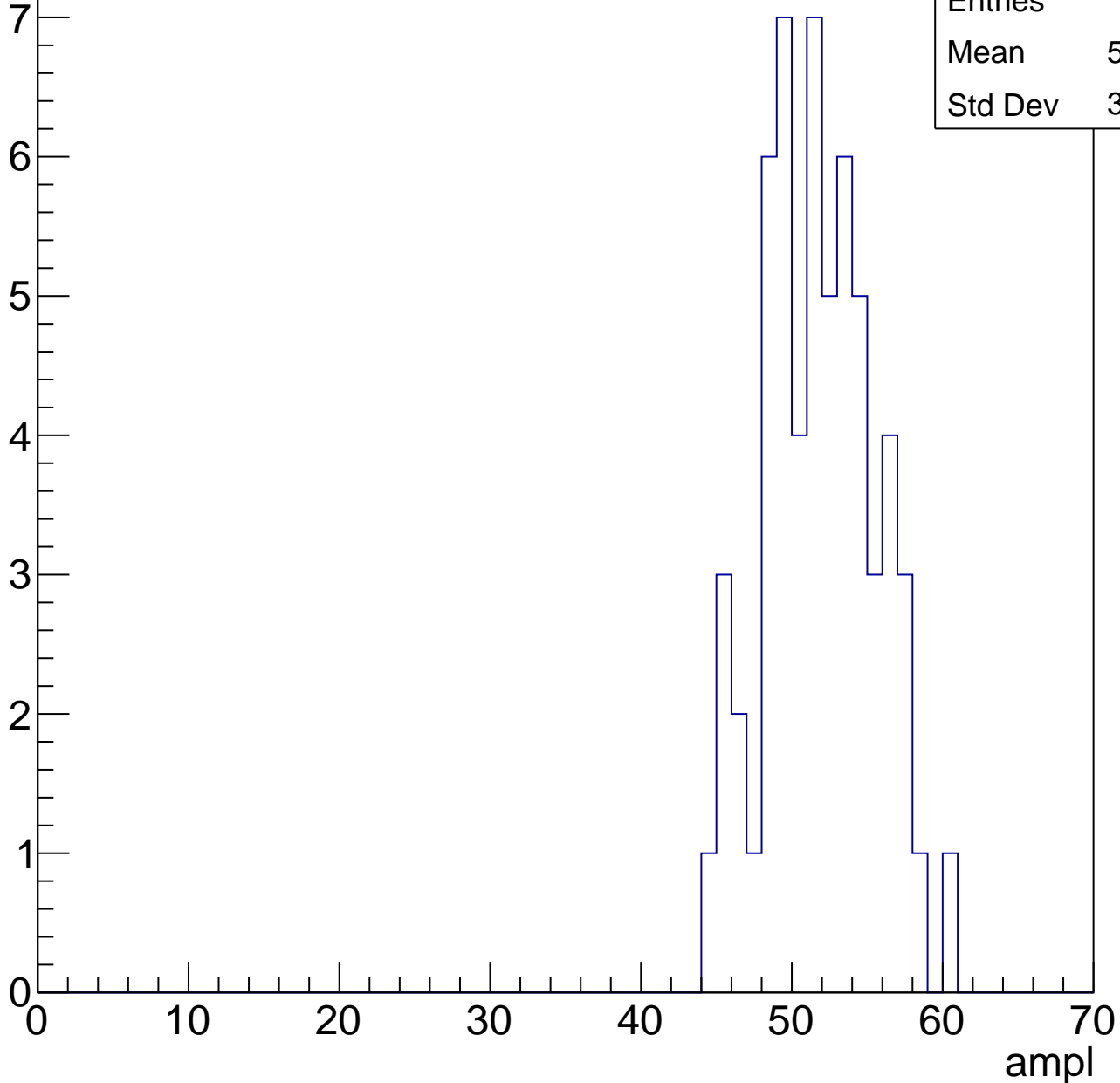


B0L001S, U11-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	51.39
Std Dev	3.589

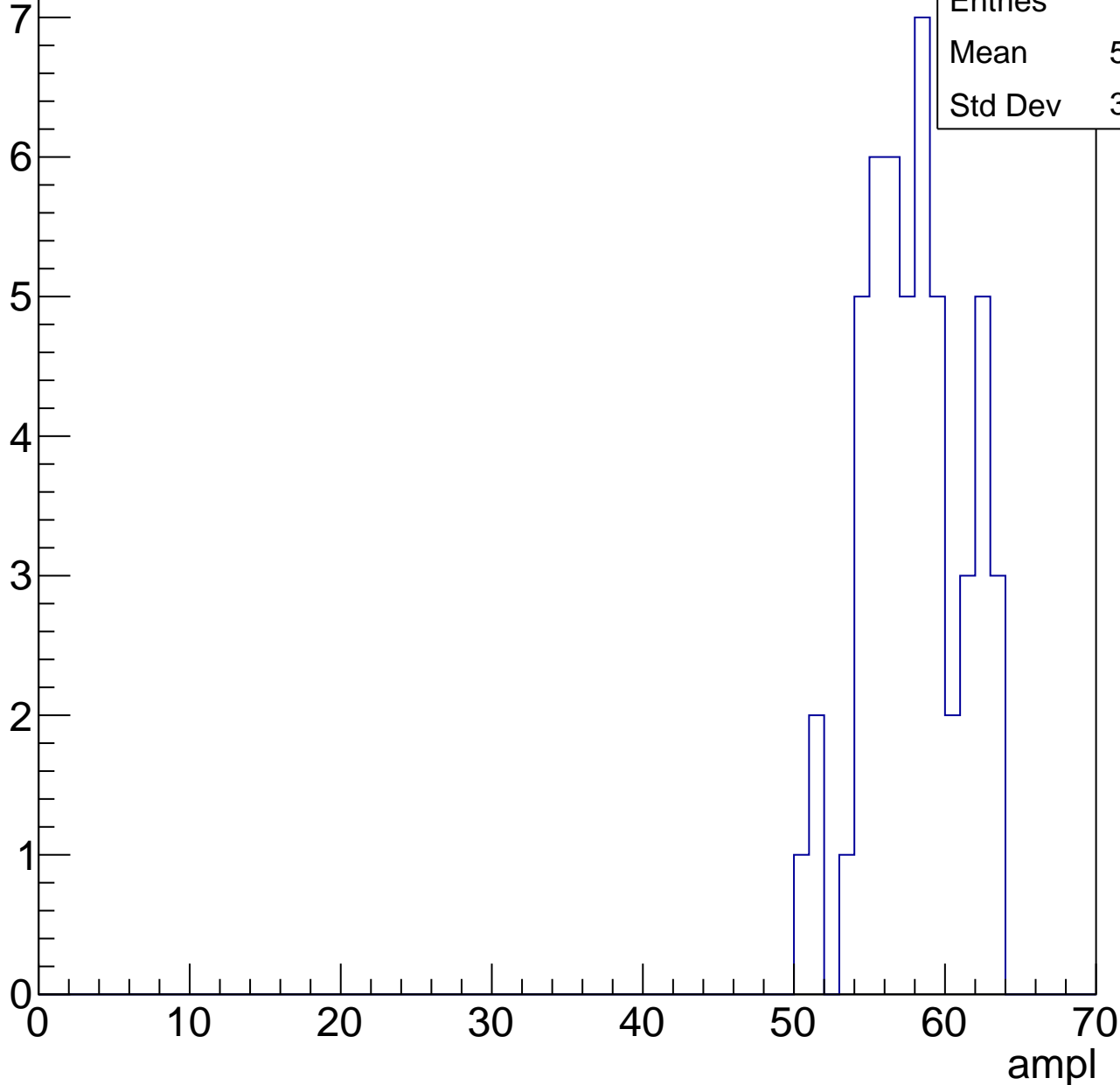


B0L001S, U11-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.43
Std Dev	3.219

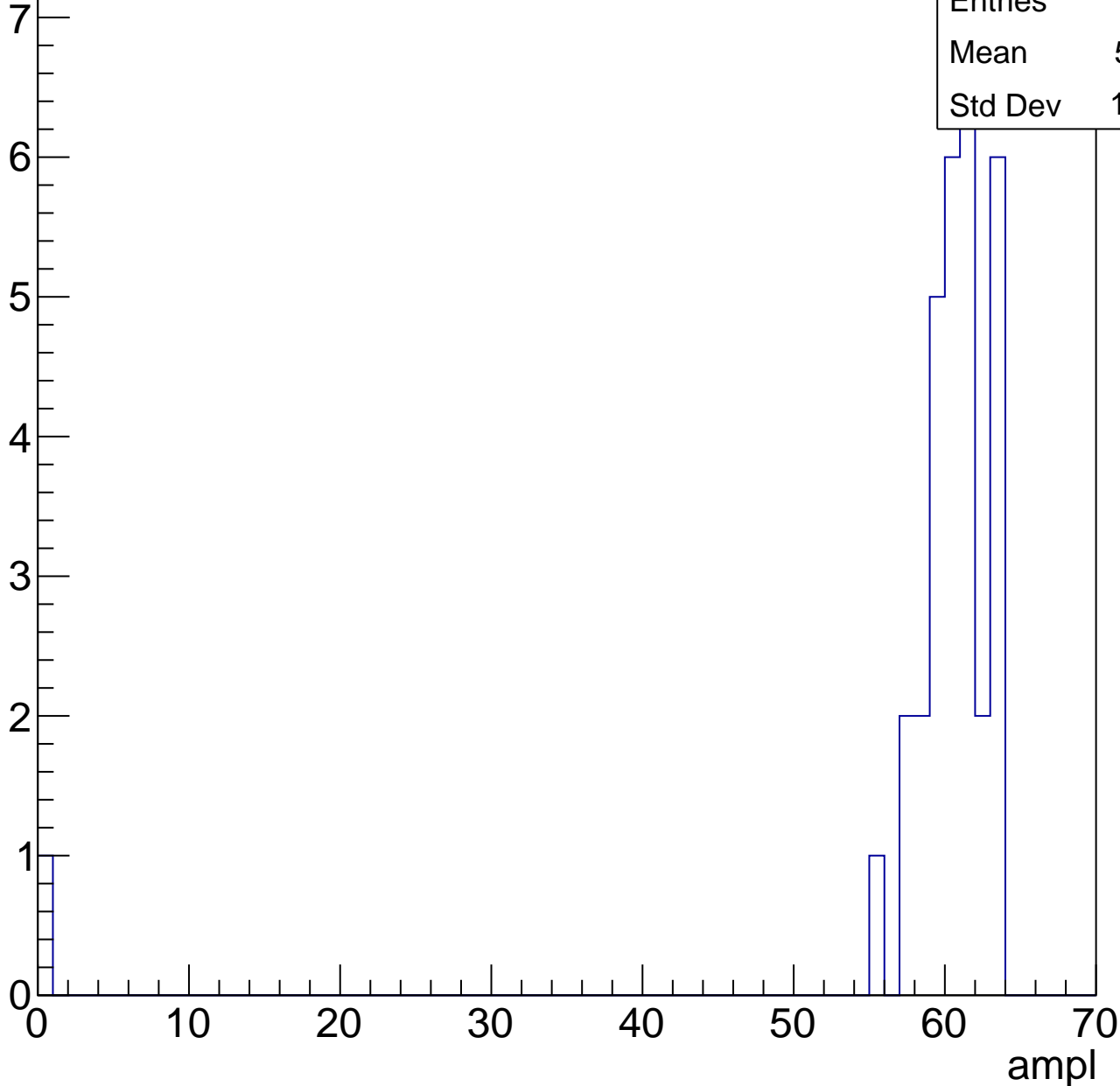


B0L001S, U11-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.41
Std Dev	10.67



B0L001S, U11-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch118, adc0

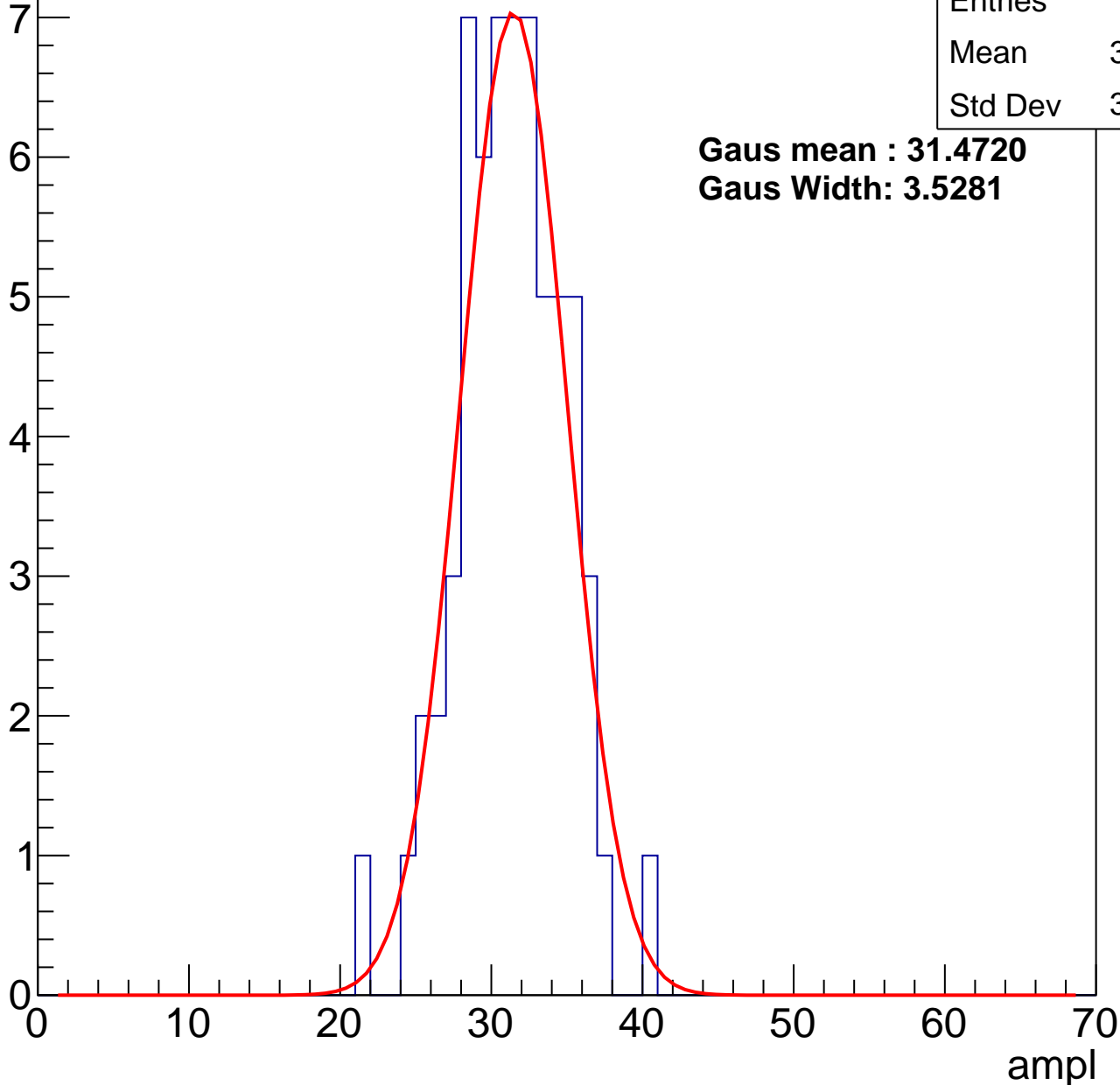
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.86
Std Dev	3.463

Gaus mean : 31.4720

Gaus Width: 3.5281



B0L001S, U11-ch118, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	37.63
Std Dev	3.583

Gaus mean : 38.1633

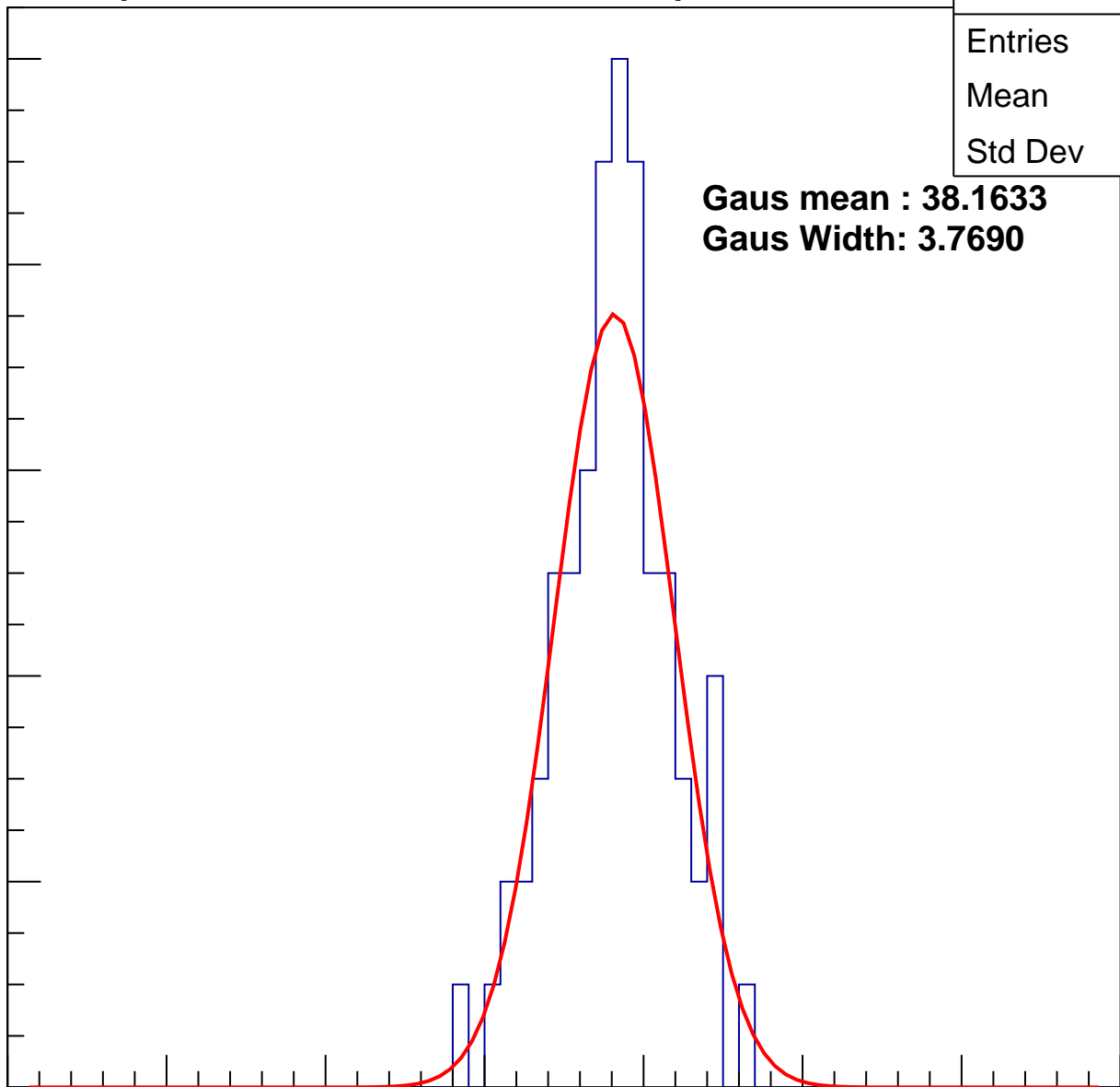
Gaus Width: 3.7690

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch118, adc2

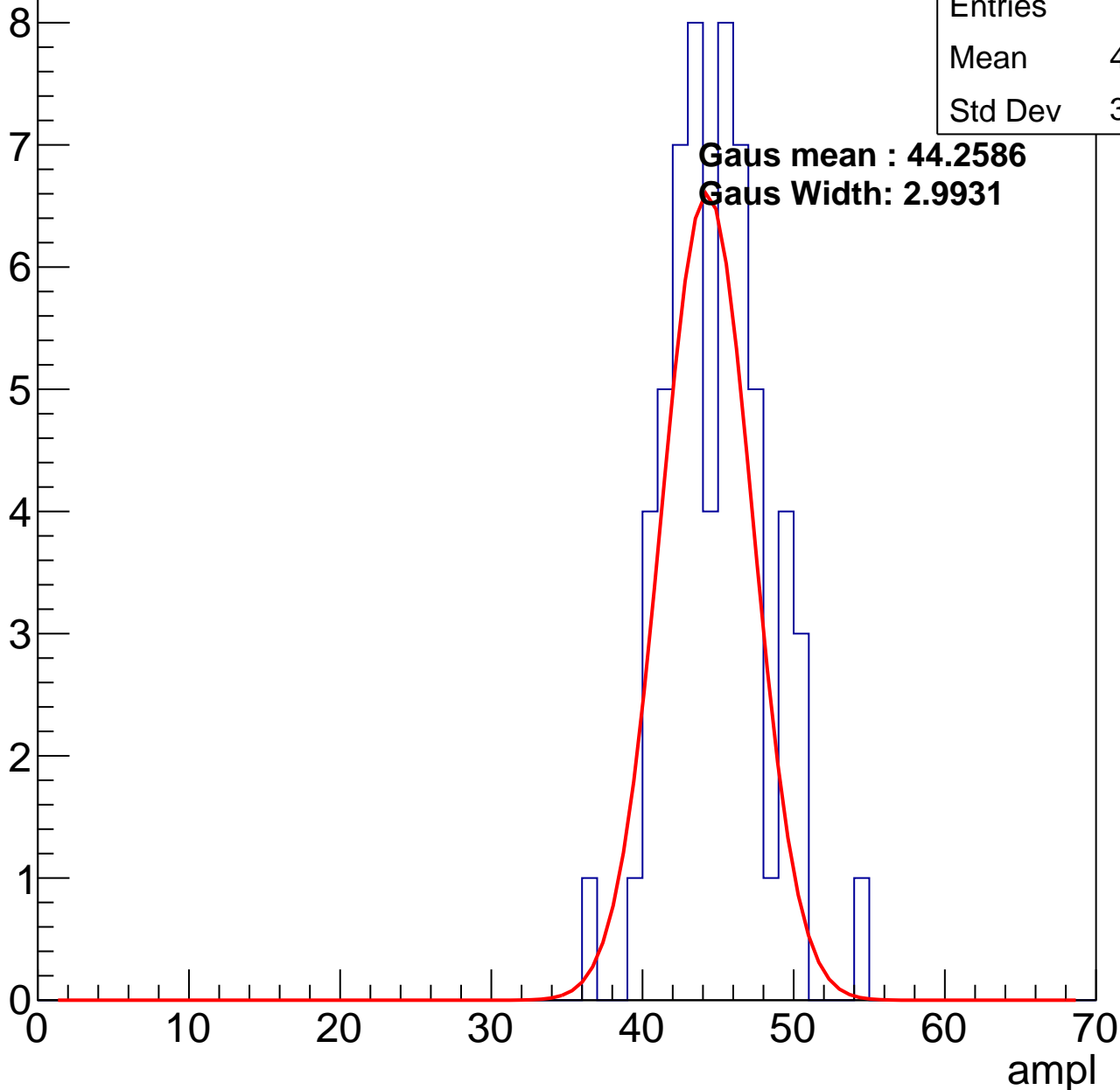
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.39
Std Dev	3.268

Gaus mean : 44.2586

Gaus Width: 2.9931

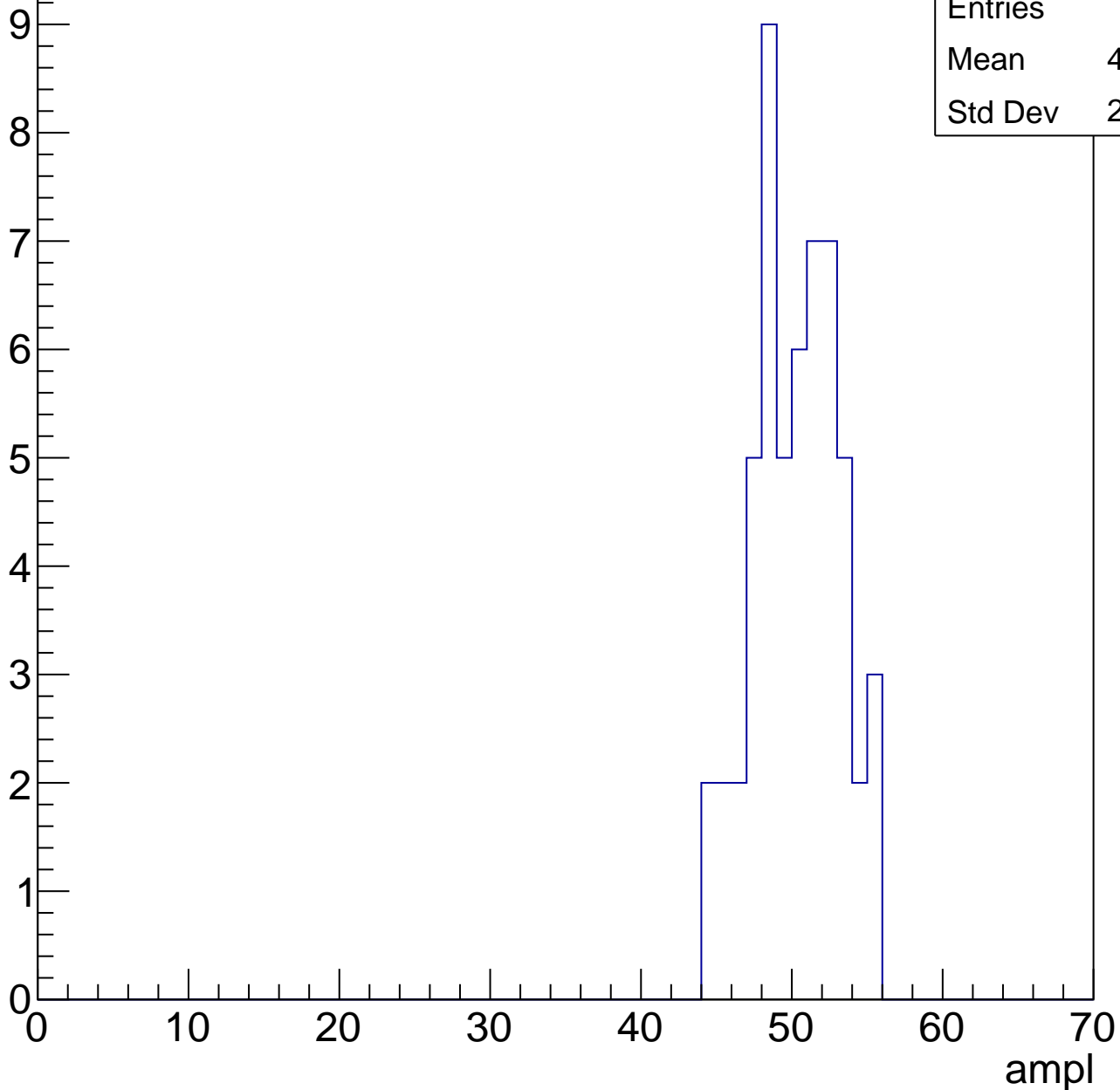


B0L001S, U11-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.84
Std Dev	2.782



B0L001S, U11-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

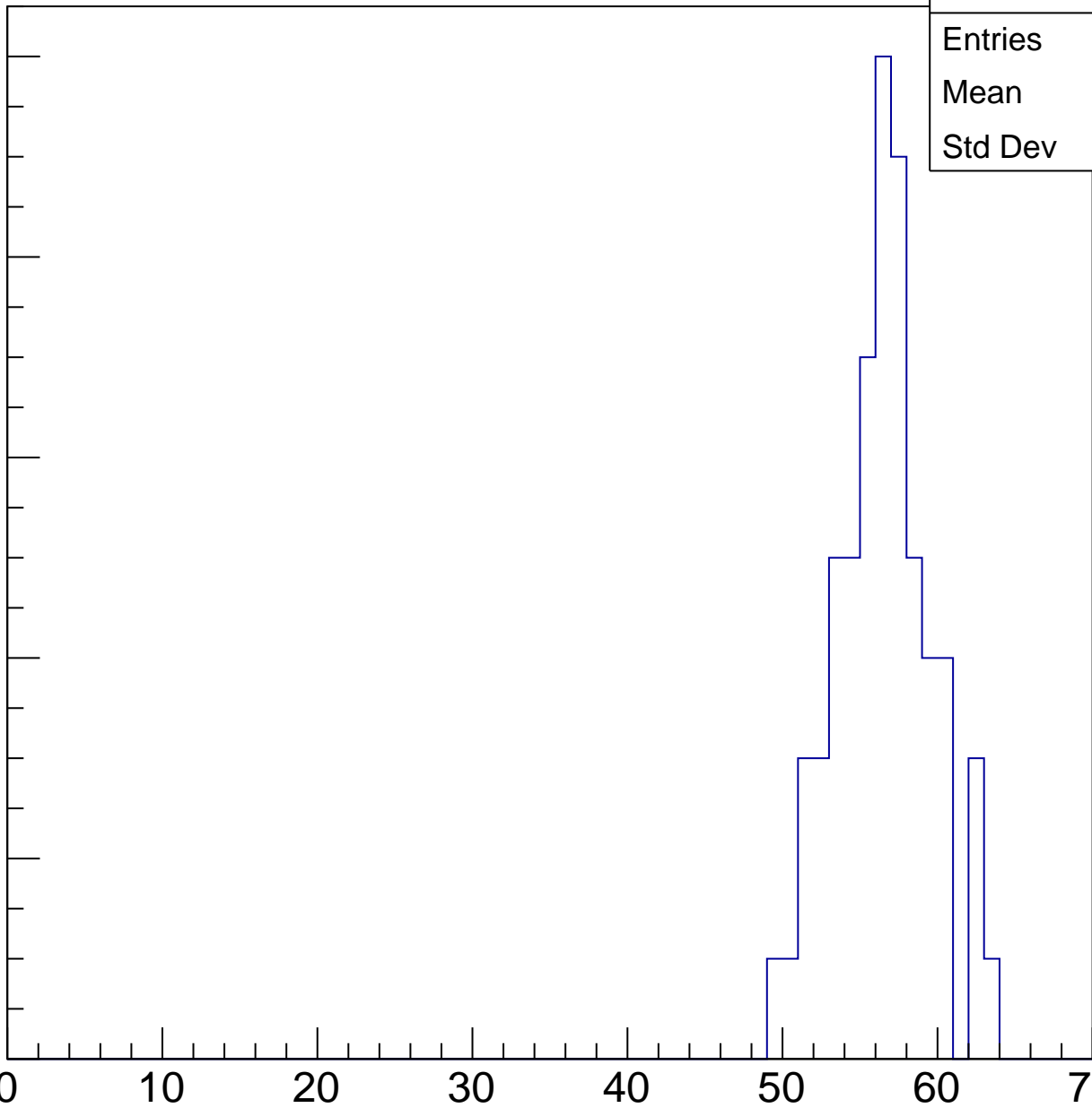
Entries	61
Mean	56
Std Dev	3.046

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

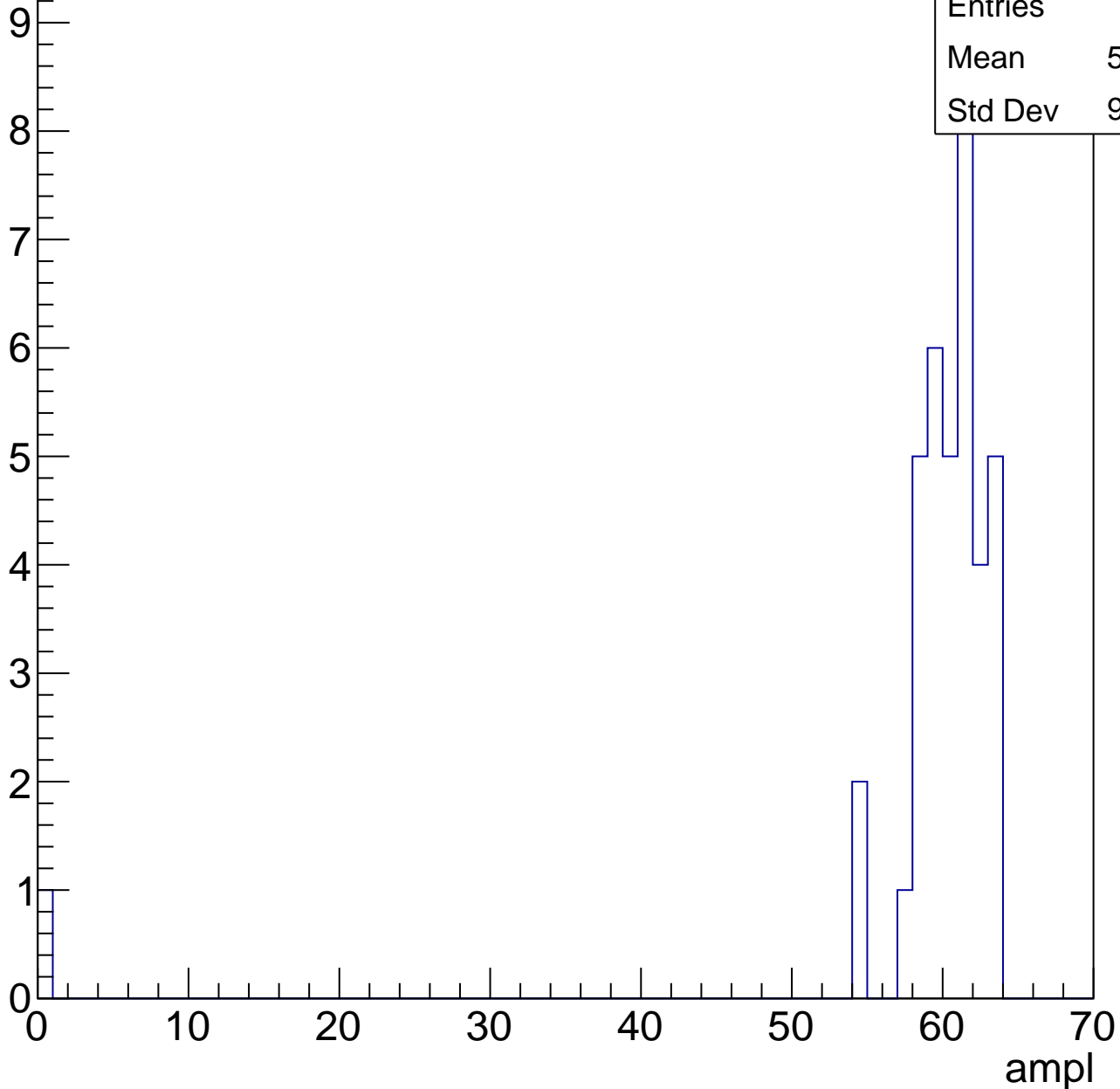


B0L001S, U11-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.45
Std Dev	9.848



B0L001S, U11-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch119, adc0

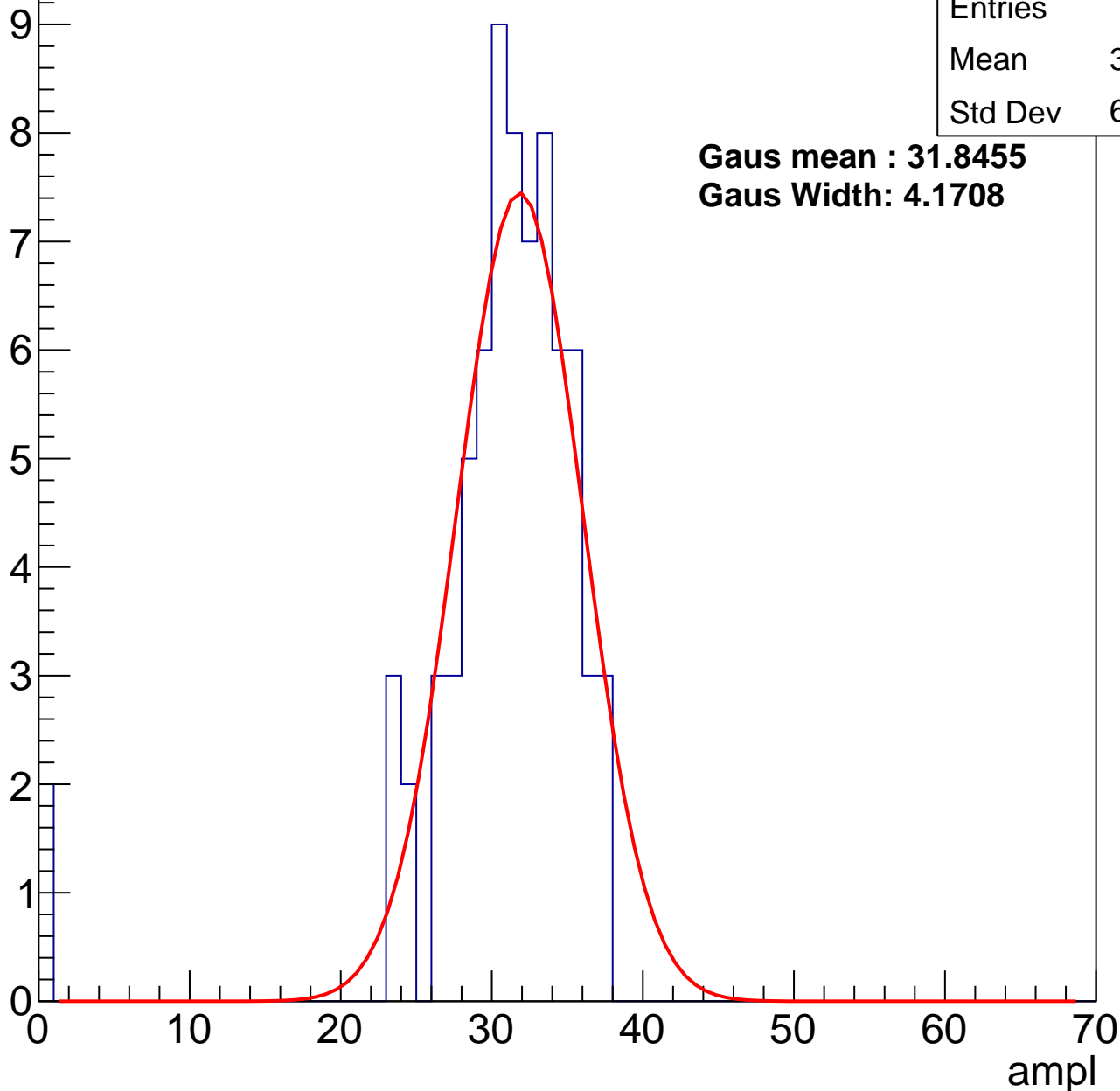
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.12
Std Dev	6.072

Gaus mean : 31.8455

Gaus Width: 4.1708



B0L001S, U11-ch119, adc1

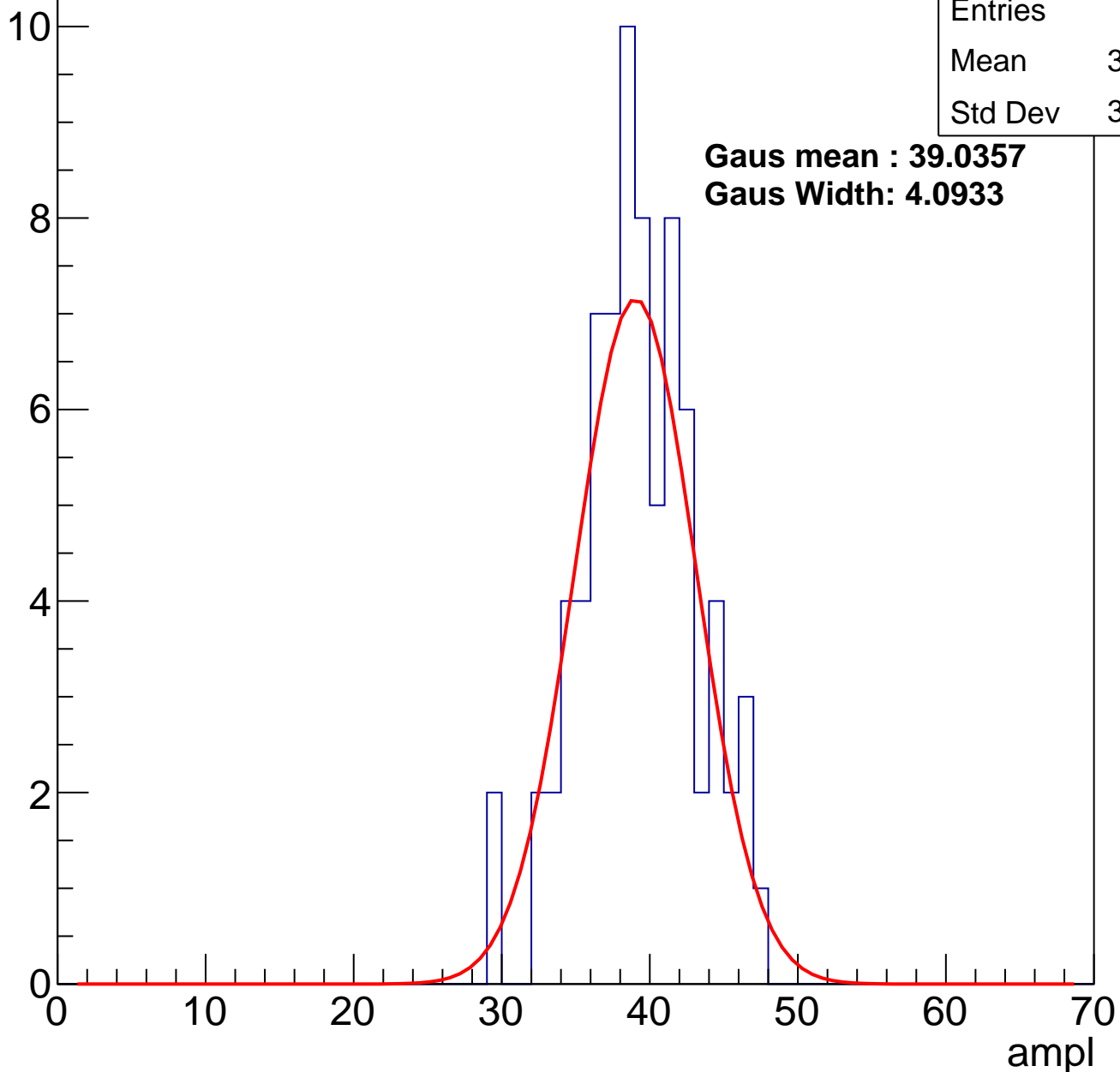
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	38.75
Std Dev	3.842

Gaus mean : 39.0357

Gaus Width: 4.0933

Entry



B0L001S, U11-ch119, adc2

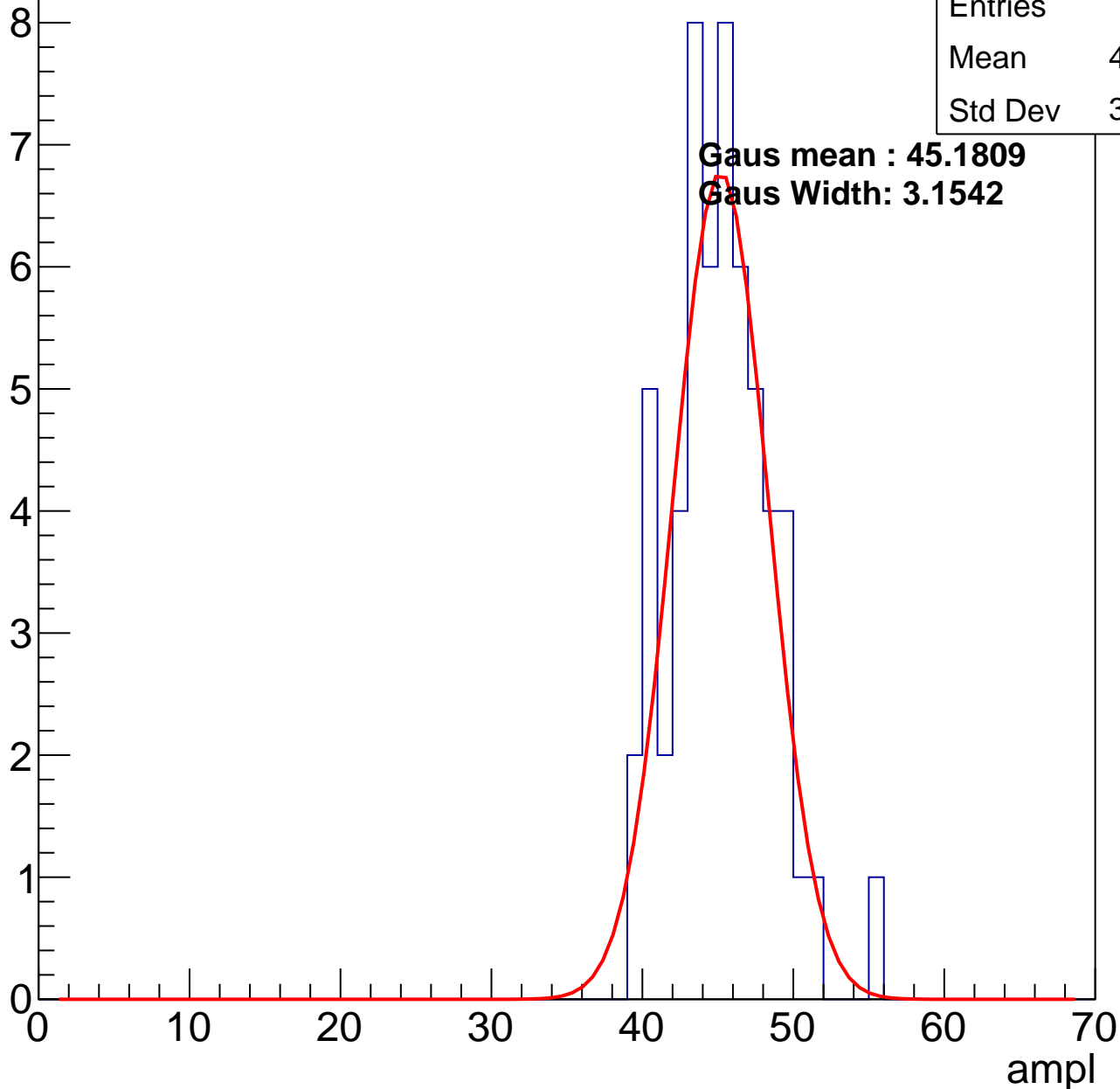
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.75
Std Dev	3.208

Gaus mean : 45.1809

Gaus Width: 3.1542

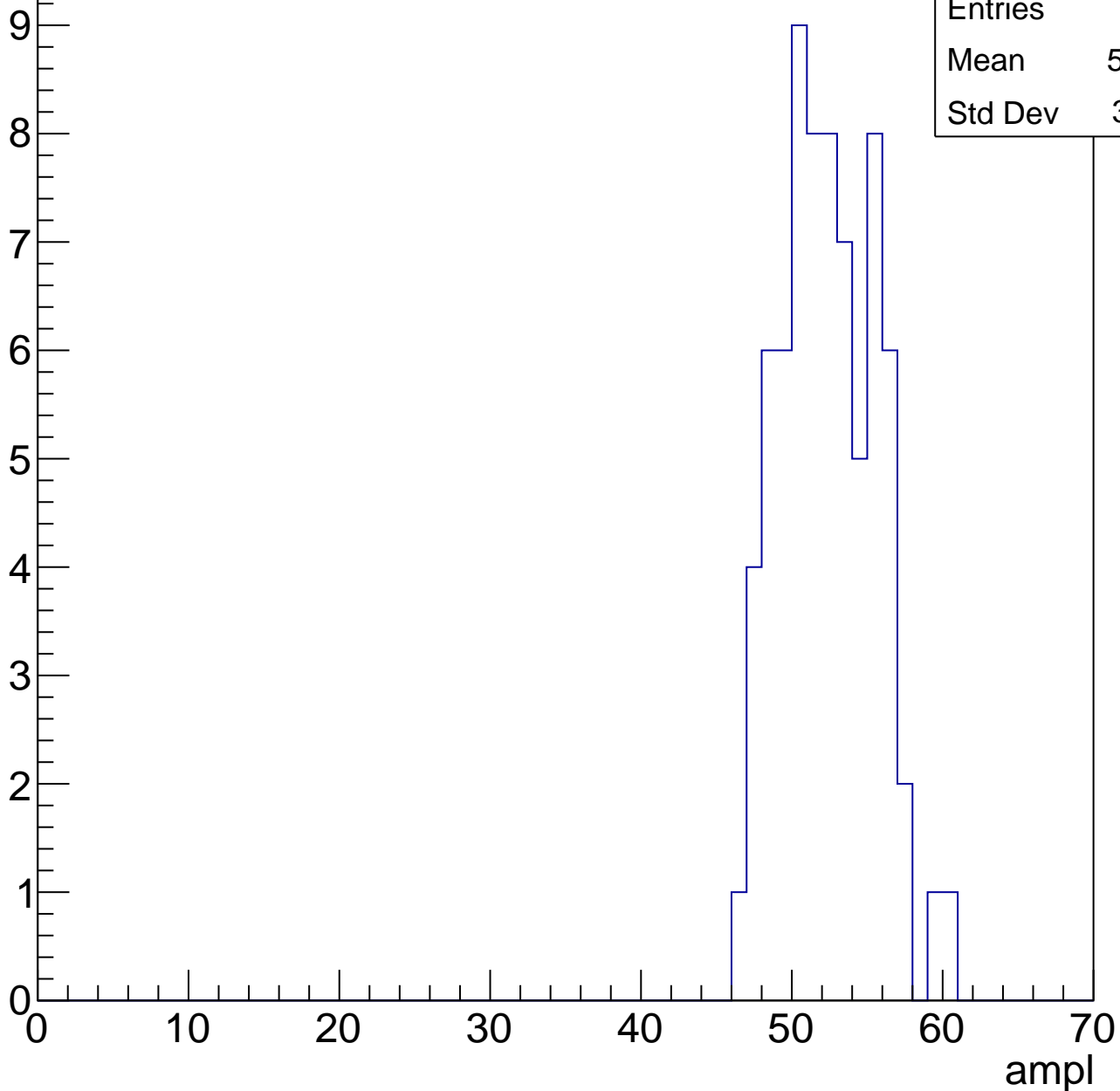


B0L001S, U11-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.94
Std Dev	3.091

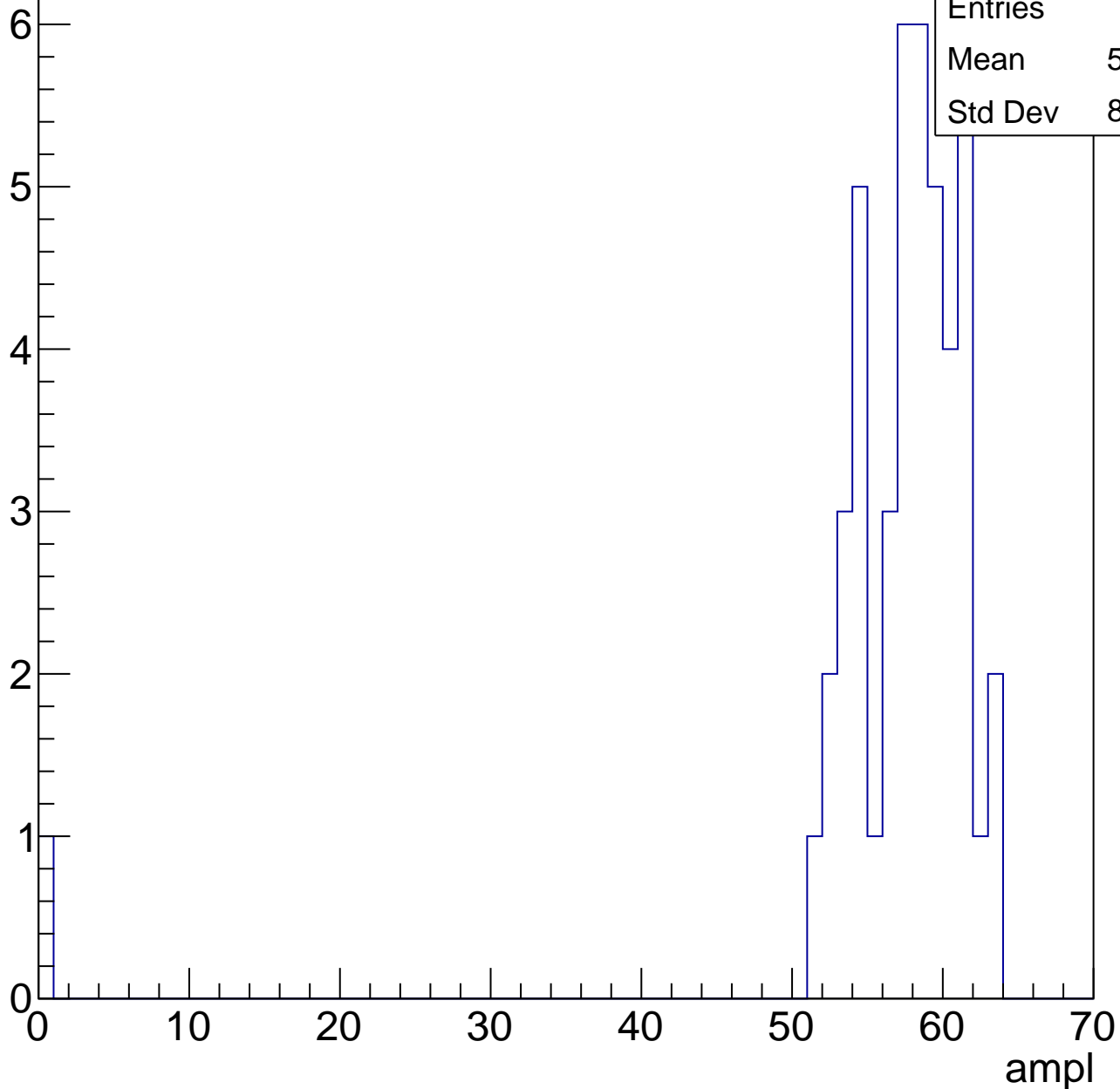


B0L001S, U11-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	56.22
Std Dev	8.922

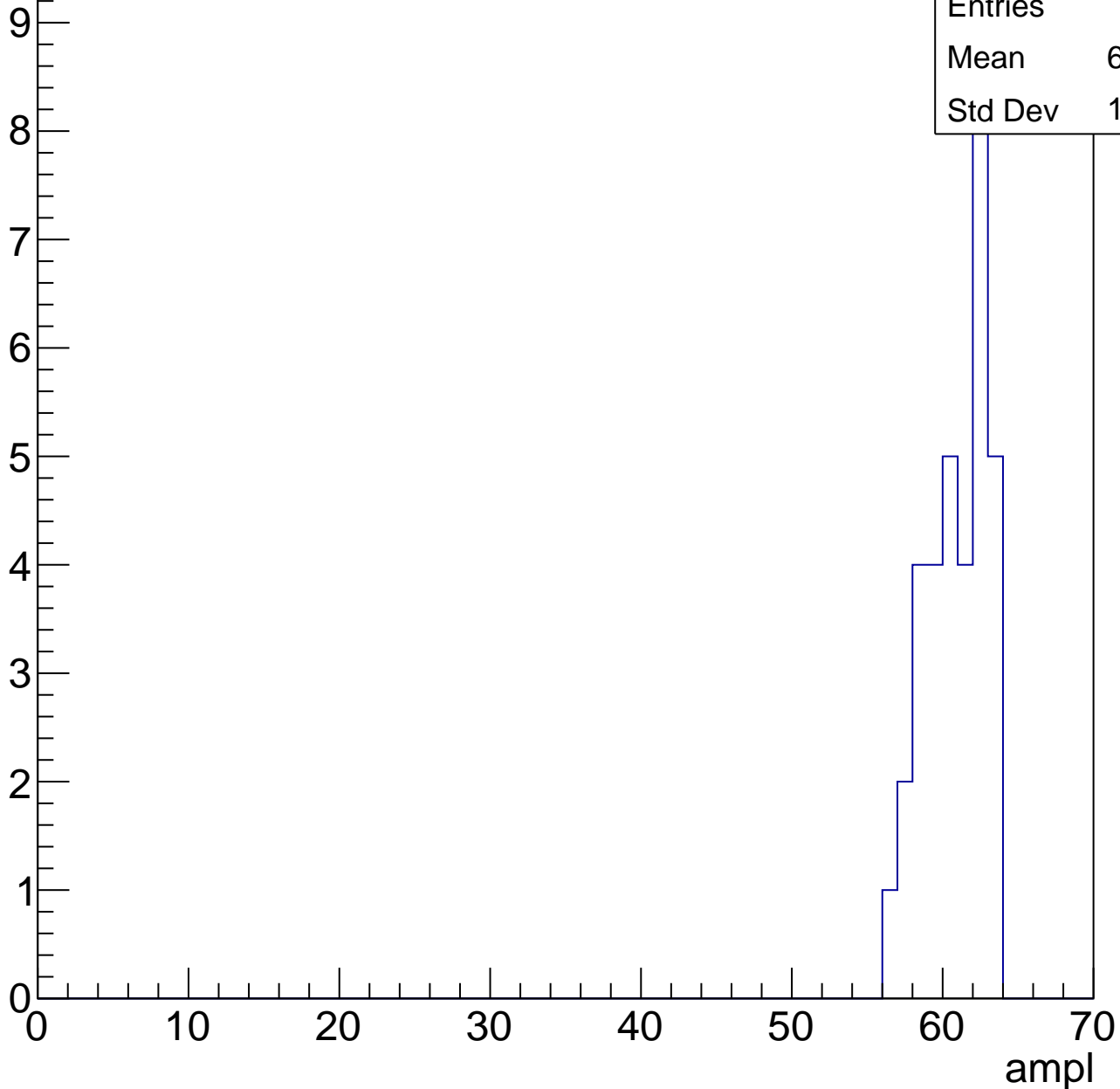


B0L001S, U11-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	60.44
Std Dev	1.973



B0L001S, U11-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch120, adc0

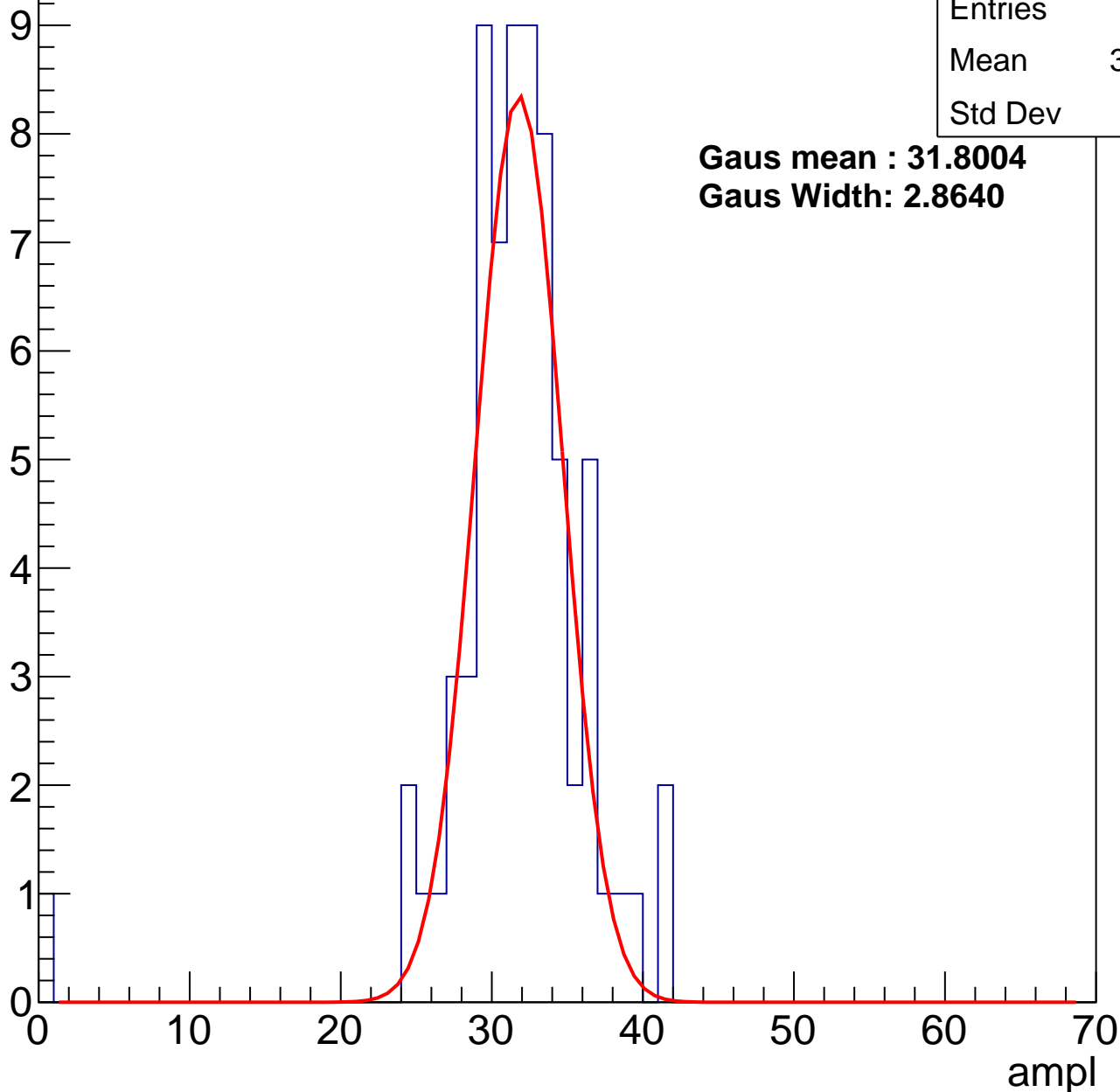
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	31.17
Std Dev	5.11

Gaus mean : 31.8004

Gaus Width: 2.8640



B0L001S, U11-ch120, adc1

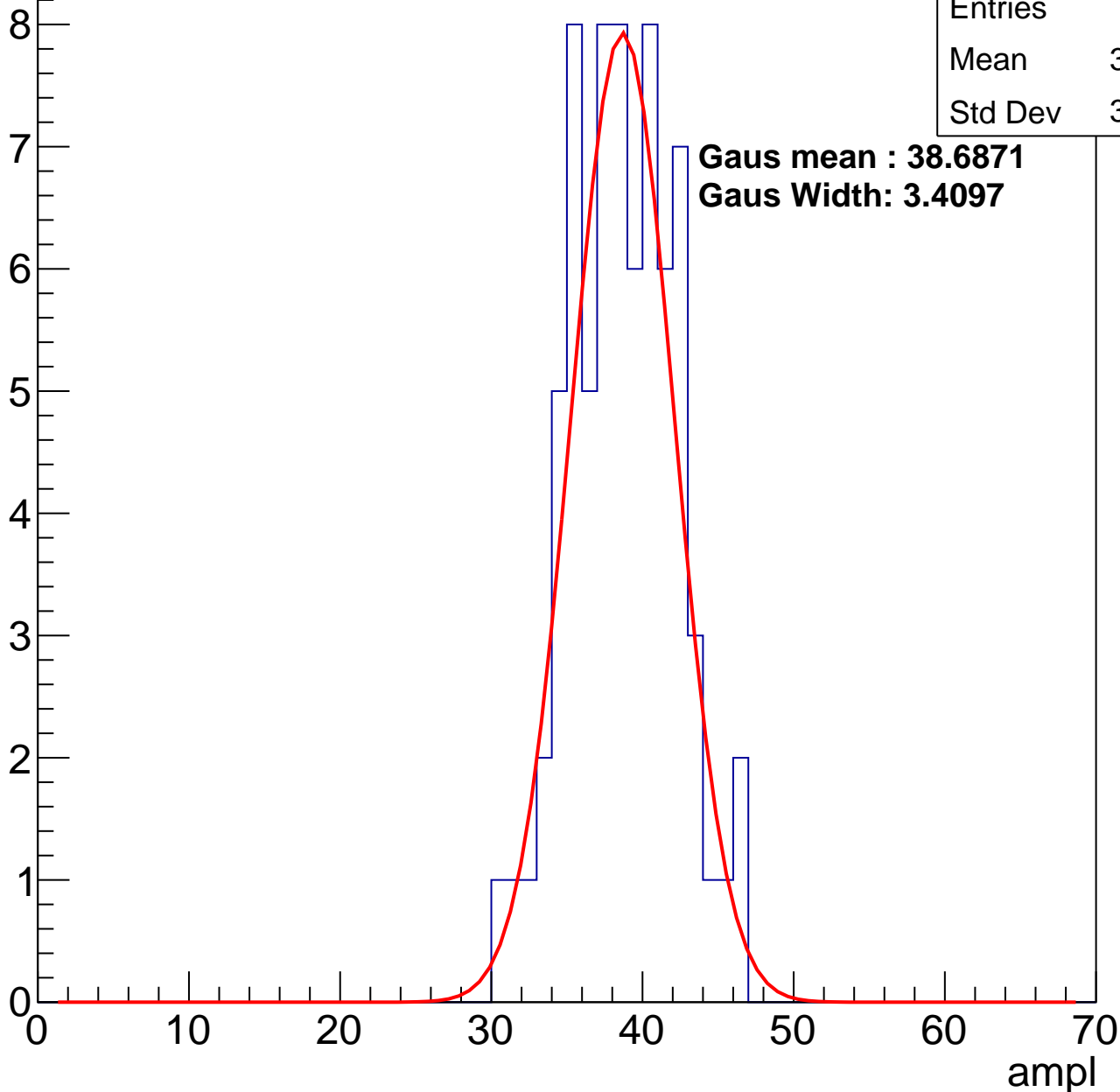
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	38.26
Std Dev	3.444

Gaus mean : 38.6871

Gaus Width: 3.4097



B0L001S, U11-ch120, adc2

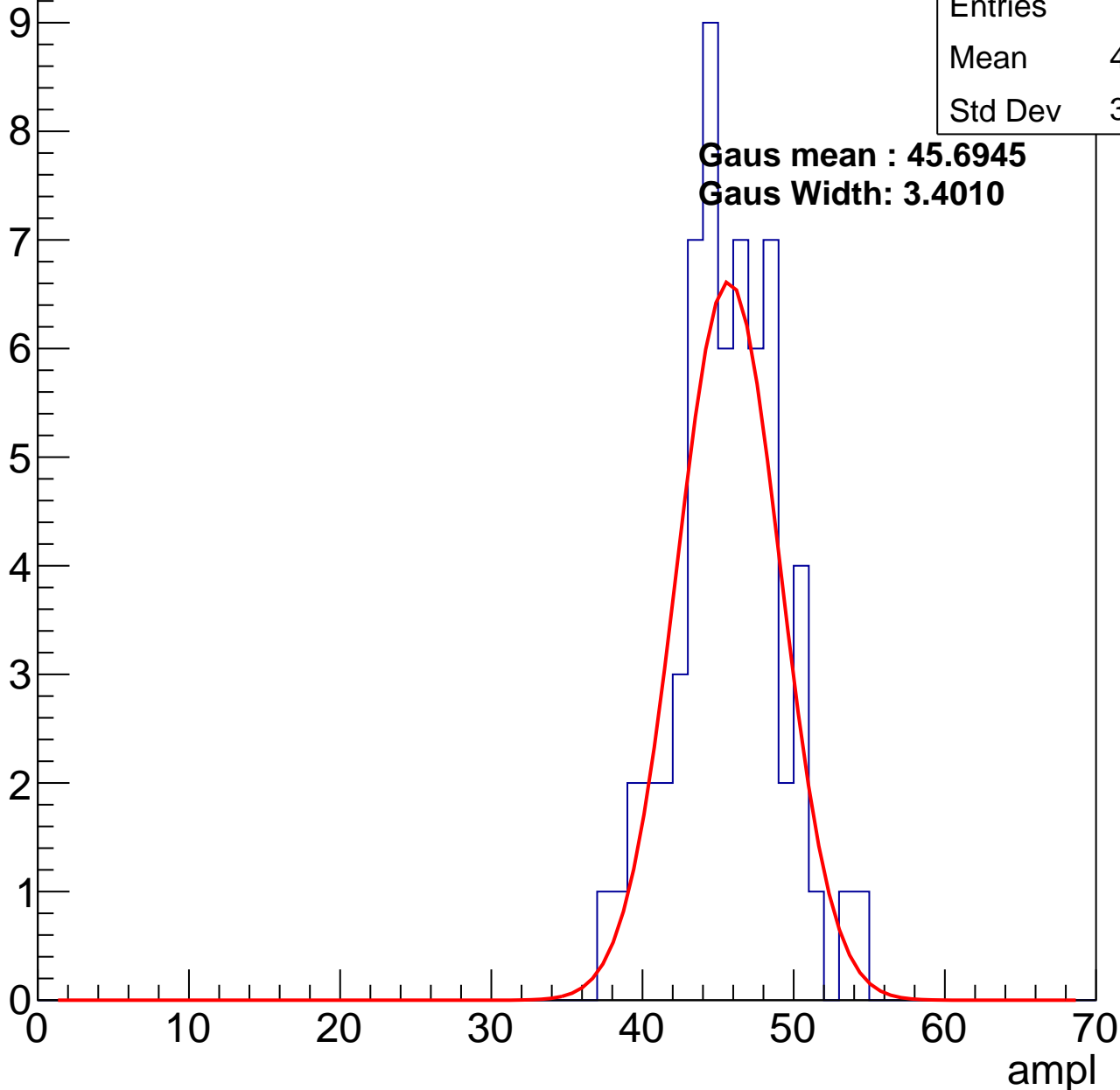
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	45.23
Std Dev	3.443

Gaus mean : 45.6945

Gaus Width: 3.4010

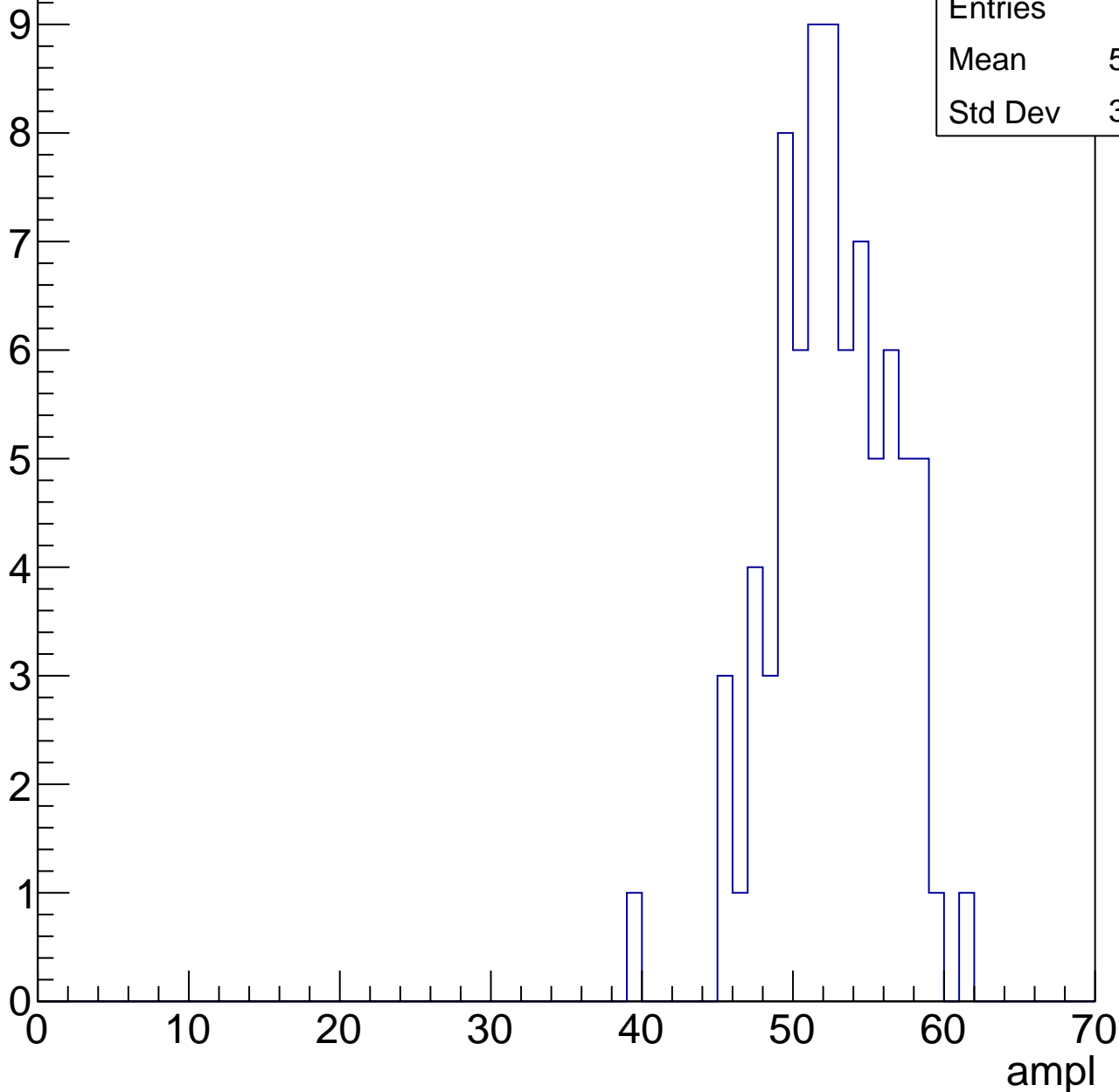


B0L001S, U11-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	52.16
Std Dev	3.894

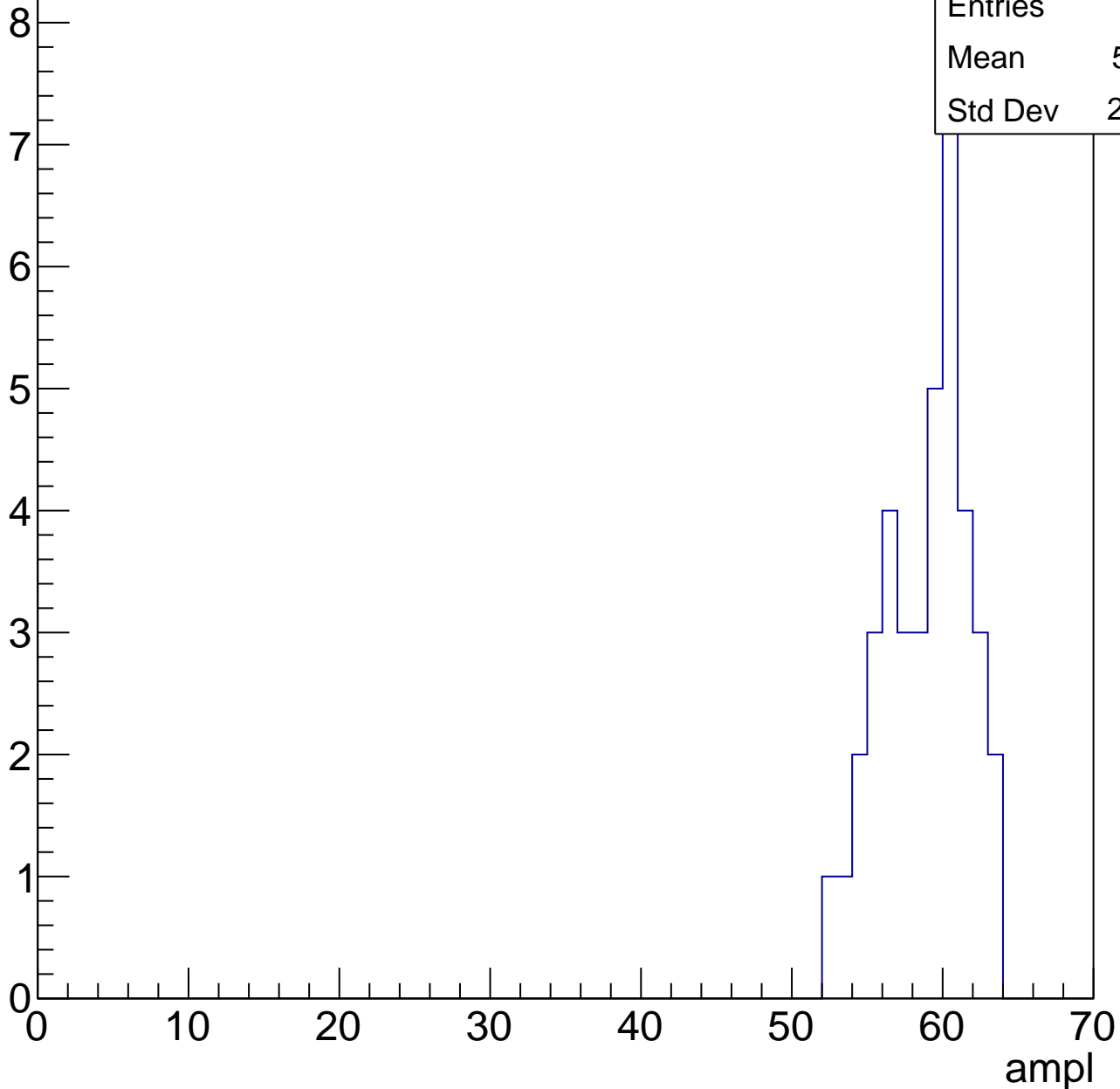


B0L001S, U11-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.41
Std Dev	2.789

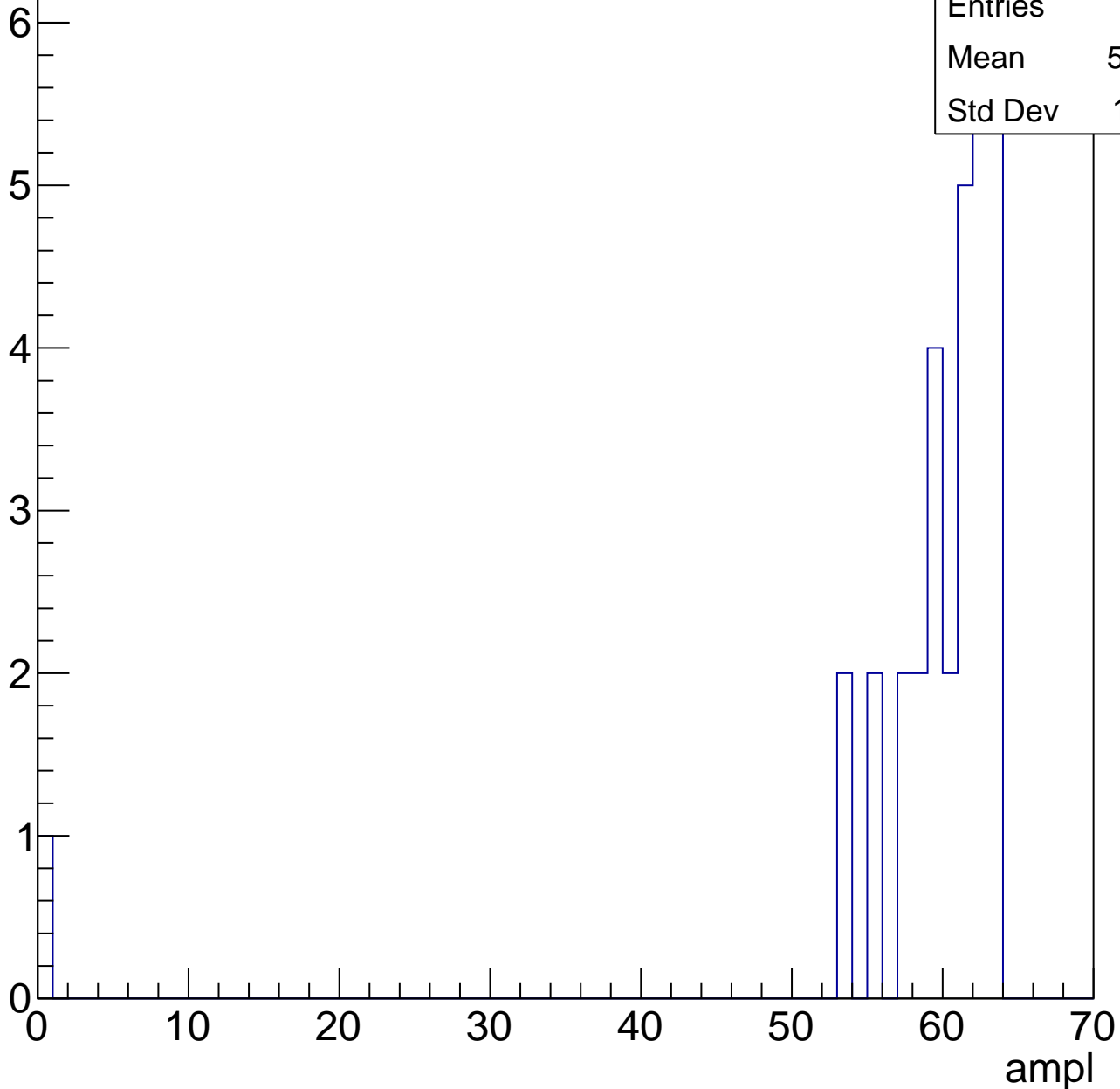


B0L001S, U11-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.03
Std Dev	10.81



B0L001S, U11-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch121, adc0

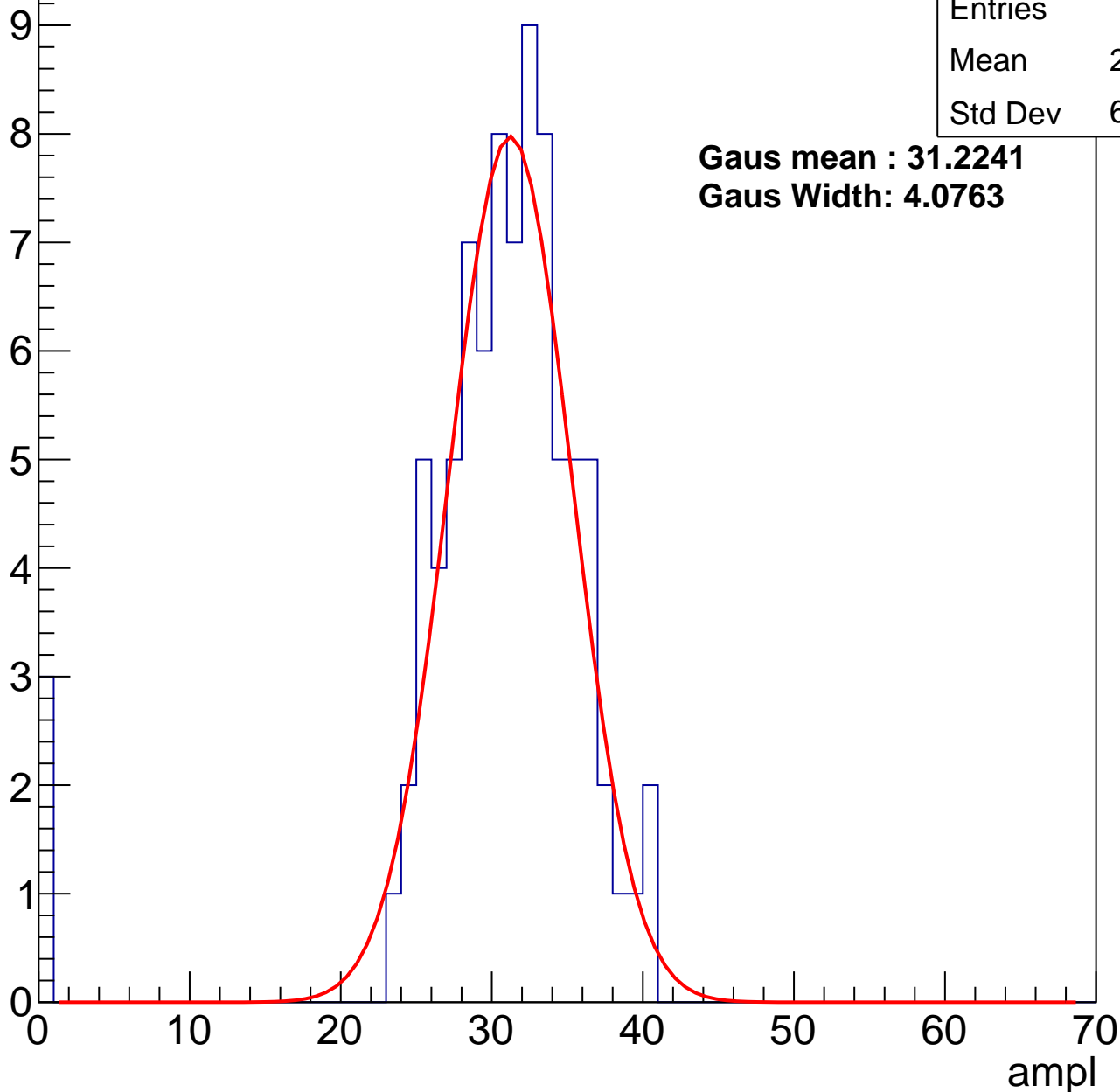
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	29.88
Std Dev	6.846

Gaus mean : 31.2241

Gaus Width: 4.0763



B0L001S, U11-ch121, adc1

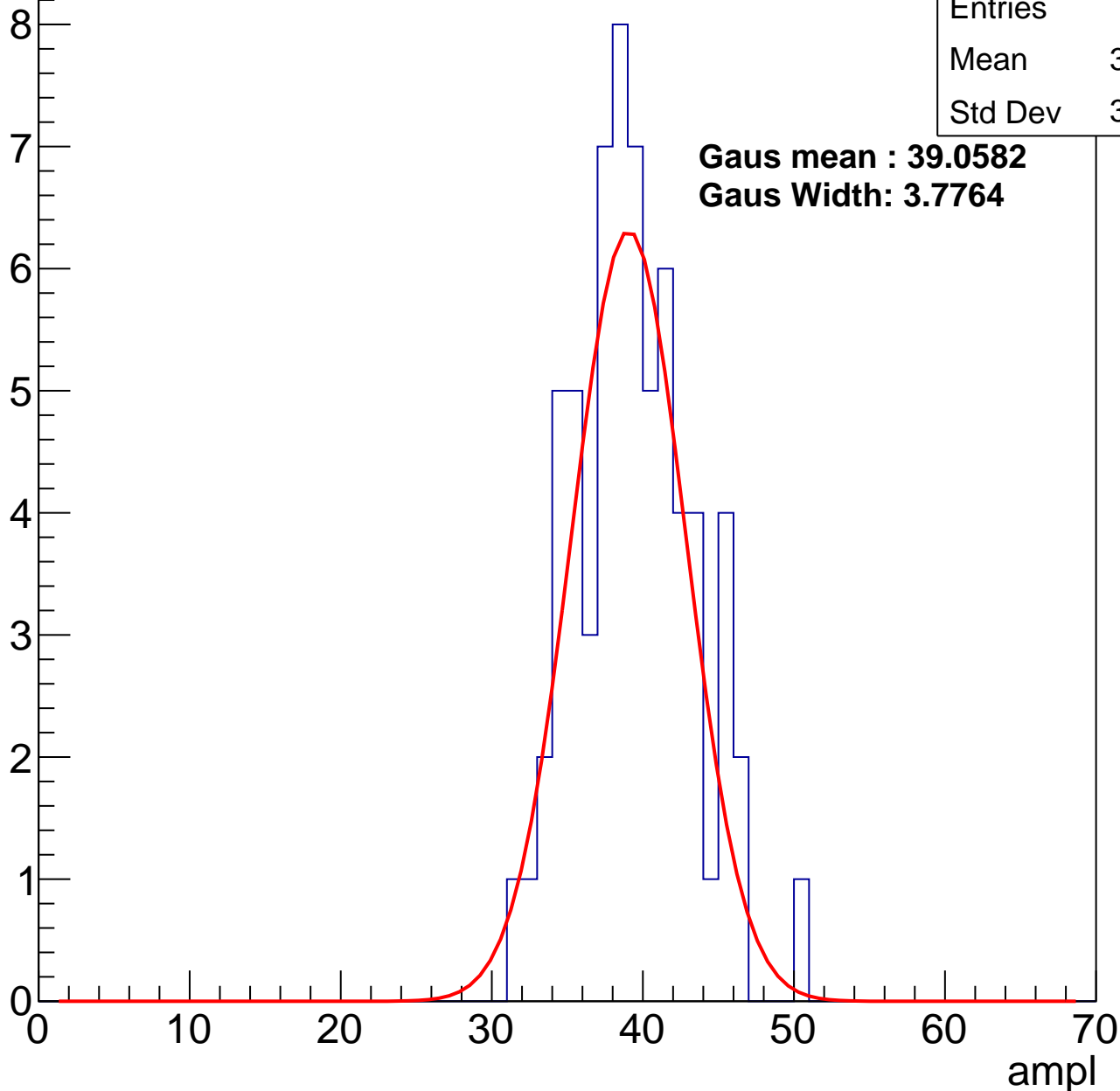
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.94
Std Dev	3.833

Gaus mean : 39.0582

Gaus Width: 3.7764



B0L001S, U11-ch121, adc2

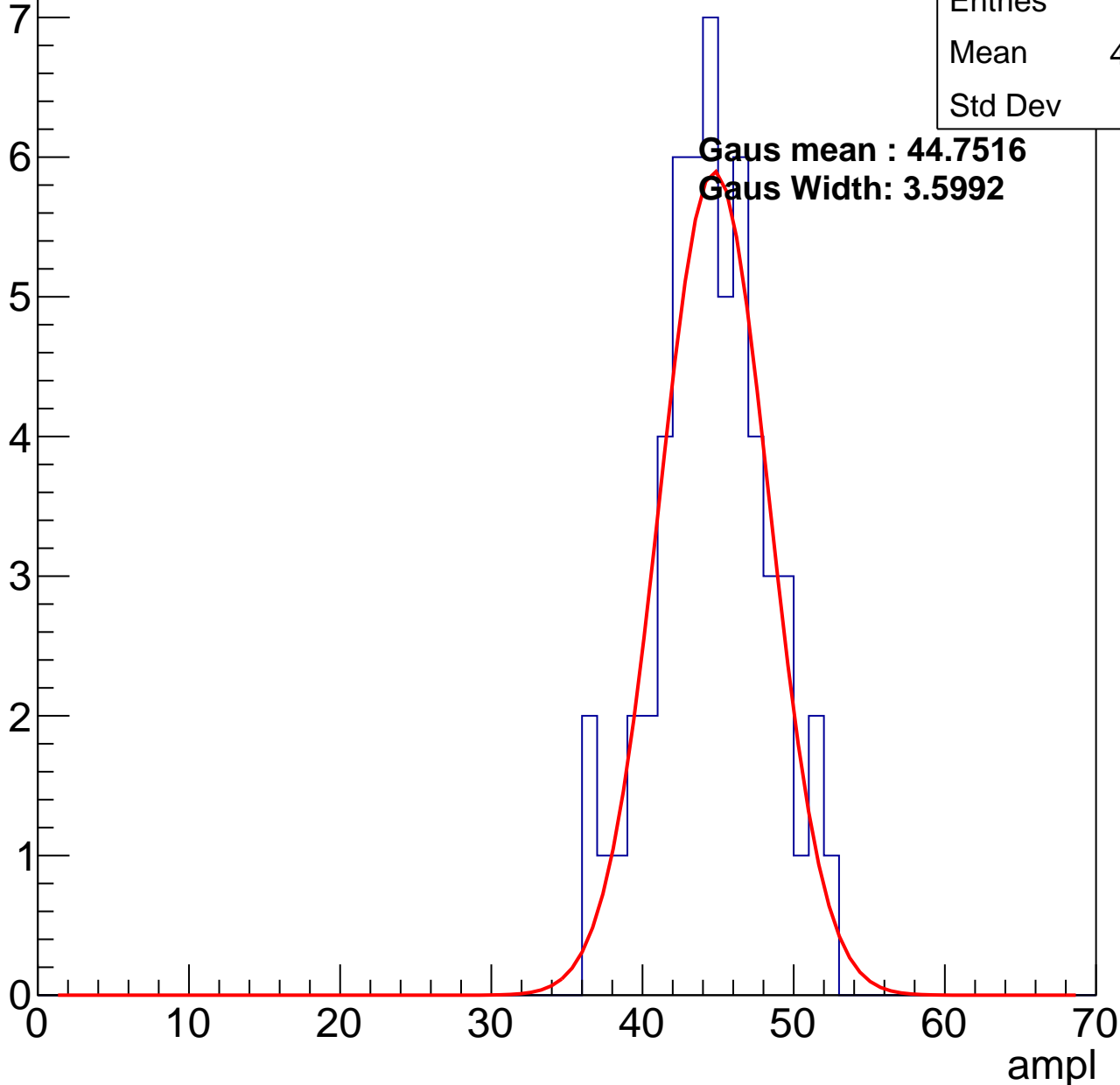
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.12
Std Dev	3.65

Gaus mean : 44.7516

Gaus Width: 3.5992

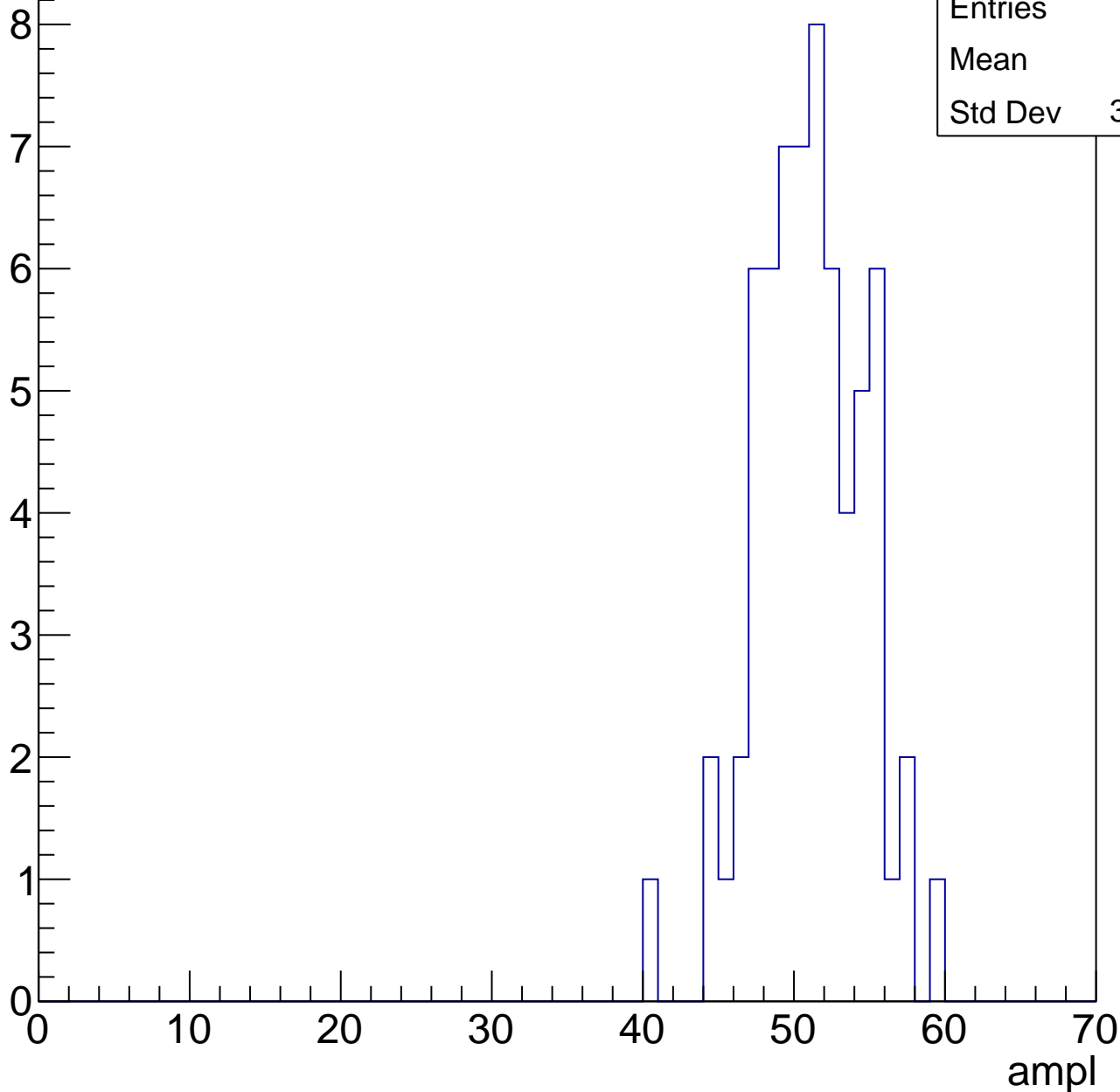


B0L001S, U11-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.6
Std Dev	3.516

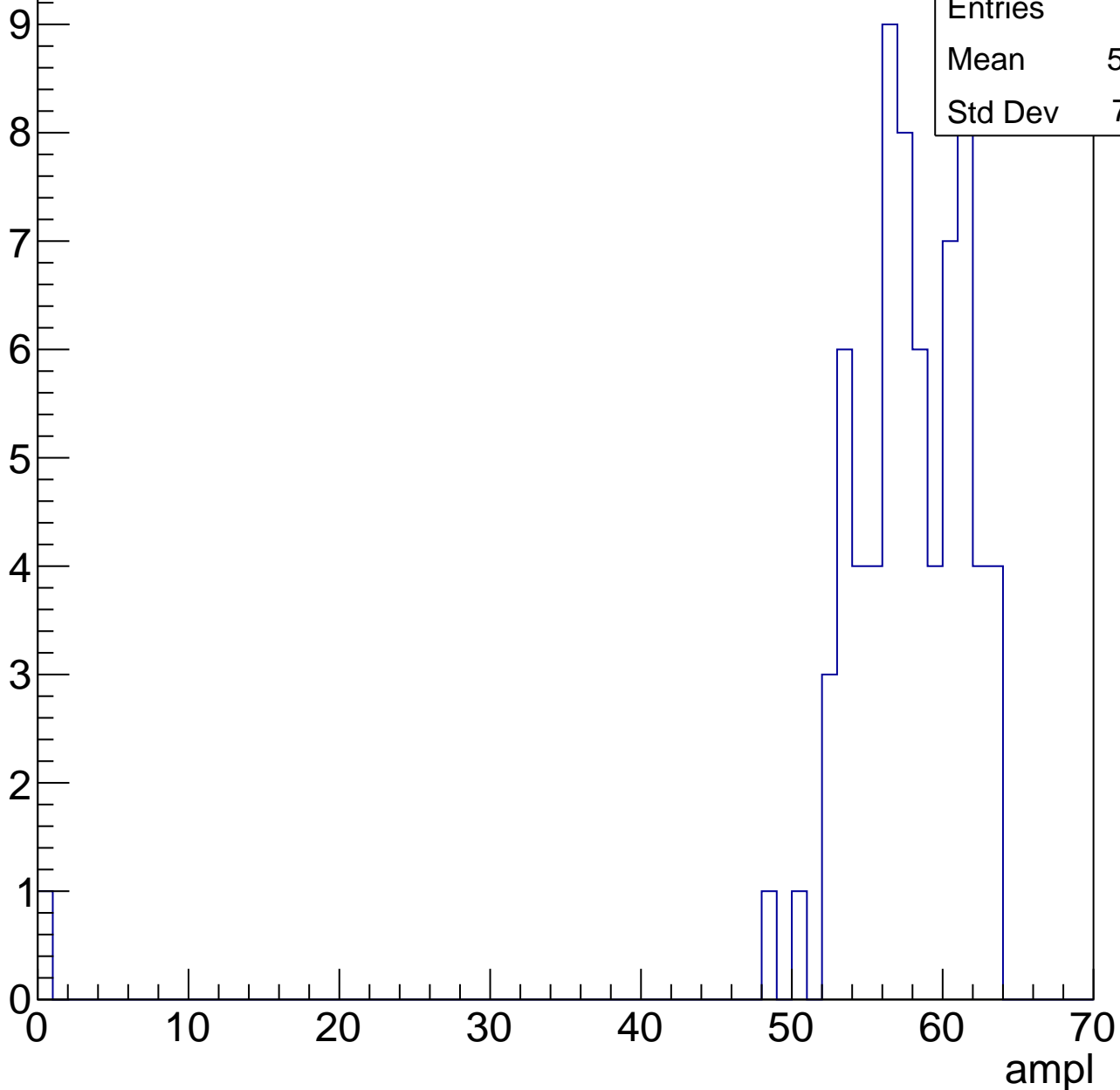


B0L001S, U11-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	56.57
Std Dev	7.611

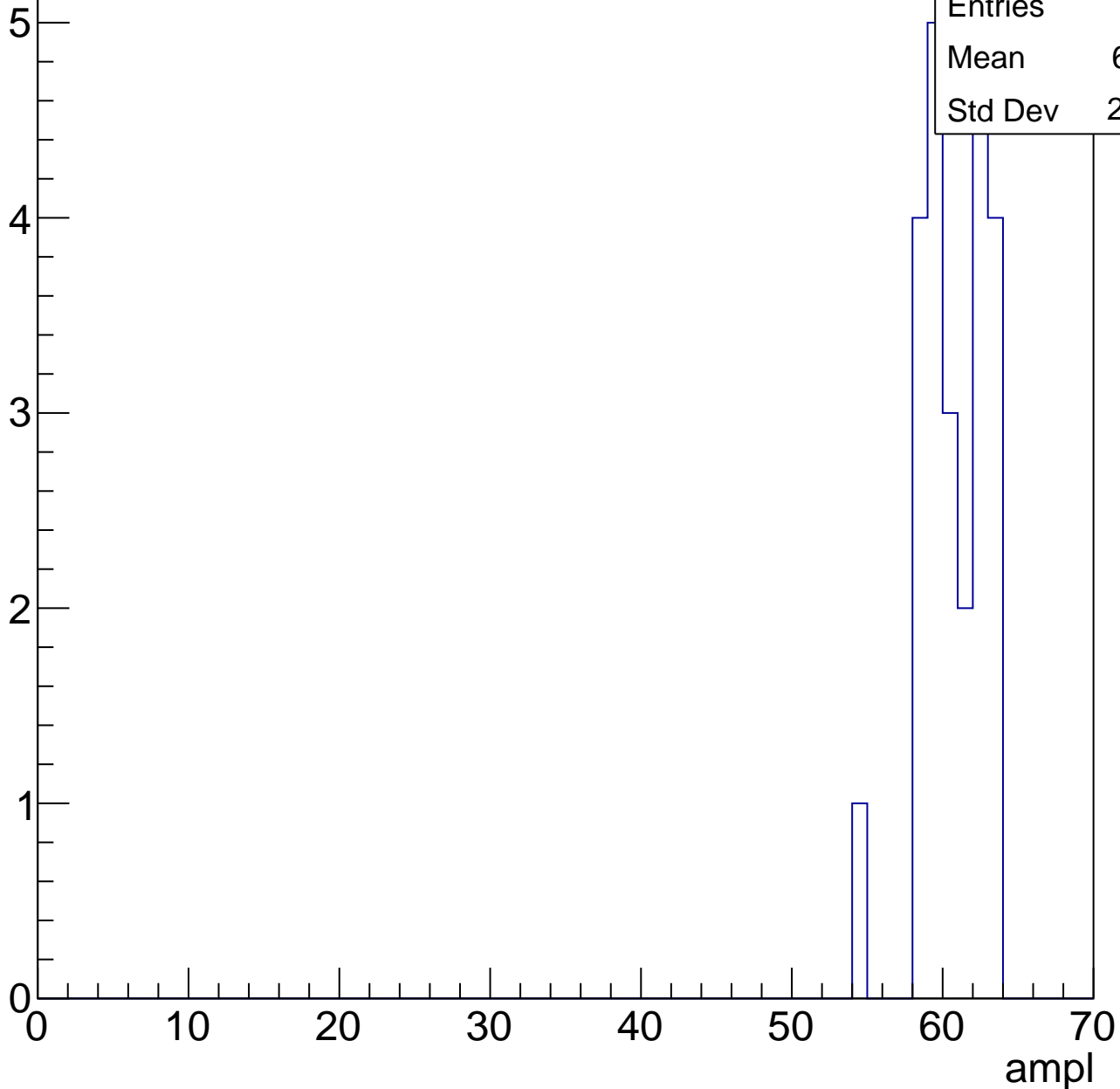


B0L001S, U11-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	60.21
Std Dev	2.179



B0L001S, U11-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

B0L001S, U11-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch122, adc0

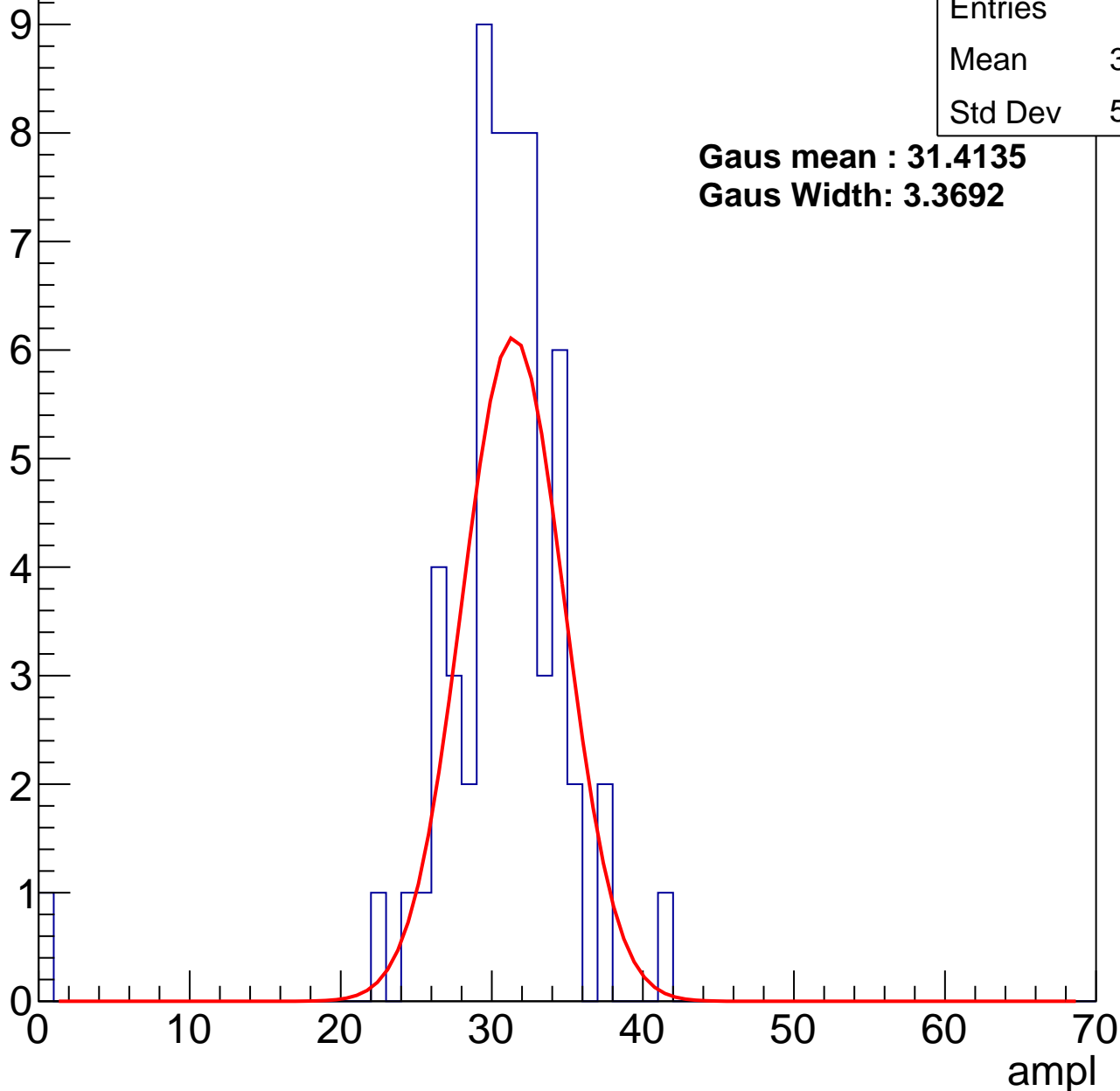
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.08
Std Dev	5.107

Gaus mean : 31.4135

Gaus Width: 3.3692



B0L001S, U11-ch122, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	37.63
Std Dev	4.189

Gaus mean : 37.7613

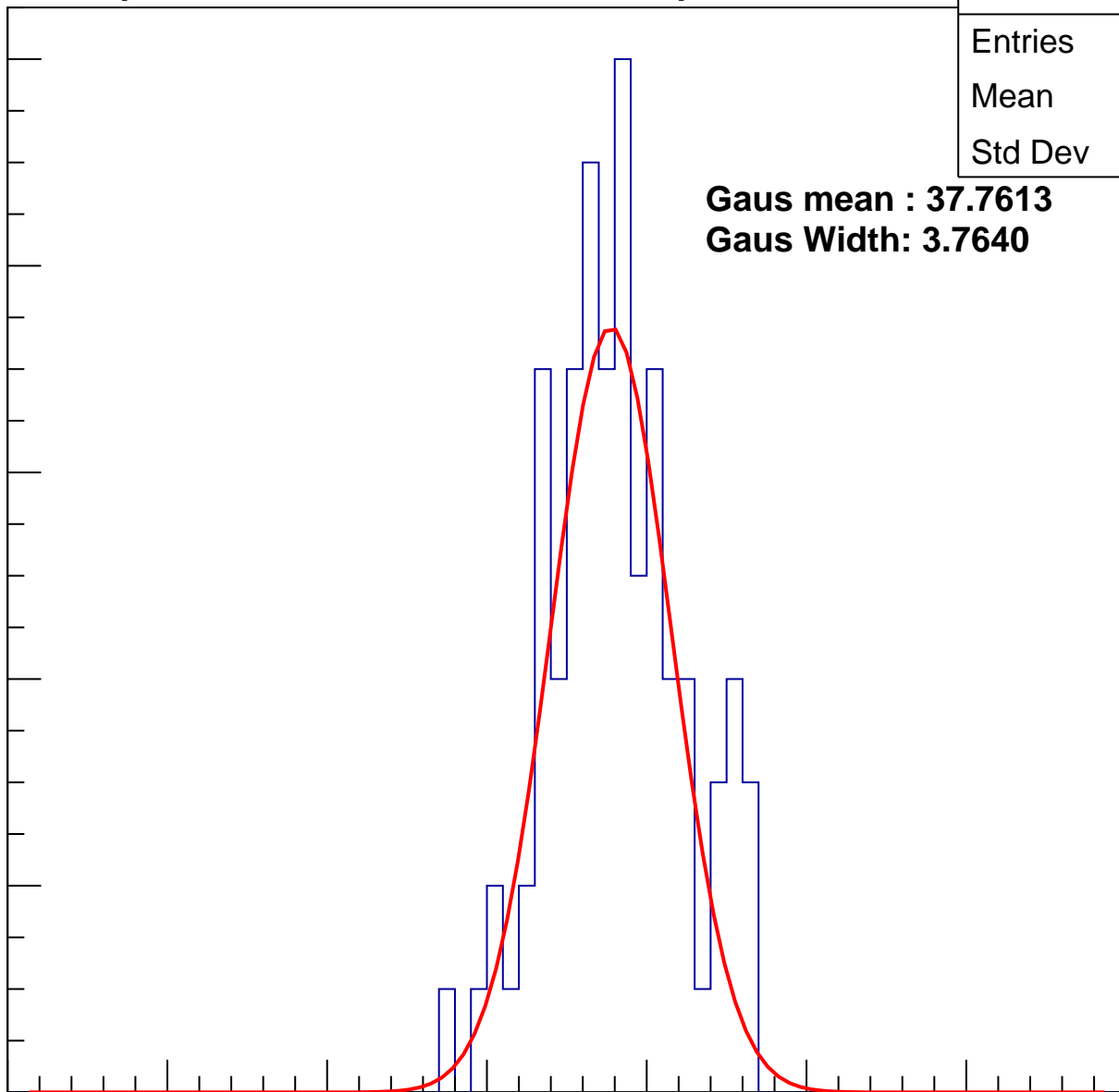
Gaus Width: 3.7640

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch122, adc2

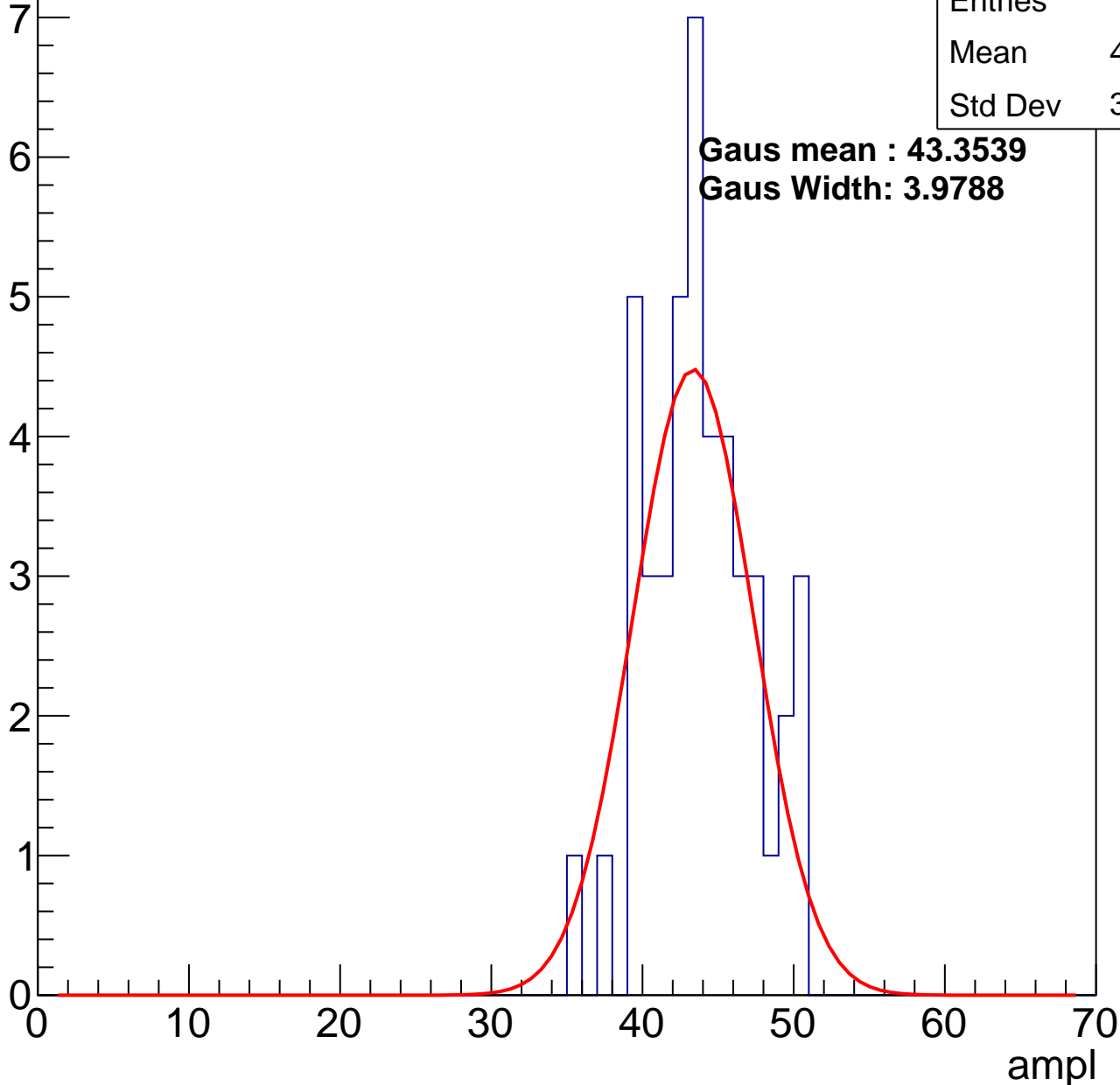
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	43.38
Std Dev	3.517

Gaus mean : 43.3539

Gaus Width: 3.9788

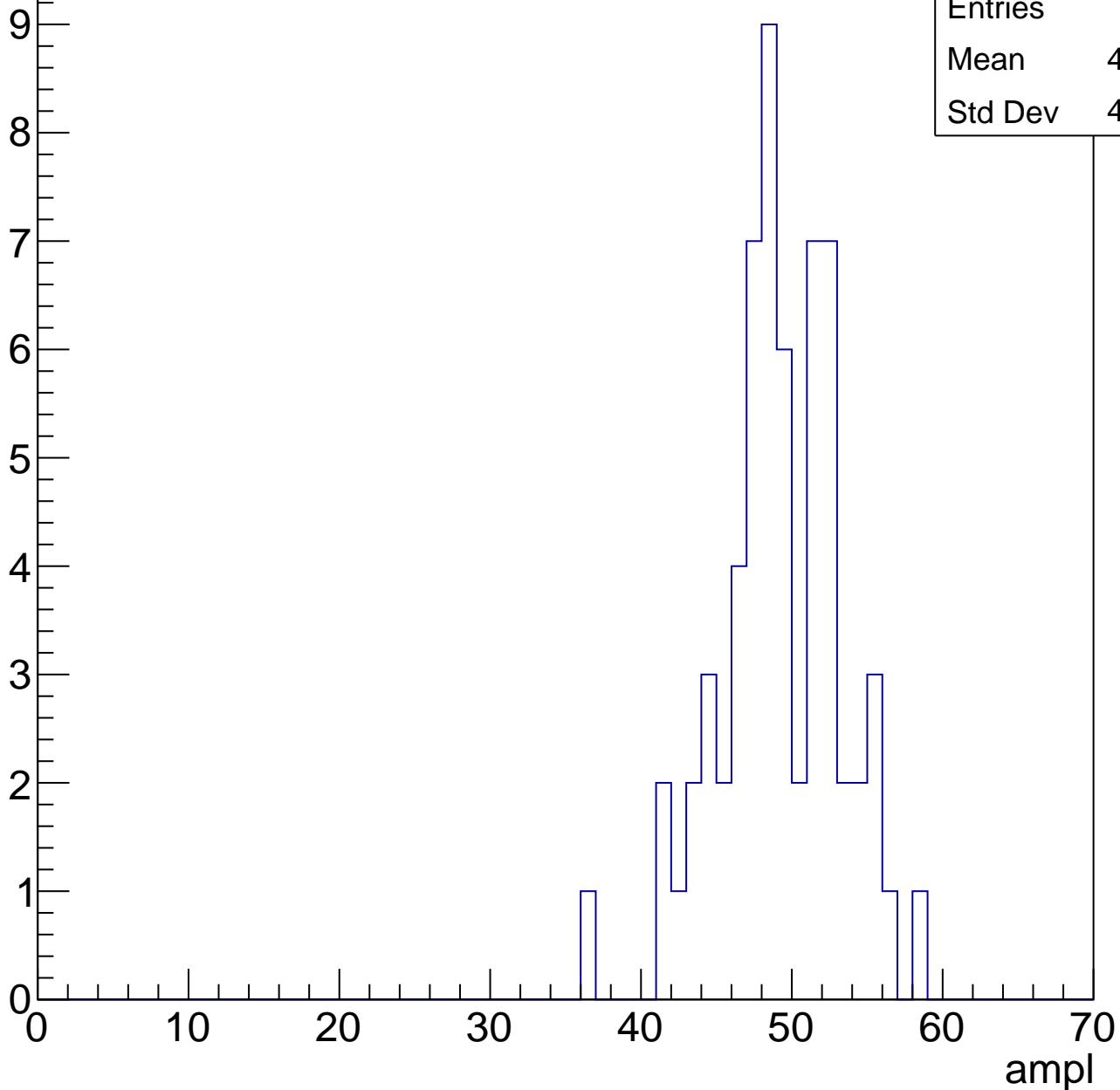


B0L001S, U11-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	48.73
Std Dev	4.029

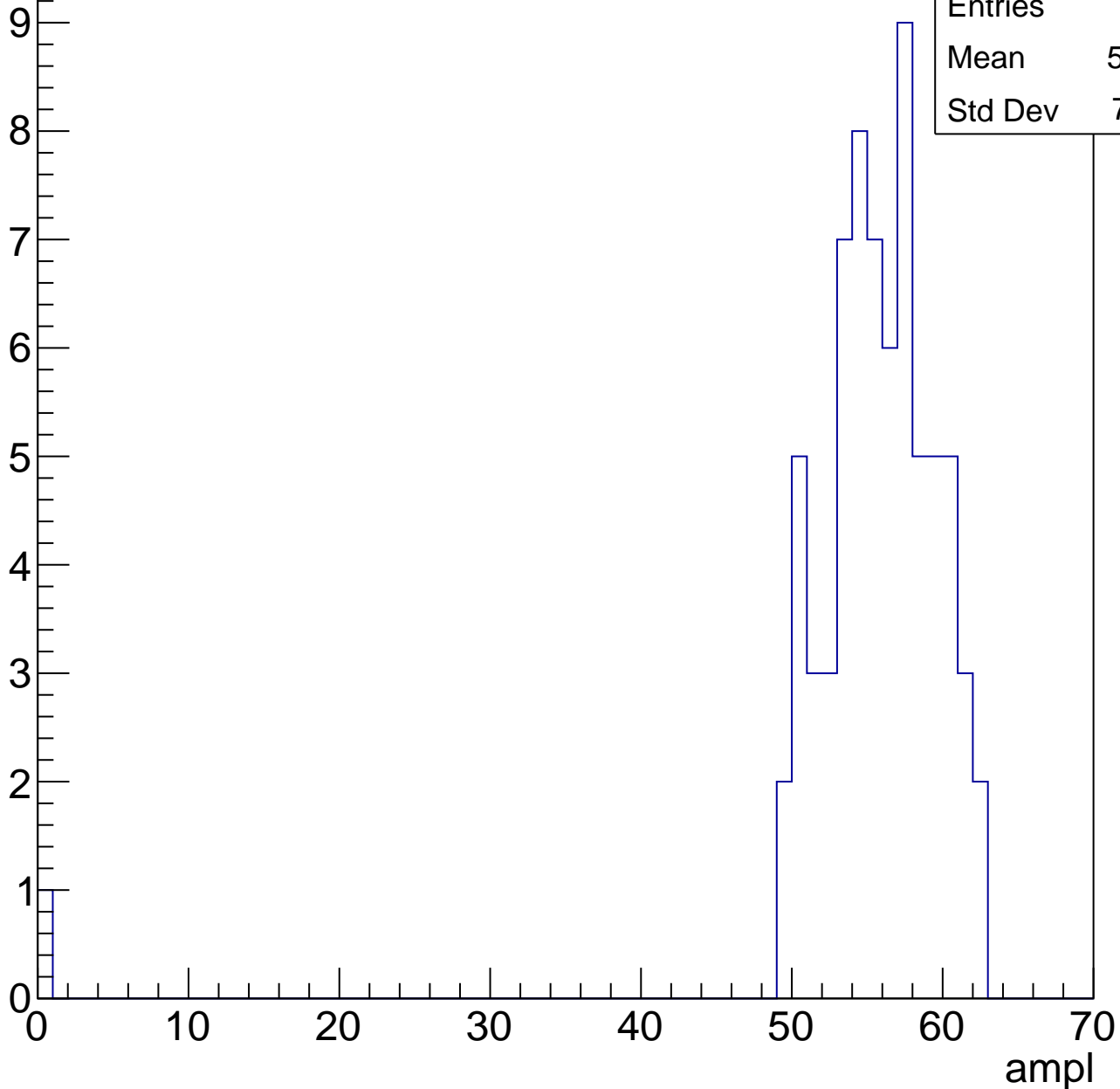


B0L001S, U11-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	54.73
Std Dev	7.341

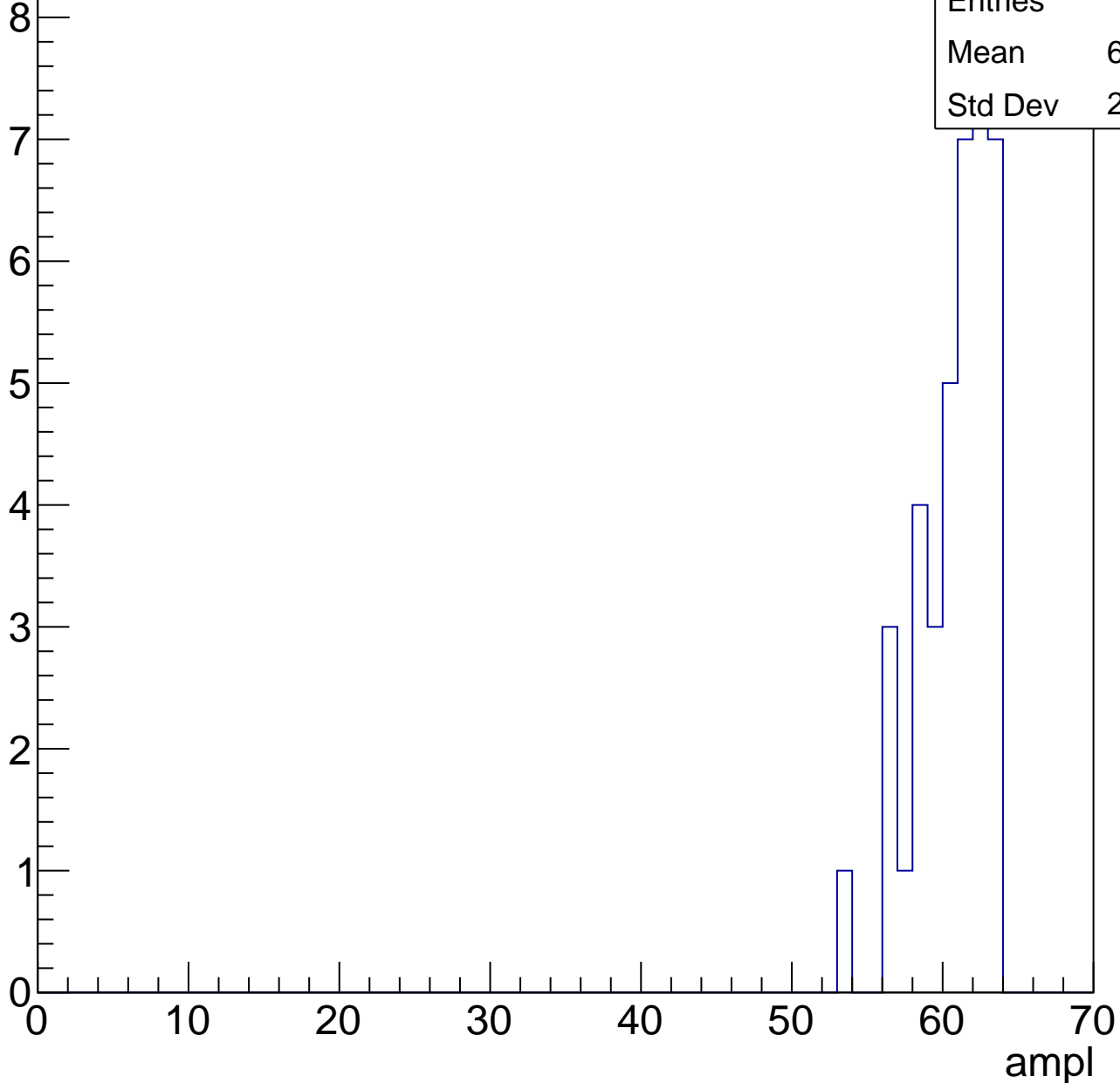


B0L001S, U11-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

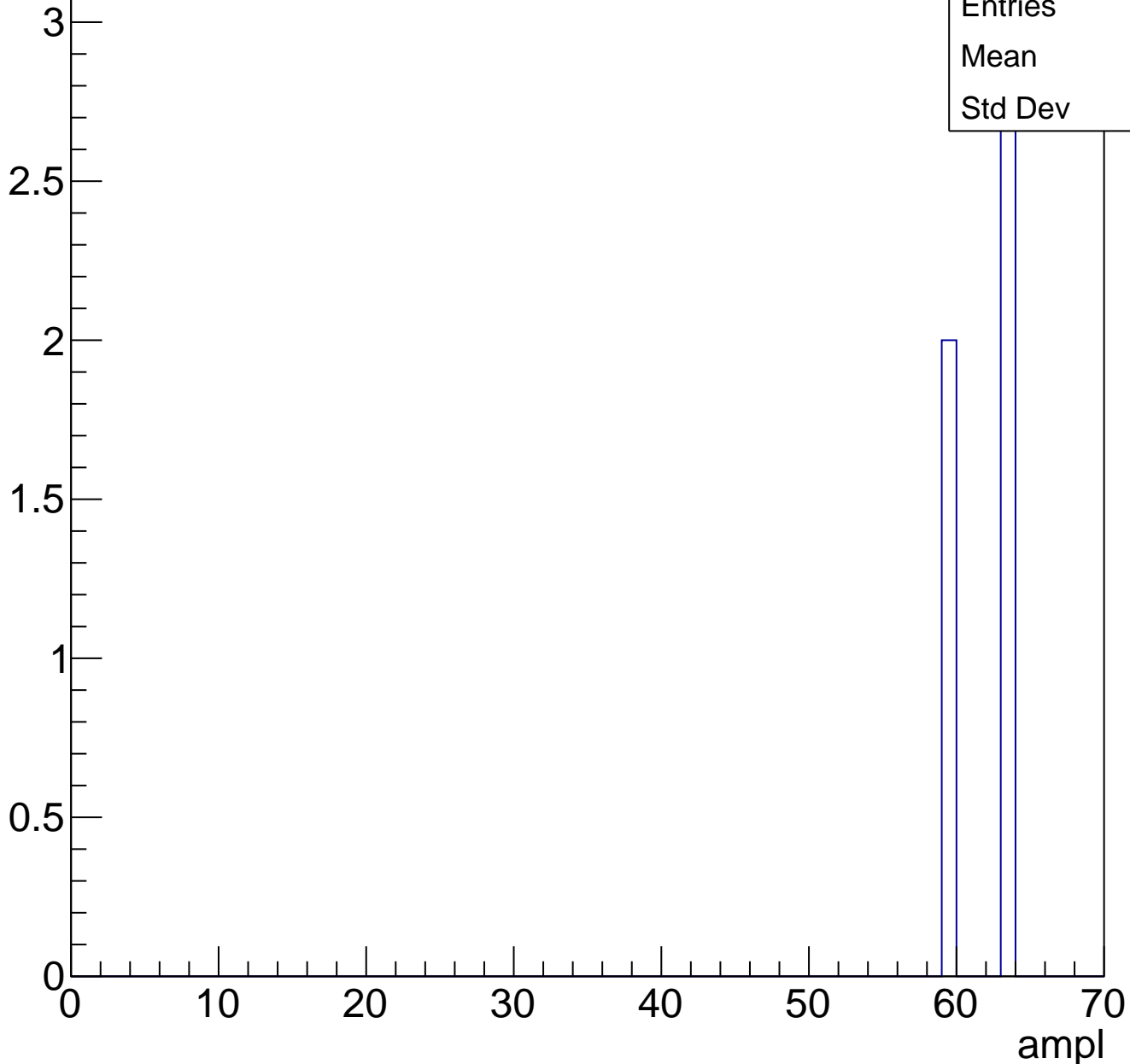
Entries	39
Mean	60.28
Std Dev	2.396



B0L001S, U11-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U11-ch123, adc0

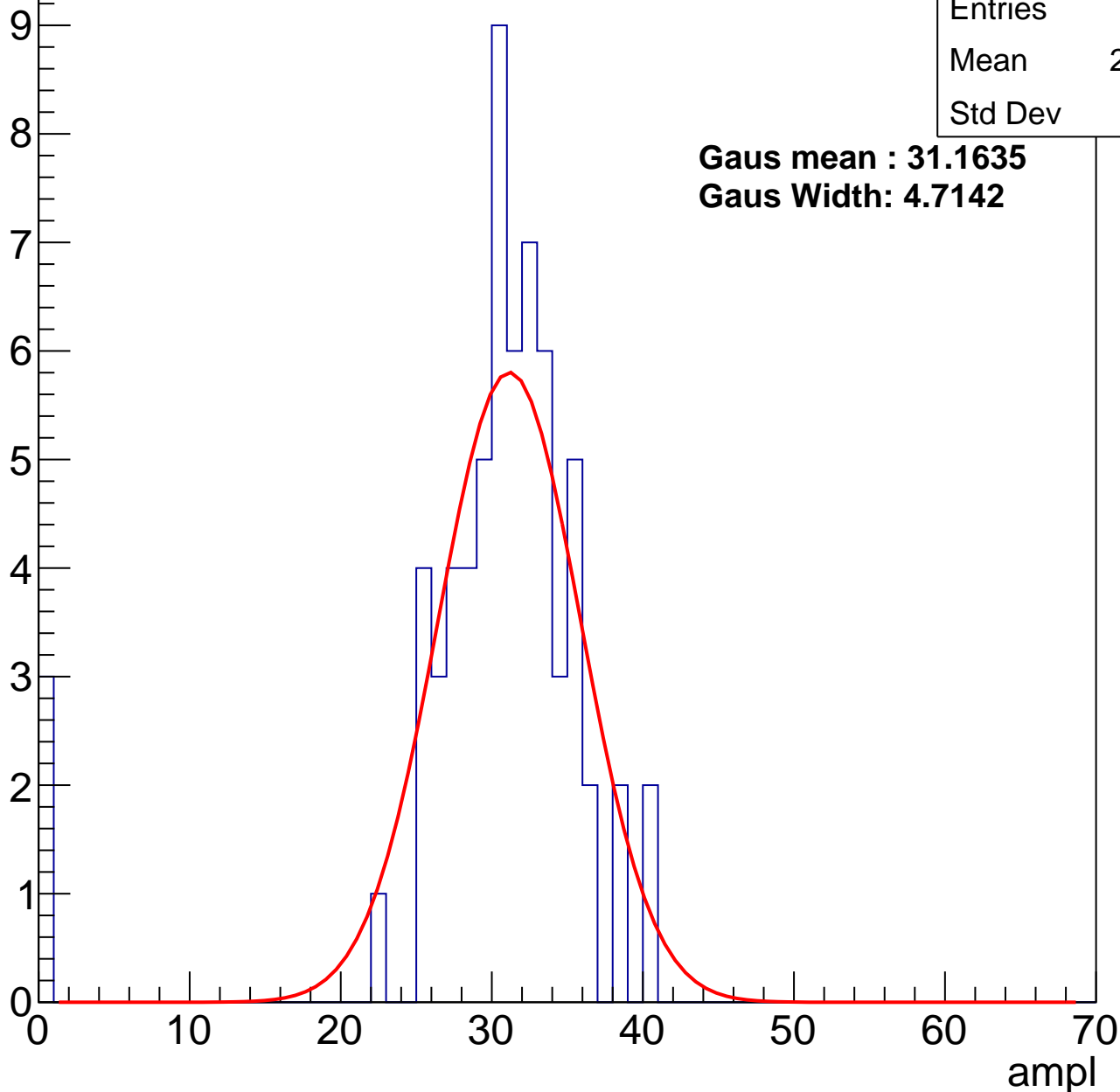
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.52
Std Dev	7.4

Gaus mean : 31.1635

Gaus Width: 4.7142



B0L001S, U11-ch123, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	38.44
Std Dev	3.64

Gaus mean : 38.9516

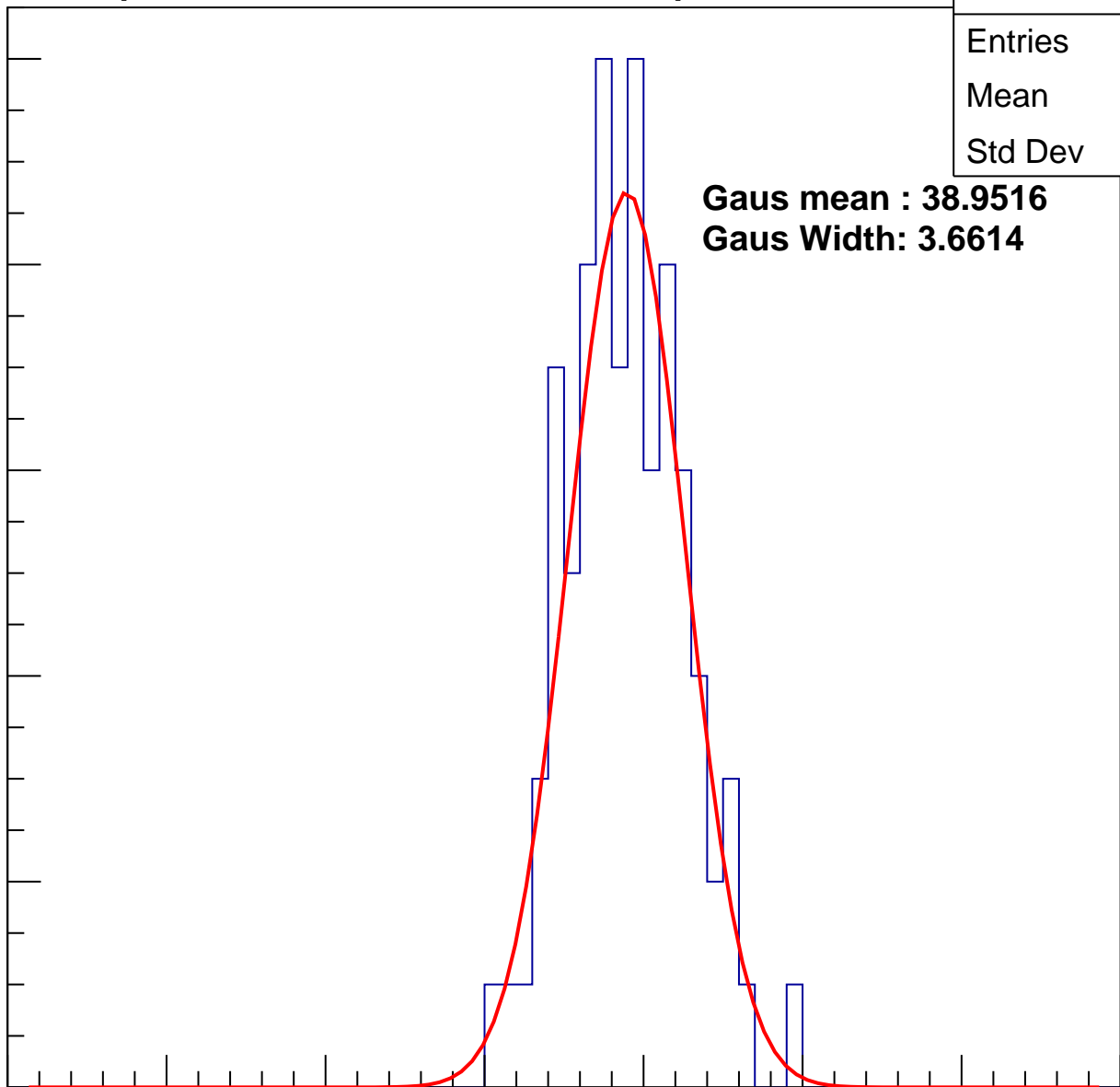
Gaus Width: 3.6614

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U11-ch123, adc2

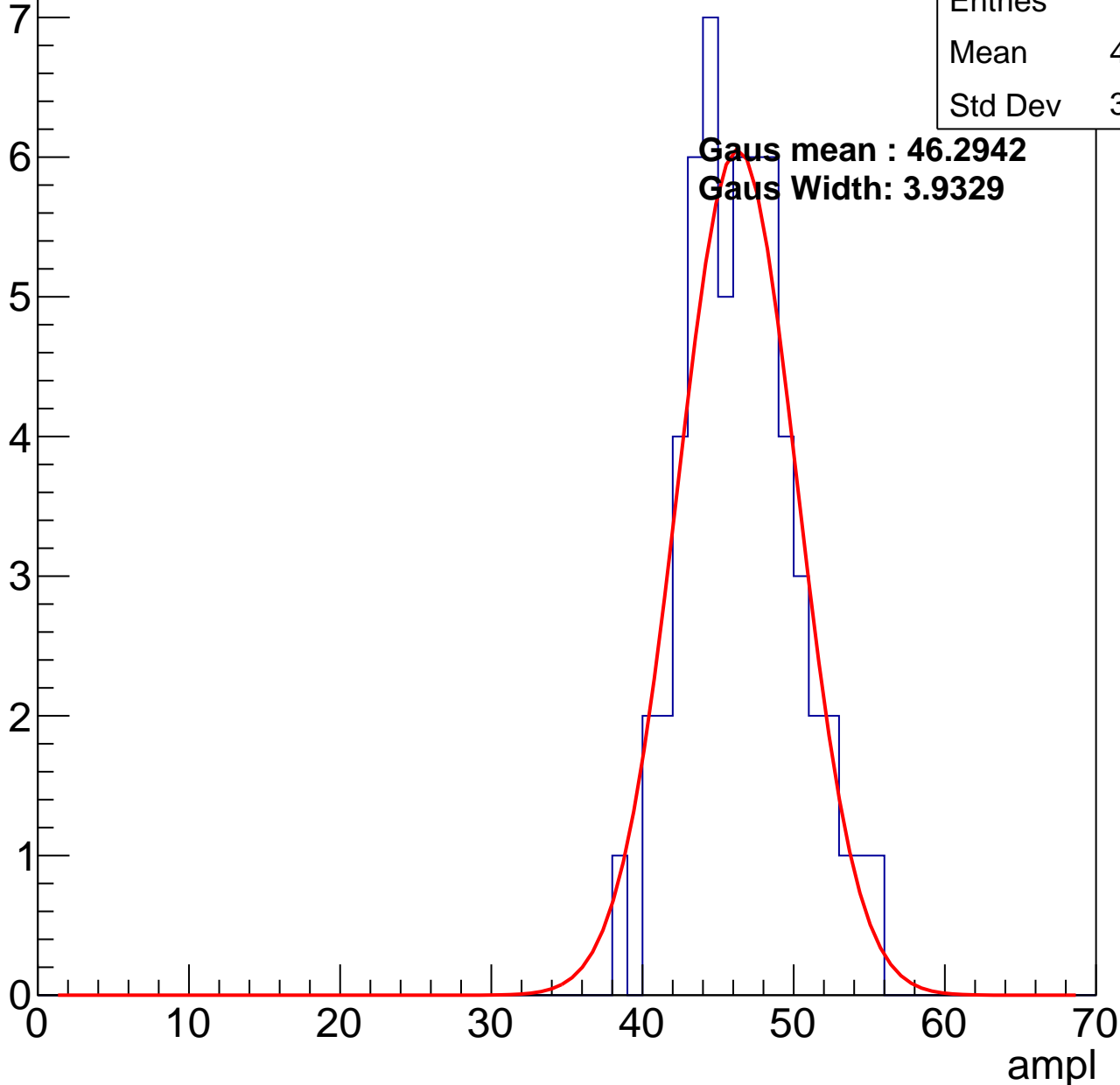
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	46.08
Std Dev	3.605

Gaus mean : 46.2942

Gaus Width: 3.9329

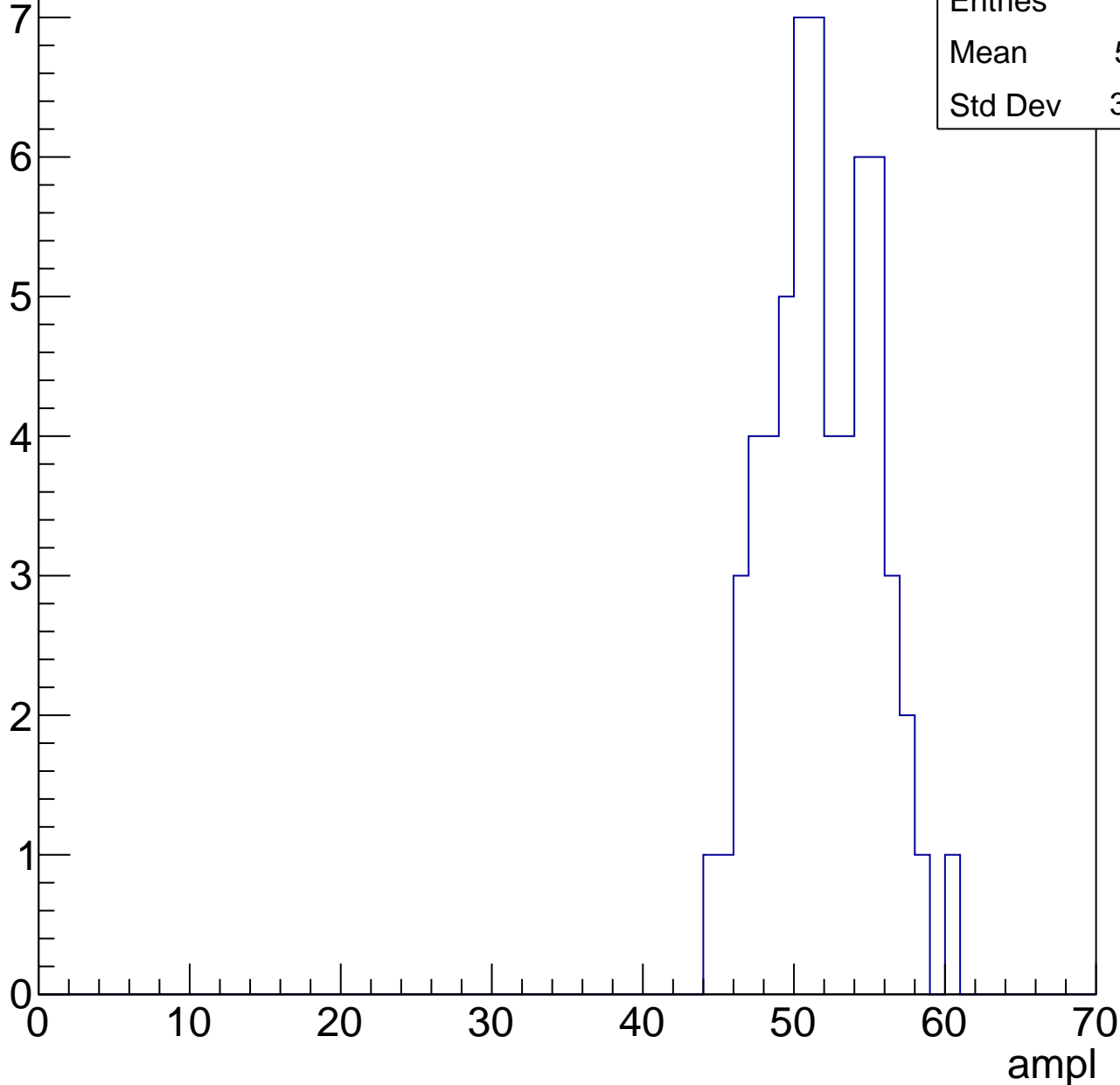


B0L001S, U11-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	51.41
Std Dev	3.504

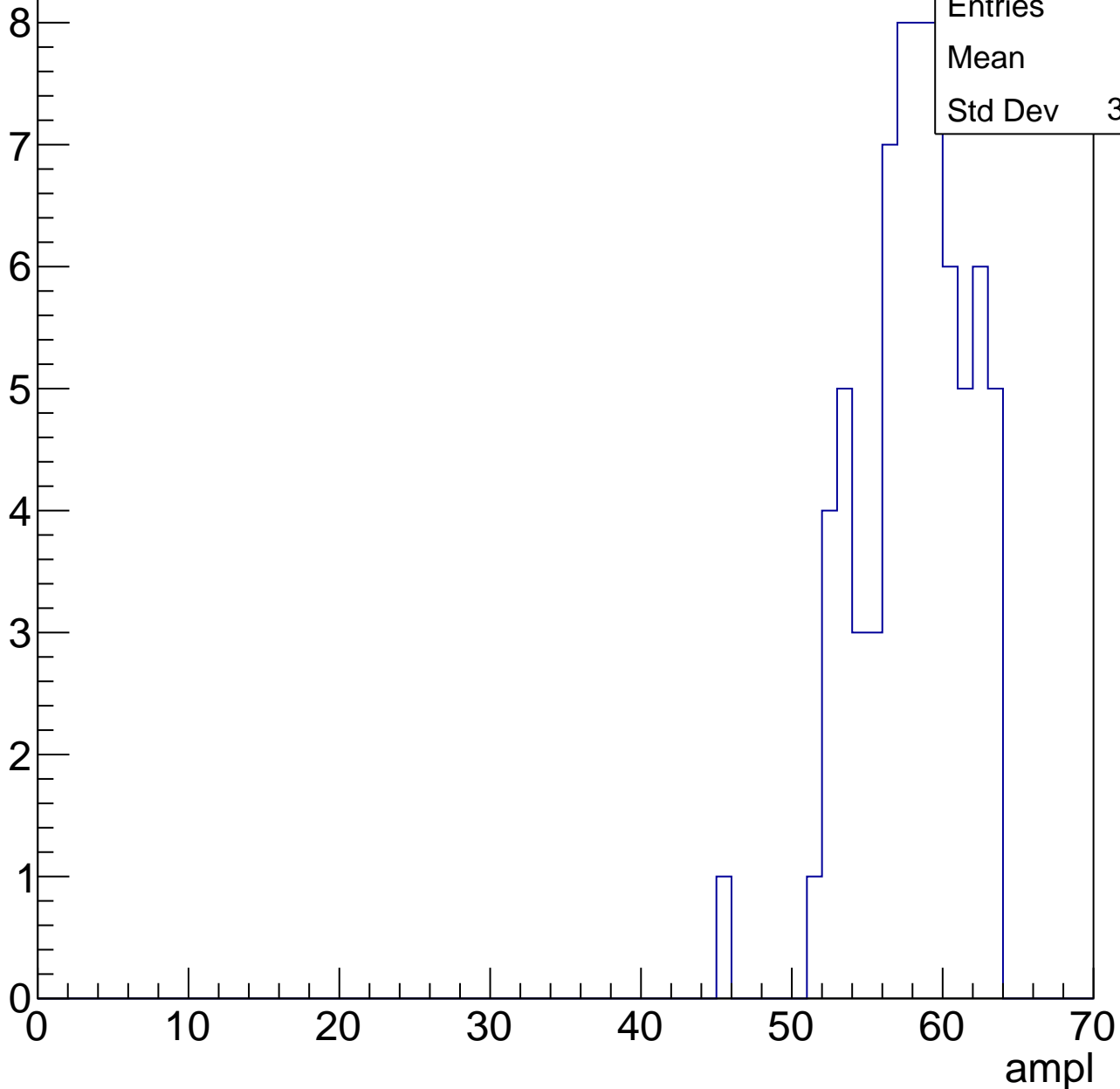


B0L001S, U11-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	57.6
Std Dev	3.555

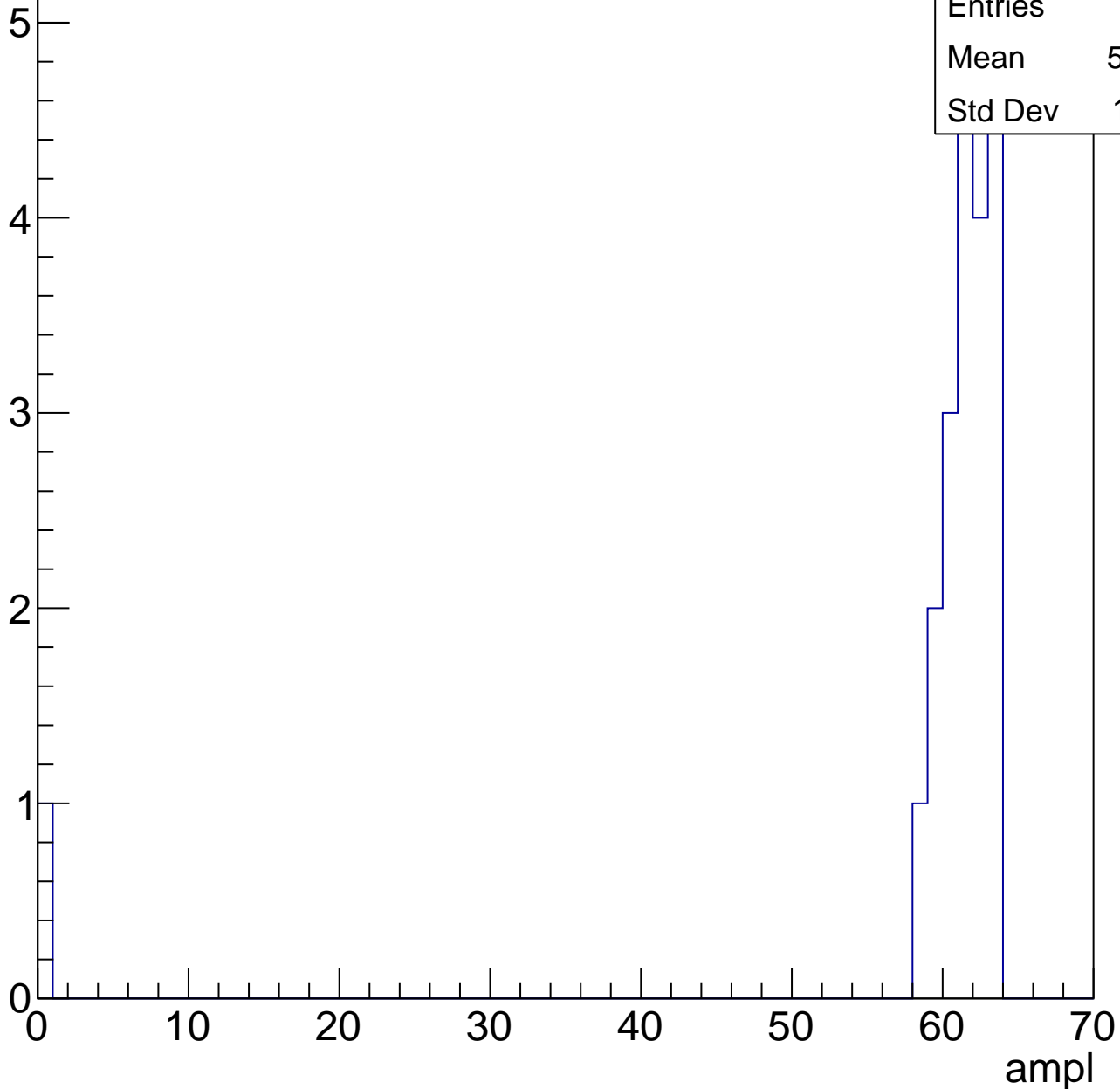


B0L001S, U11-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.29
Std Dev	13.11



B0L001S, U11-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch124, adc0

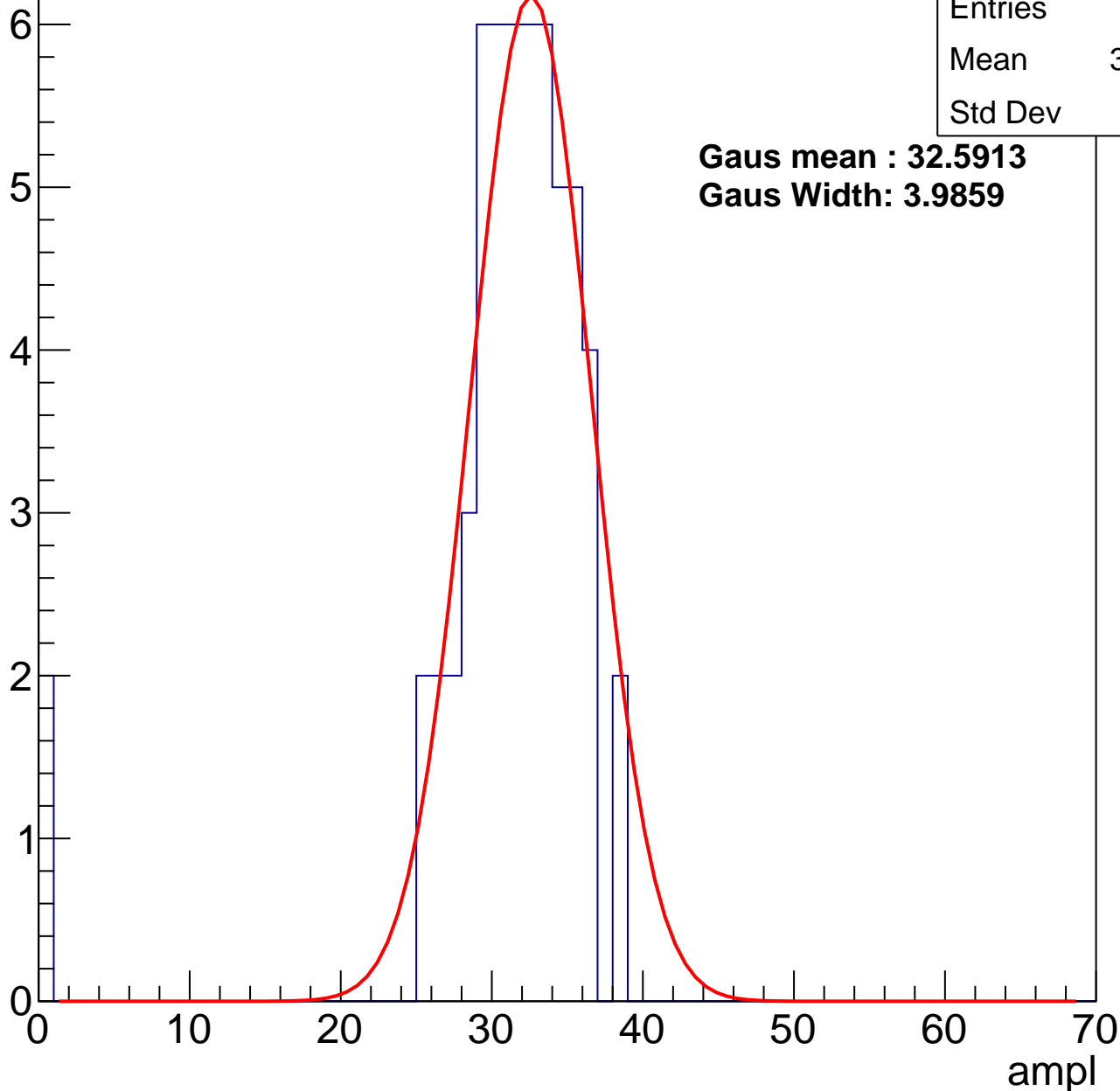
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.44
Std Dev	6.58

Gaus mean : 32.5913

Gaus Width: 3.9859



B0L001S, U11-ch124, adc1

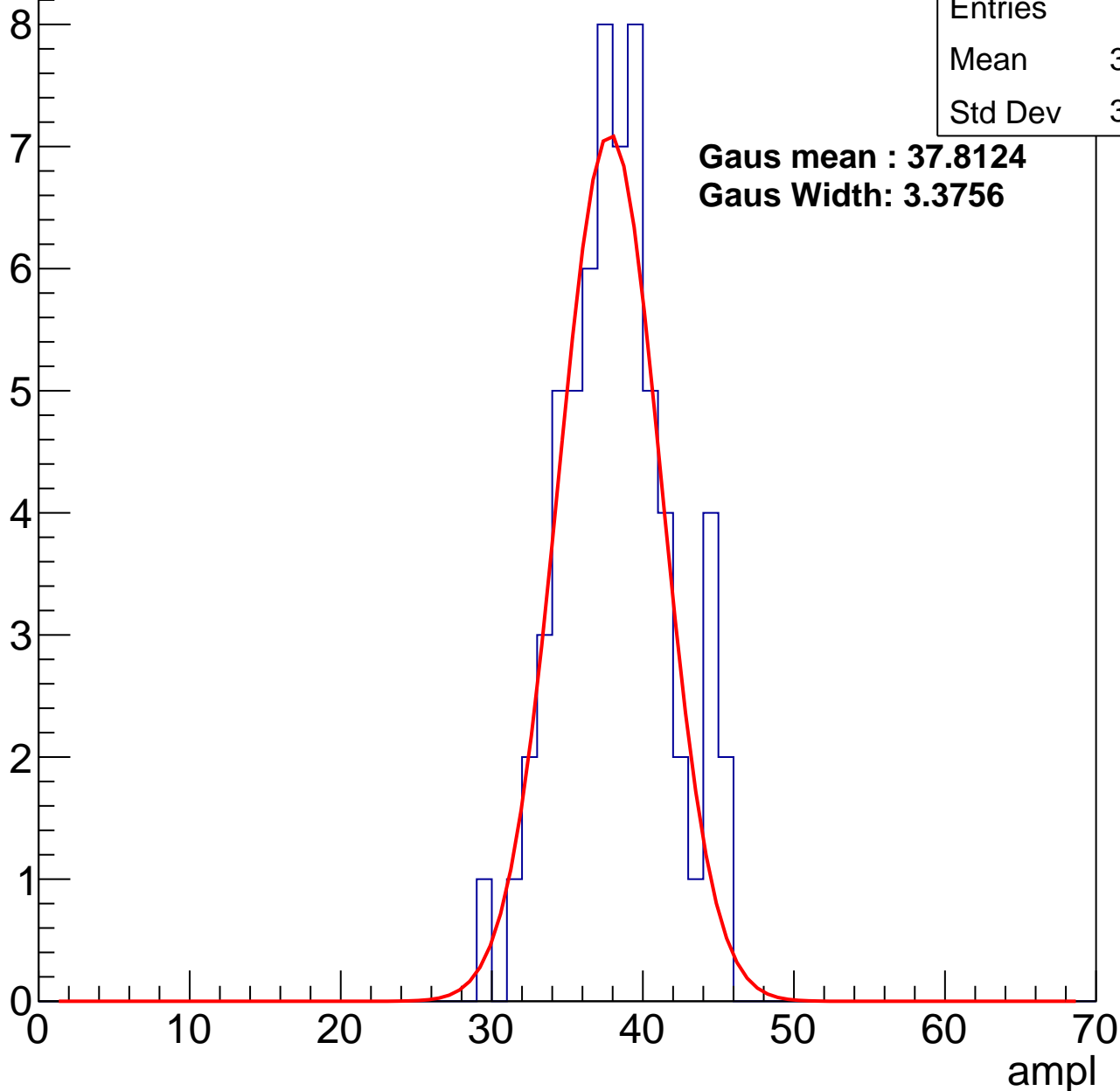
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.73
Std Dev	3.519

Gaus mean : 37.8124

Gaus Width: 3.3756



B0L001S, U11-ch124, adc2

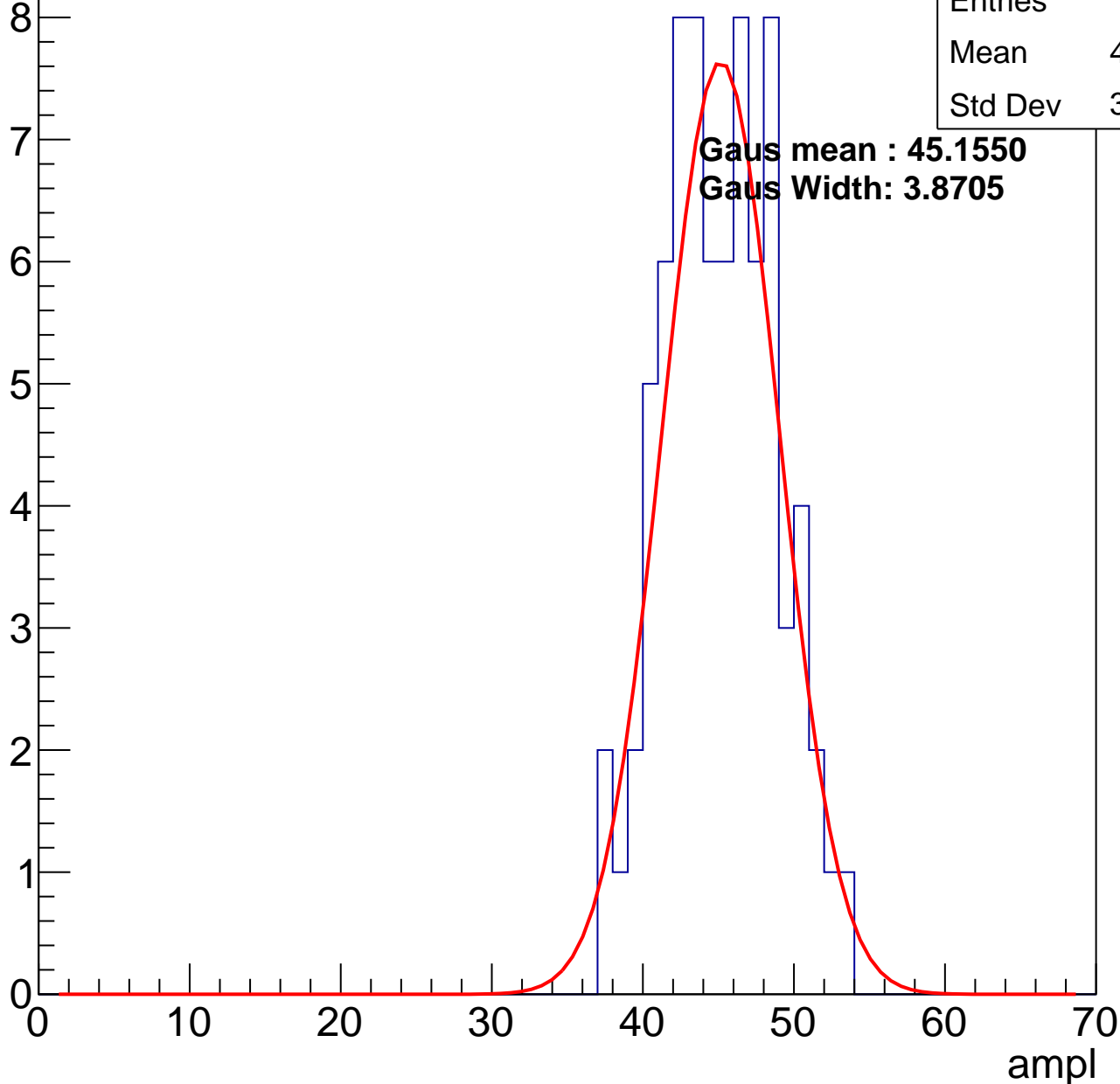
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.65
Std Dev	3.617

Gaus mean : 45.1550

Gaus Width: 3.8705

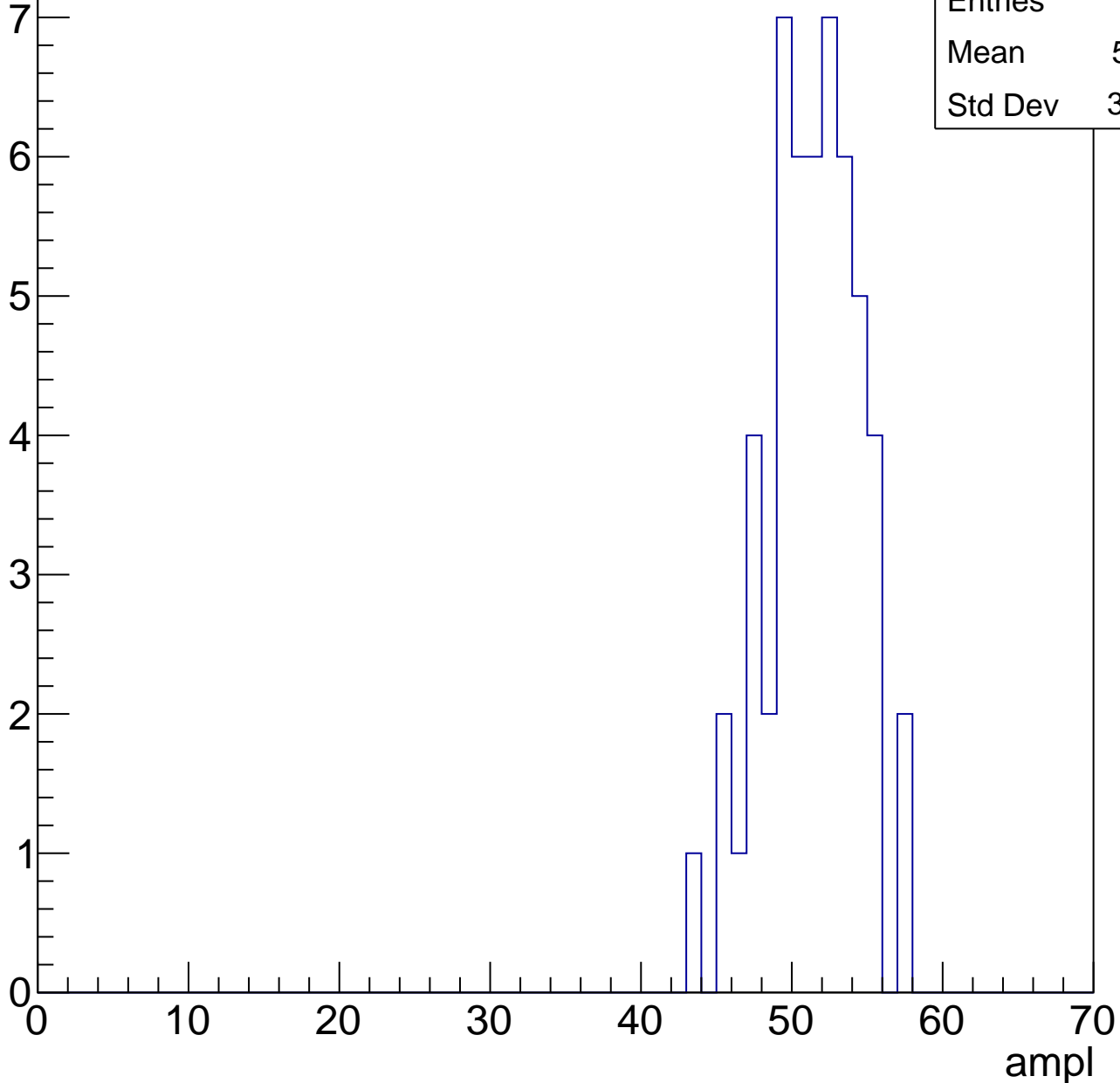


B0L001S, U11-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.91
Std Dev	3.036

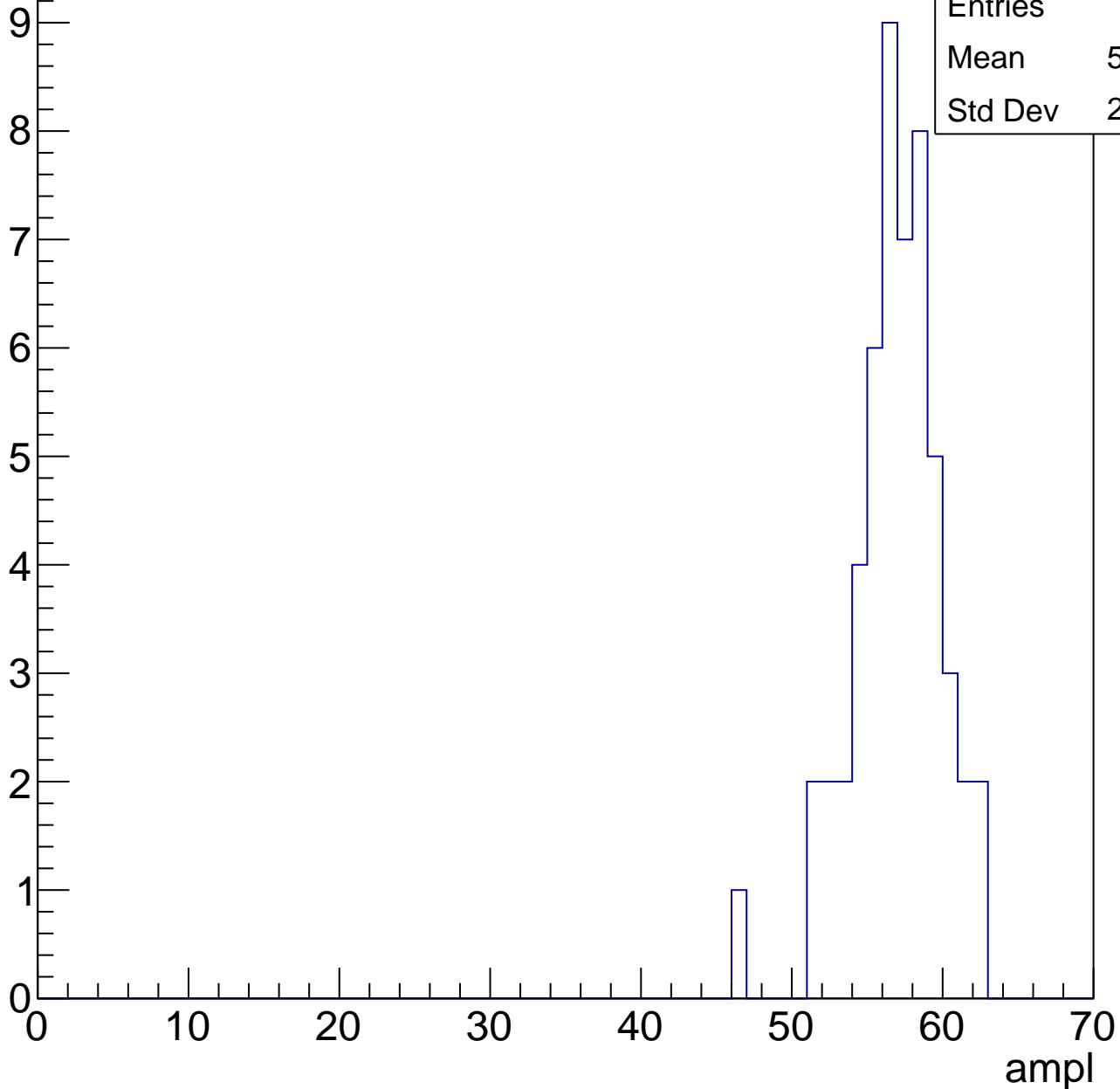


B0L001S, U11-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.45
Std Dev	2.962

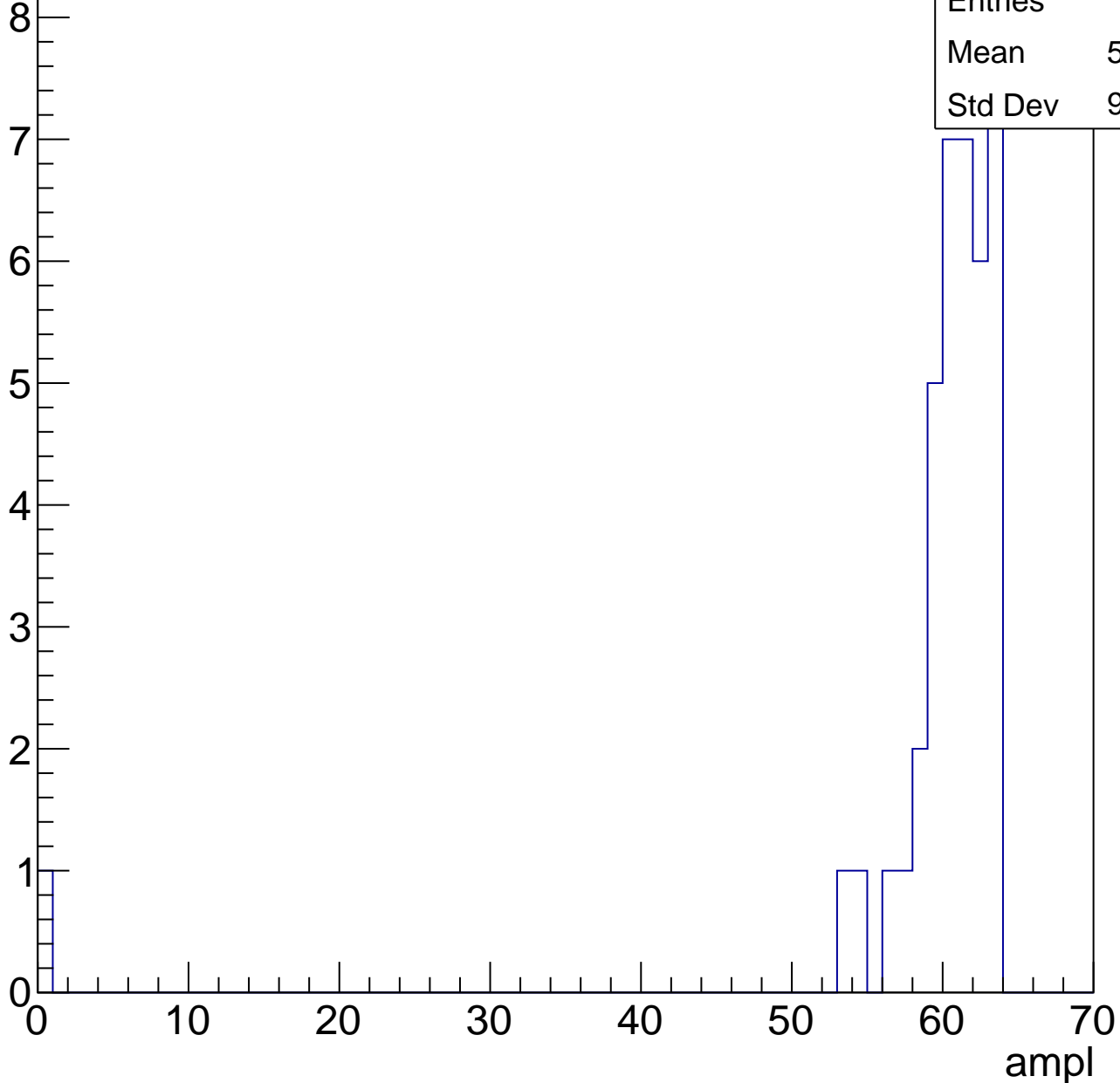


B0L001S, U11-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.85
Std Dev	9.712



B0L001S, U11-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

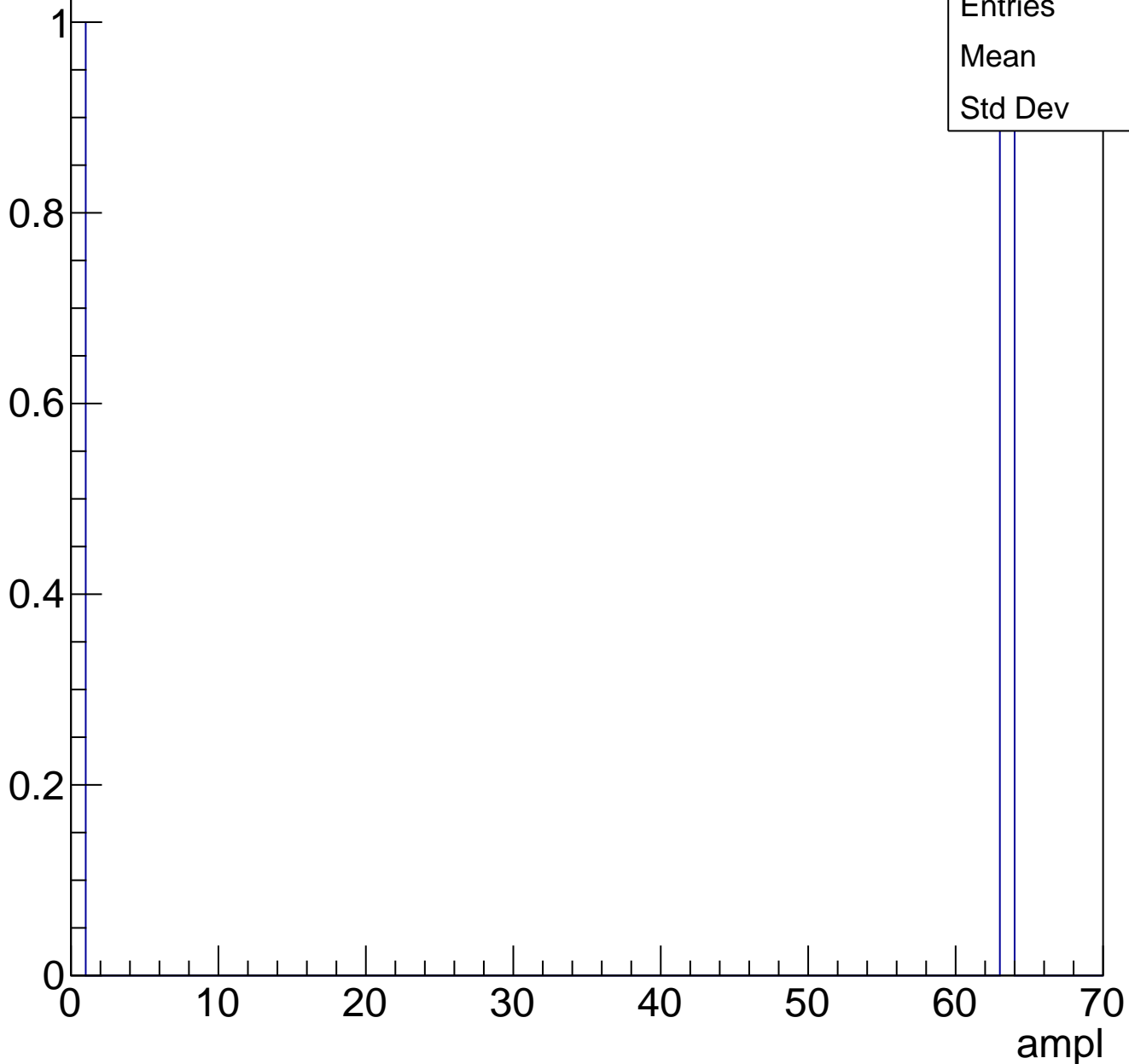


Entries	4
Mean	62
Std Dev	0.7071

B0L001S, U11-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch125, adc0

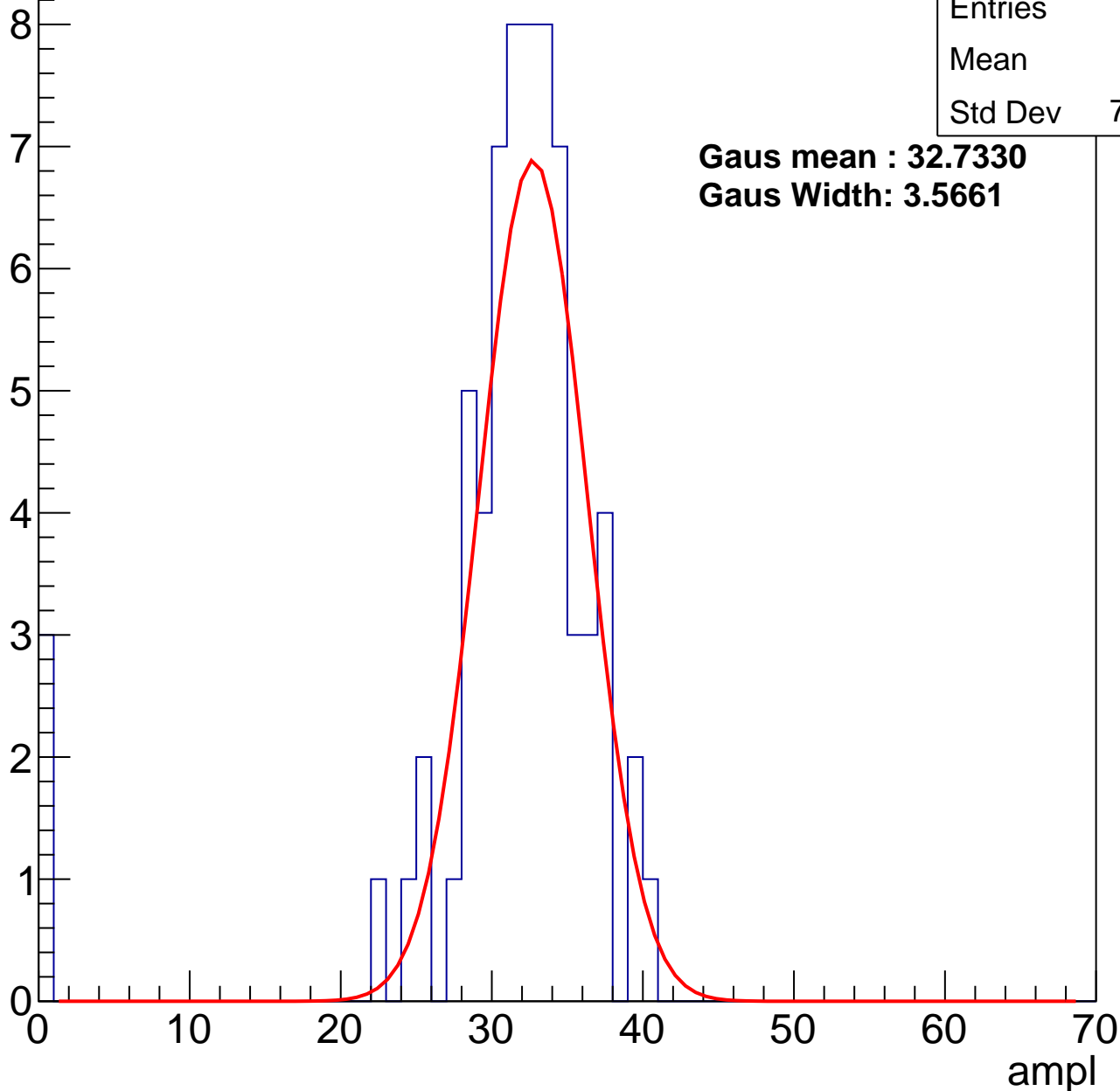
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.5
Std Dev	7.407

Gaus mean : 32.7330

Gaus Width: 3.5661



B0L001S, U11-ch125, adc1

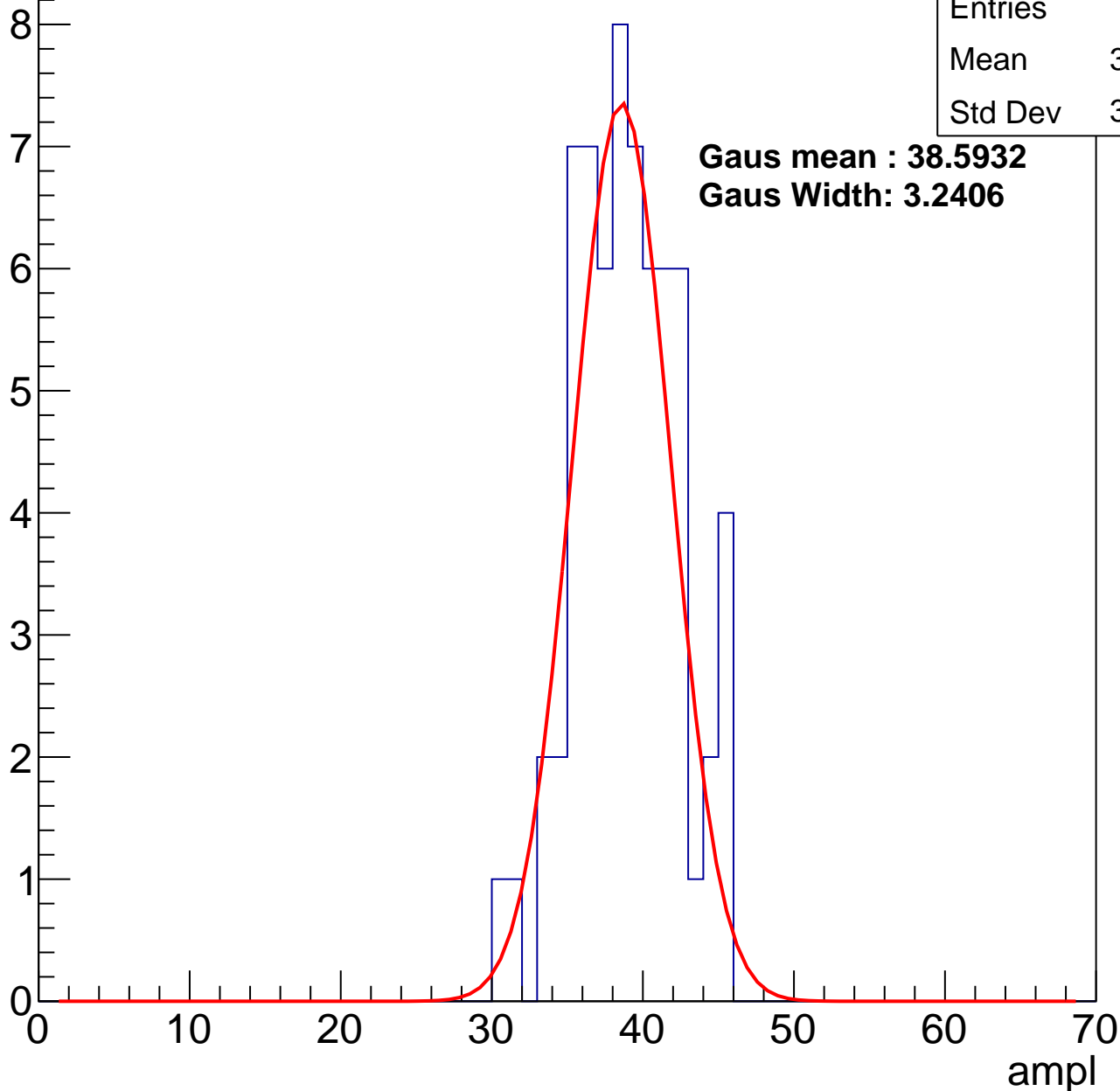
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.48
Std Dev	3.372

Gaus mean : 38.5932

Gaus Width: 3.2406



B0L001S, U11-ch125, adc2

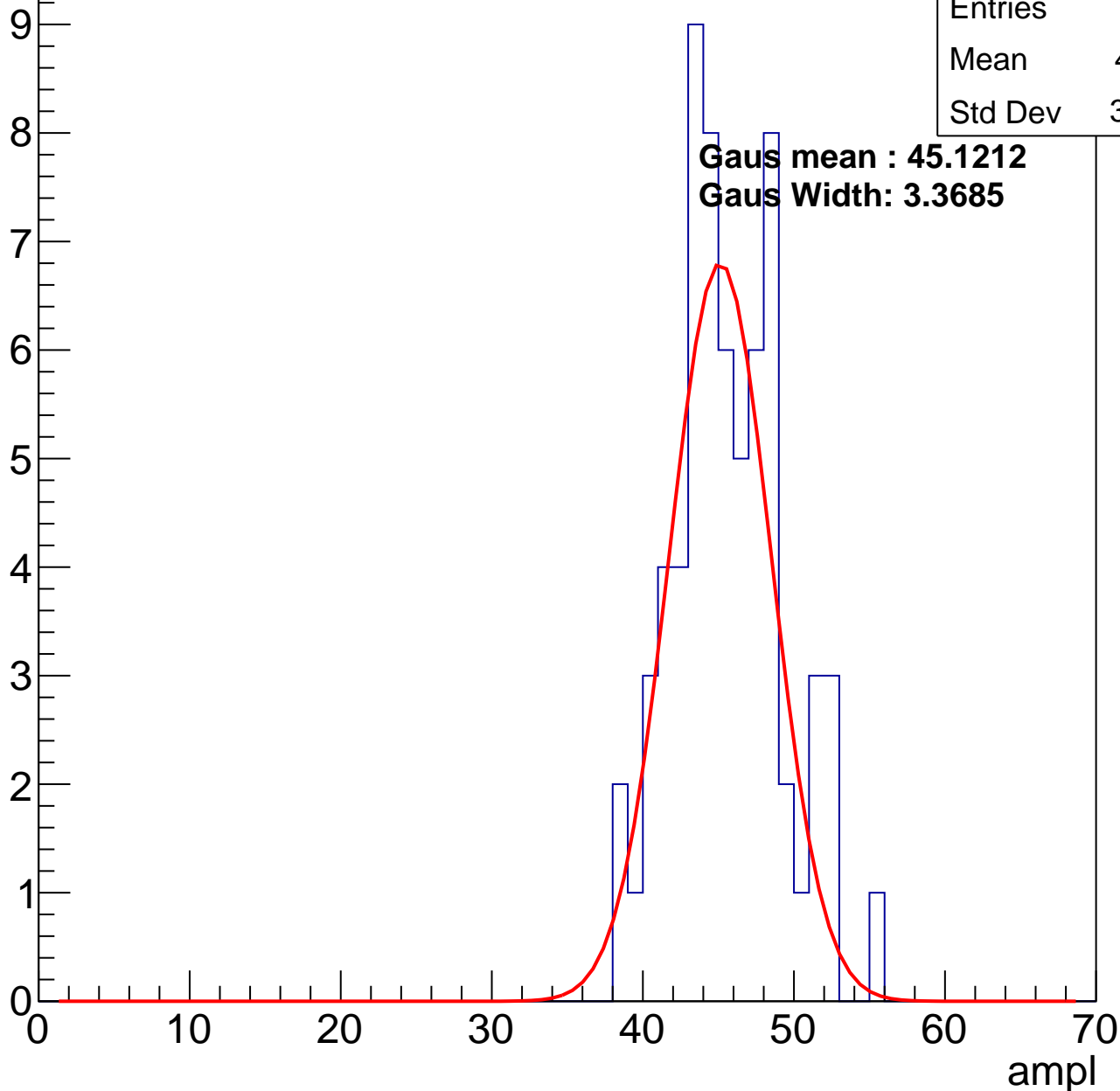
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.21
Std Dev	3.616

Gaus mean : 45.1212

Gaus Width: 3.3685

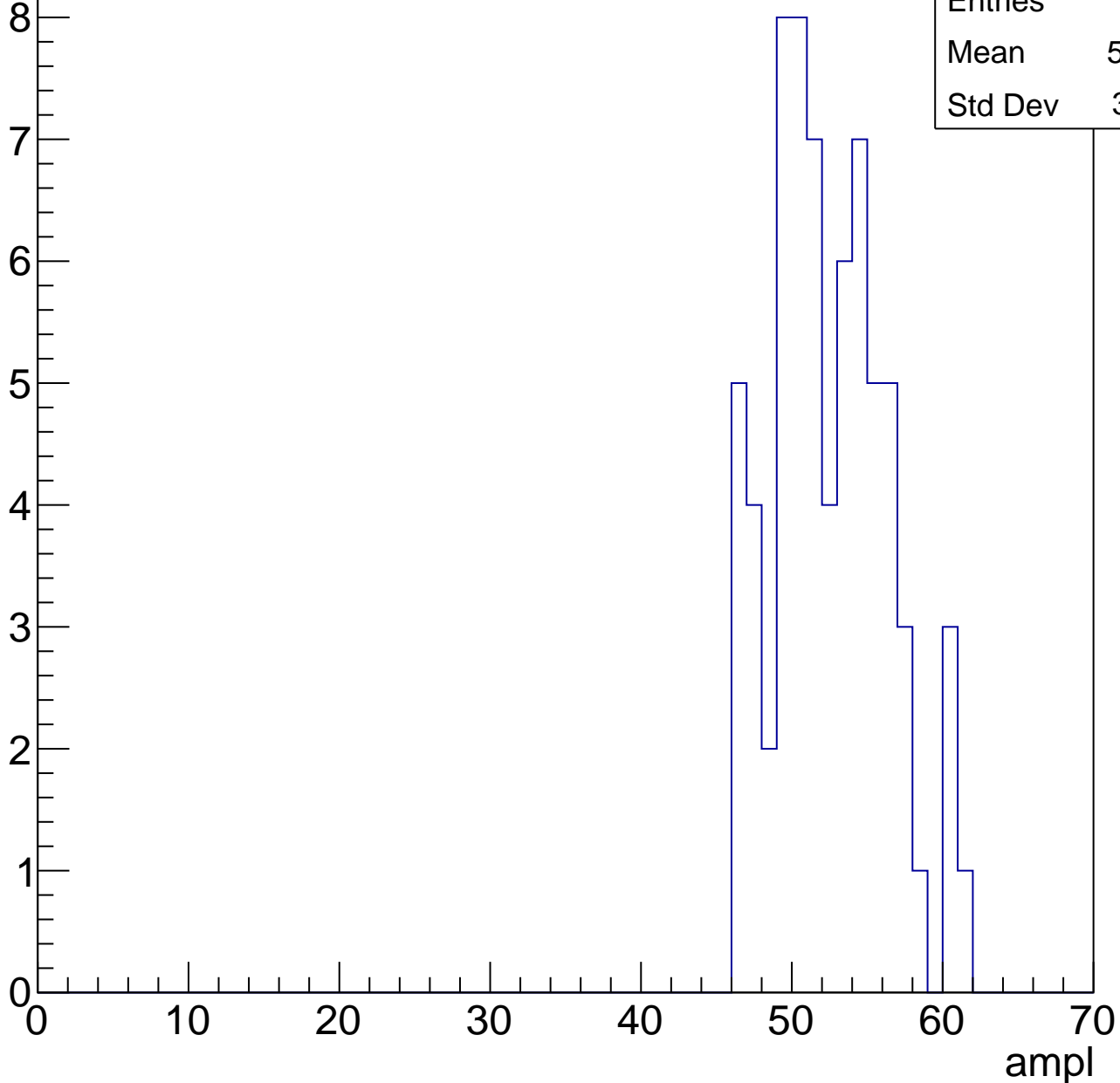


B0L001S, U11-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

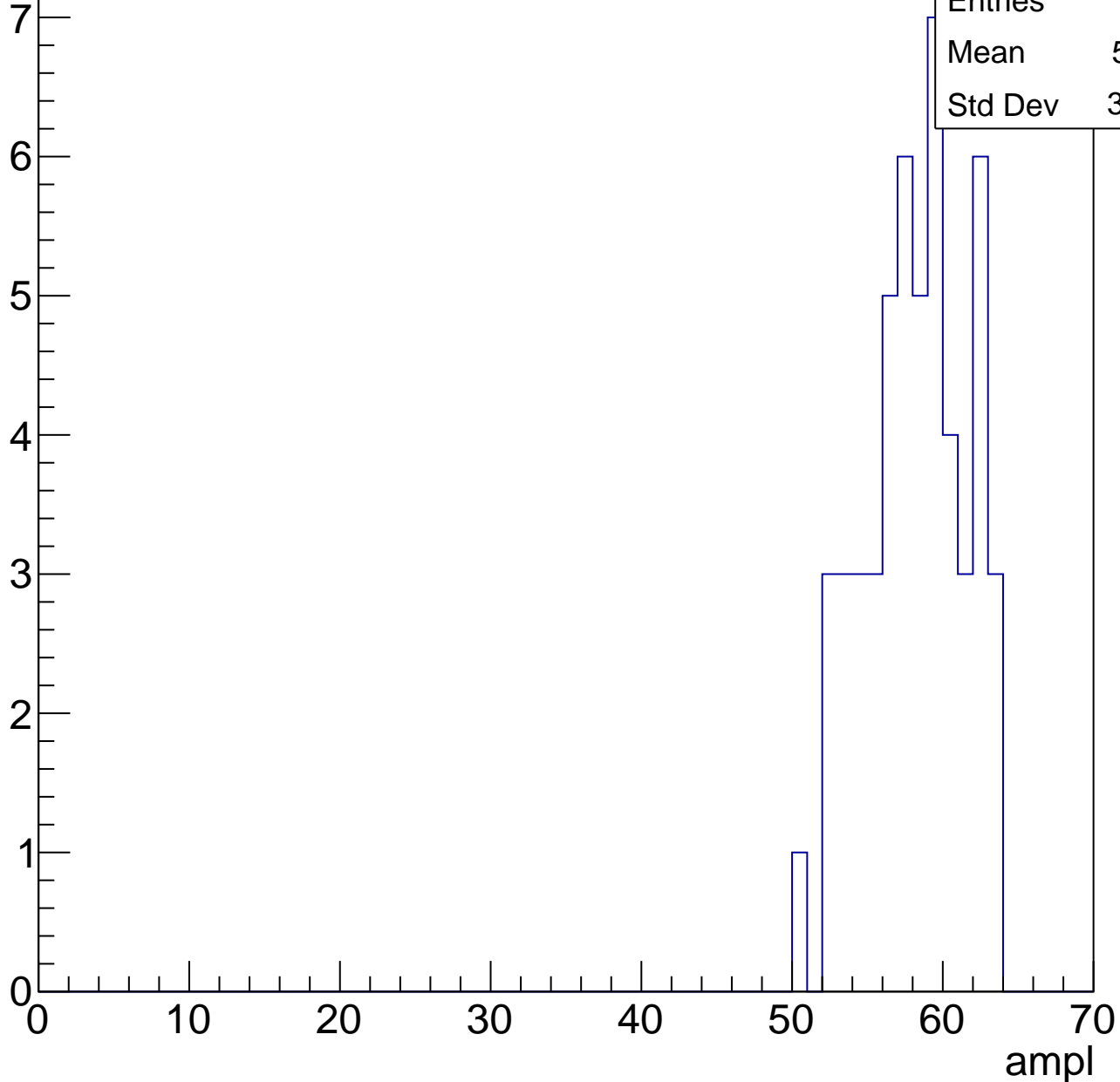
Entries	69
Mean	52.06
Std Dev	3.741



B0L001S, U11-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

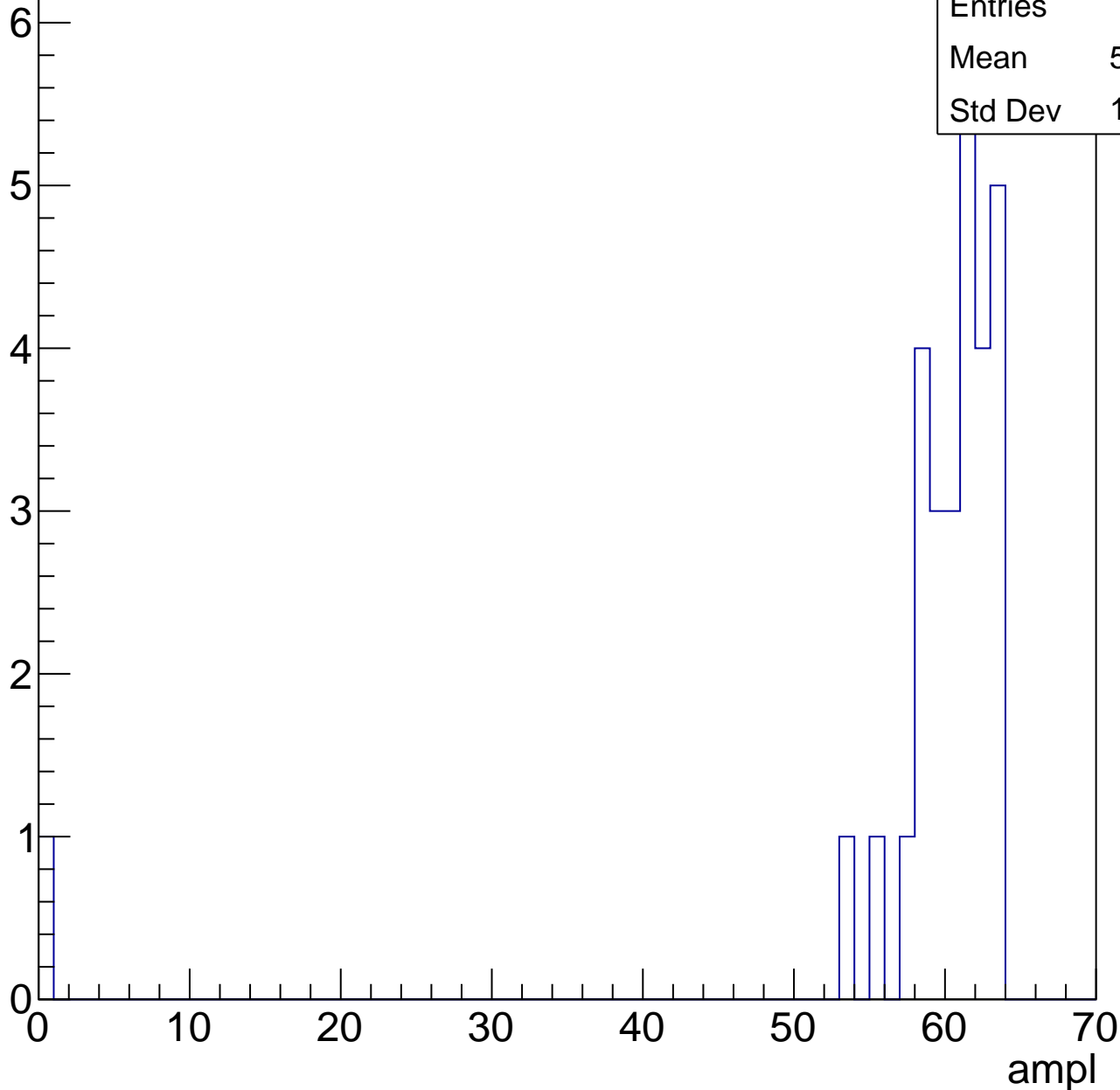


B0L001S, U11-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.03
Std Dev	11.23



B0L001S, U11-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch126, adc0

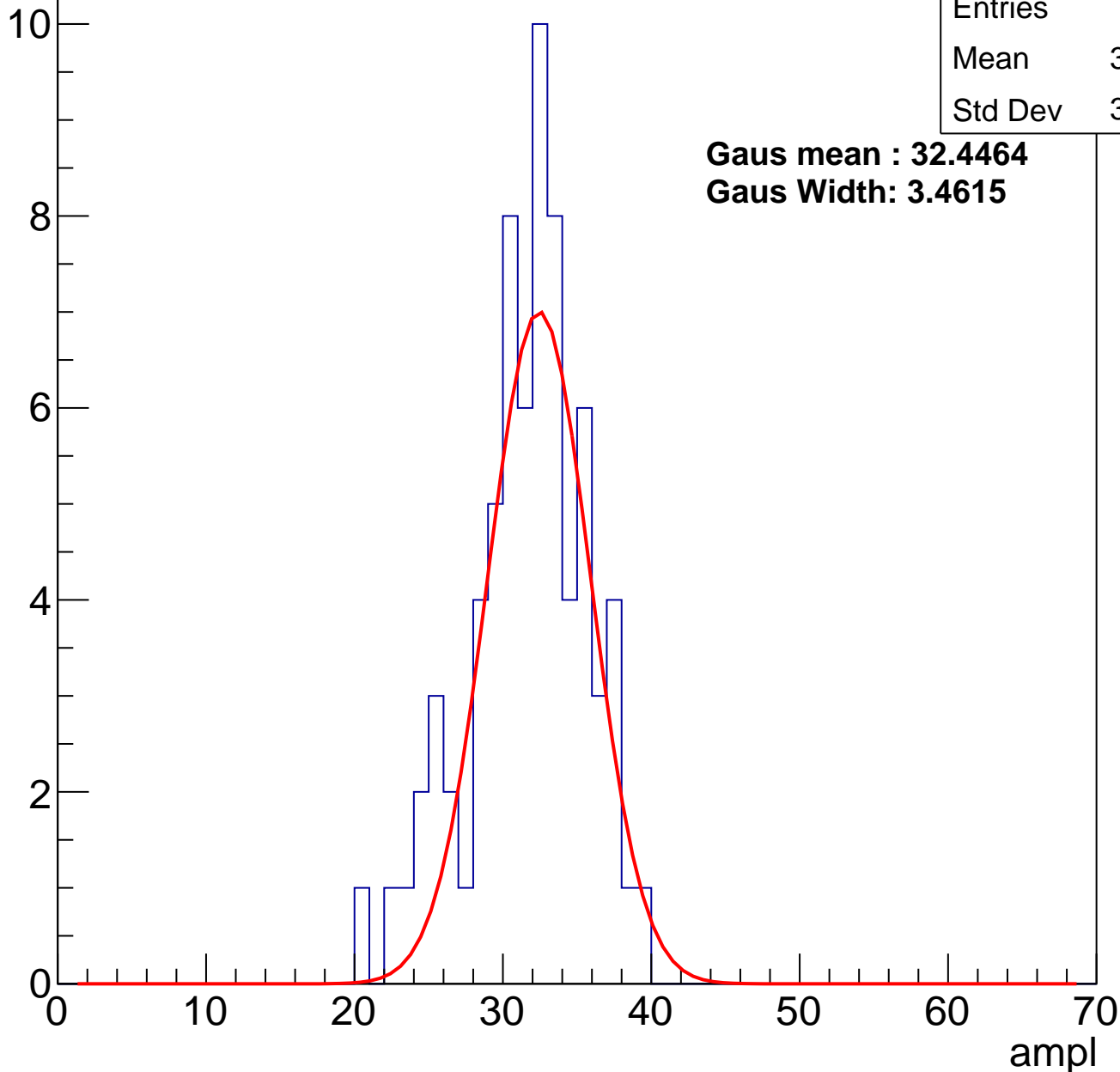
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	31.17
Std Dev	3.947

Gaus mean : 32.4464

Gaus Width: 3.4615

Entry



B0L001S, U11-ch126, adc1

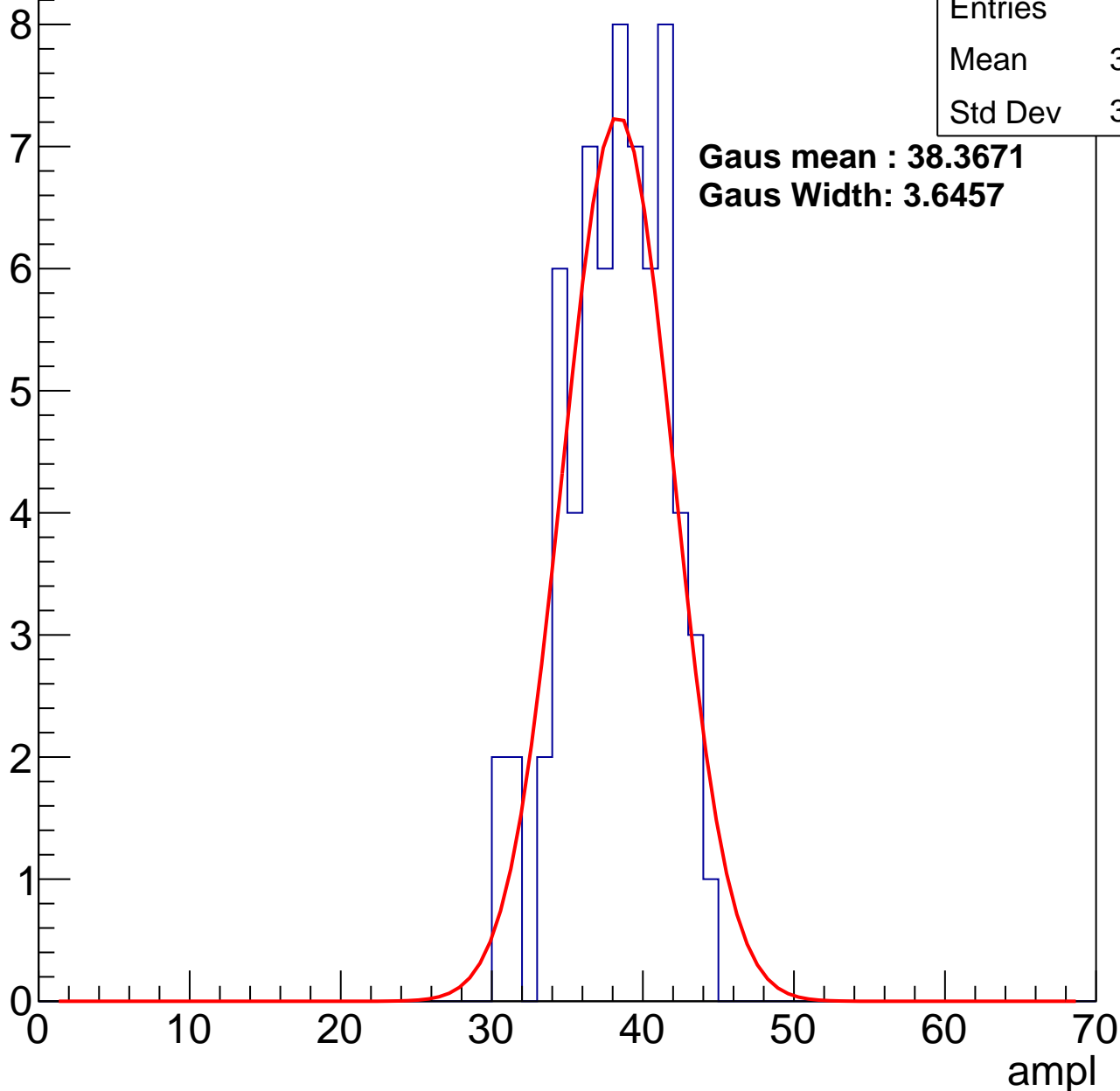
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.76
Std Dev	3.294

Gaus mean : 38.3671

Gaus Width: 3.6457



B0L001S, U11-ch126, adc2

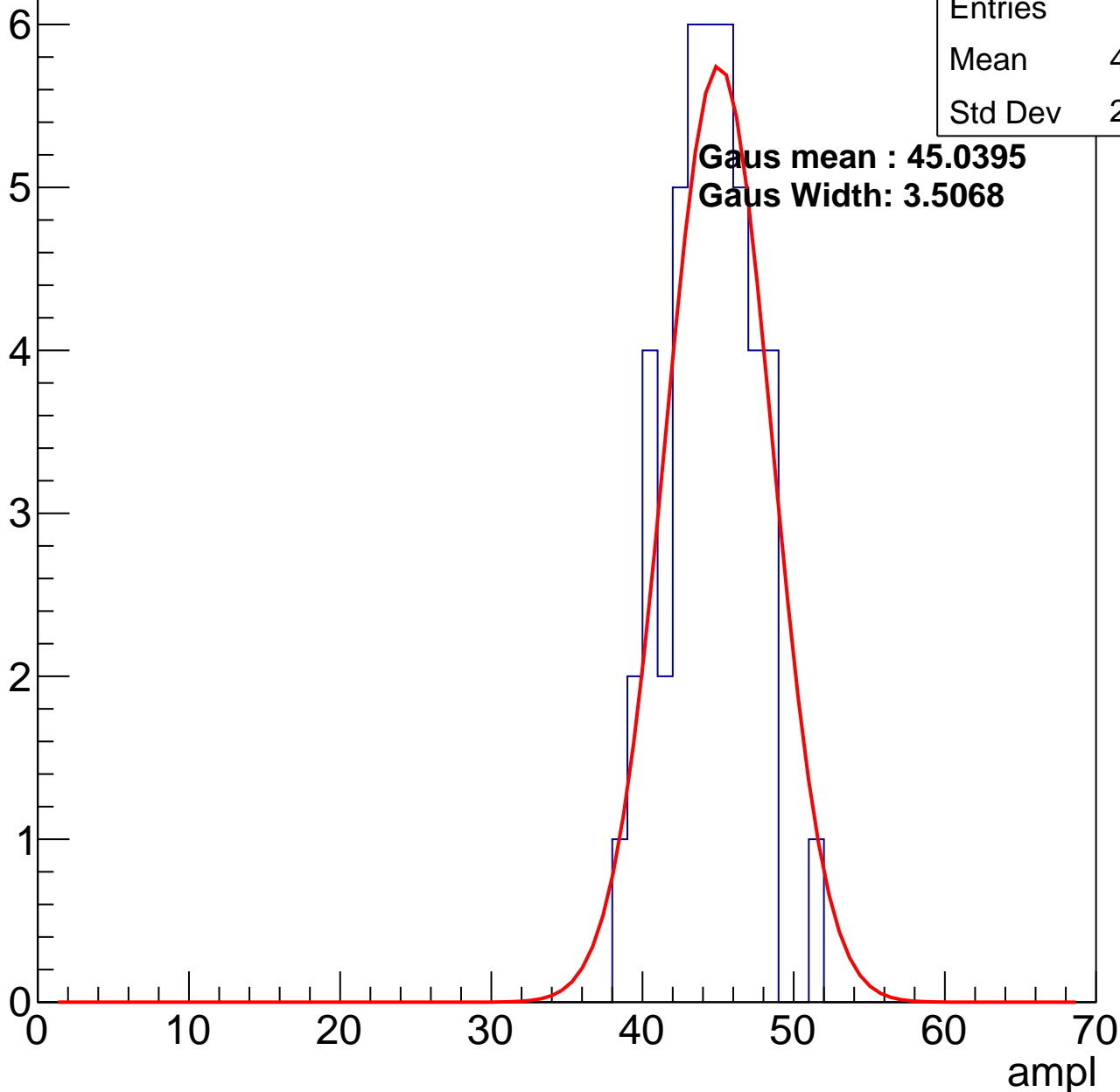
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	43.93
Std Dev	2.832

Gaus mean : 45.0395

Gaus Width: 3.5068



B0L001S, U11-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

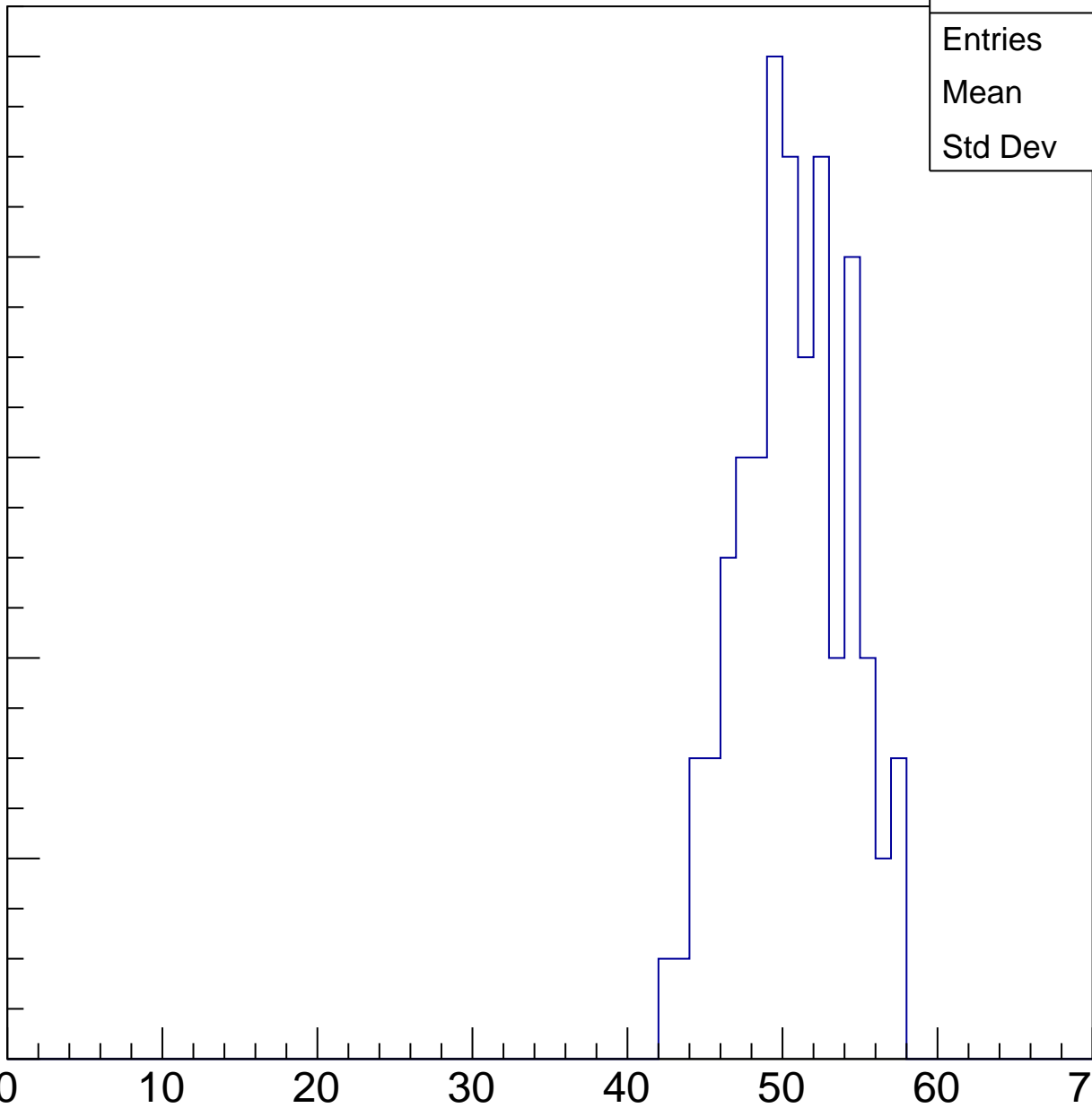
Entries	81
Mean	50.17
Std Dev	3.492

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

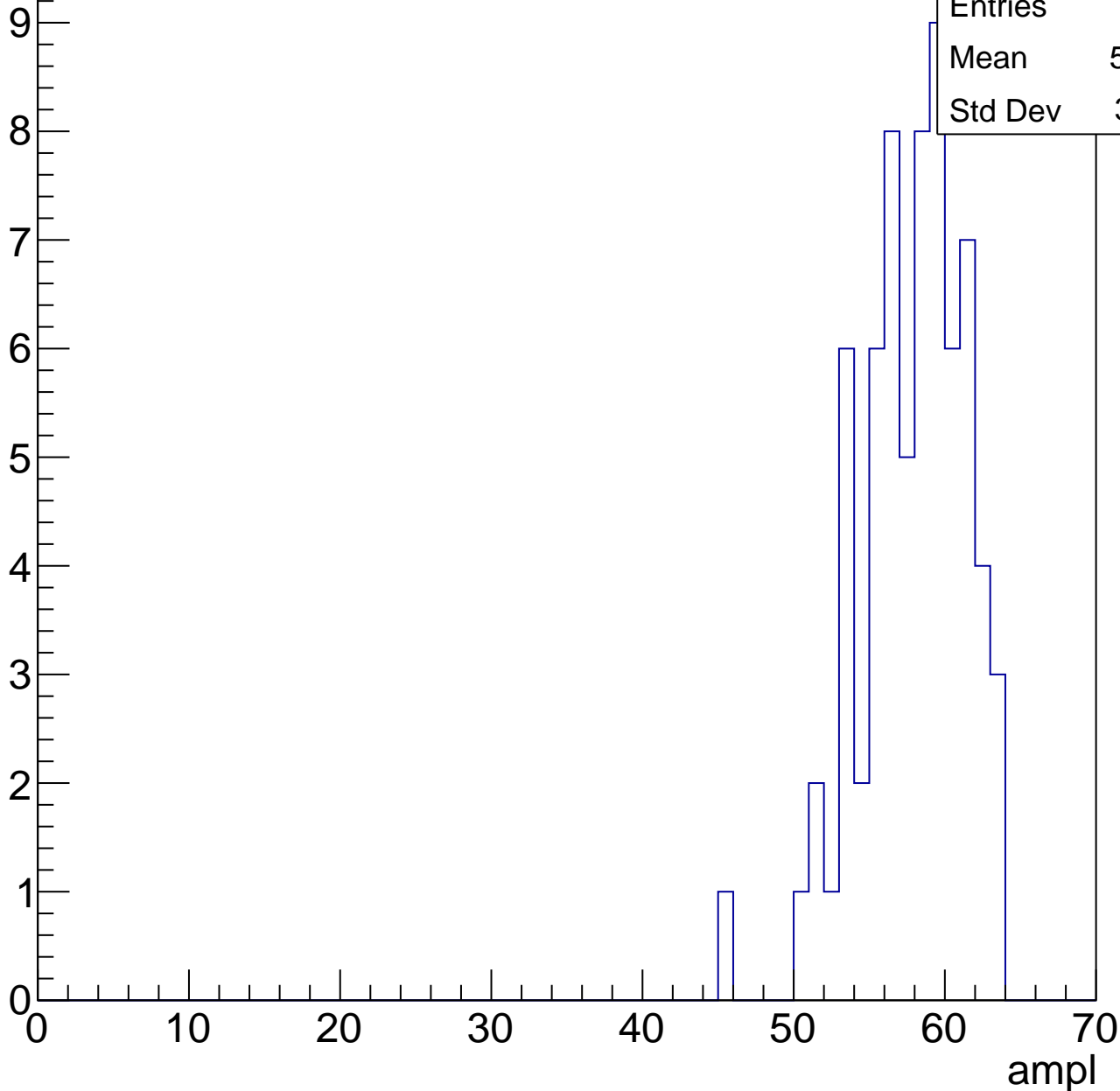


B0L001S, U11-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	57.35
Std Dev	3.501

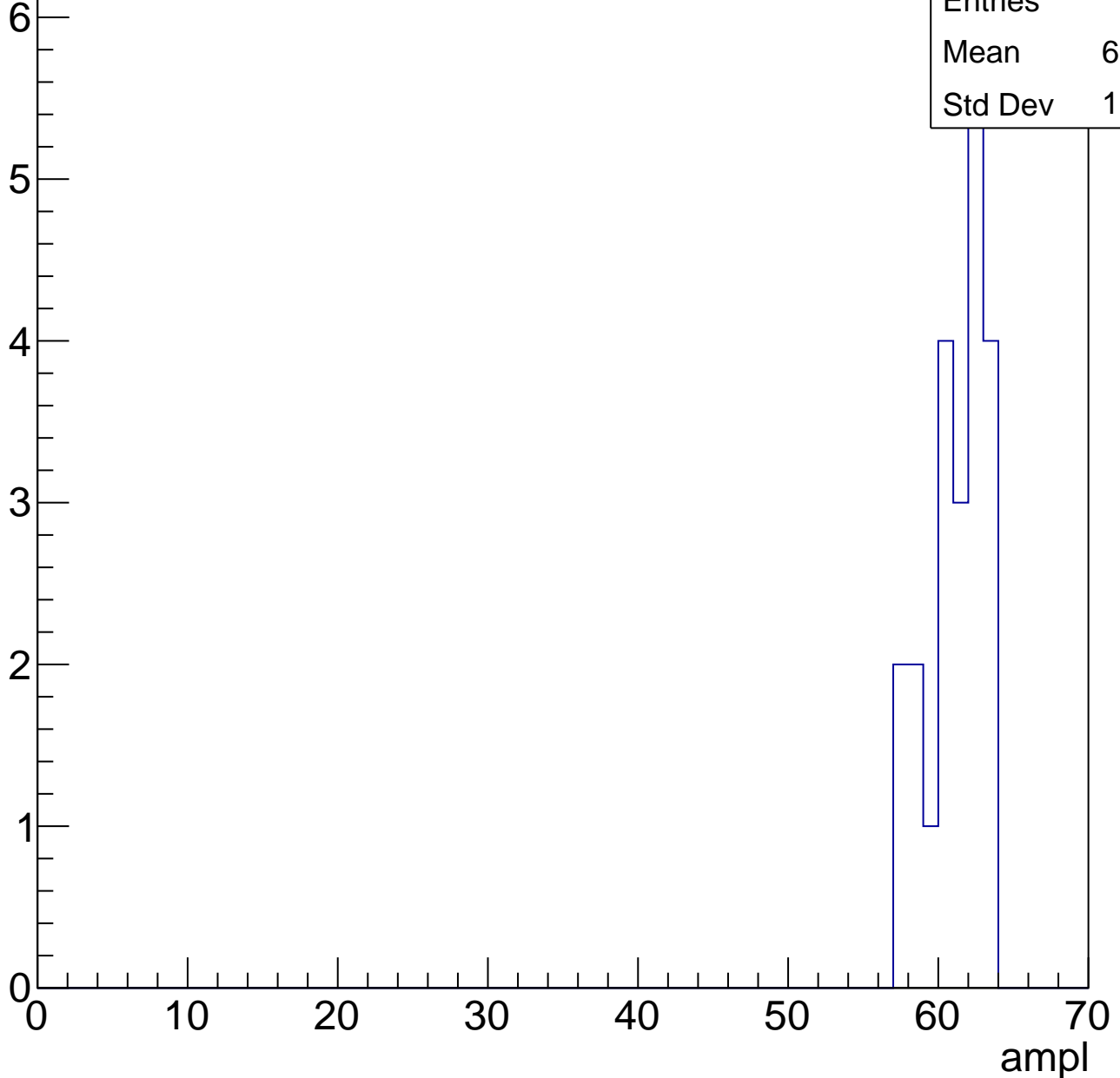


B0L001S, U11-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

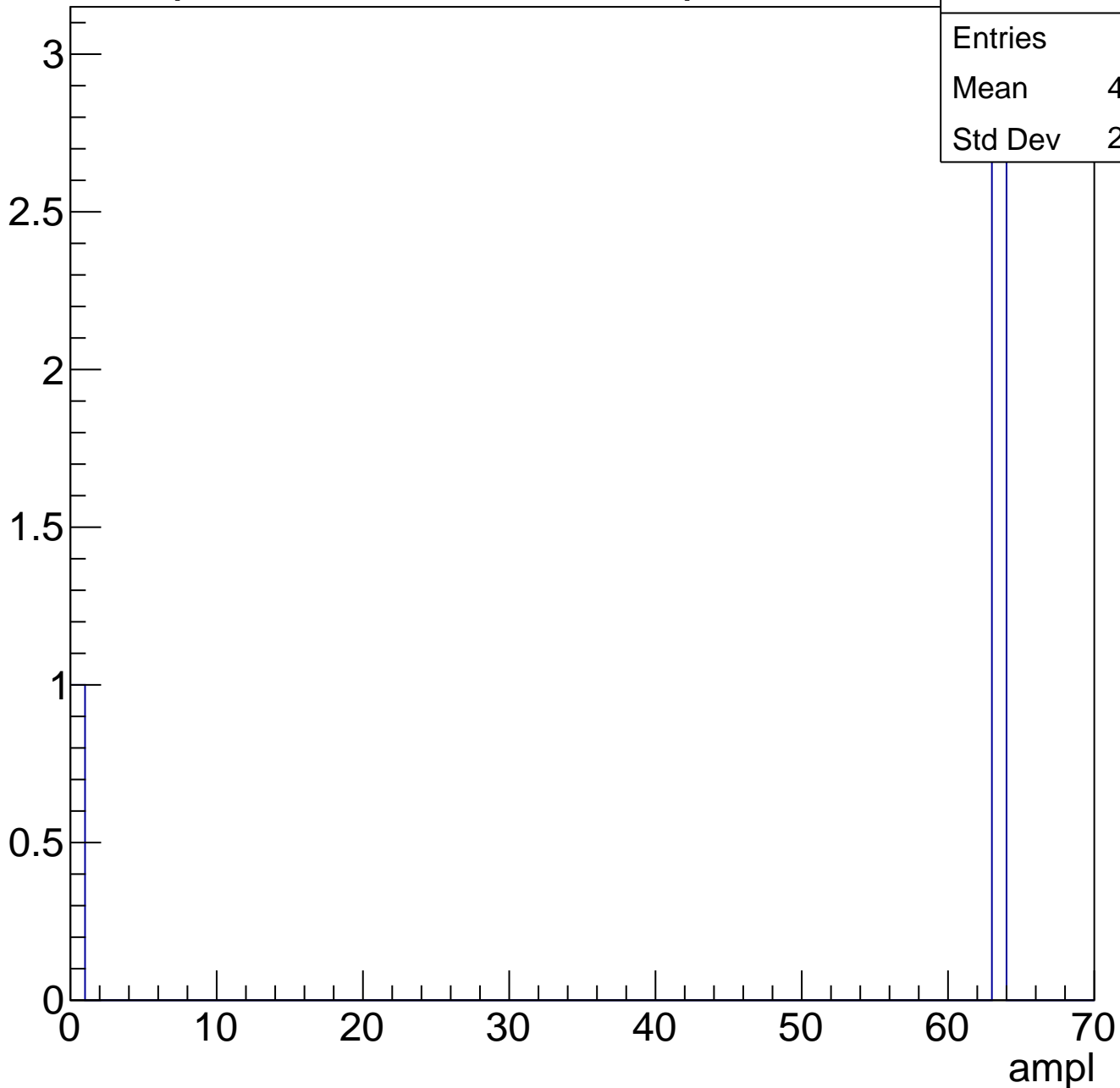
Entries	22
Mean	60.73
Std Dev	1.887



B0L001S, U11-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

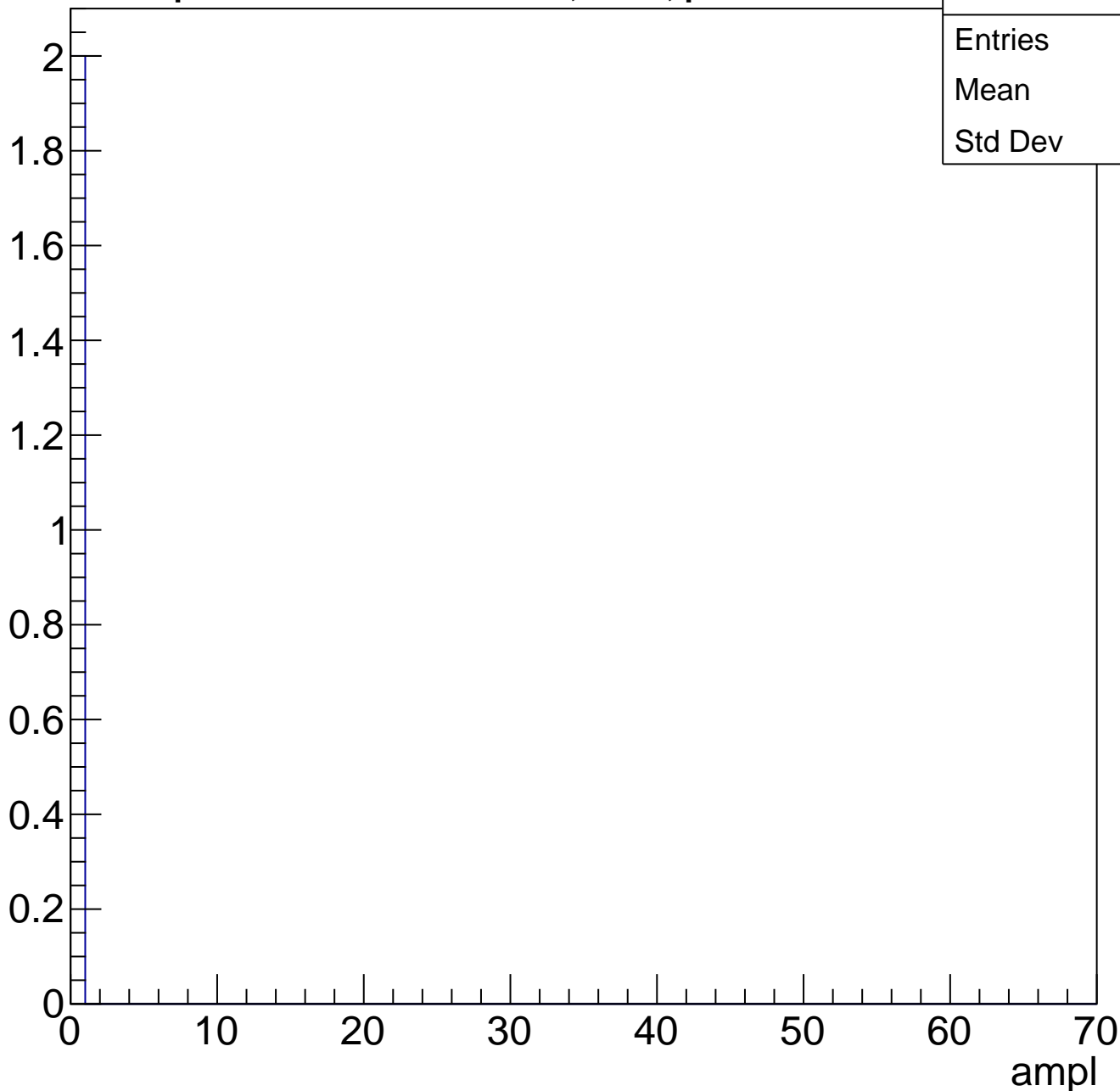
Entry



B0L001S, U11-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U11-ch127, adc0

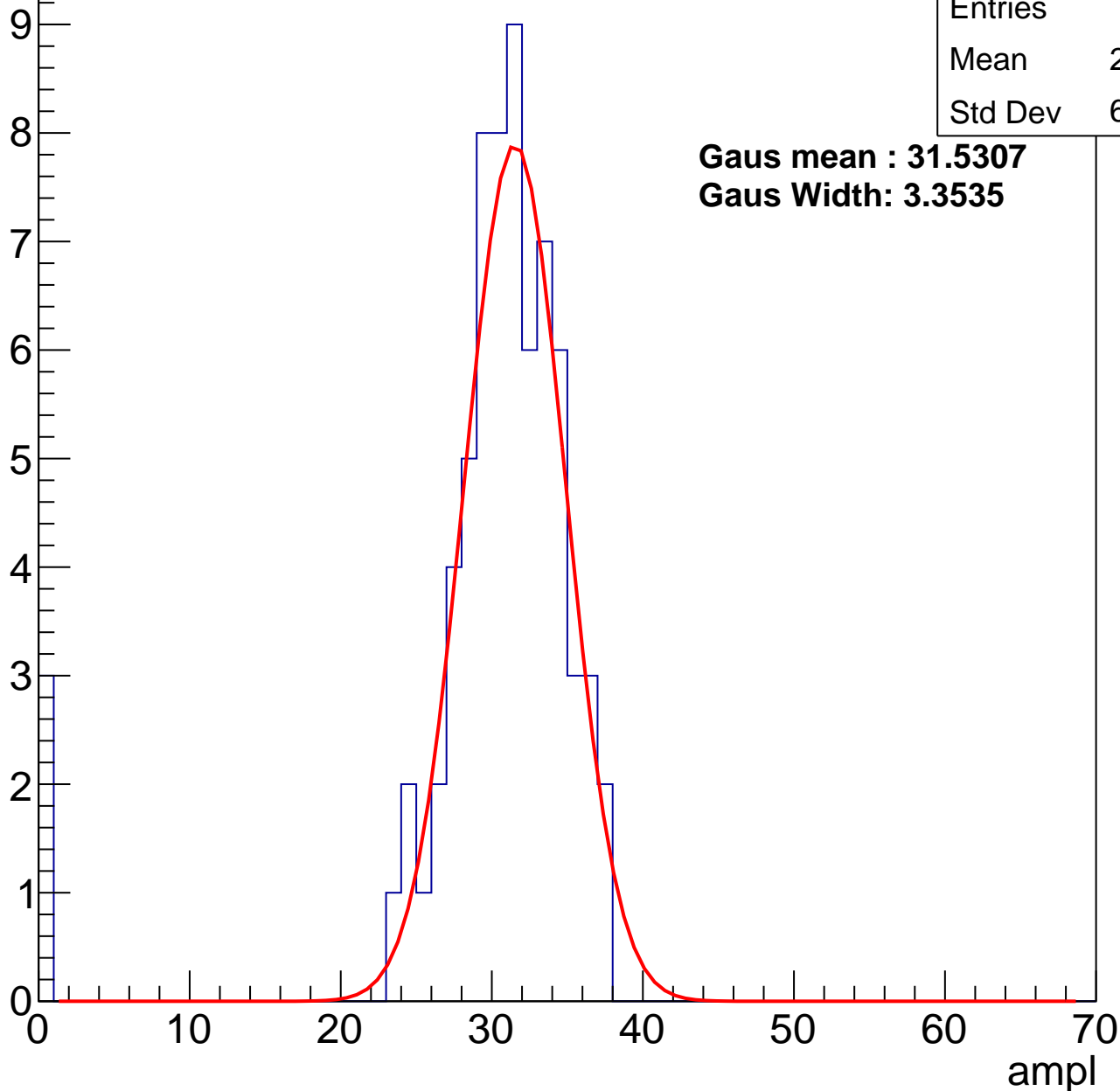
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.44
Std Dev	6.962

Gaus mean : 31.5307

Gaus Width: 3.3535



B0L001S, U11-ch127, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	38.01
Std Dev	3.705

Gaus mean : 38.3214

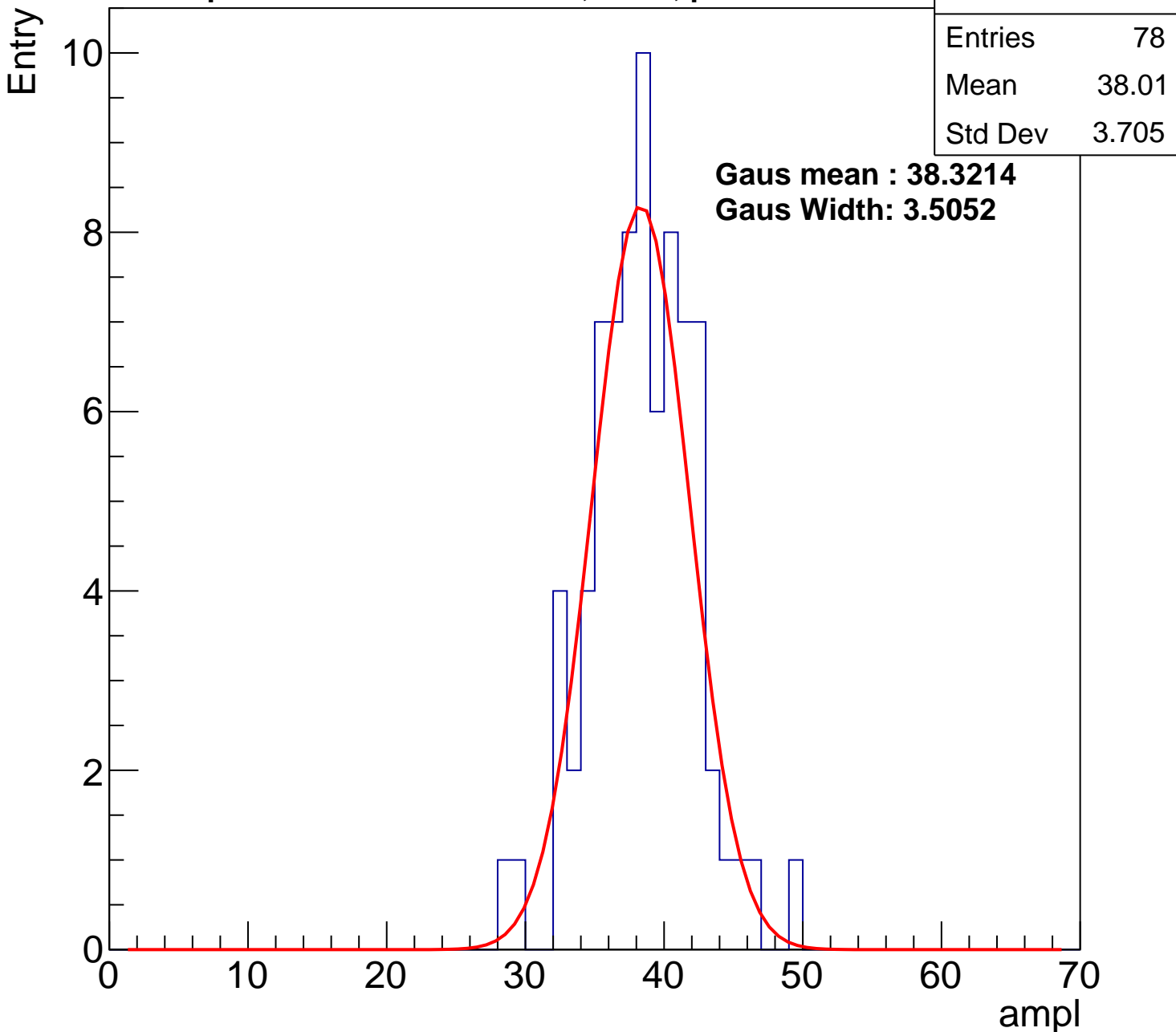
Gaus Width: 3.5052

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U11-ch127, adc2

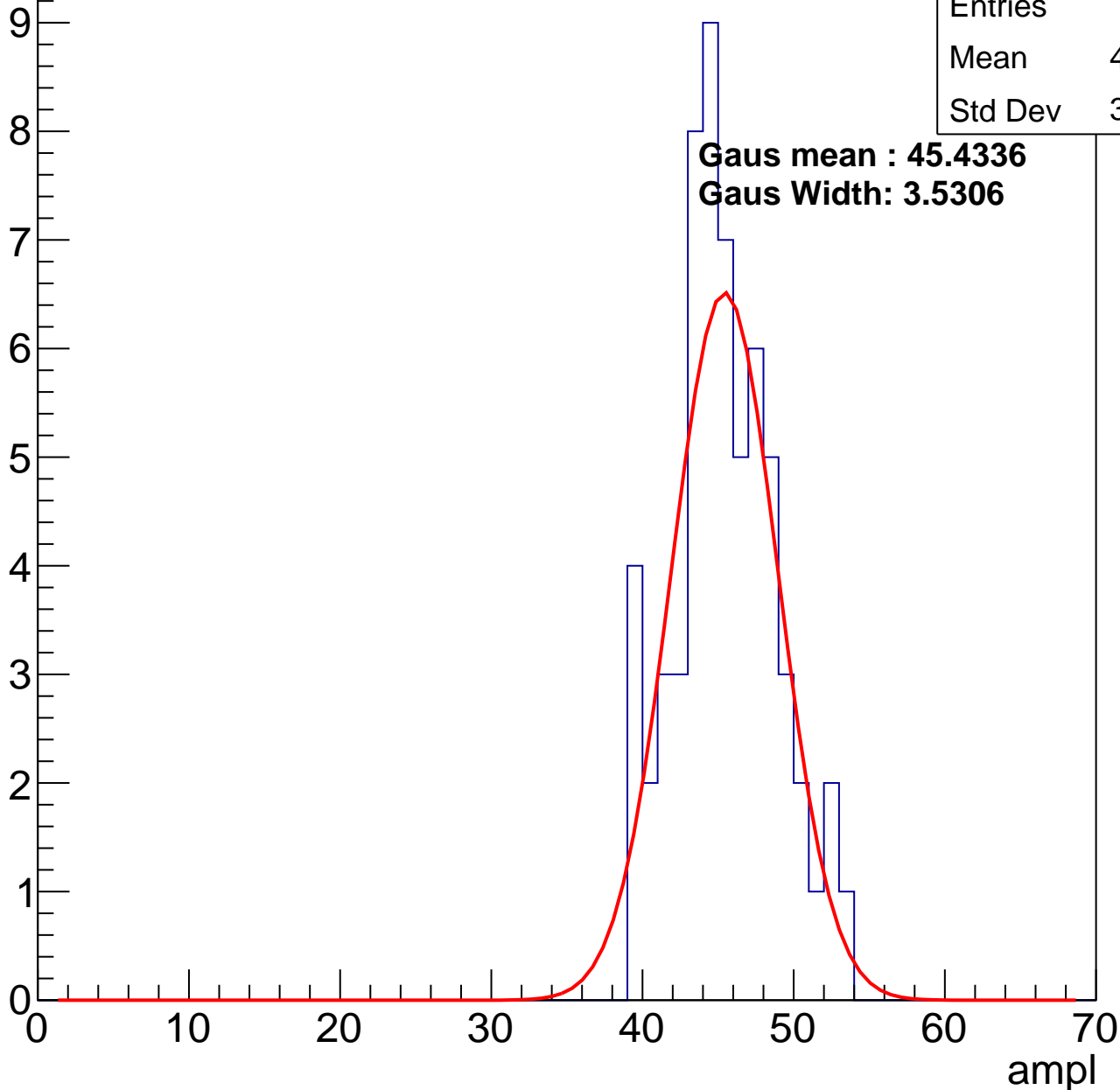
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	45.03
Std Dev	3.339

Gaus mean : 45.4336

Gaus Width: 3.5306

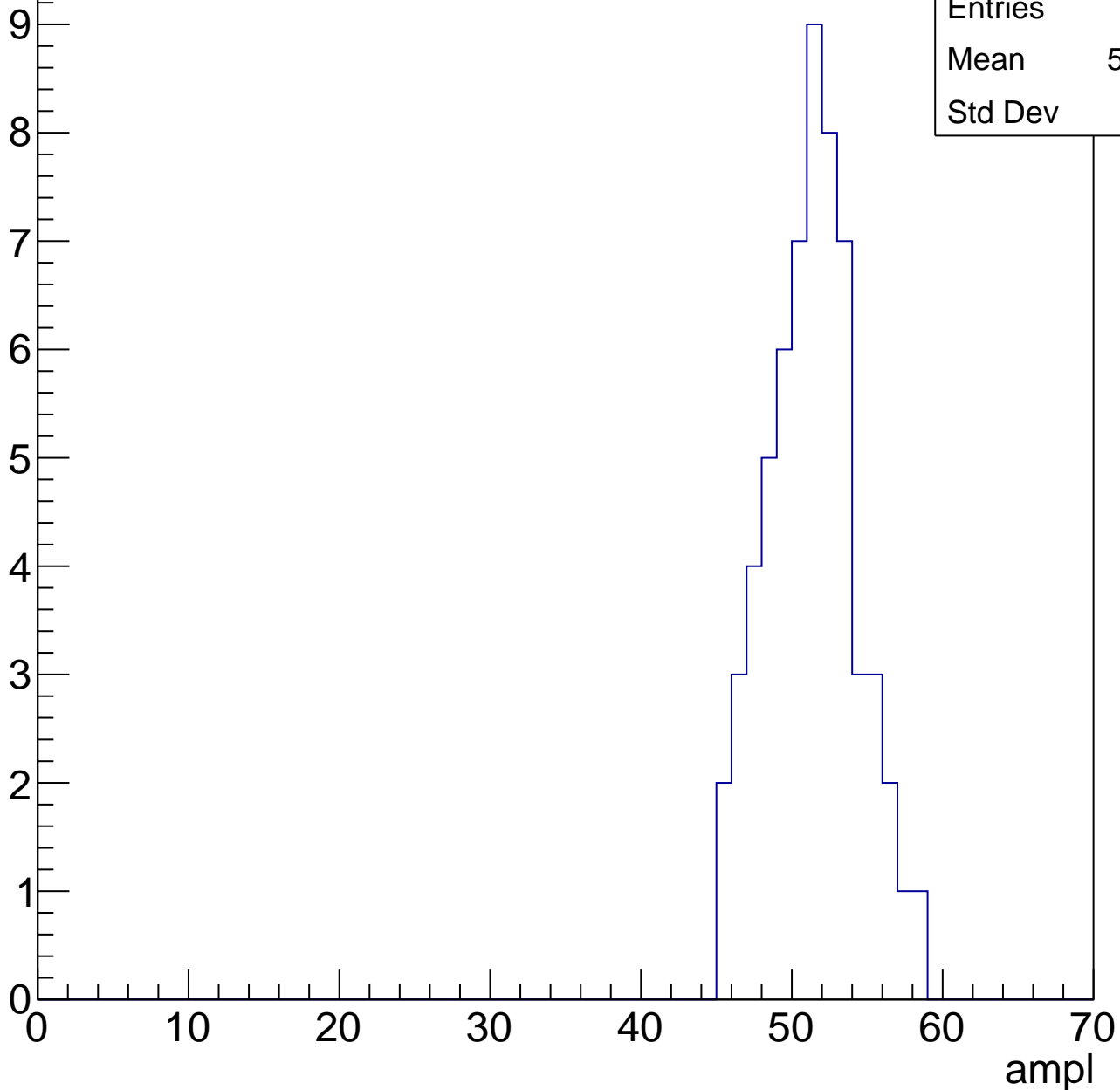


B0L001S, U11-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.82
Std Dev	2.95

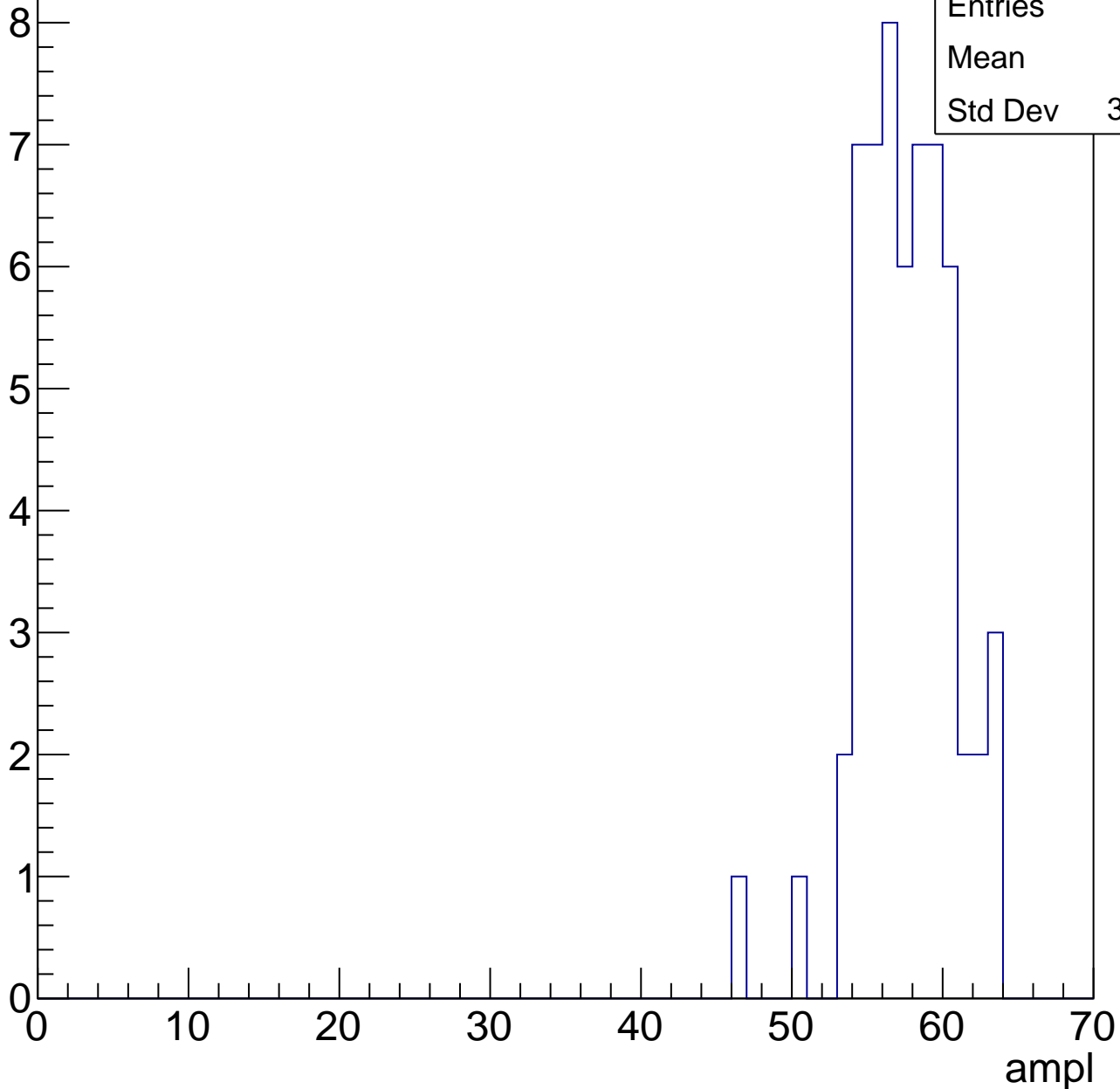


B0L001S, U11-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.1
Std Dev	3.134

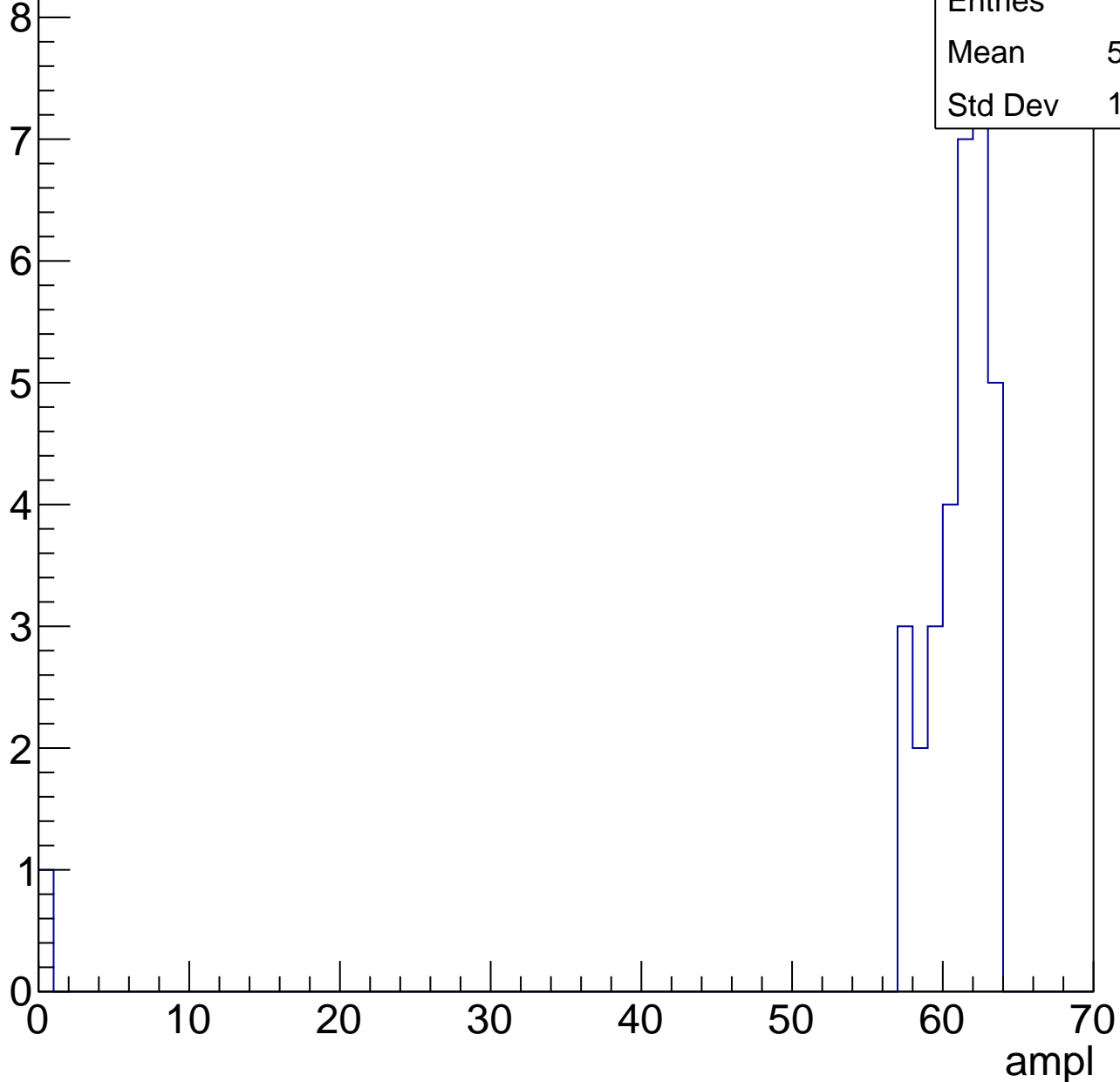


B0L001S, U11-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.85
Std Dev	10.56



B0L001S, U11-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

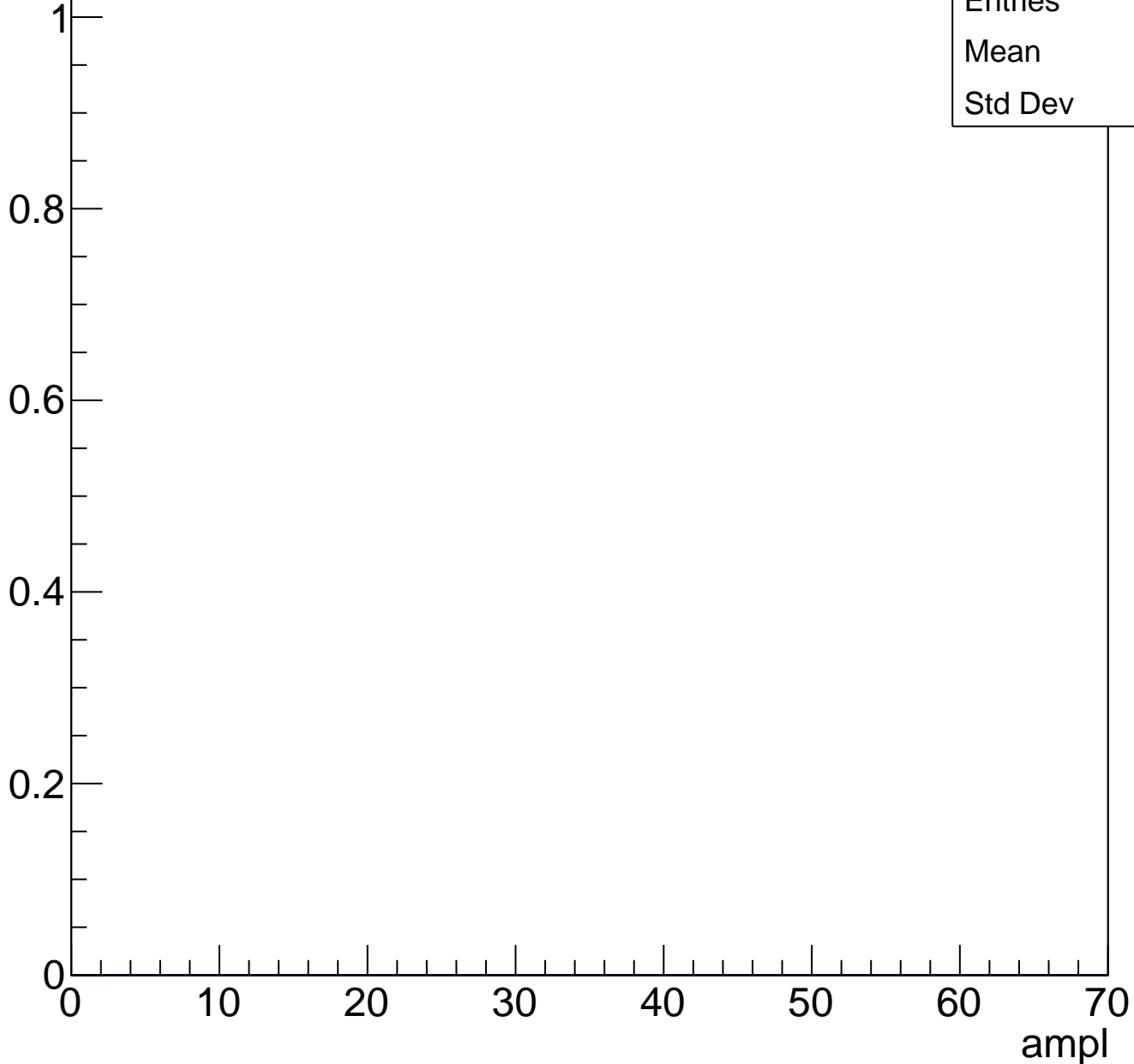


Entries	0
Mean	0
Std Dev	0

B0L001S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0