

B1L000S, U23-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.87
Std Dev	11.66

Turn on : 25.1405

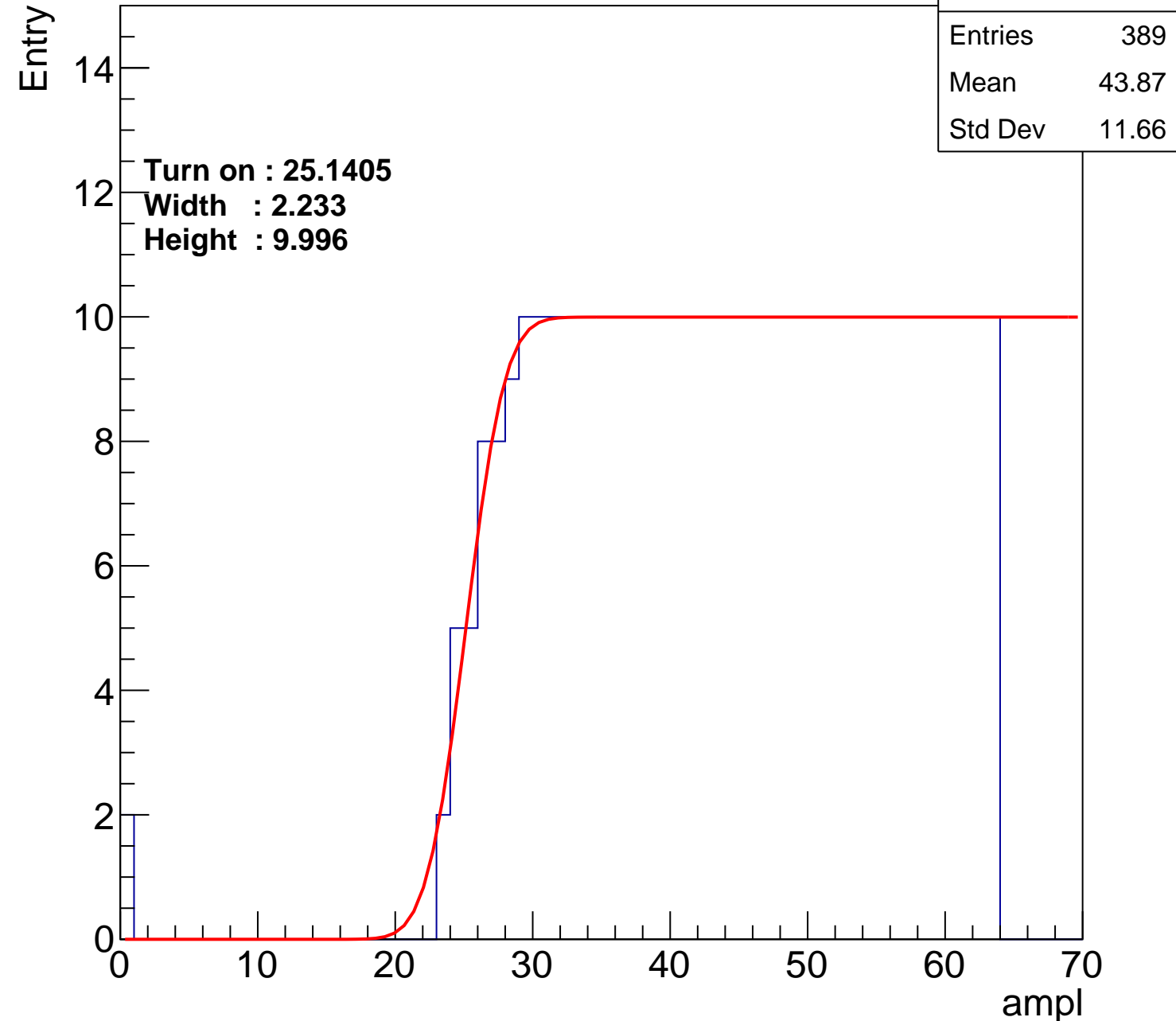
Width : 2.233

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch1

calib_packv5_042523_0143.root, FC#3, port B3

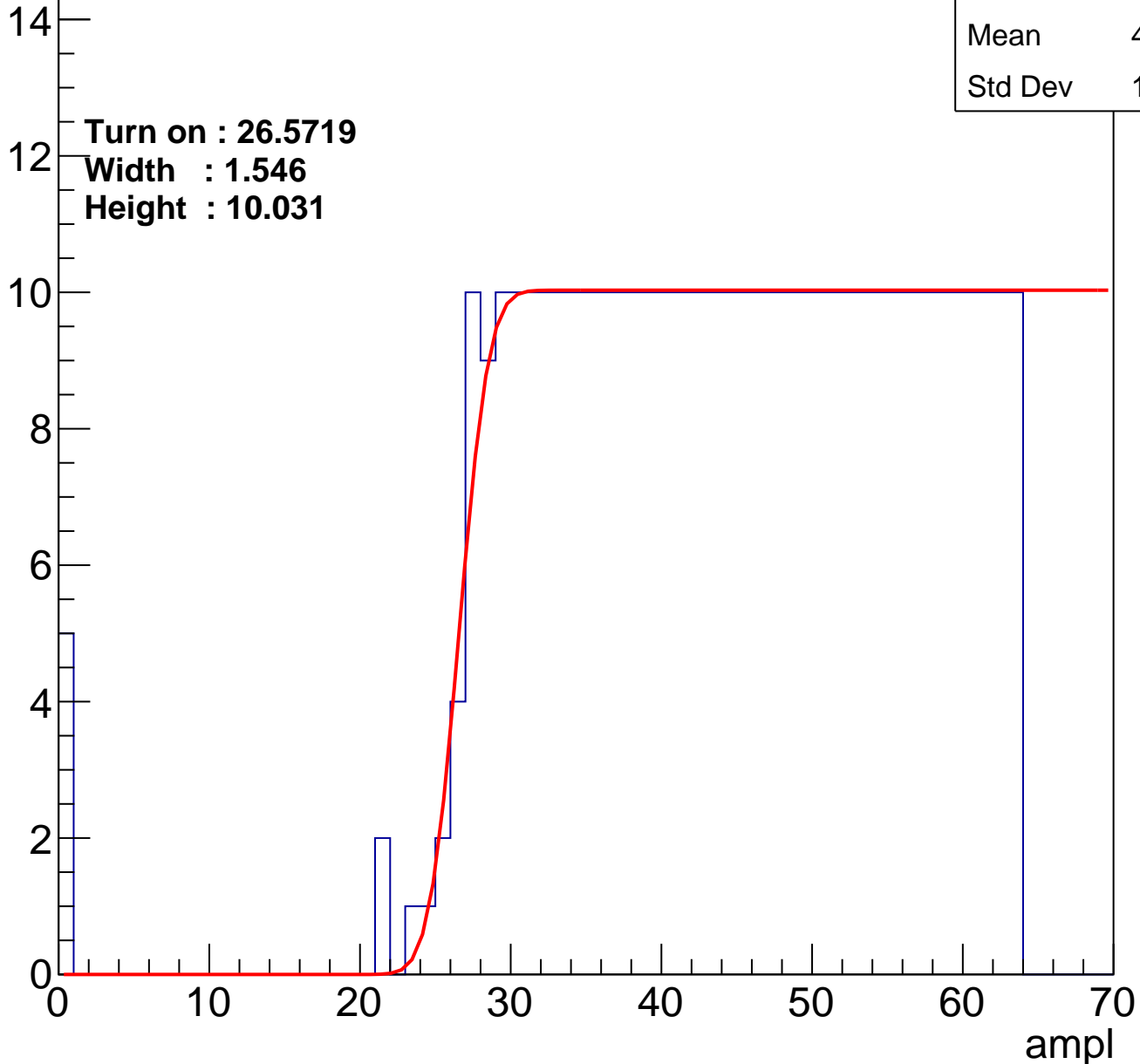
Entries	384
Mean	43.92
Std Dev	12.06

Turn on : 26.5719

Width : 1.546

Height : 10.031

Entry



B1L000S, U23-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.05
Std Dev	11.58

Turn on : 25.7201

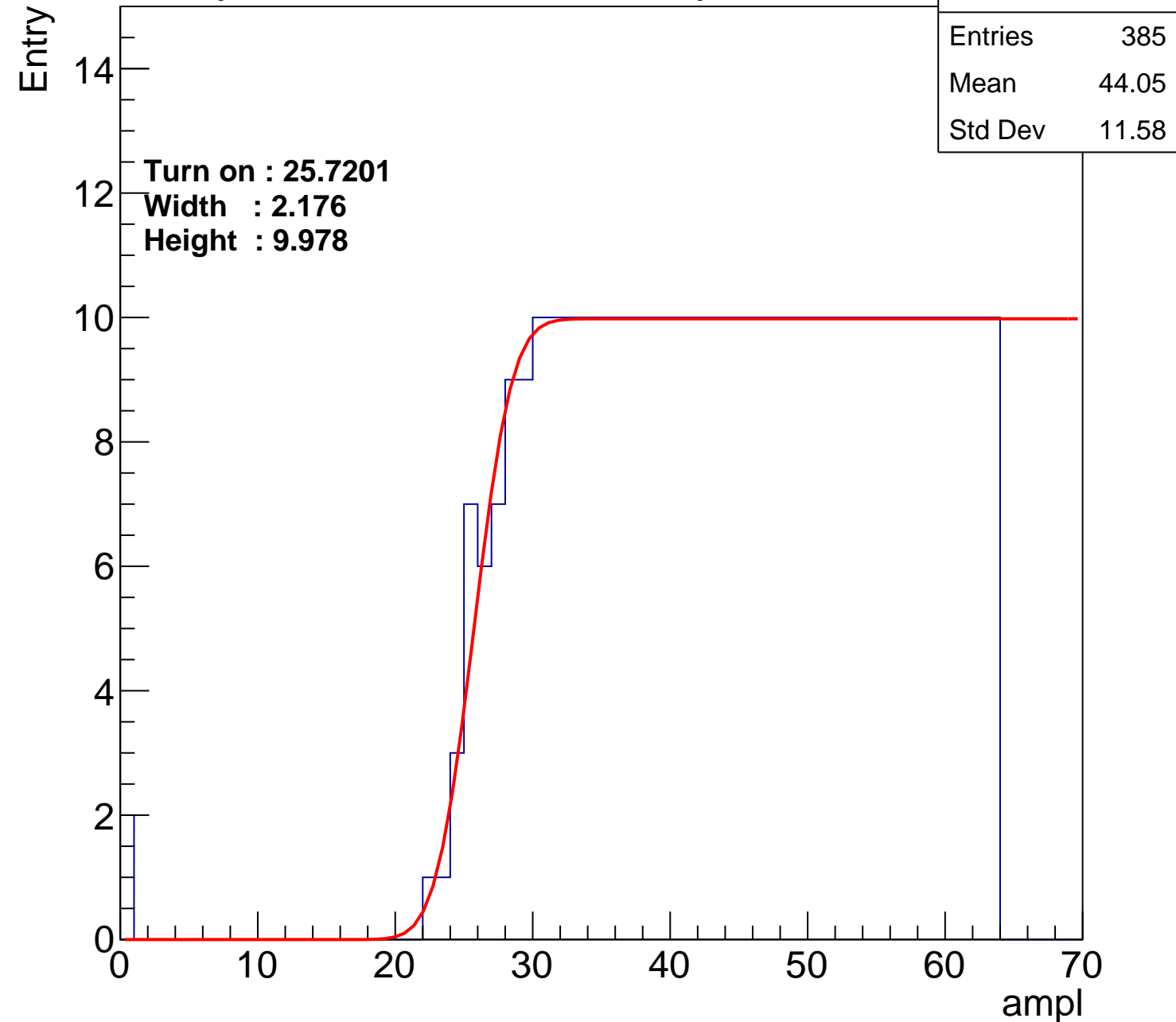
Width : 2.176

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.1475

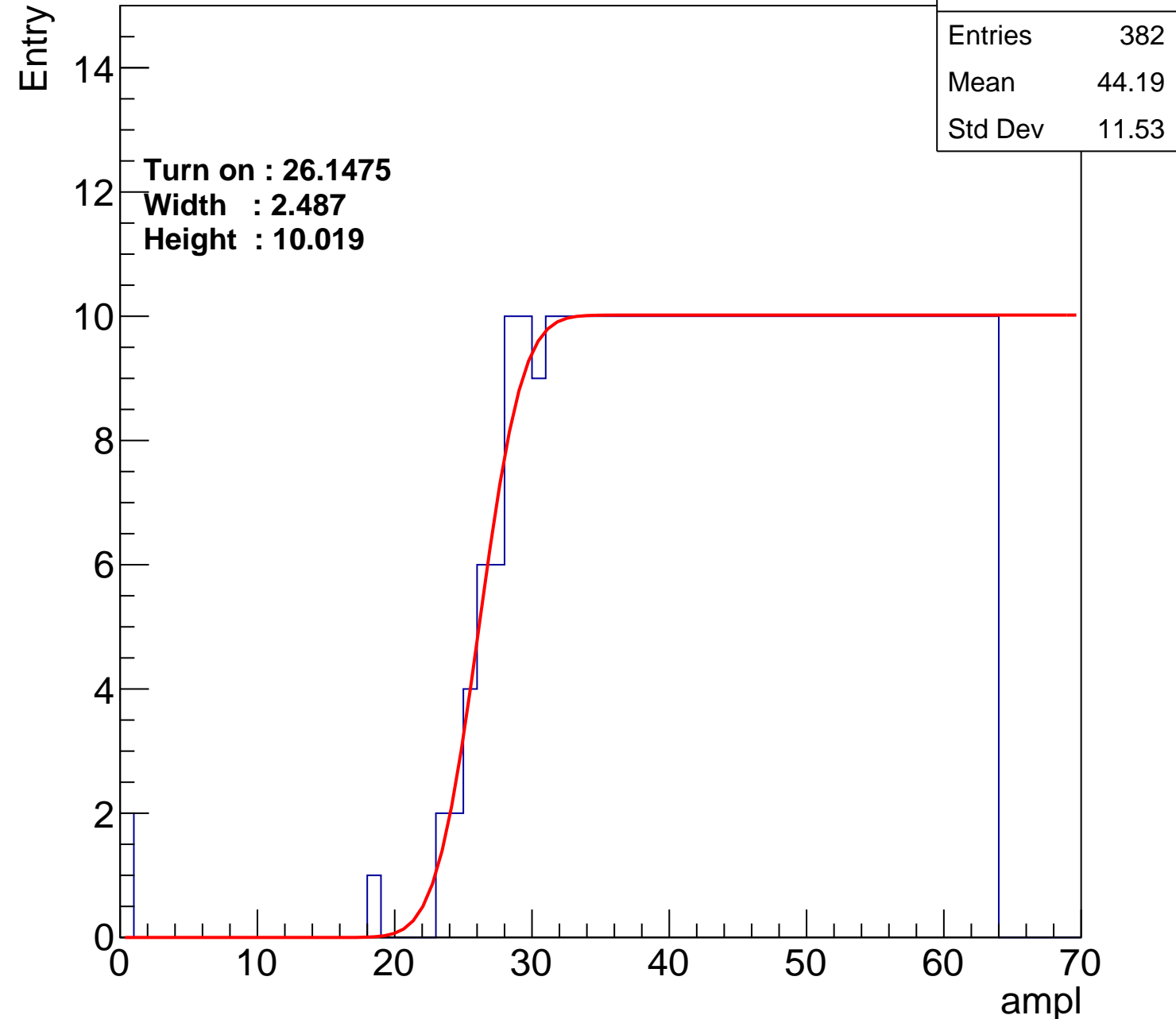
Width : 2.487

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.8594

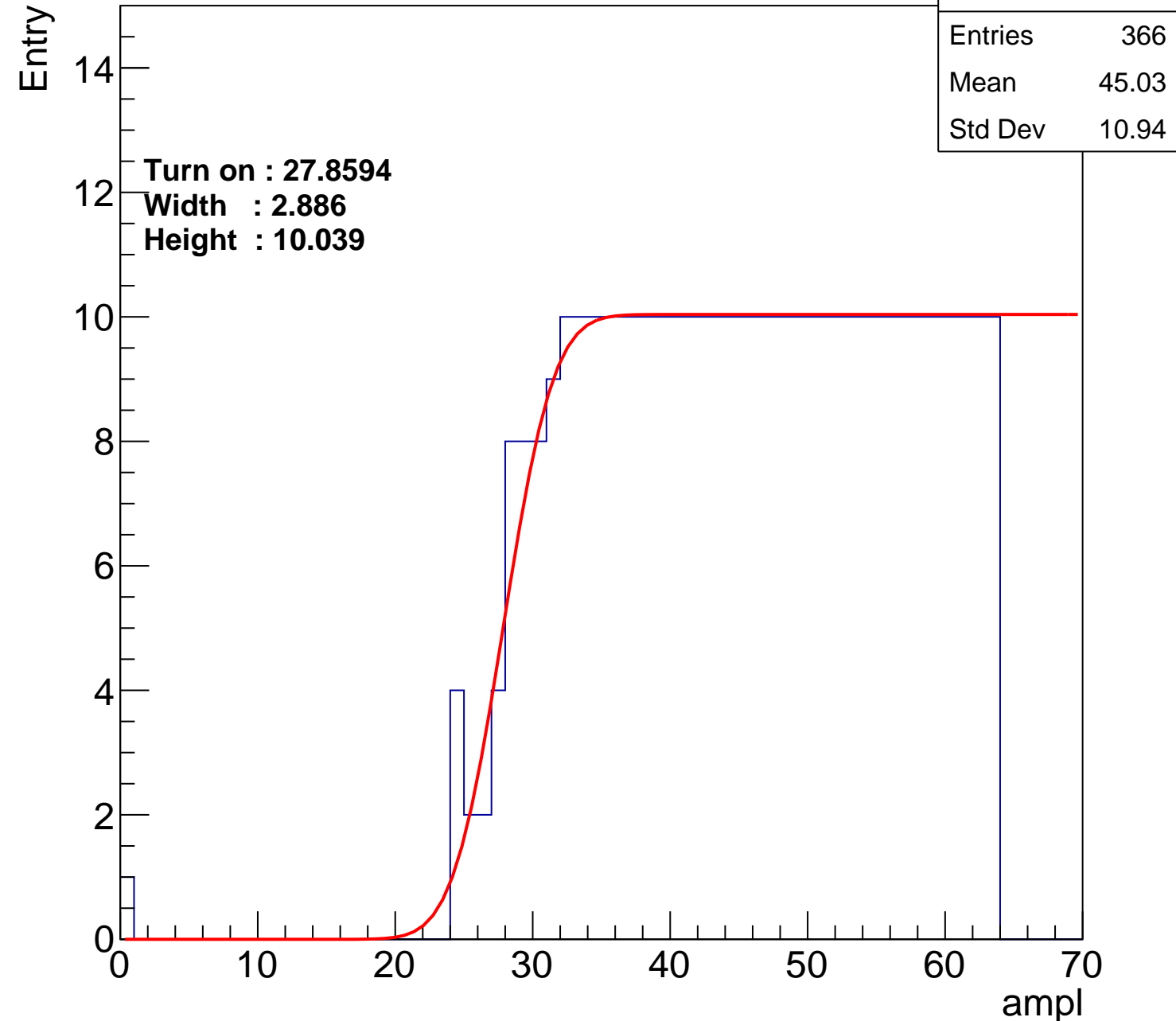
Width : 2.886

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch5

calib_packv5_042523_0143.root, FC#3, port B3

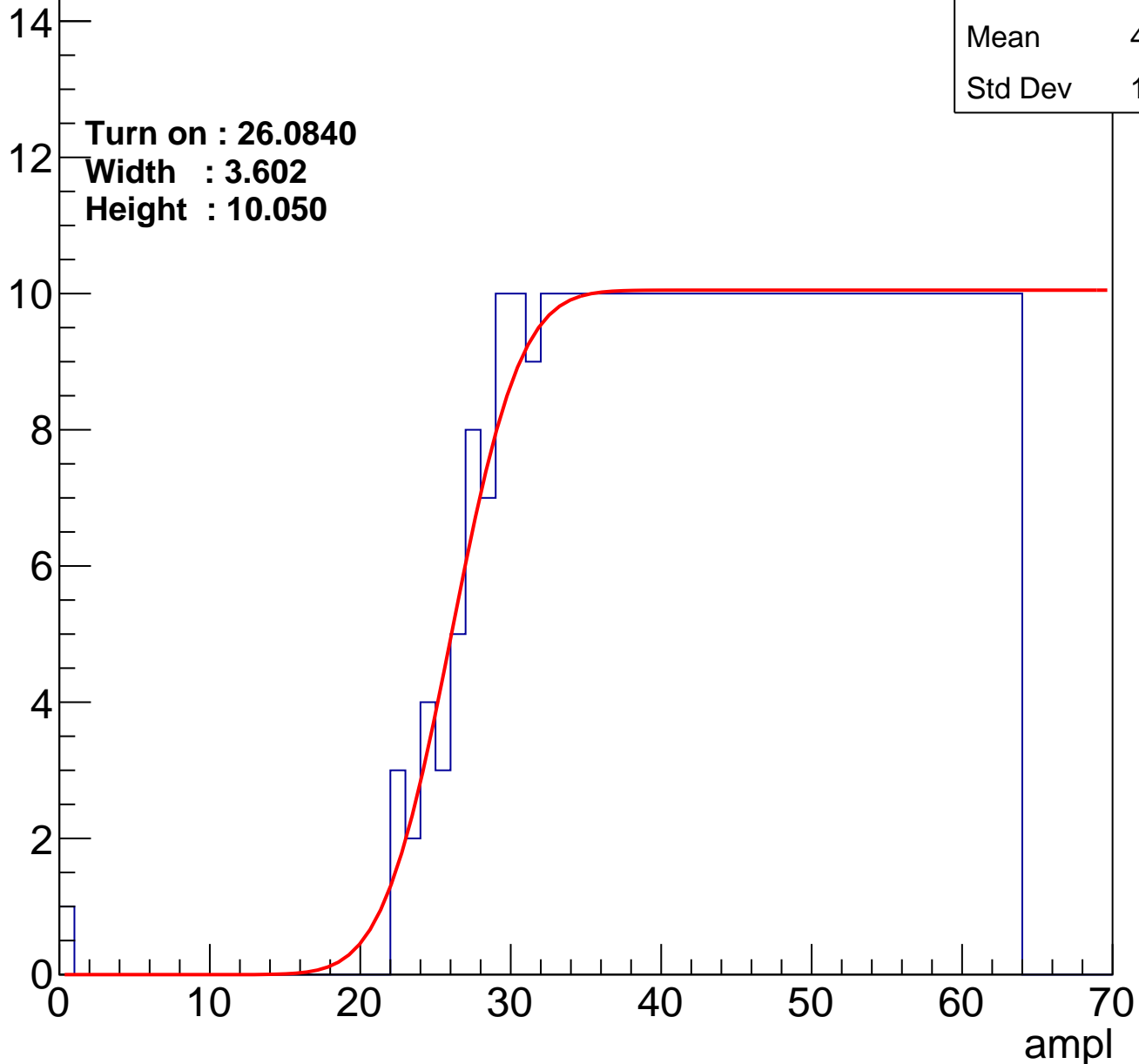
Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 26.0840

Width : 3.602

Height : 10.050

Entry



B1L000S, U23-ch6

calib_packv5_042523_0143.root, FC#3, port B3

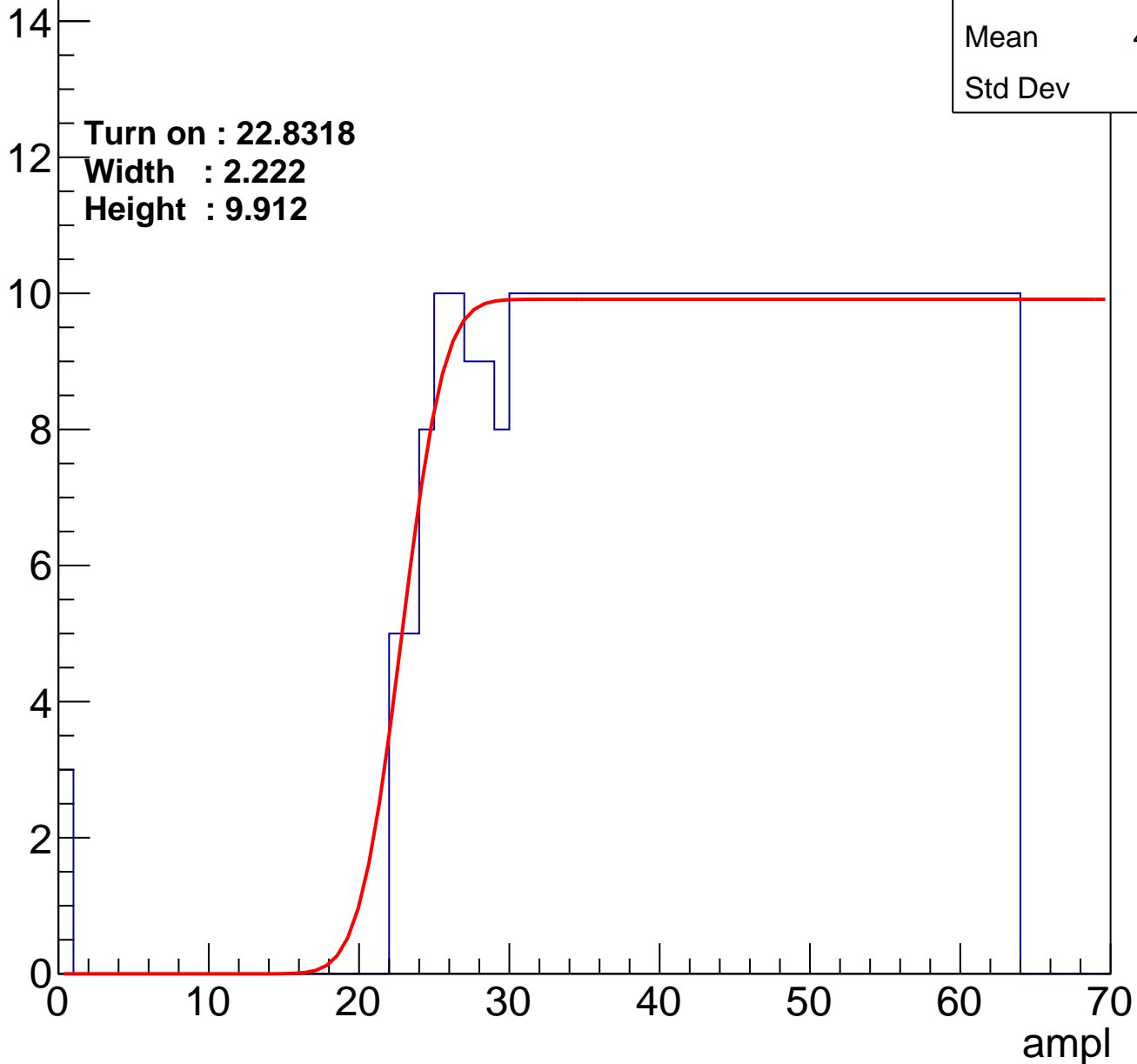
Entries	407
Mean	42.91
Std Dev	12.3

Turn on : 22.8318

Width : 2.222

Height : 9.912

Entry



B1L000S, U23-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.22
Std Dev	12.4

Turn on : 25.1744

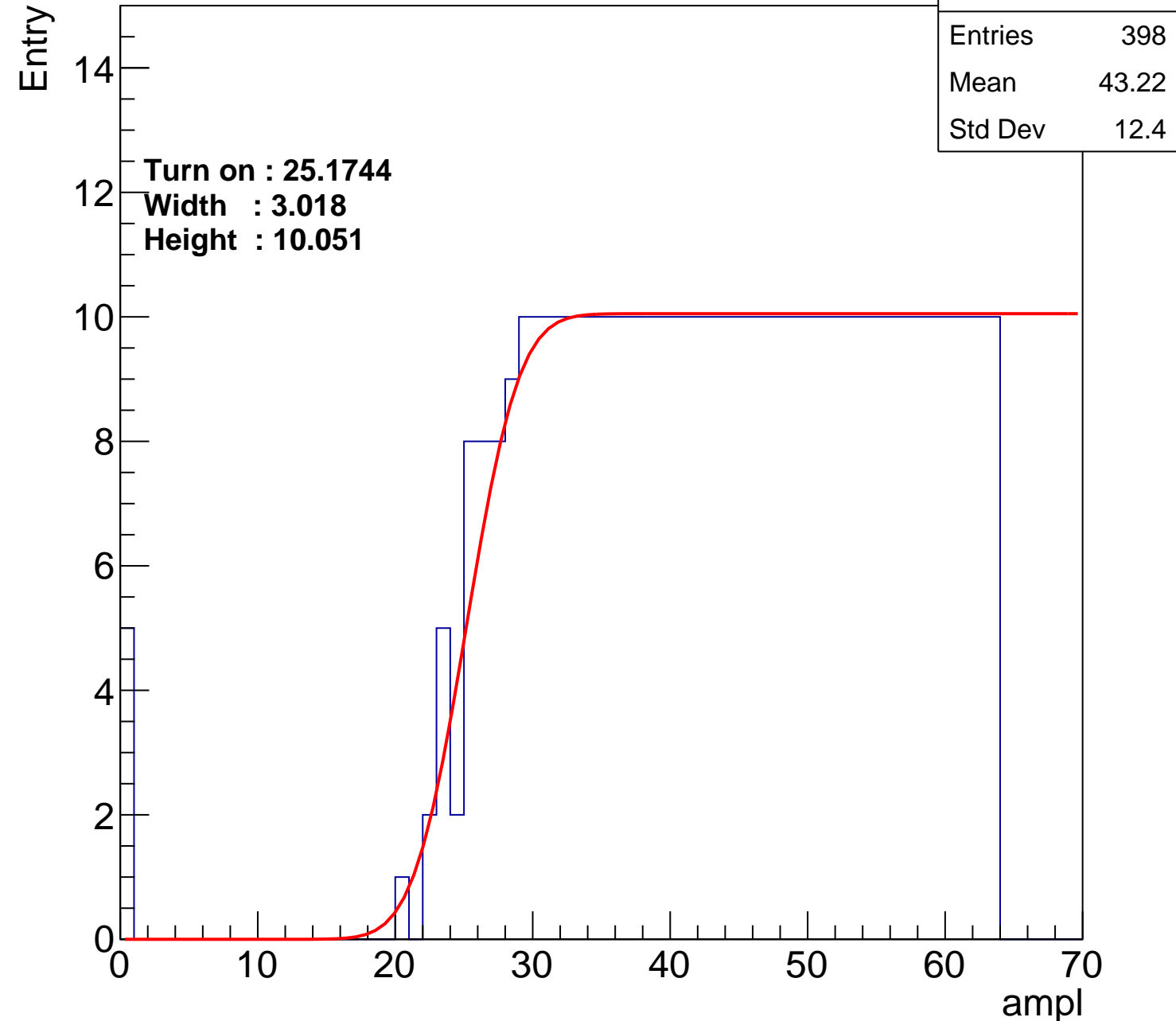
Width : 3.018

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
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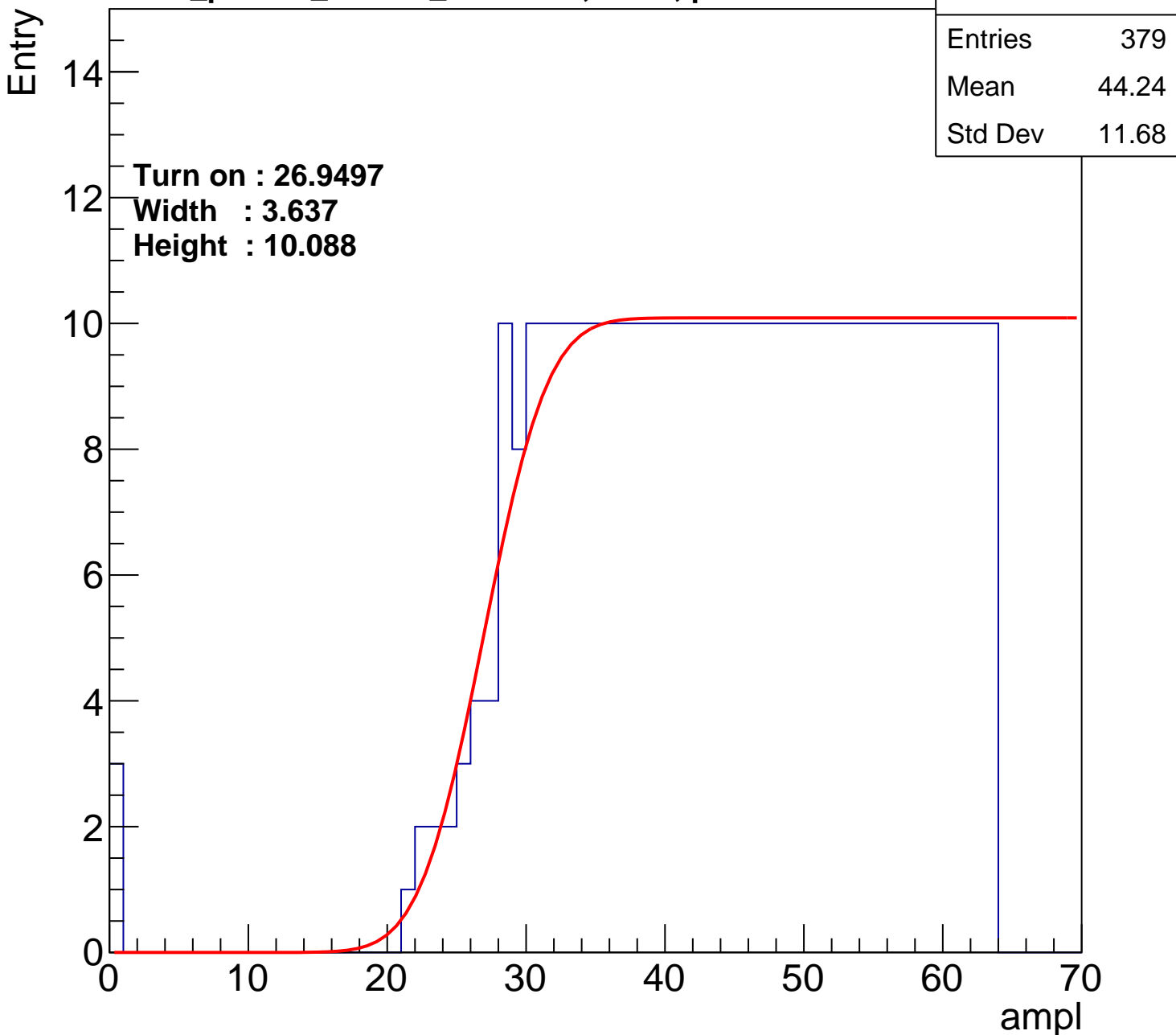
Mean	44.24
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Std Dev	11.68
---------	-------

Turn on : 26.9497

Width : 3.637

Height : 10.088



B1L000S, U23-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.31
Std Dev	10.96

Turn on : 27.8913

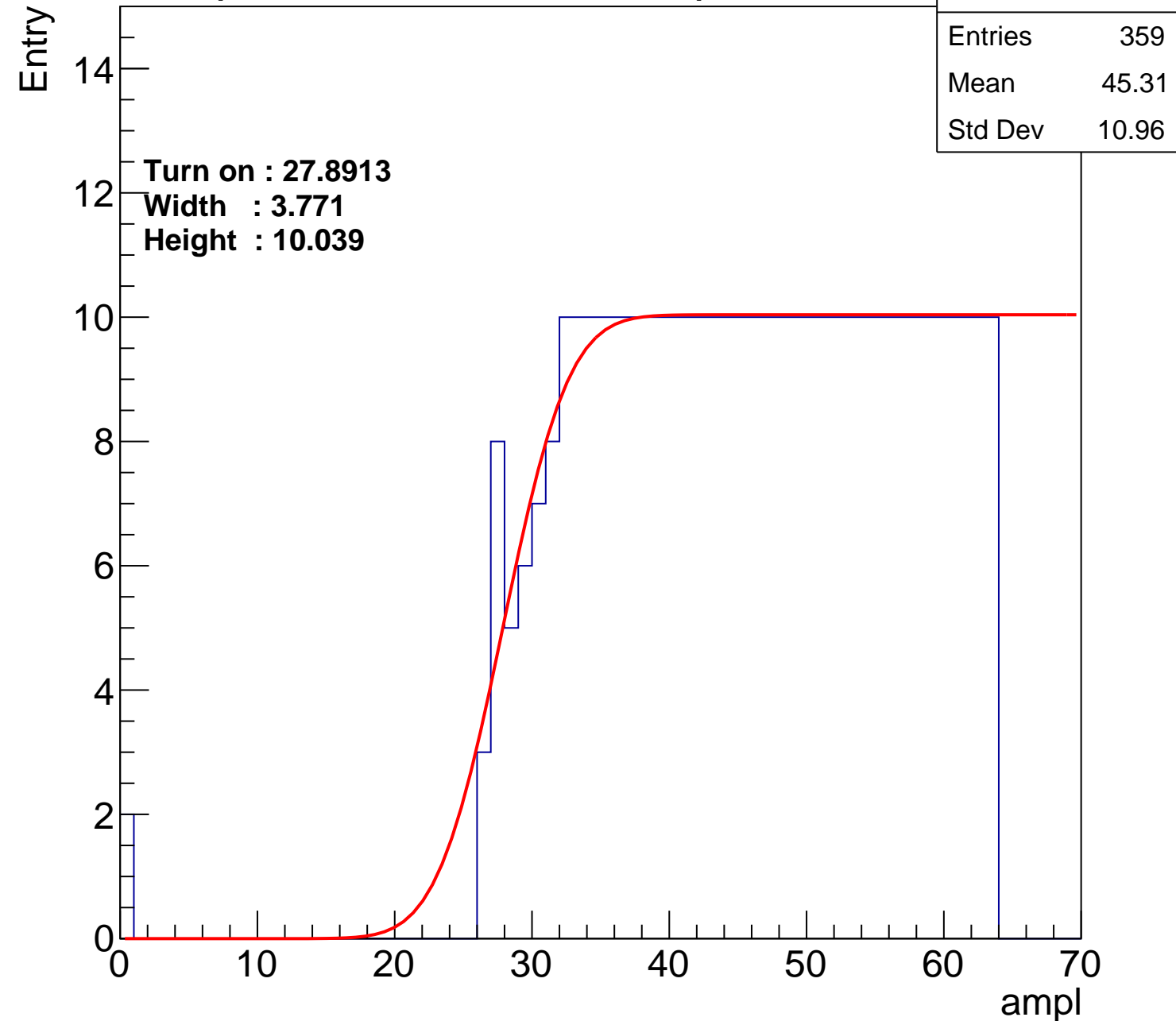
Width : 3.771

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.5495

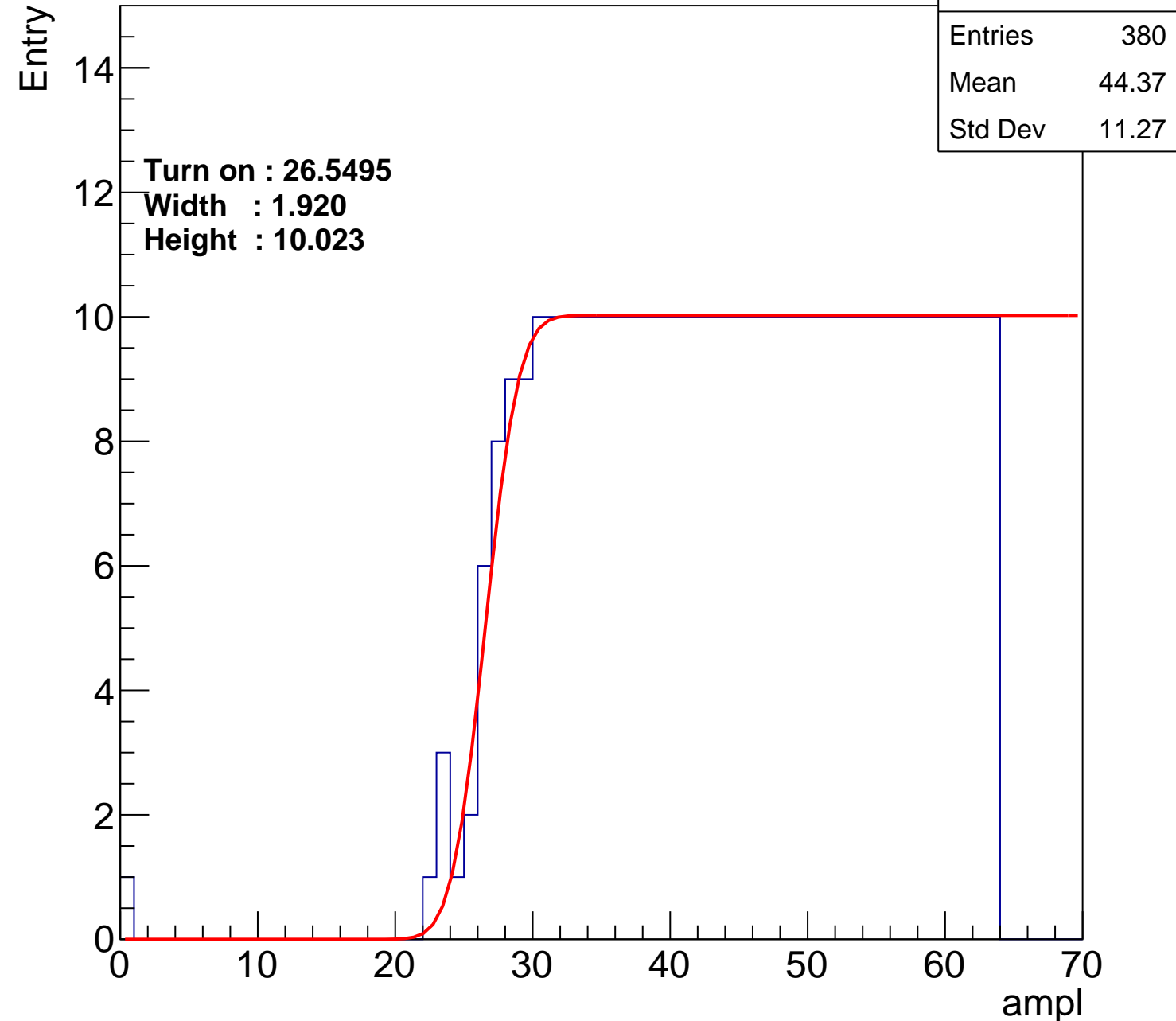
Width : 1.920

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch11

calib_packv5_042523_0143.root, FC#3, port B3

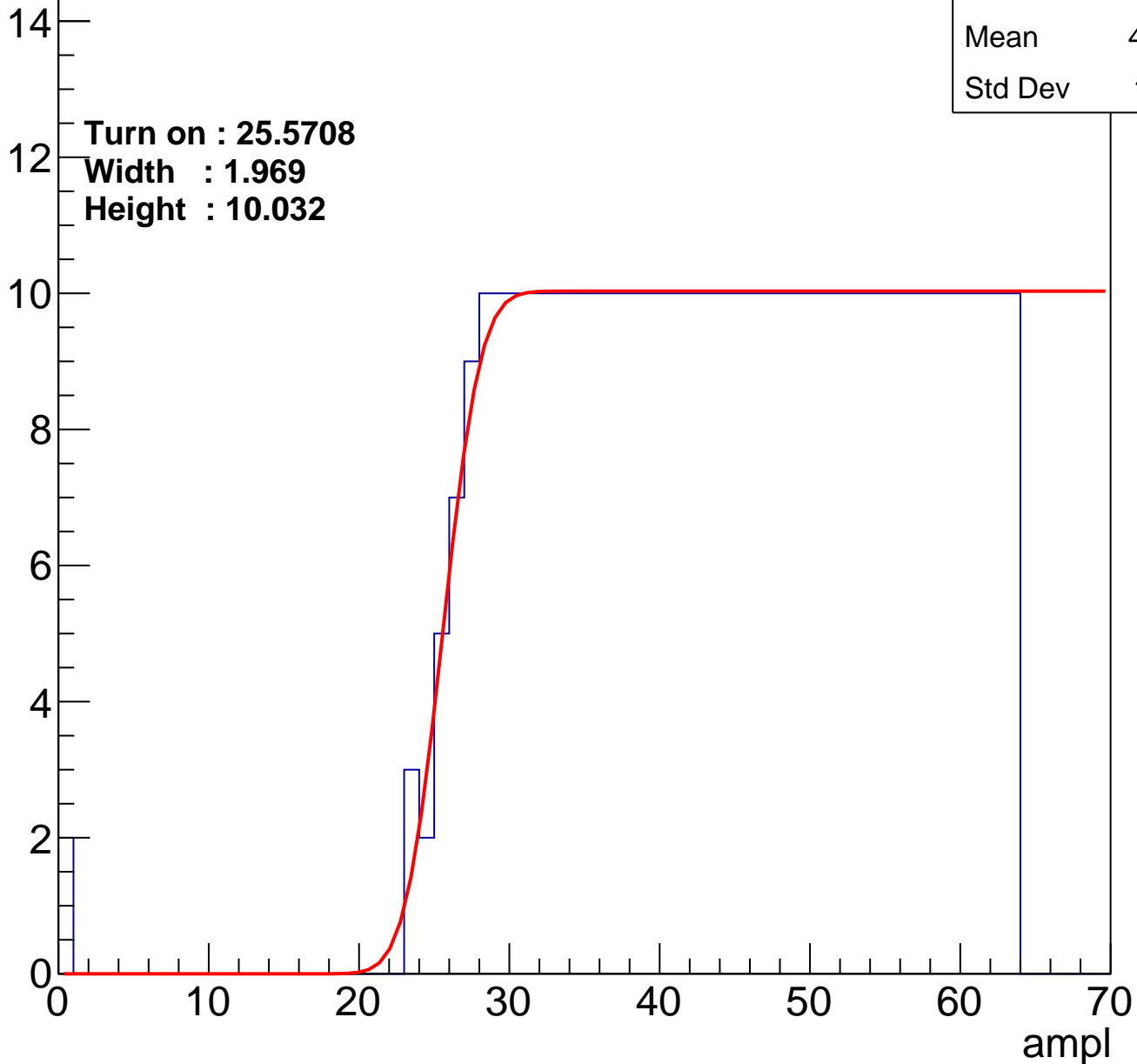
Entries	388
Mean	43.94
Std Dev	11.61

Turn on : 25.5708

Width : 1.969

Height : 10.032

Entry



B1L000S, U23-ch12

calib_packv5_042523_0143.root, FC#3, port B3

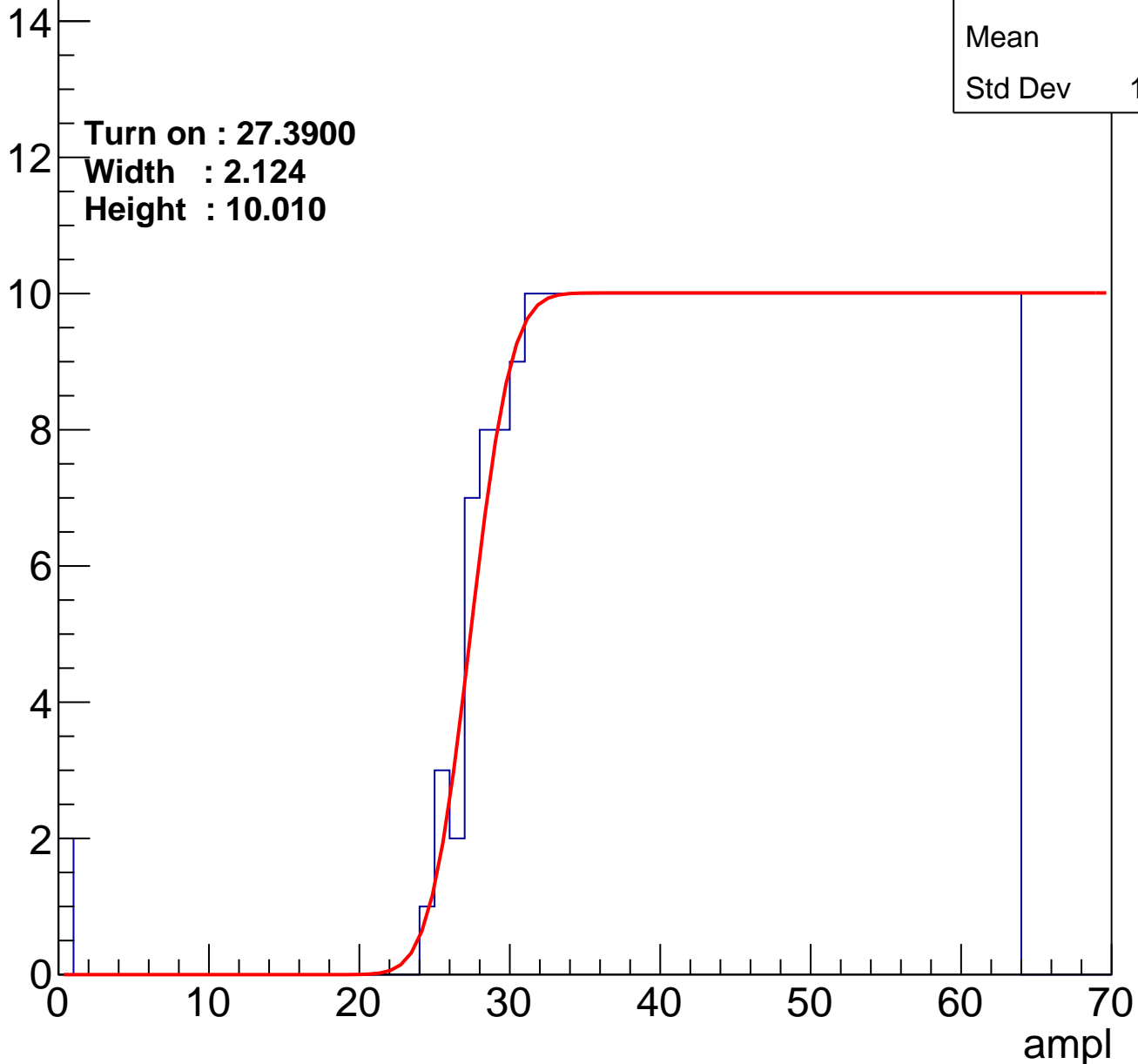
Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.3900

Width : 2.124

Height : 10.010

Entry



B1L000S, U23-ch13

calib_packv5_042523_0143.root, FC#3, port B3

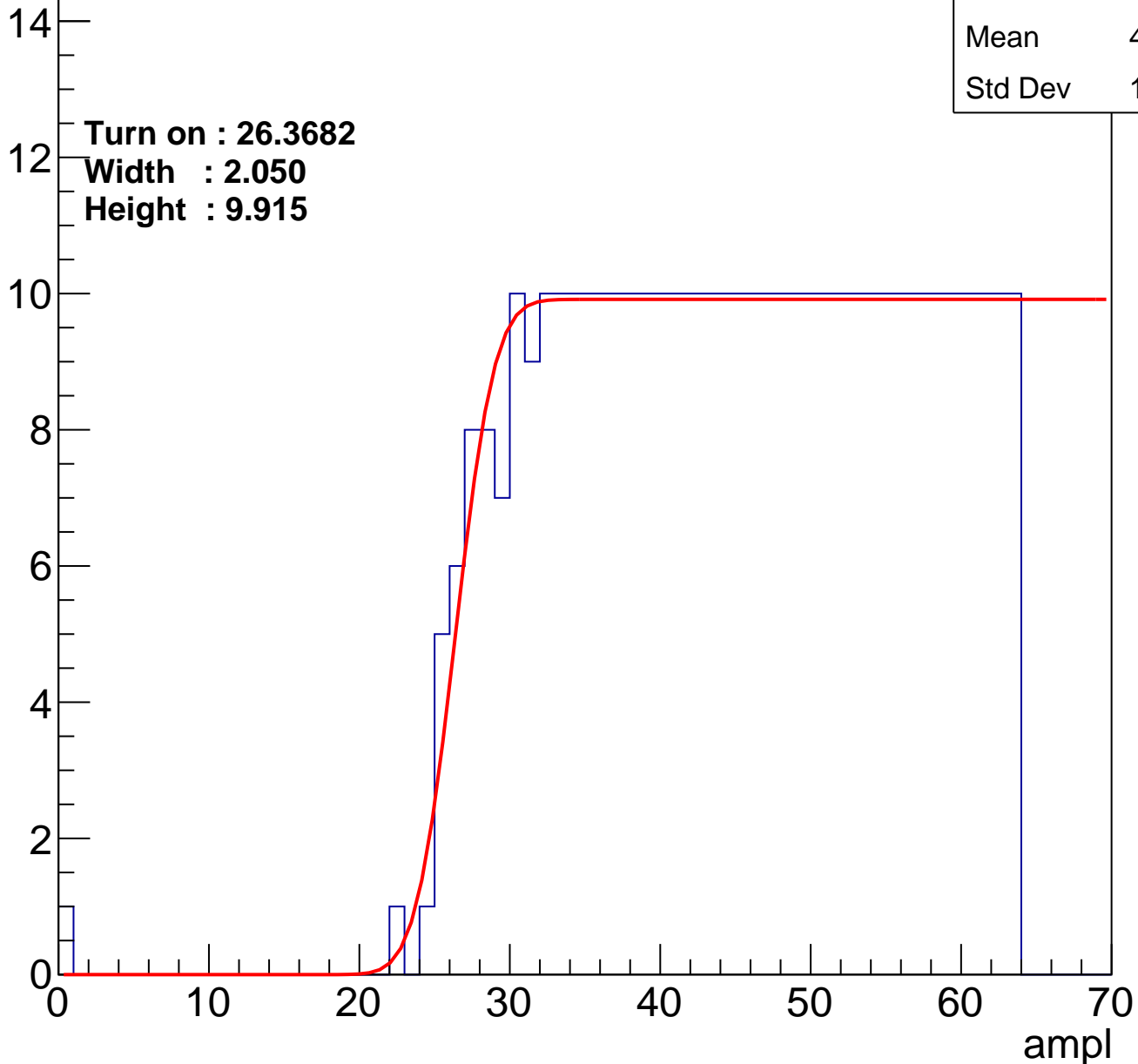
Entries	376
Mean	44.55
Std Dev	11.19

Turn on : 26.3682

Width : 2.050

Height : 9.915

Entry



B1L000S, U23-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 27.5915

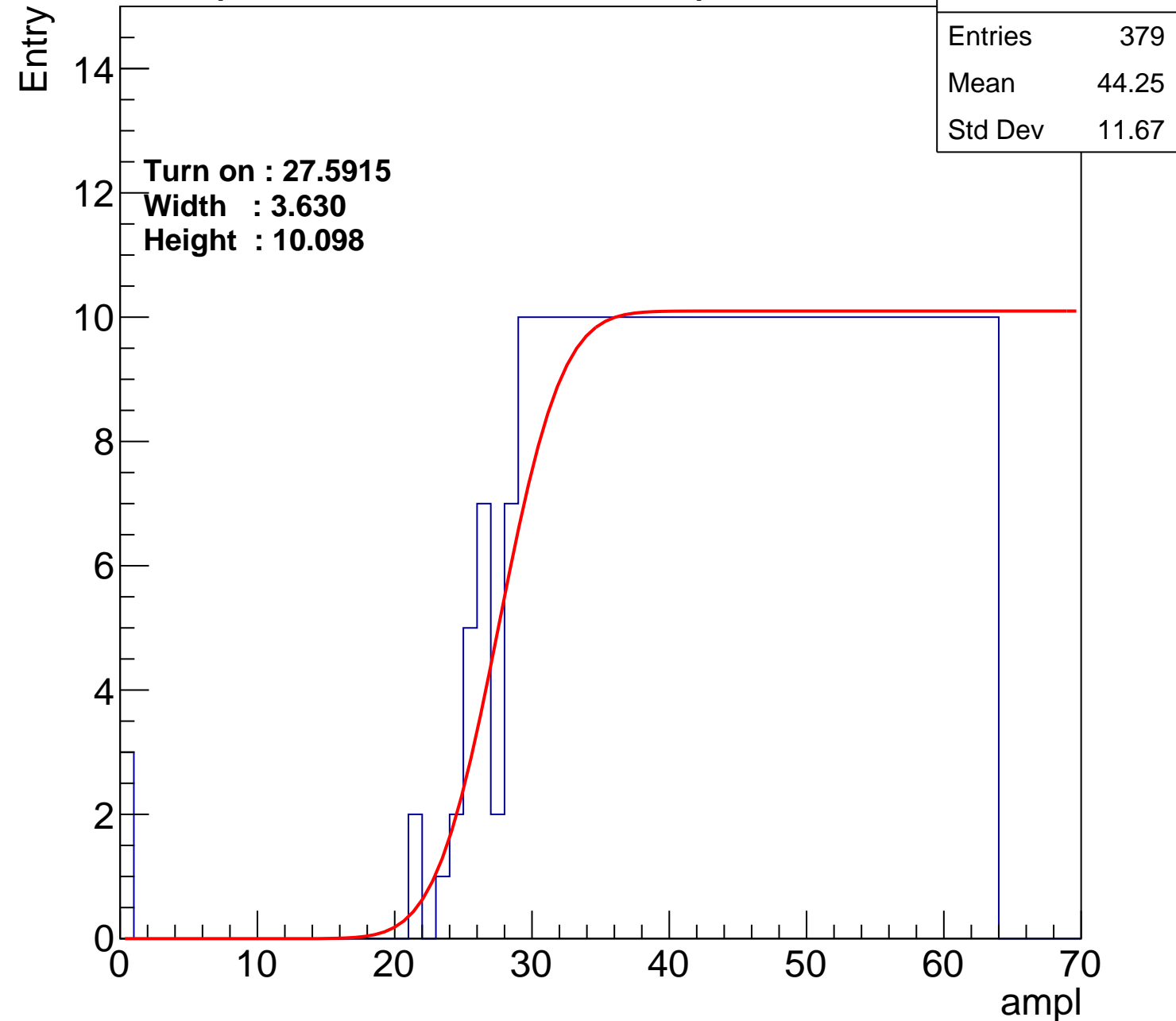
Width : 3.630

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.28
Std Dev	11.4

Turn on : 25.6989

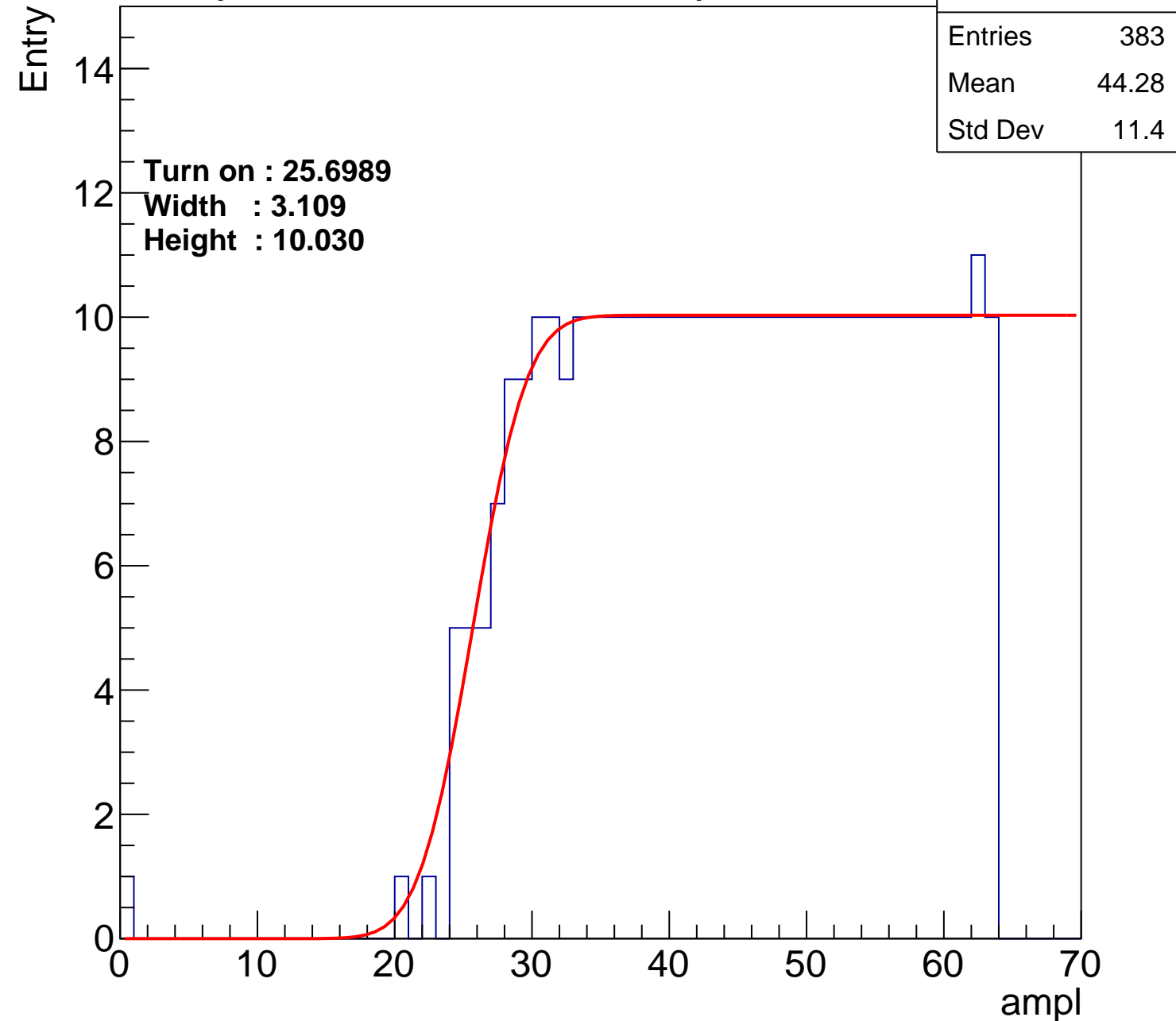
Width : 3.109

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch16

calib_packv5_042523_0143.root, FC#3, port B3

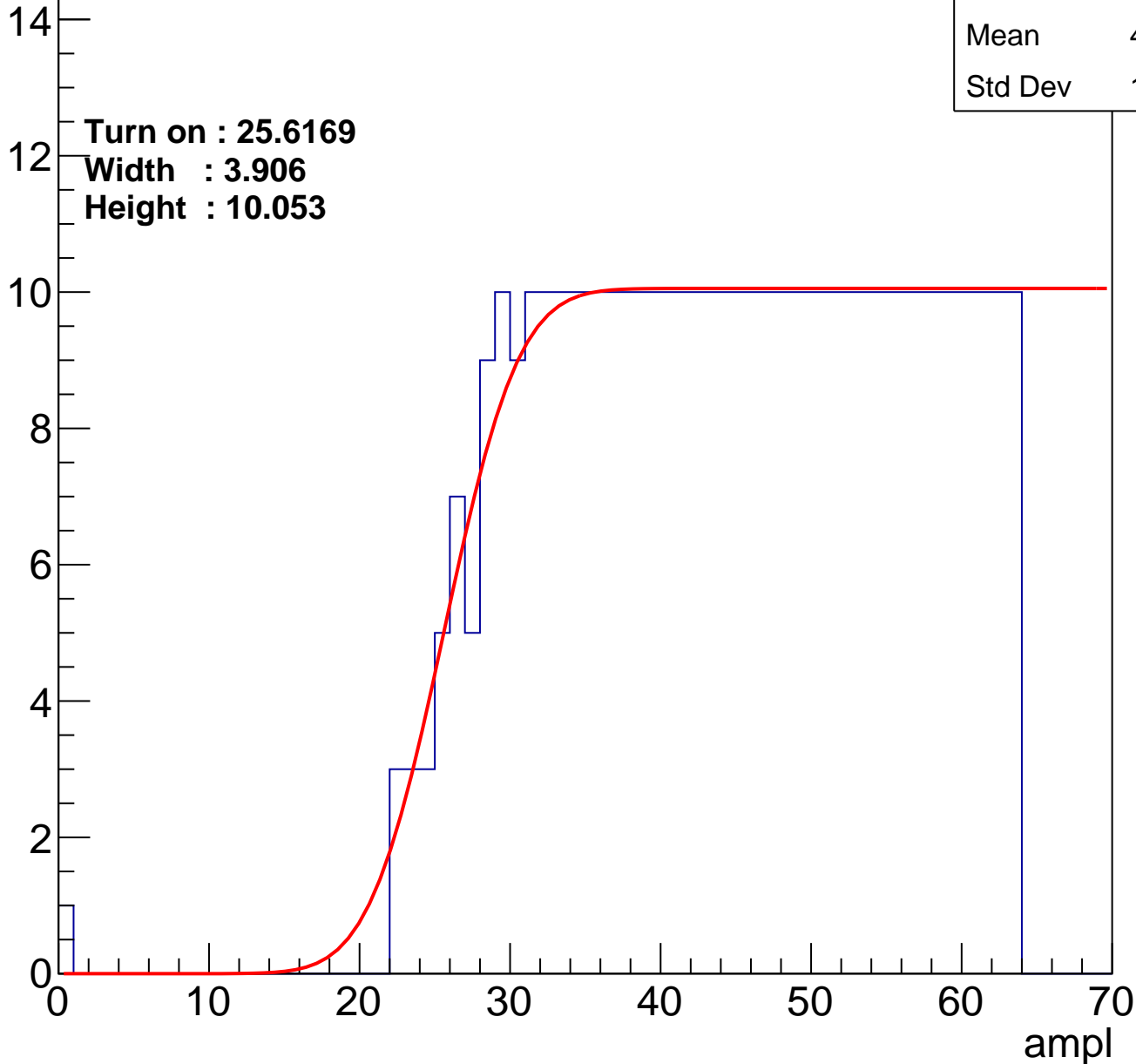
Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.6169

Width : 3.906

Height : 10.053

Entry



B1L000S, U23-ch17

calib_packv5_042523_0143.root, FC#3, port B3

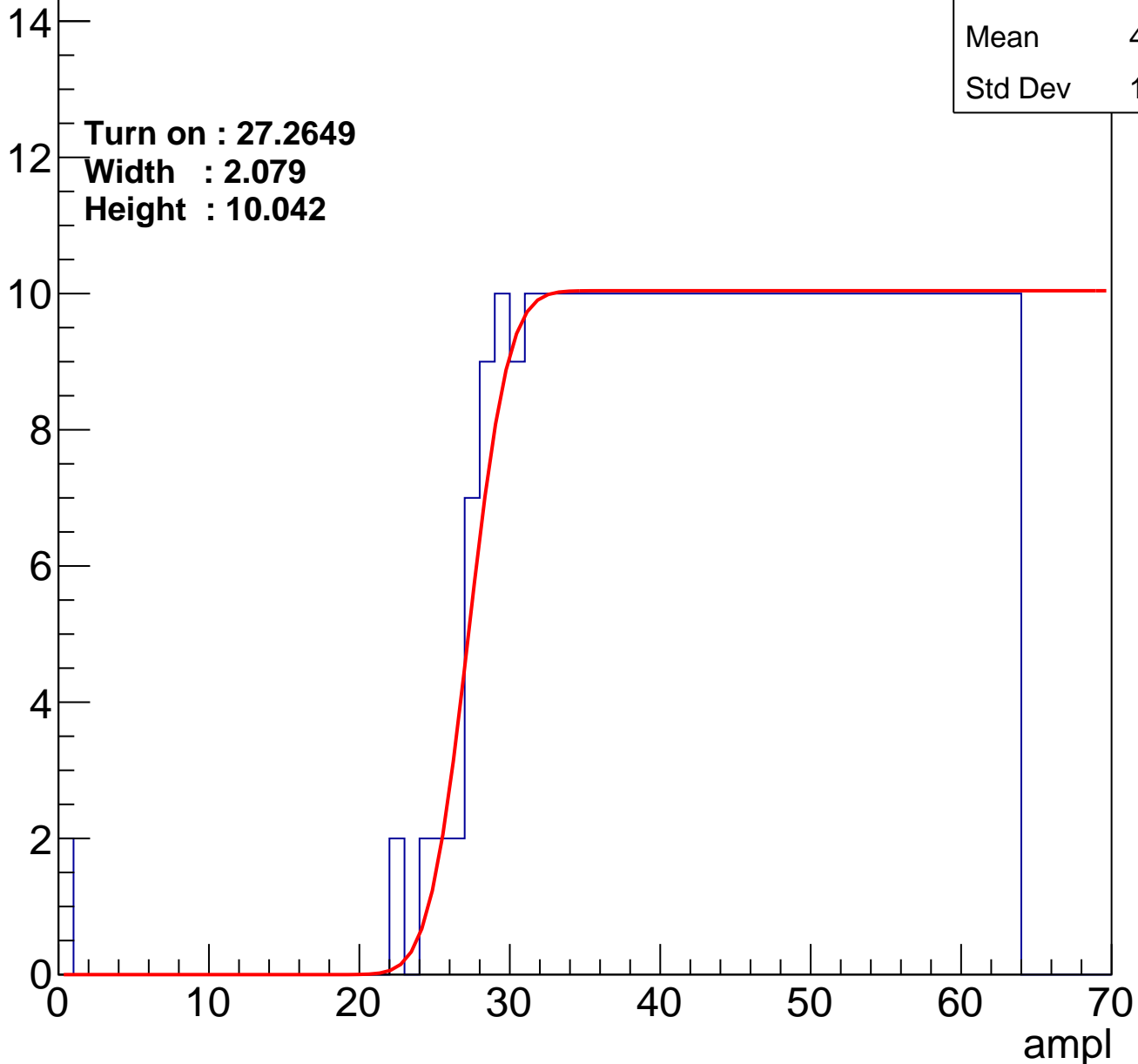
Entries	375
Mean	44.55
Std Dev	11.33

Turn on : 27.2649

Width : 2.079

Height : 10.042

Entry



B1L000S, U23-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.81
Std Dev	11.03

Turn on : 27.0392

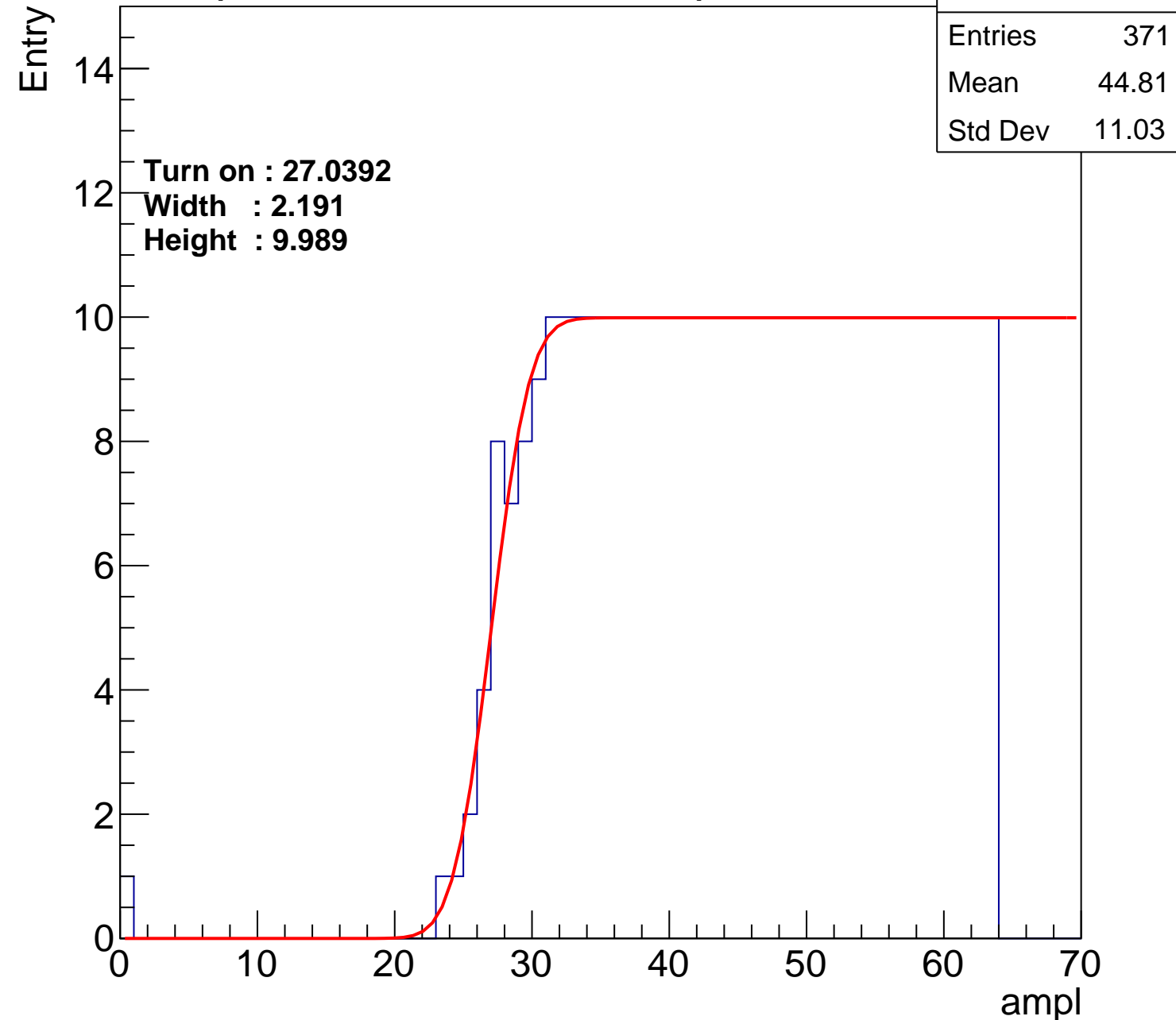
Width : 2.191

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 26.1197

Width : 1.667

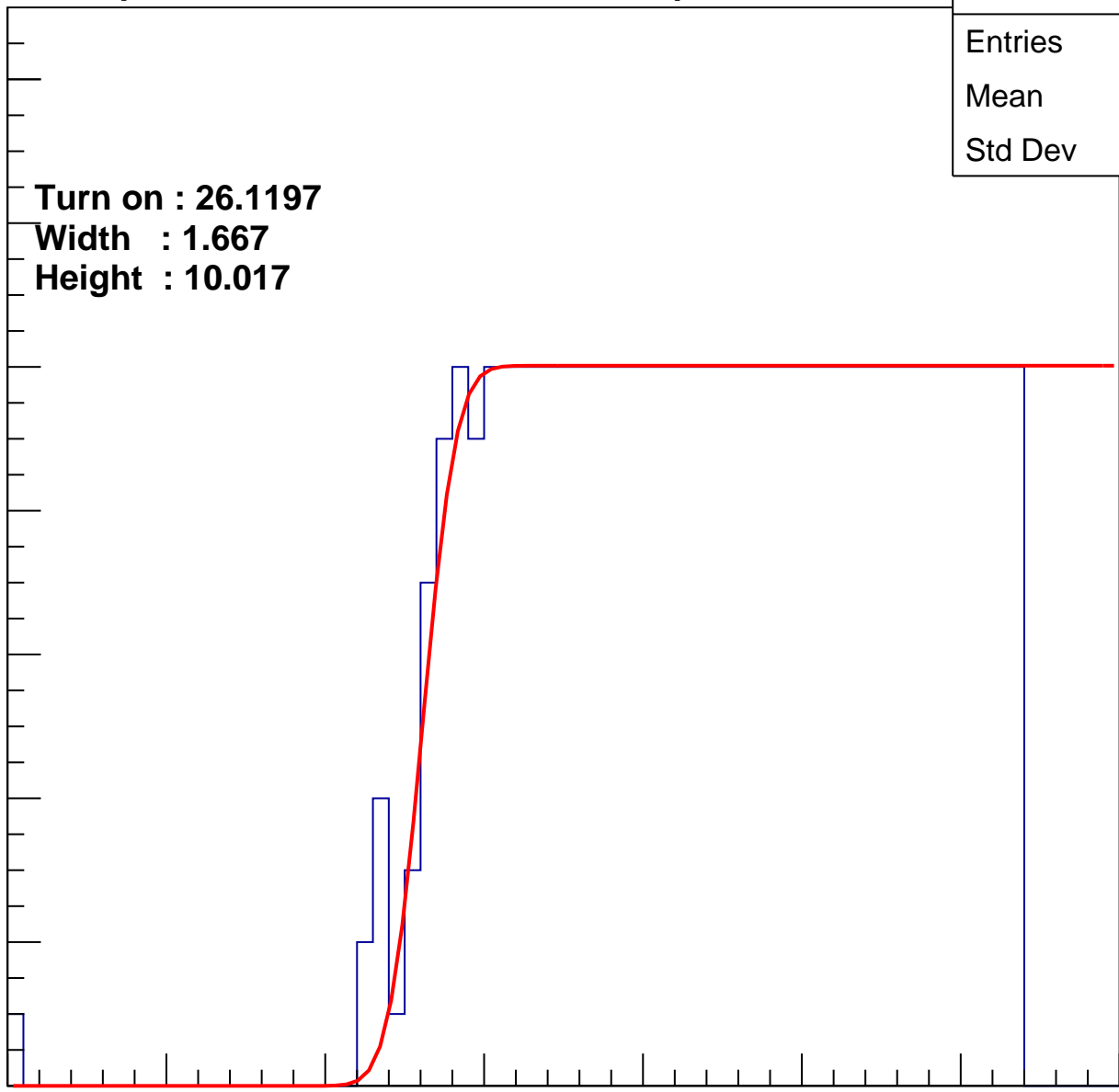
Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U23-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.65
Std Dev	11.93

Turn on : 25.3243

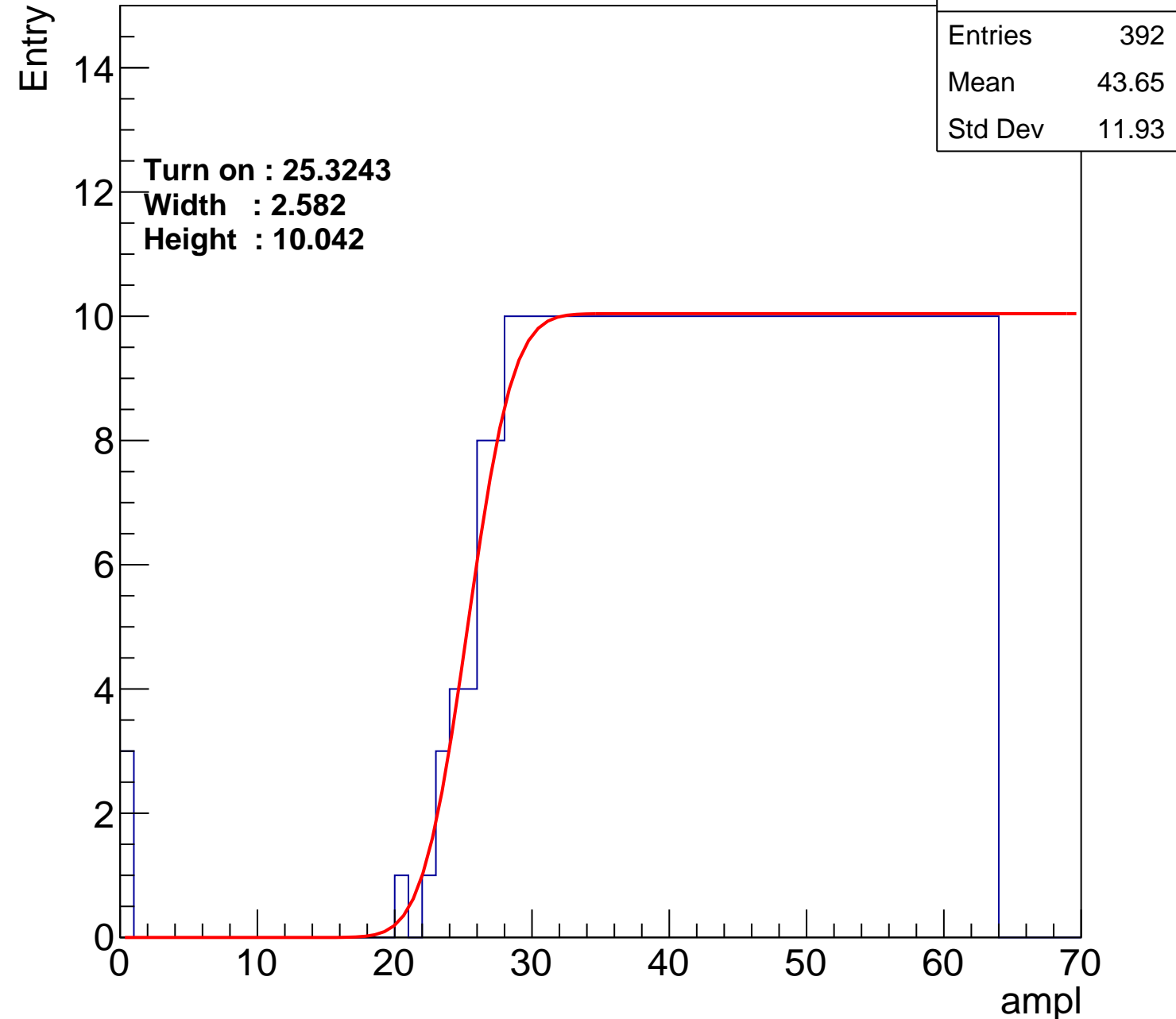
Width : 2.582

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch21

calib_packv5_042523_0143.root, FC#3, port B3

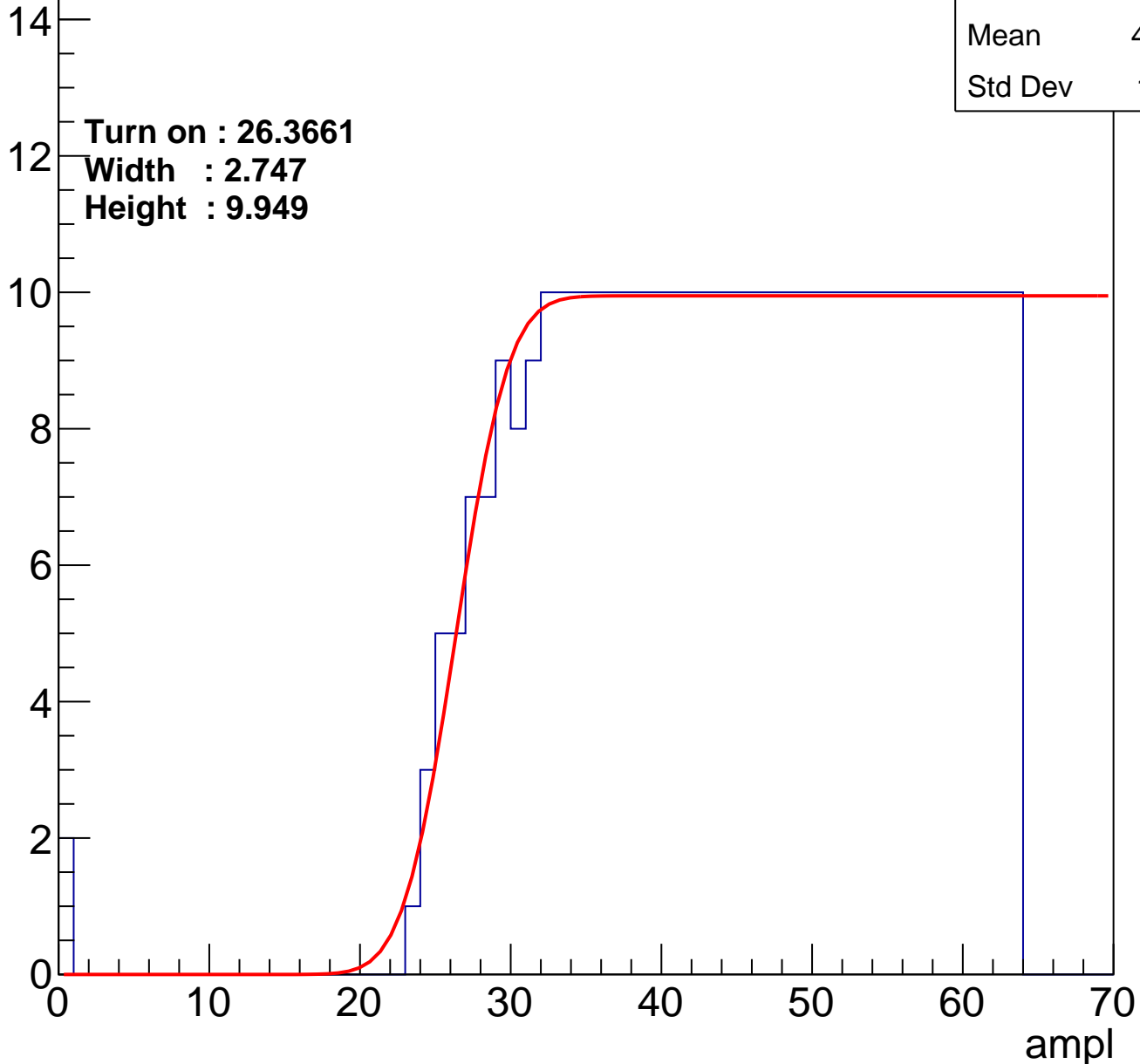
Entries	376
Mean	44.45
Std Dev	11.41

Turn on : 26.3661

Width : 2.747

Height : 9.949

Entry



B1L000S, U23-ch22

calib_packv5_042523_0143.root, FC#3, port B3

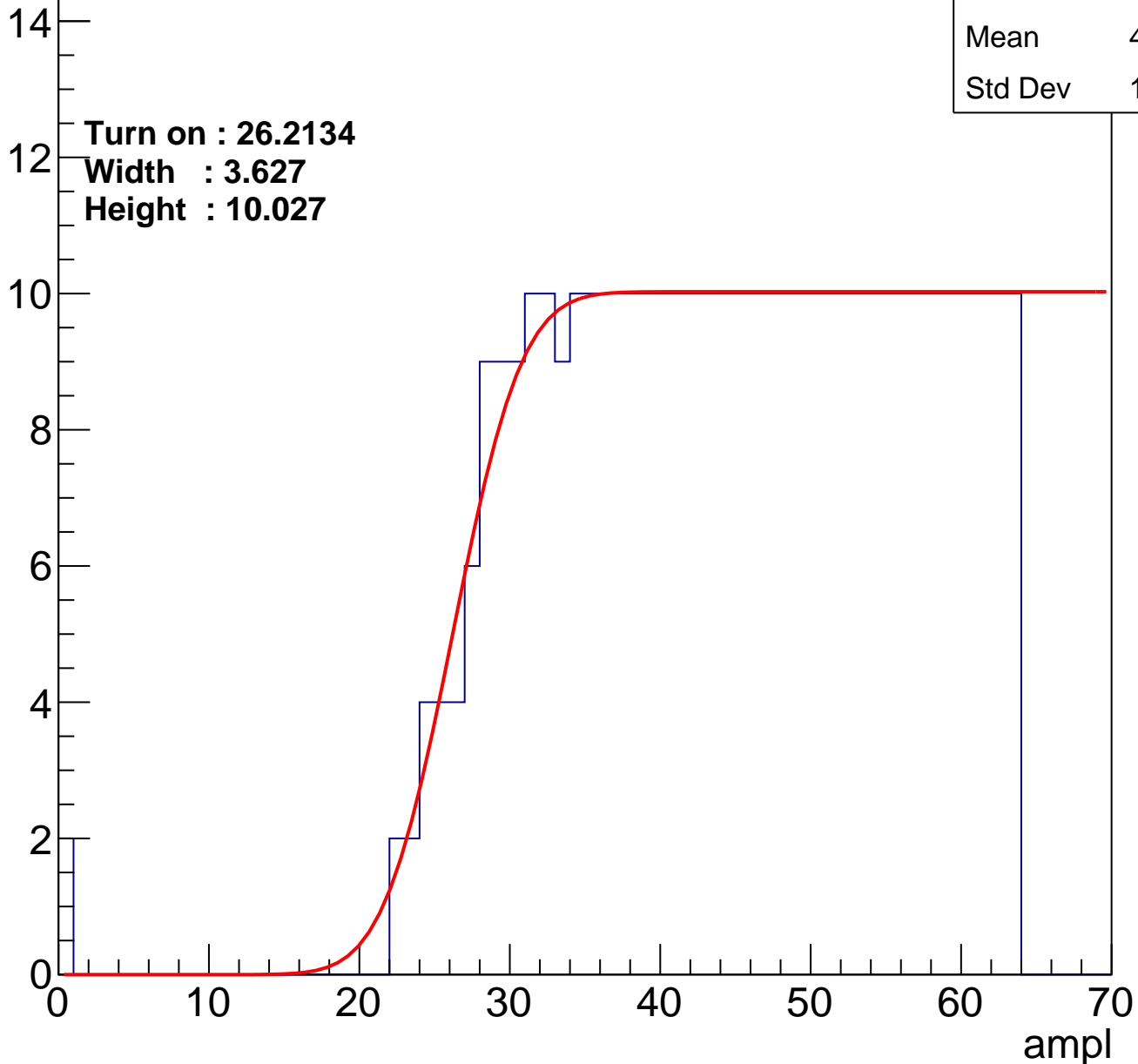
Entries	380
Mean	44.24
Std Dev	11.54

Turn on : 26.2134

Width : 3.627

Height : 10.027

Entry



B1L000S, U23-ch23

calib_packv5_042523_0143.root, FC#3, port B3

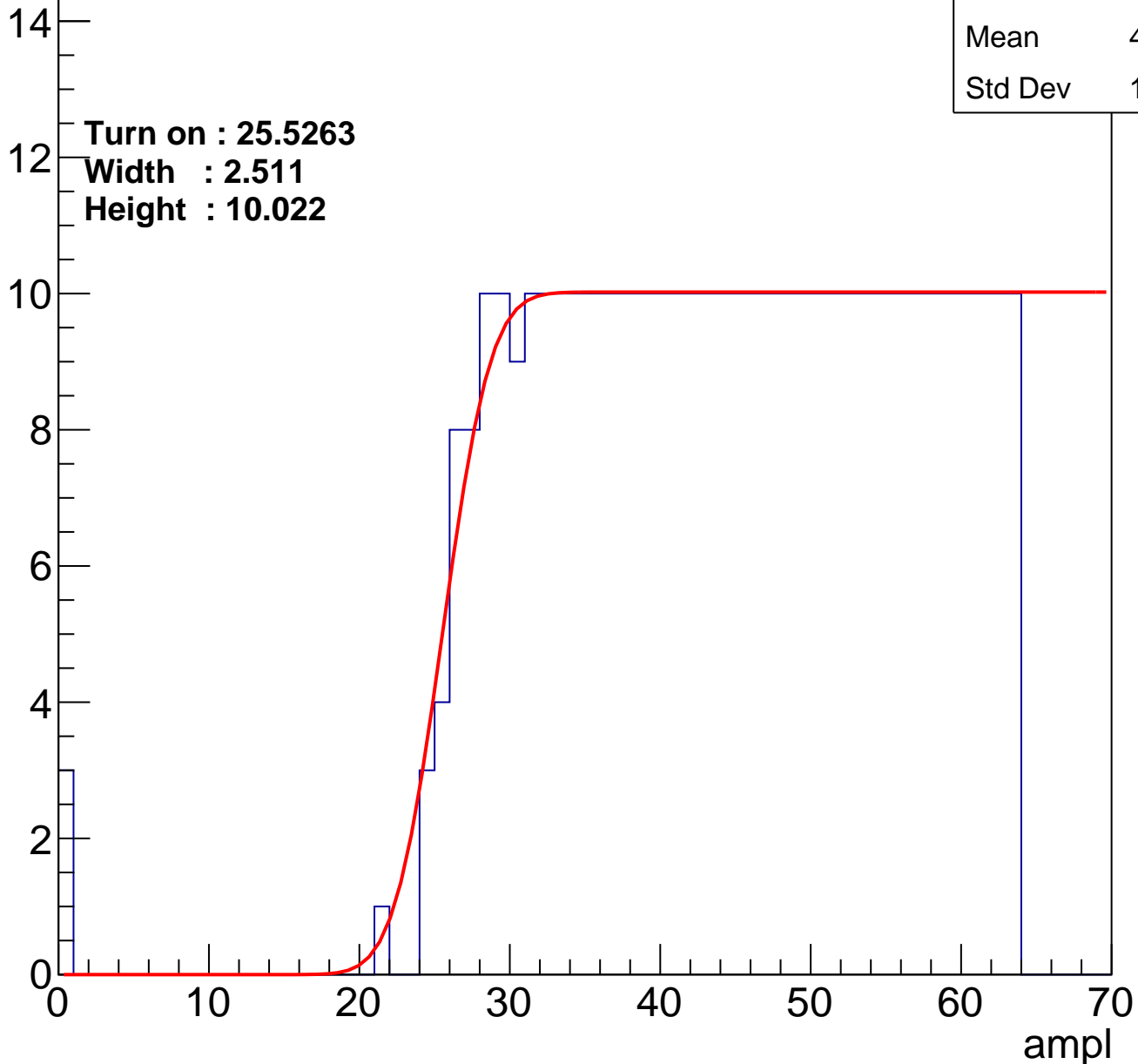
Entries	386
Mean	43.96
Std Dev	11.76

Turn on : 25.5263

Width : 2.511

Height : 10.022

Entry



B1L000S, U23-ch24

calib_packv5_042523_0143.root, FC#3, port B3

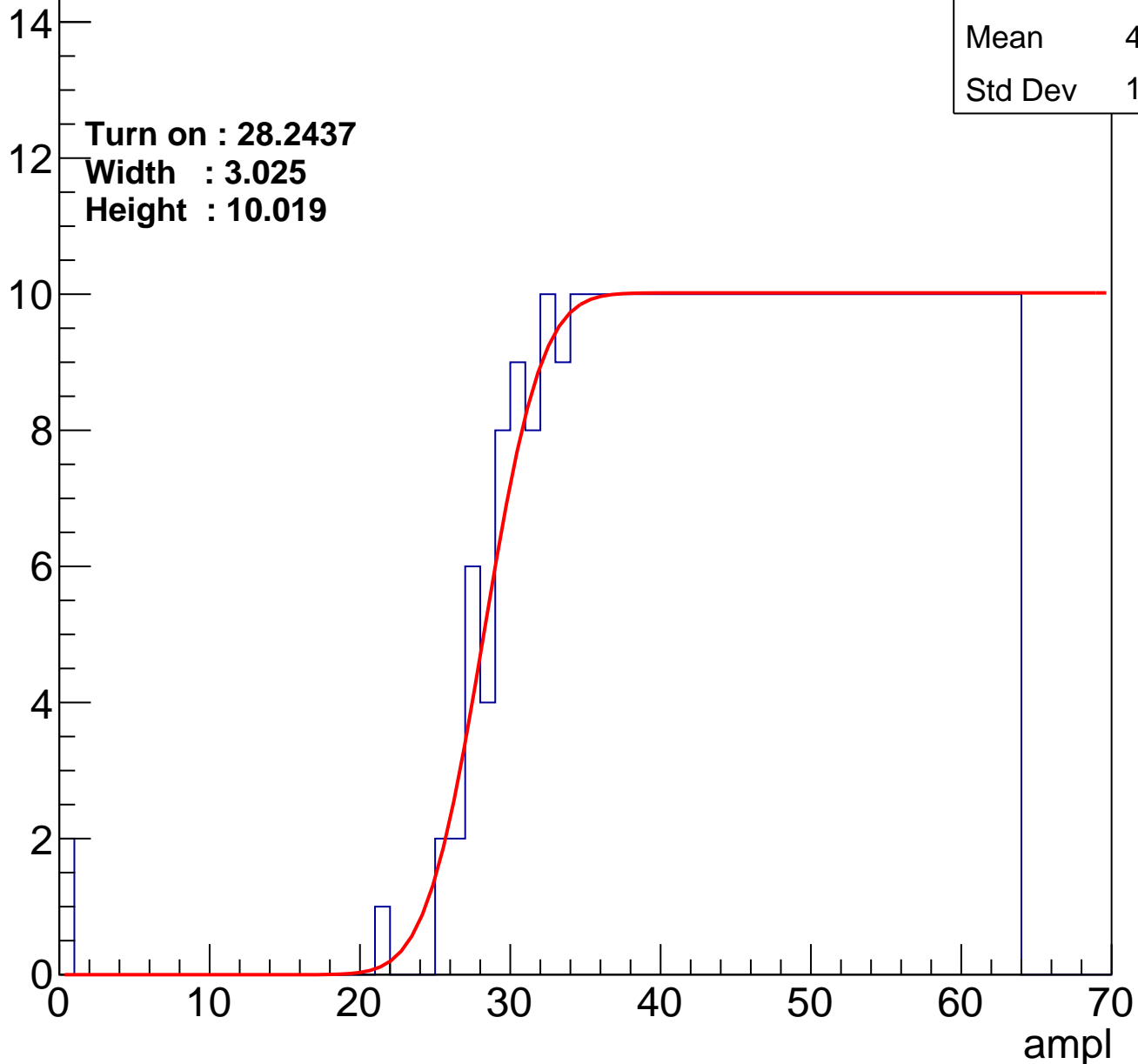
Entries	361
Mean	45.19
Std Dev	11.04

Turn on : 28.2437

Width : 3.025

Height : 10.019

Entry



B1L000S, U23-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.41
Std Dev	10.92

Turn on : 28.5389

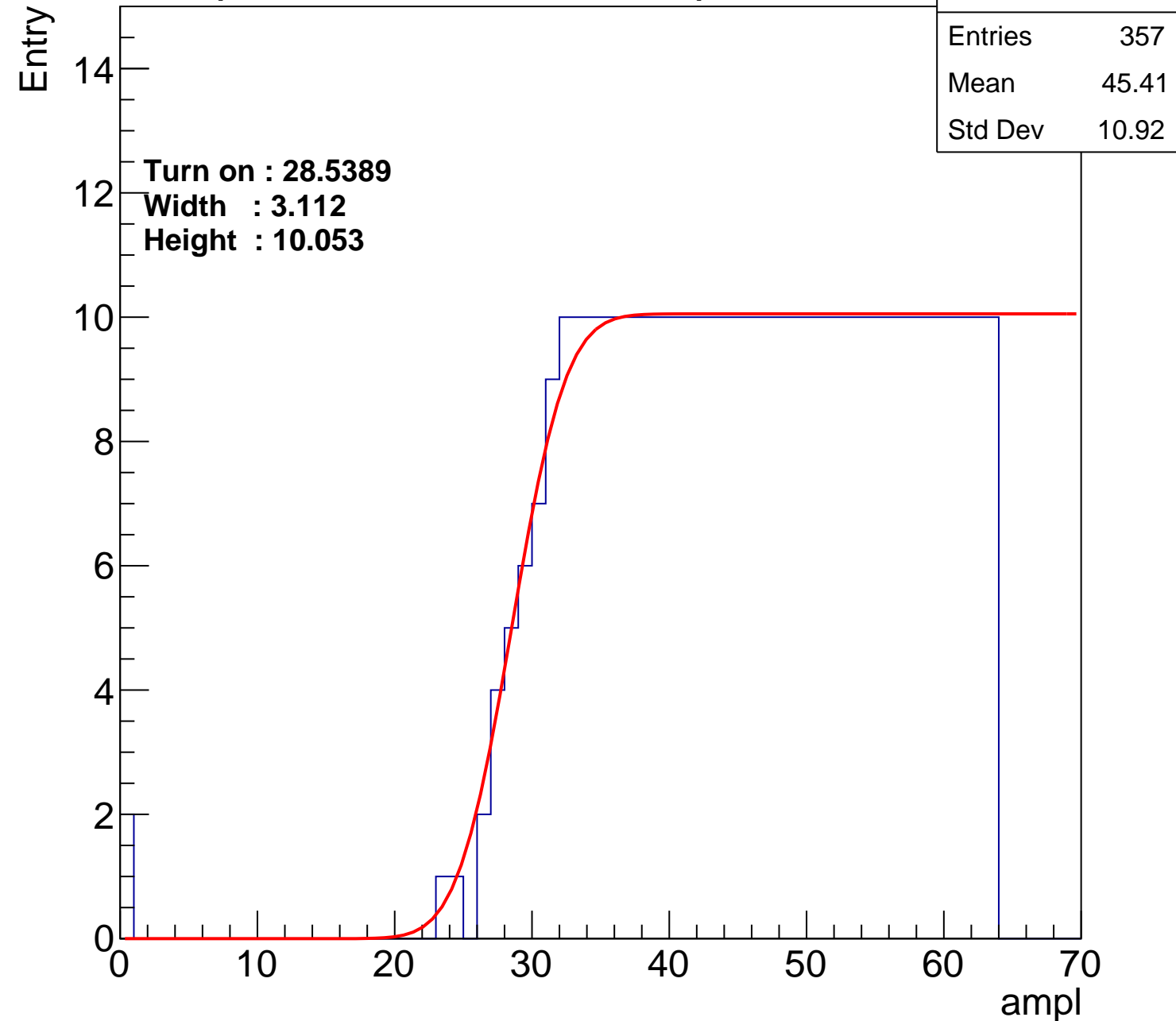
Width : 3.112

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.45
Std Dev	11.57

Turn on : 27.5581

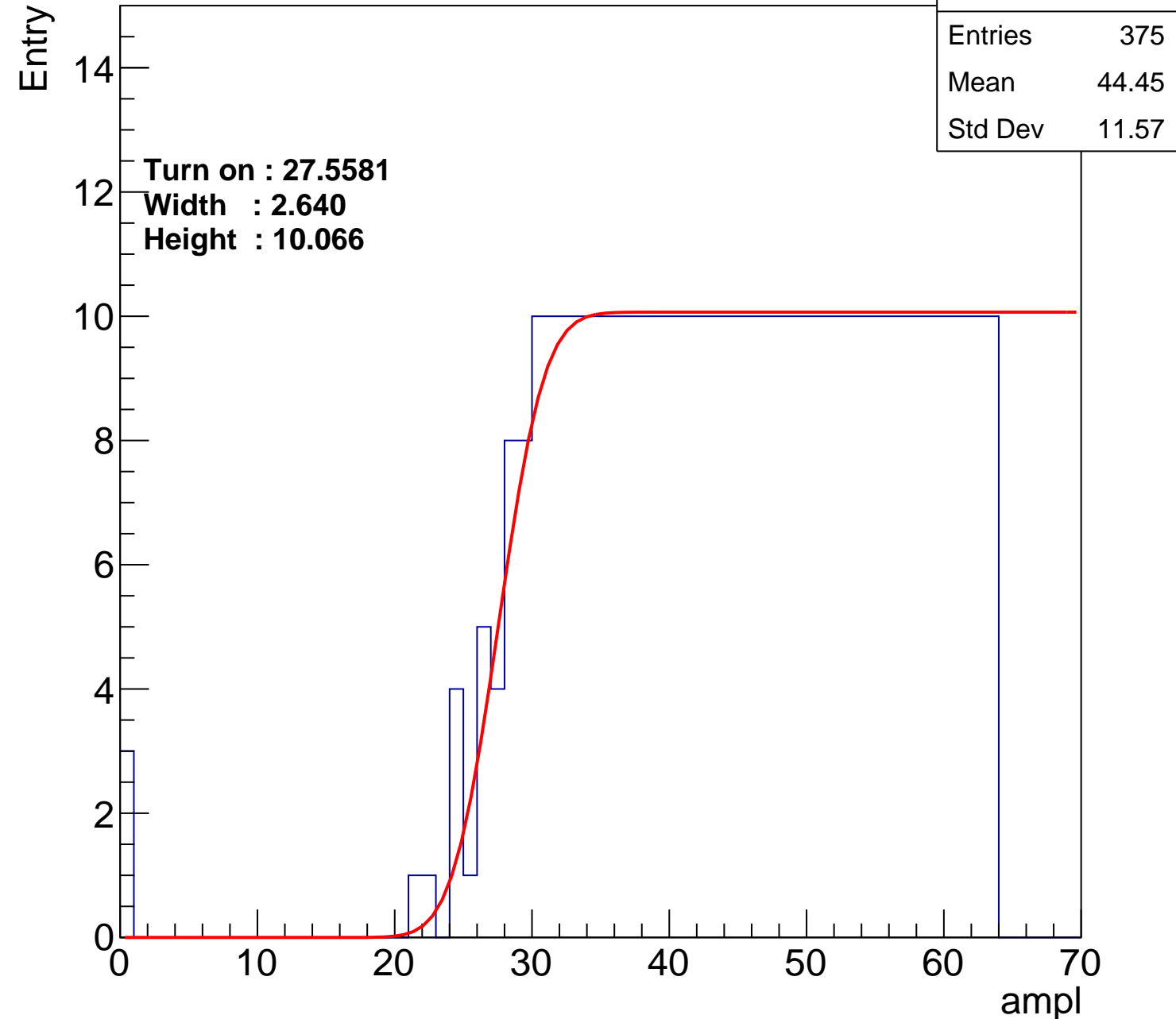
Width : 2.640

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch27

calib_packv5_042523_0143.root, FC#3, port B3

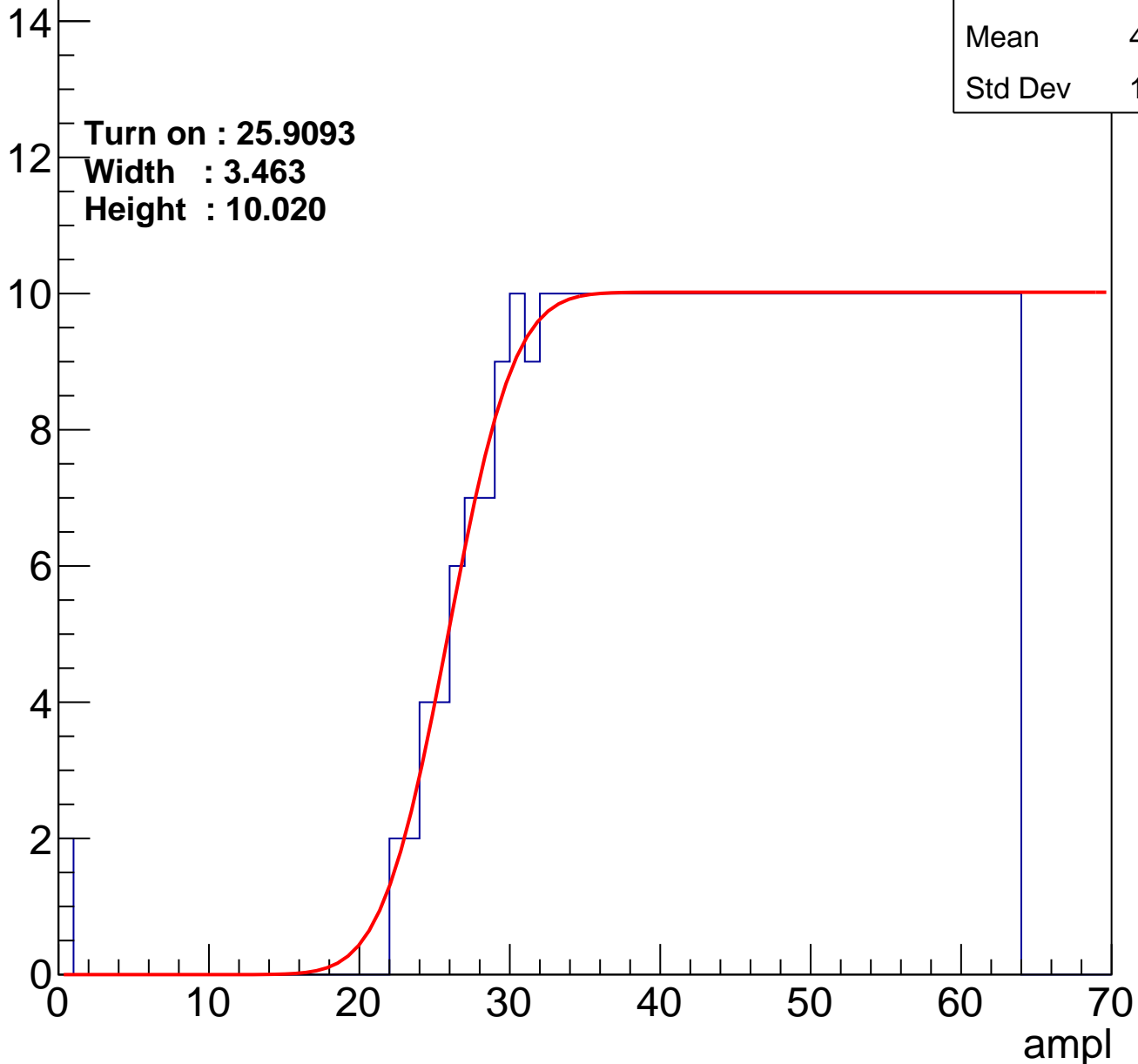
Entries	382
Mean	44.15
Std Dev	11.58

Turn on : 25.9093

Width : 3.463

Height : 10.020

Entry



B1L000S, U23-ch28

calib_packv5_042523_0143.root, FC#3, port B3

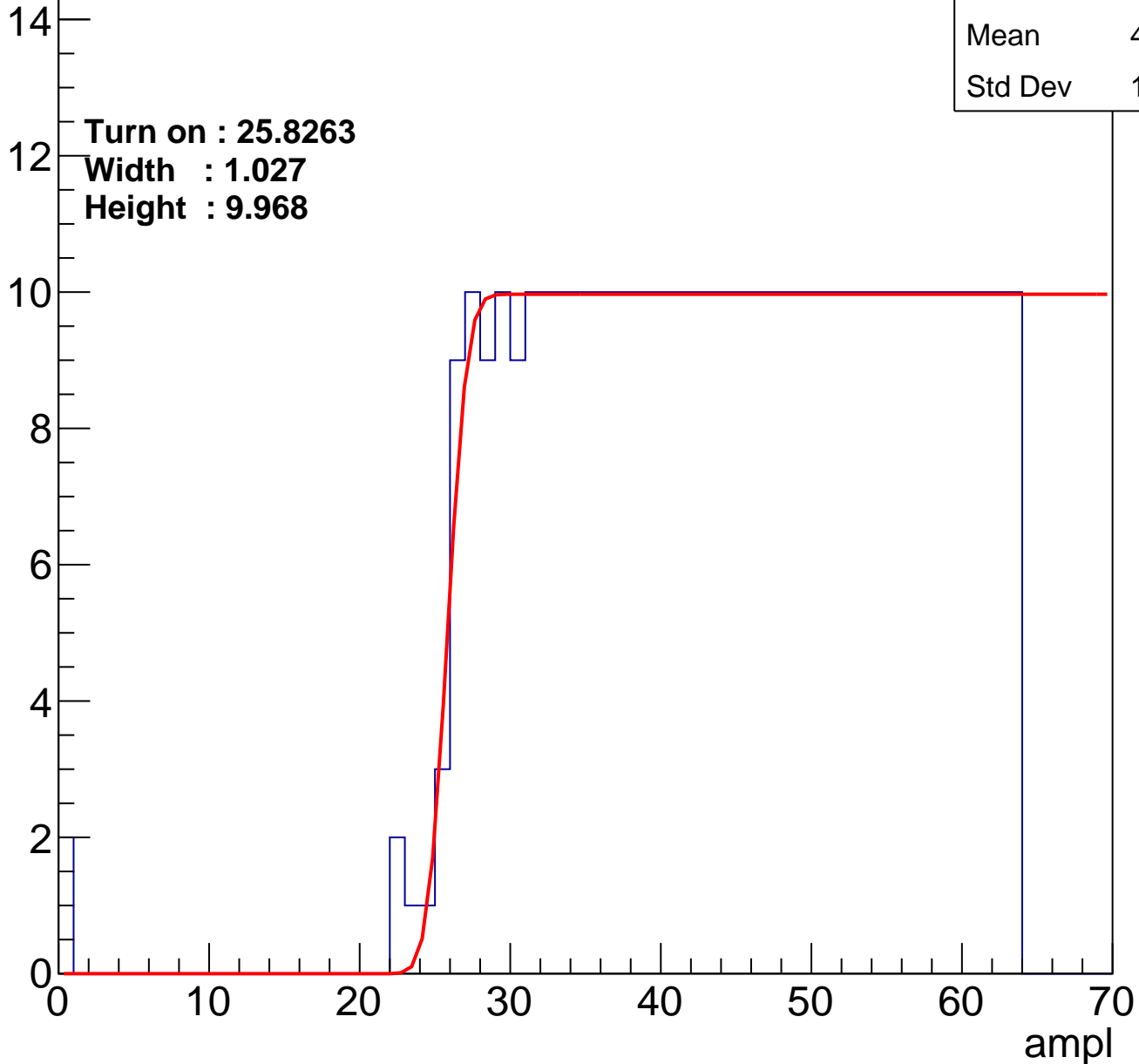
Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 25.8263

Width : 1.027

Height : 9.968

Entry



B1L000S, U23-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.97
Std Dev	11.05

Turn on : 28.1795

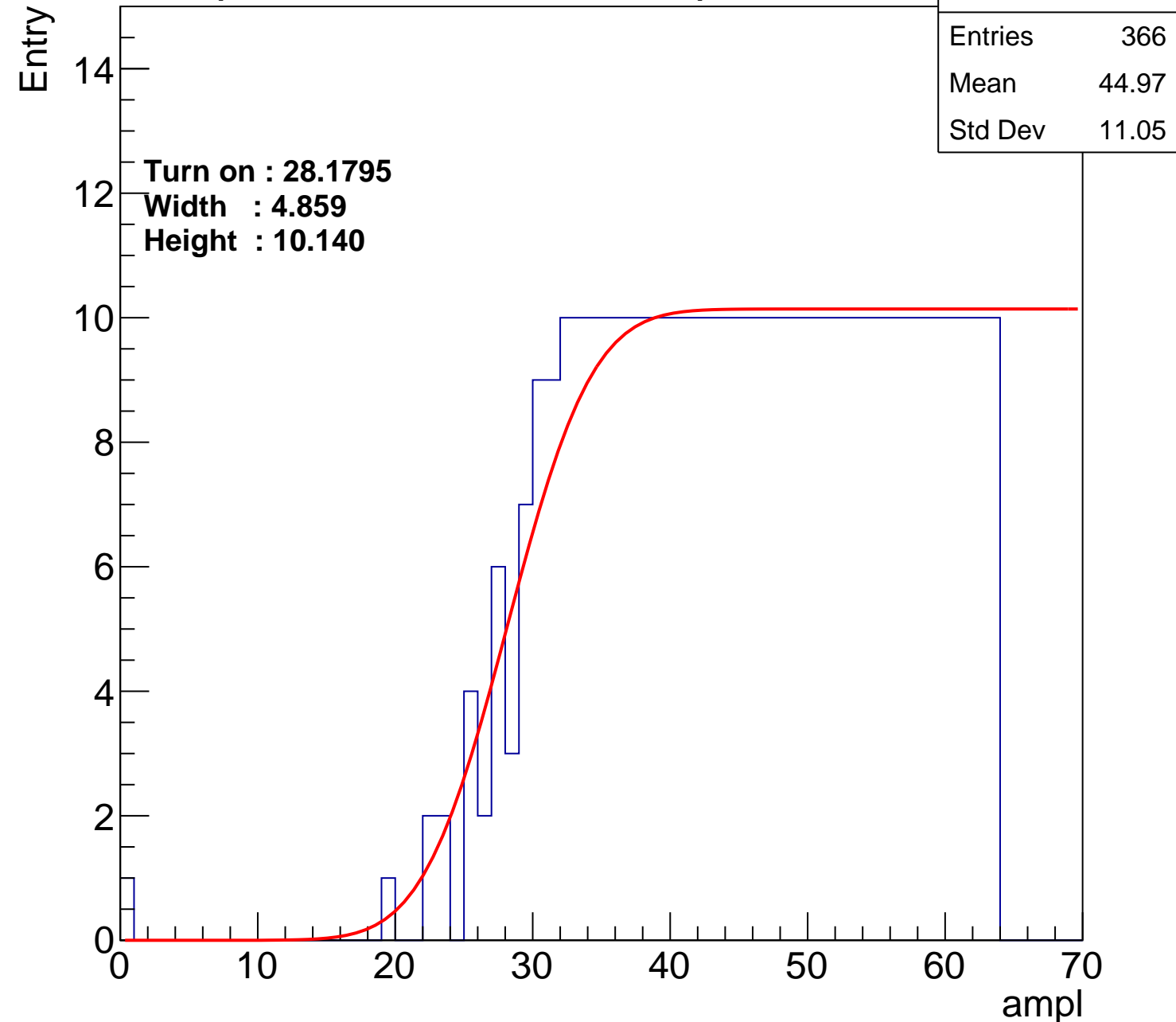
Width : 4.859

Height : 10.140

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.06
Std Dev	11.58

Turn on : 25.6016

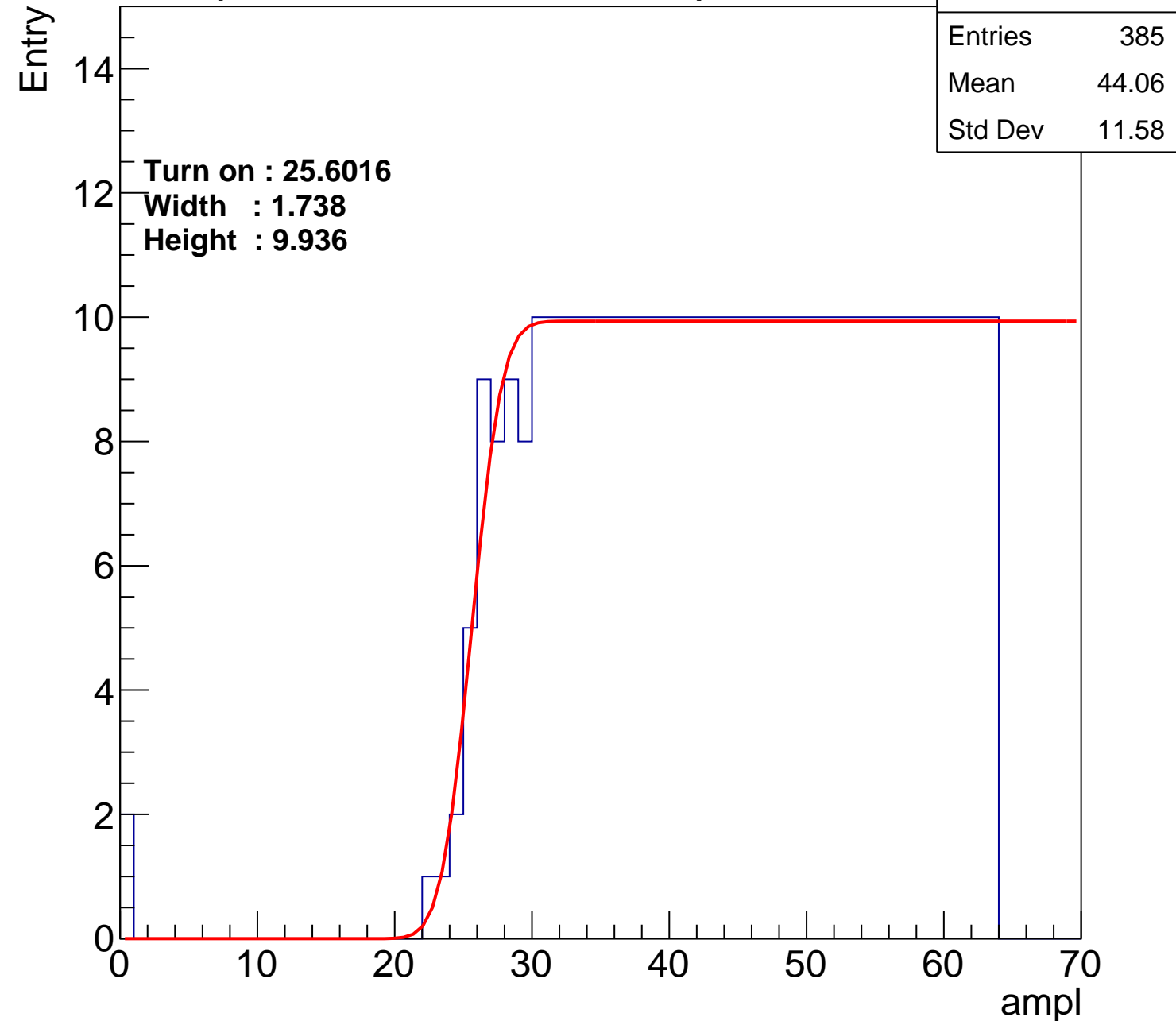
Width : 1.738

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.92
Std Dev	11.93

Turn on : 25.9412

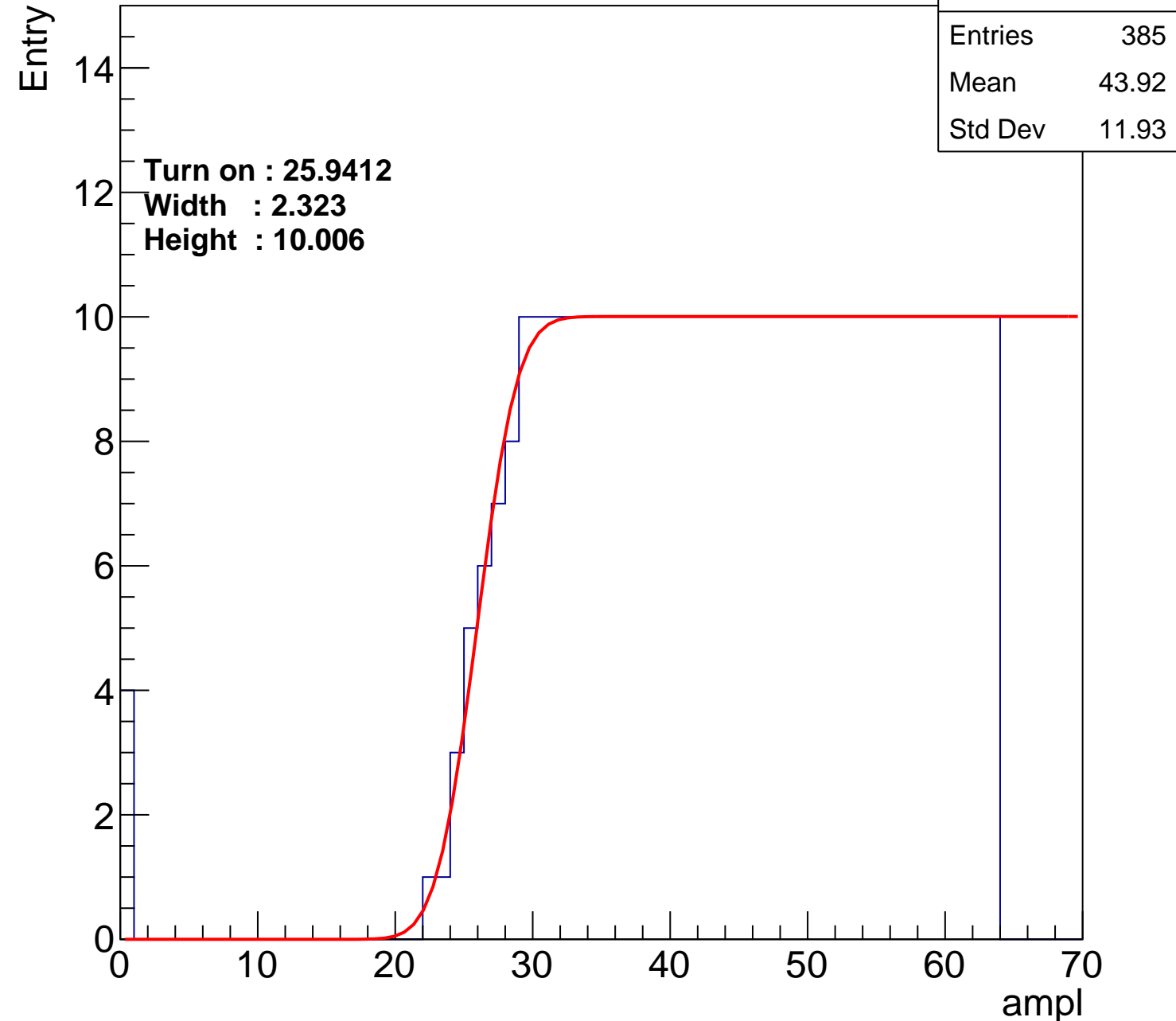
Width : 2.323

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.3382

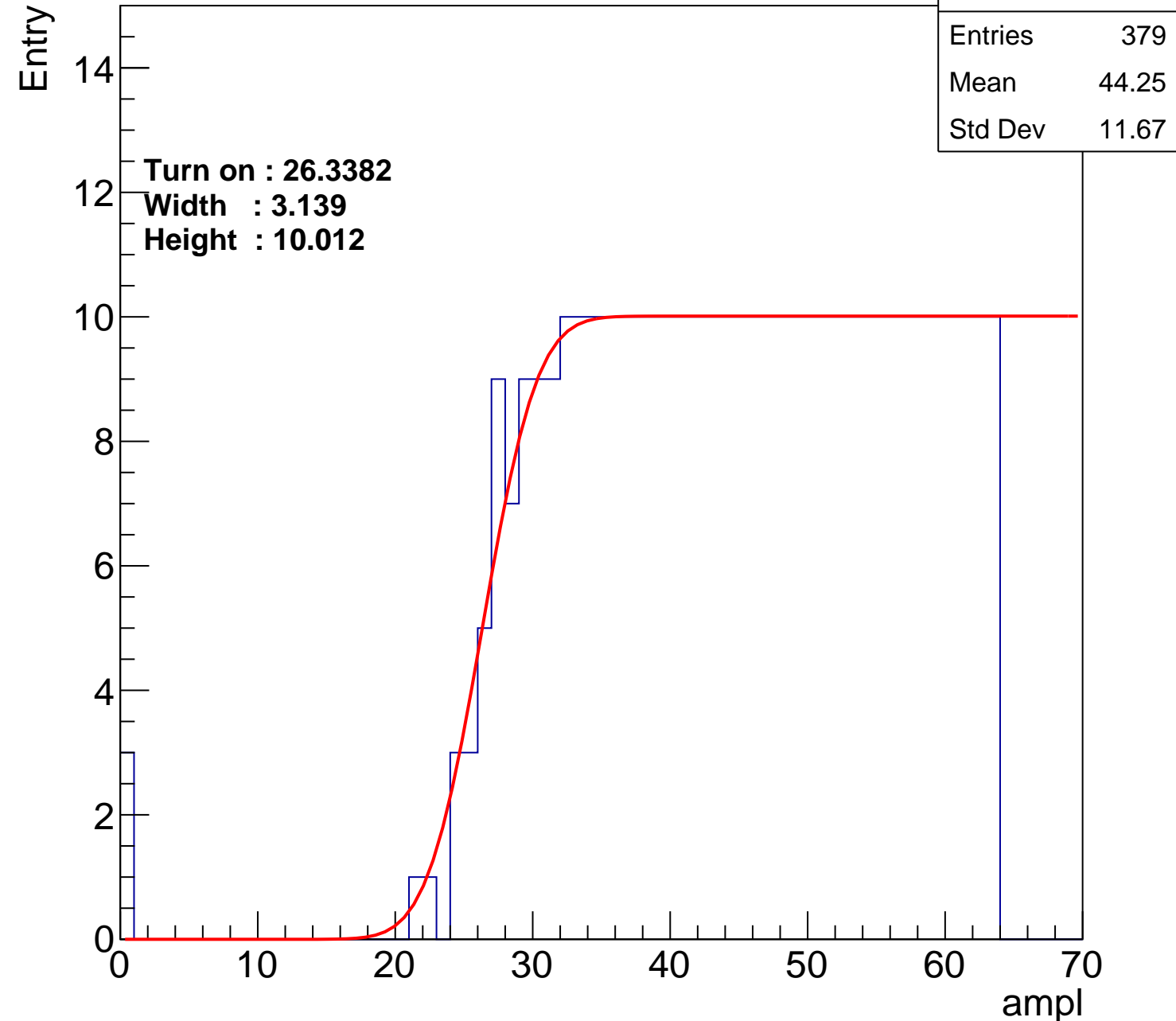
Width : 3.139

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.64
Std Dev	11.64

Turn on : 24.3790

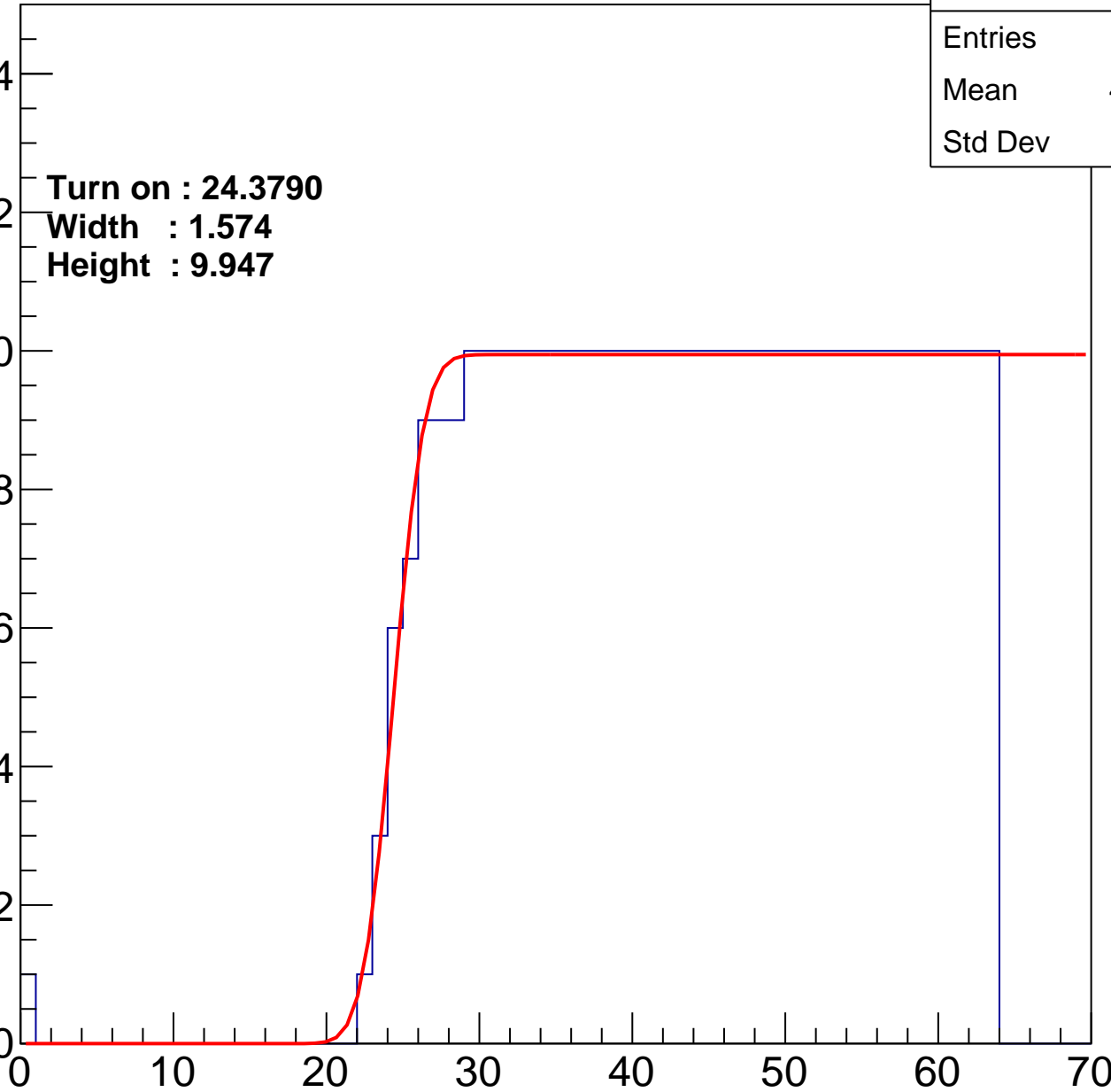
Width : 1.574

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch34

calib_packv5_042523_0143.root, FC#3, port B3

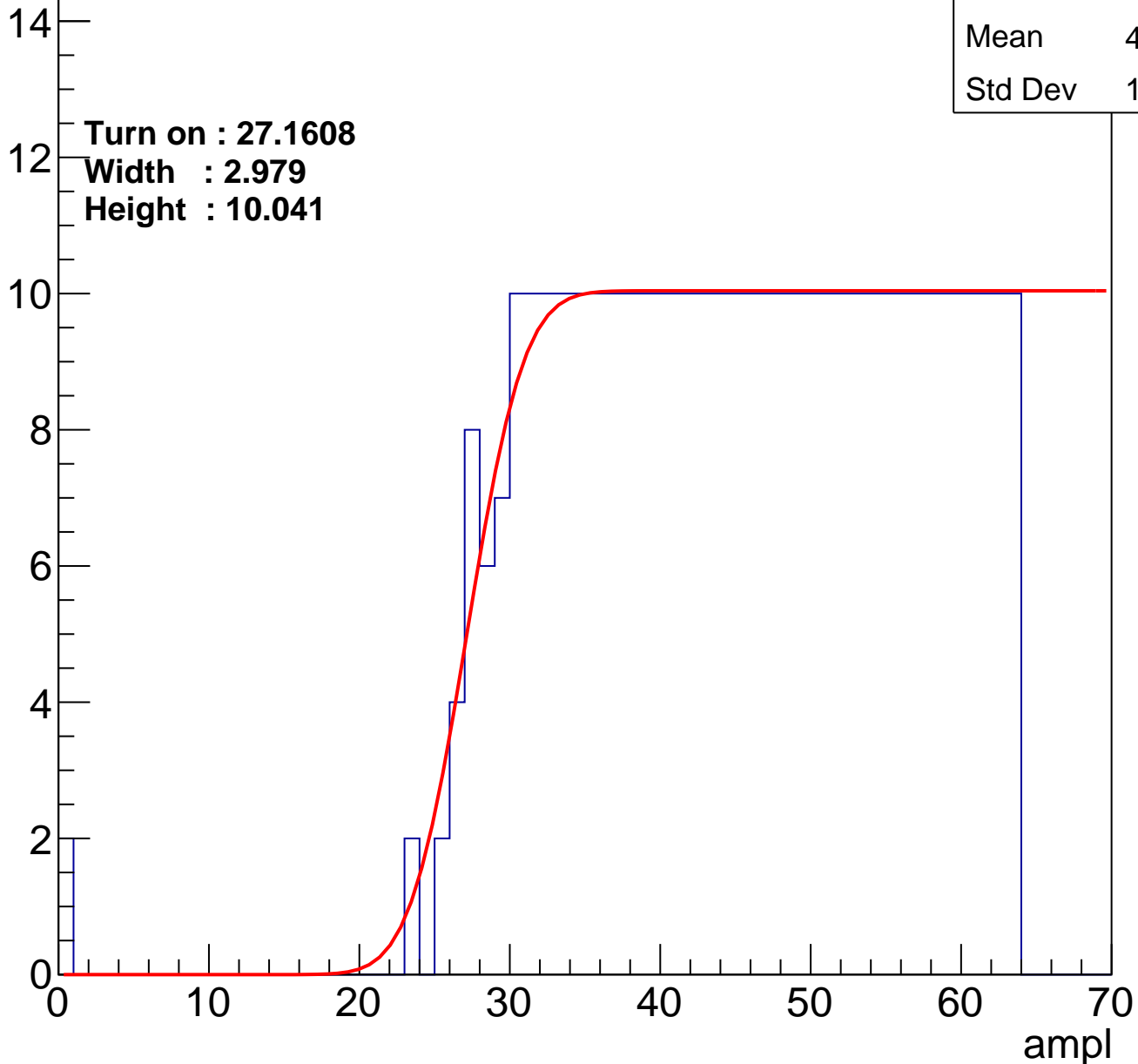
Entries	371
Mean	44.74
Std Dev	11.24

Turn on : 27.1608

Width : 2.979

Height : 10.041

Entry



B1L000S, U23-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.36
Std Dev	10.98

Turn on : 29.0033

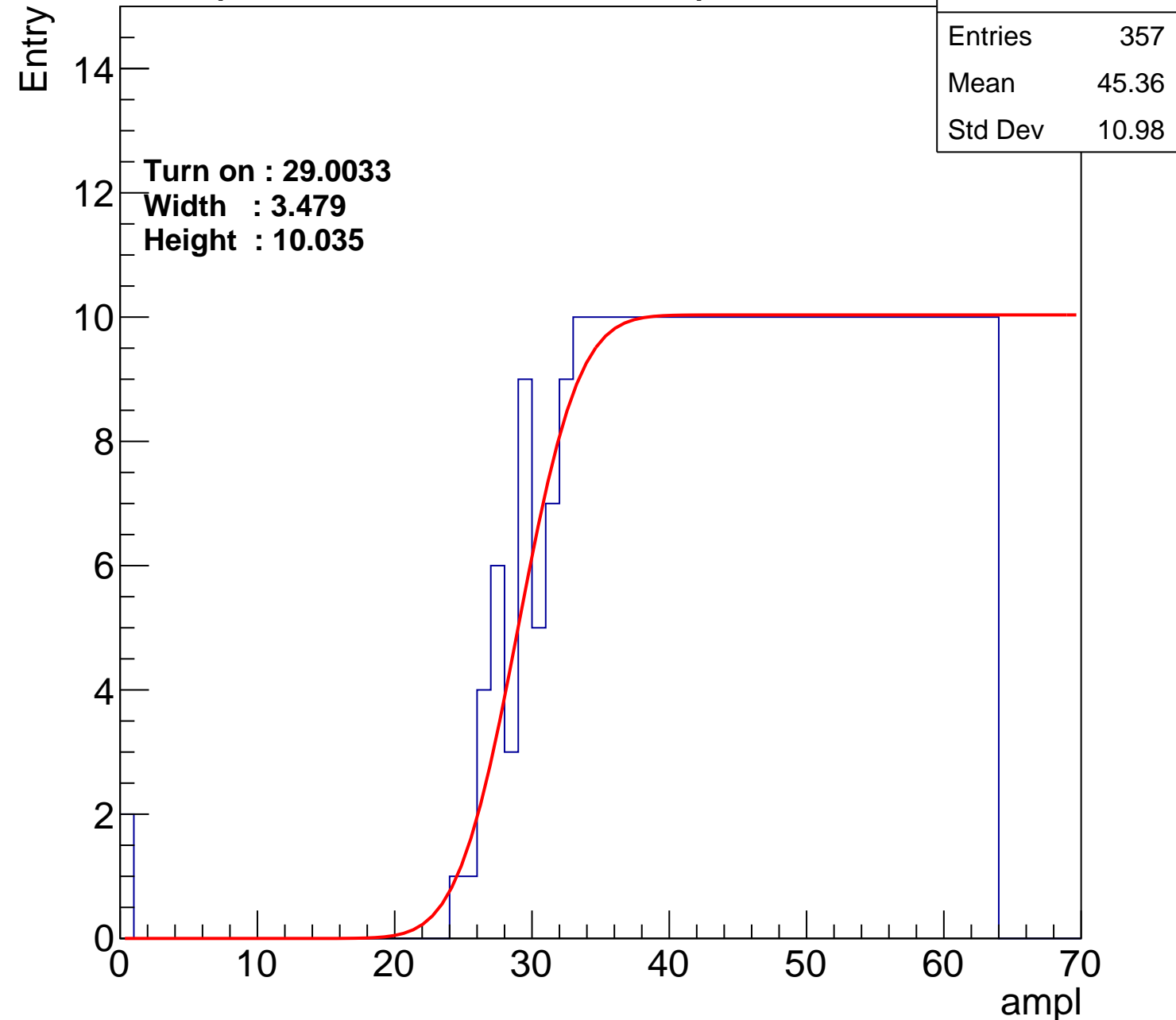
Width : 3.479

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 25.6157

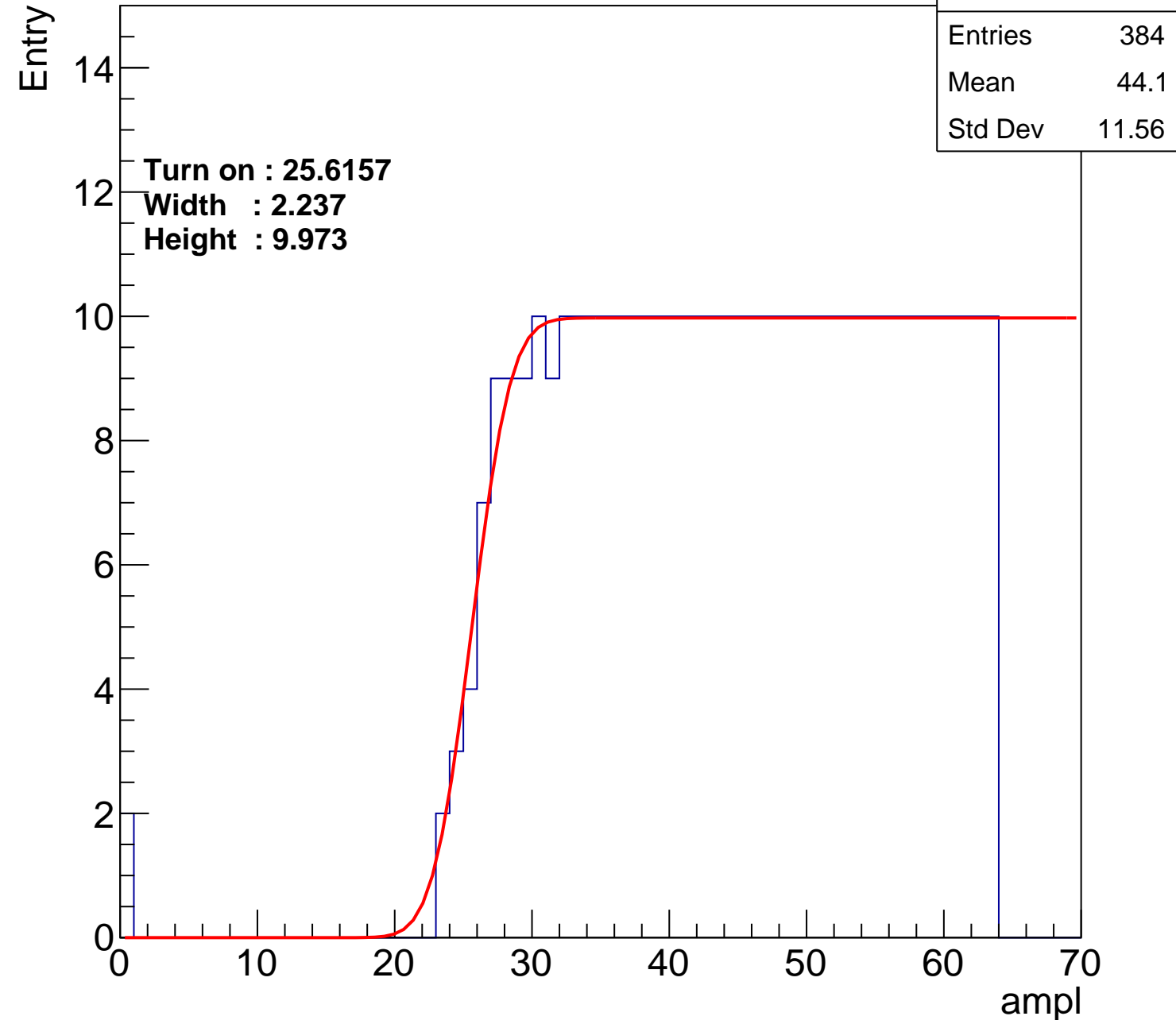
Width : 2.237

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.57

Turn on : 25.3714

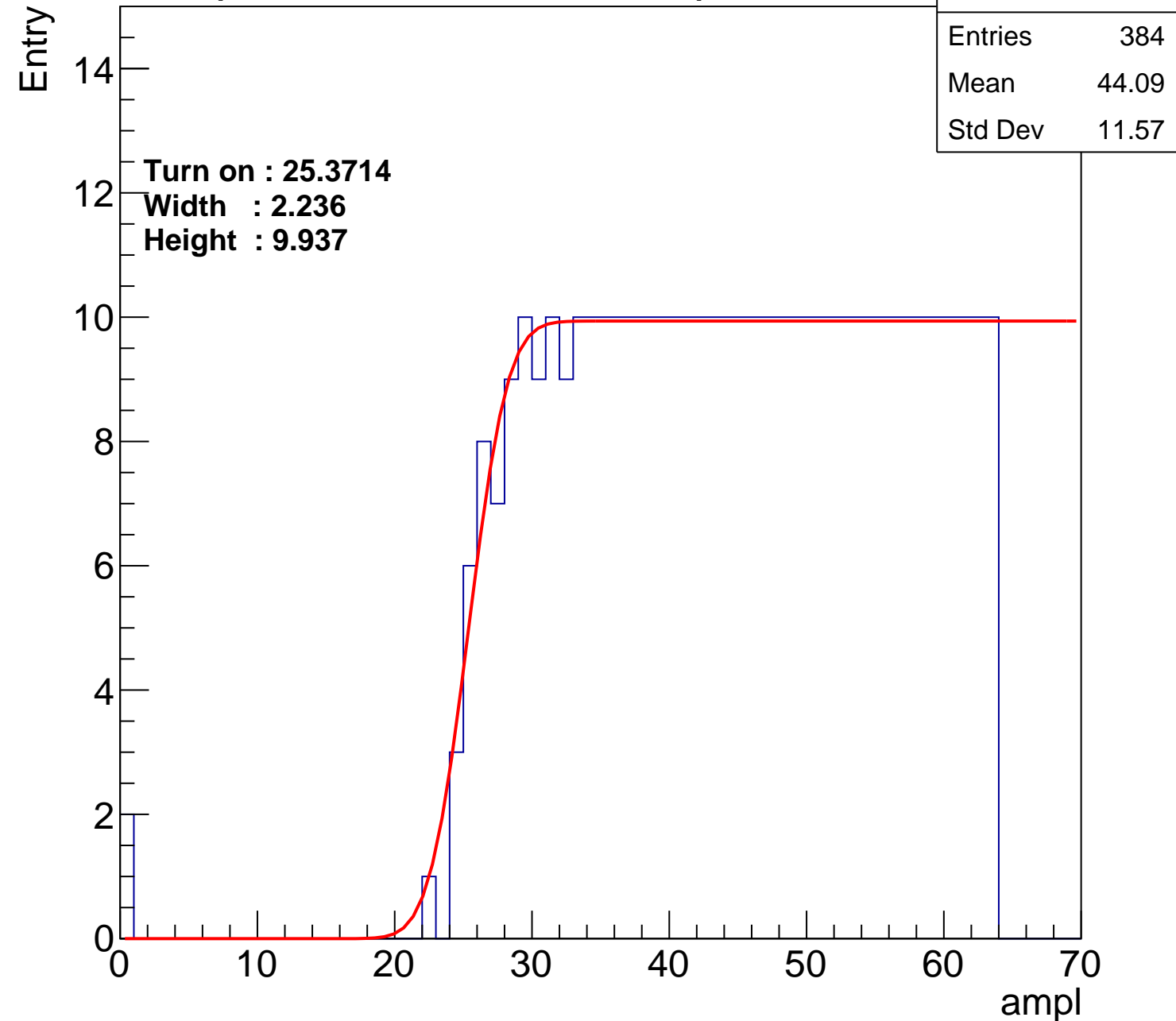
Width : 2.236

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 27.0708

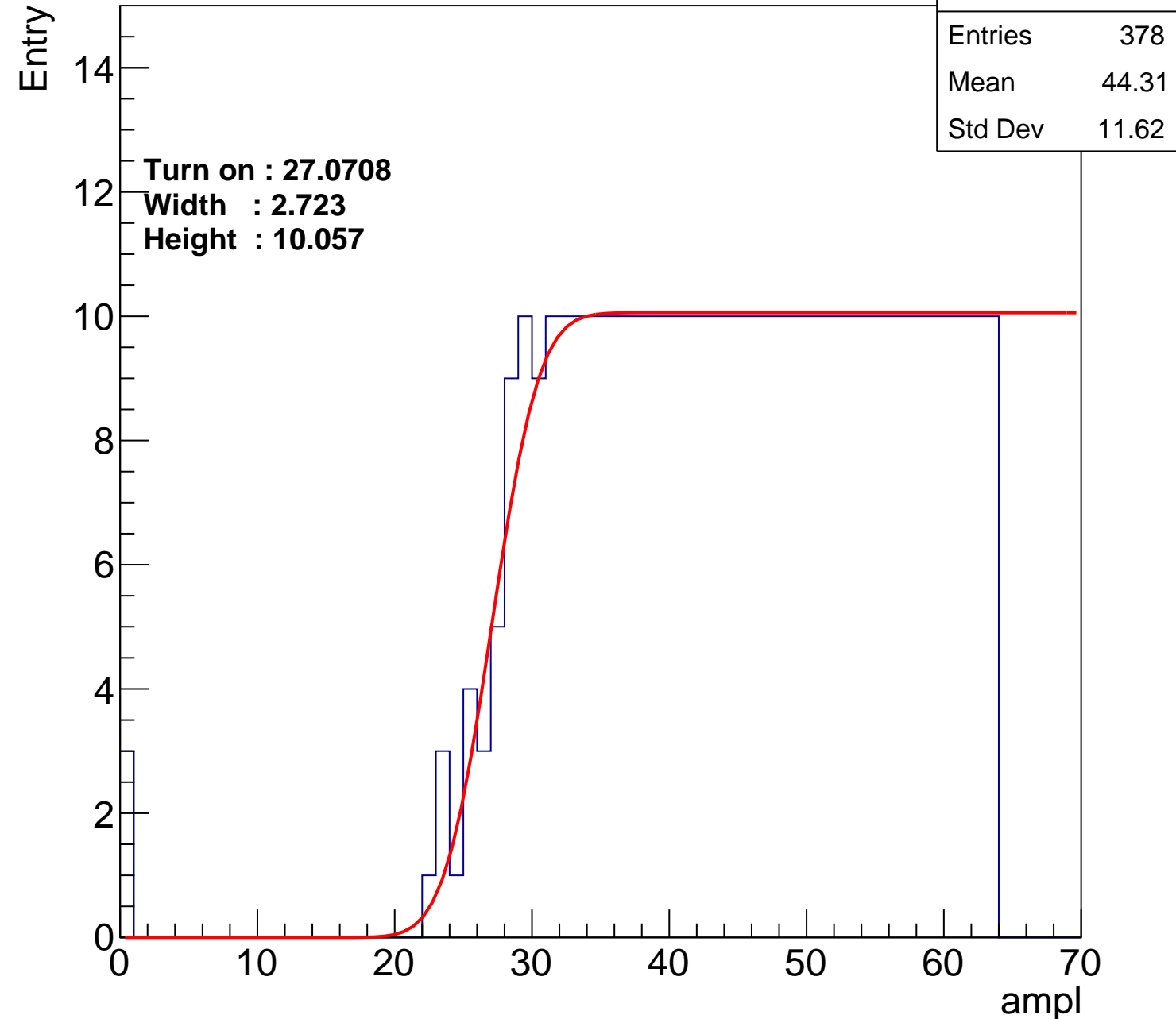
Width : 2.723

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.96
Std Dev	10.94

Mean	44.96
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Std Dev	10.94
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Width : 2.226

Height : 10.019



B1L000S, U23-ch40

calib_packv5_042523_0143.root, FC#3, port B3

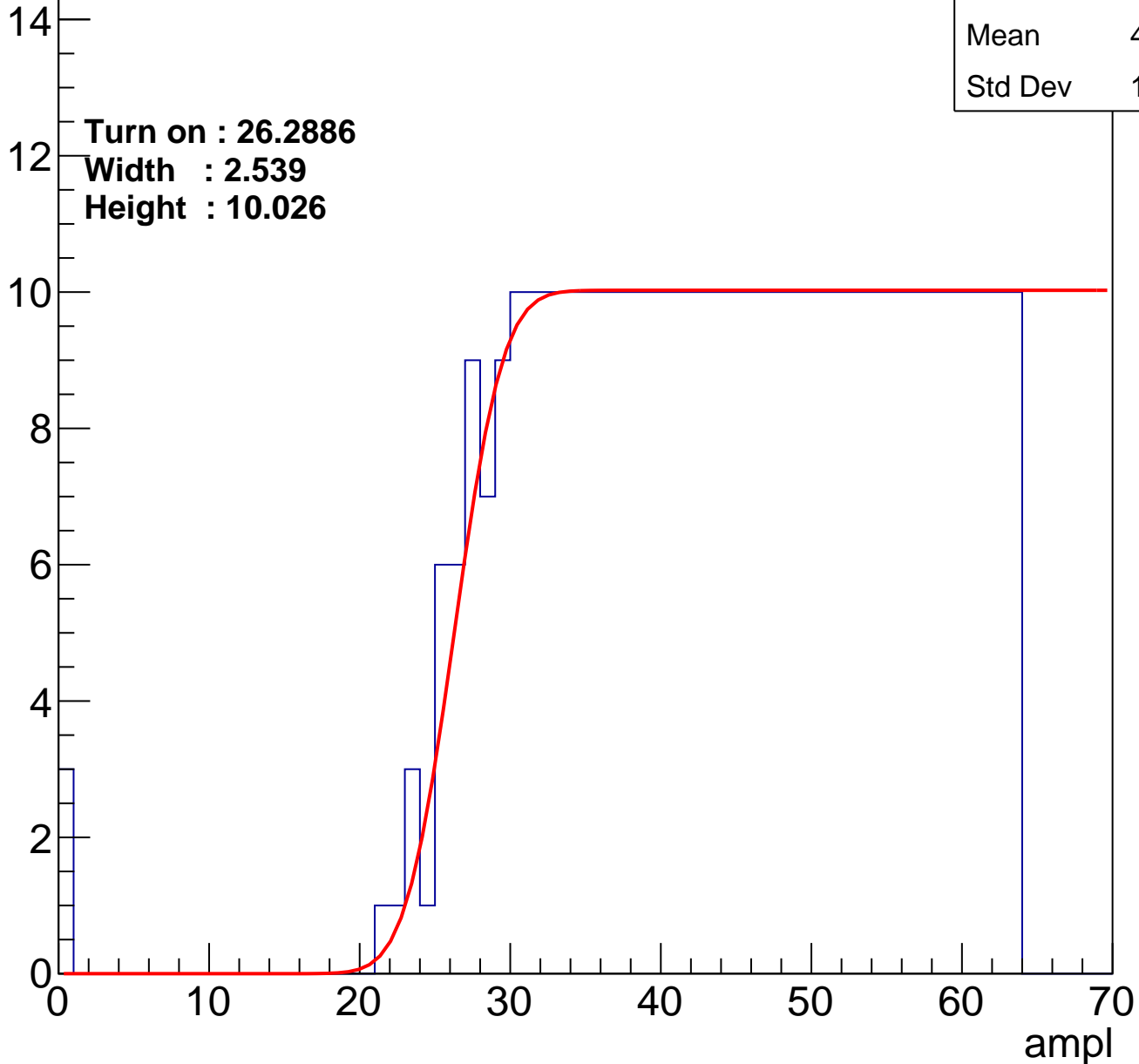
Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.2886

Width : 2.539

Height : 10.026

Entry



B1L000S, U23-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	43.04
Std Dev	12.15

Turn on : 23.8355

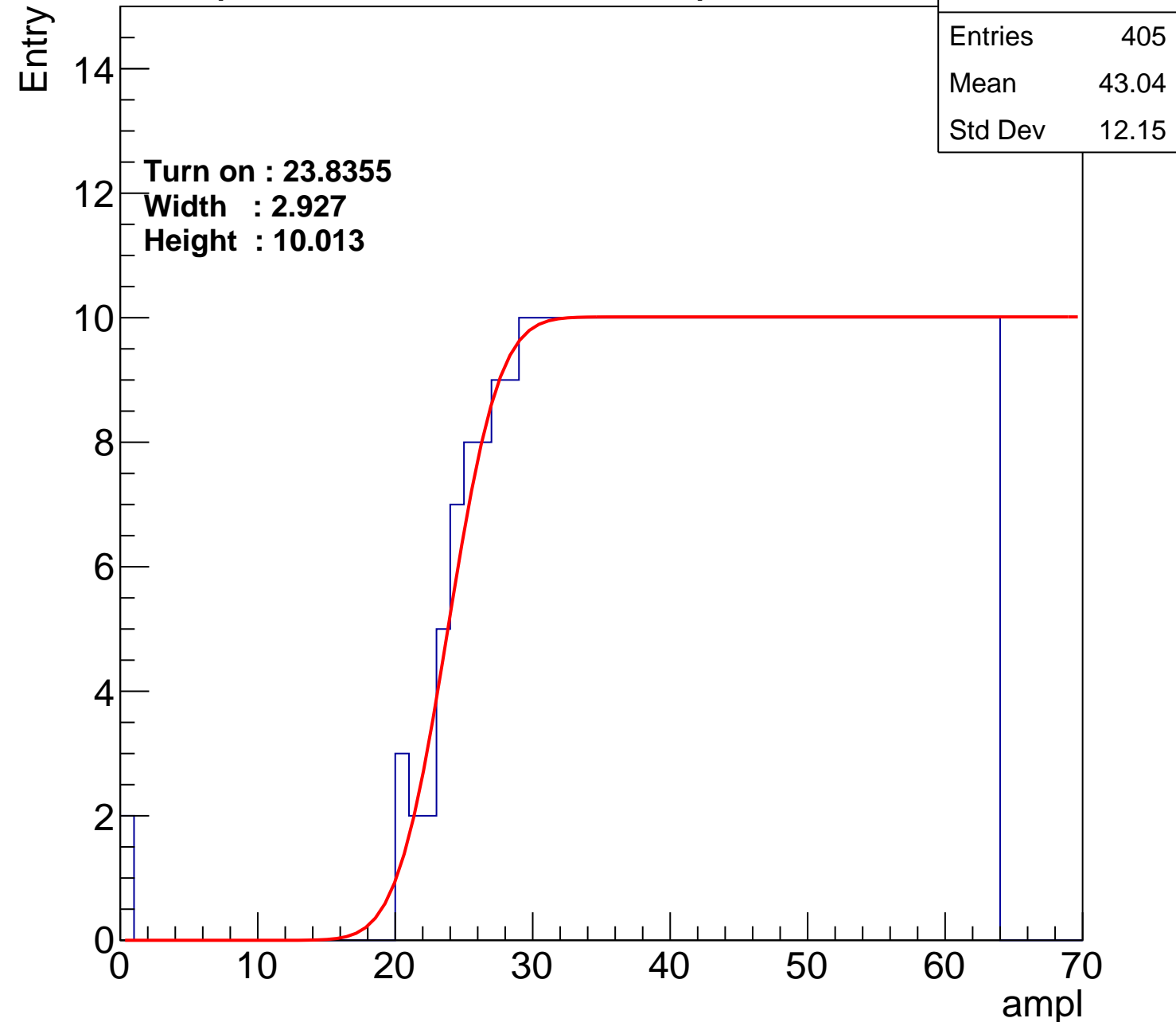
Width : 2.927

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.49
Std Dev	11.8

Turn on : 24.6895

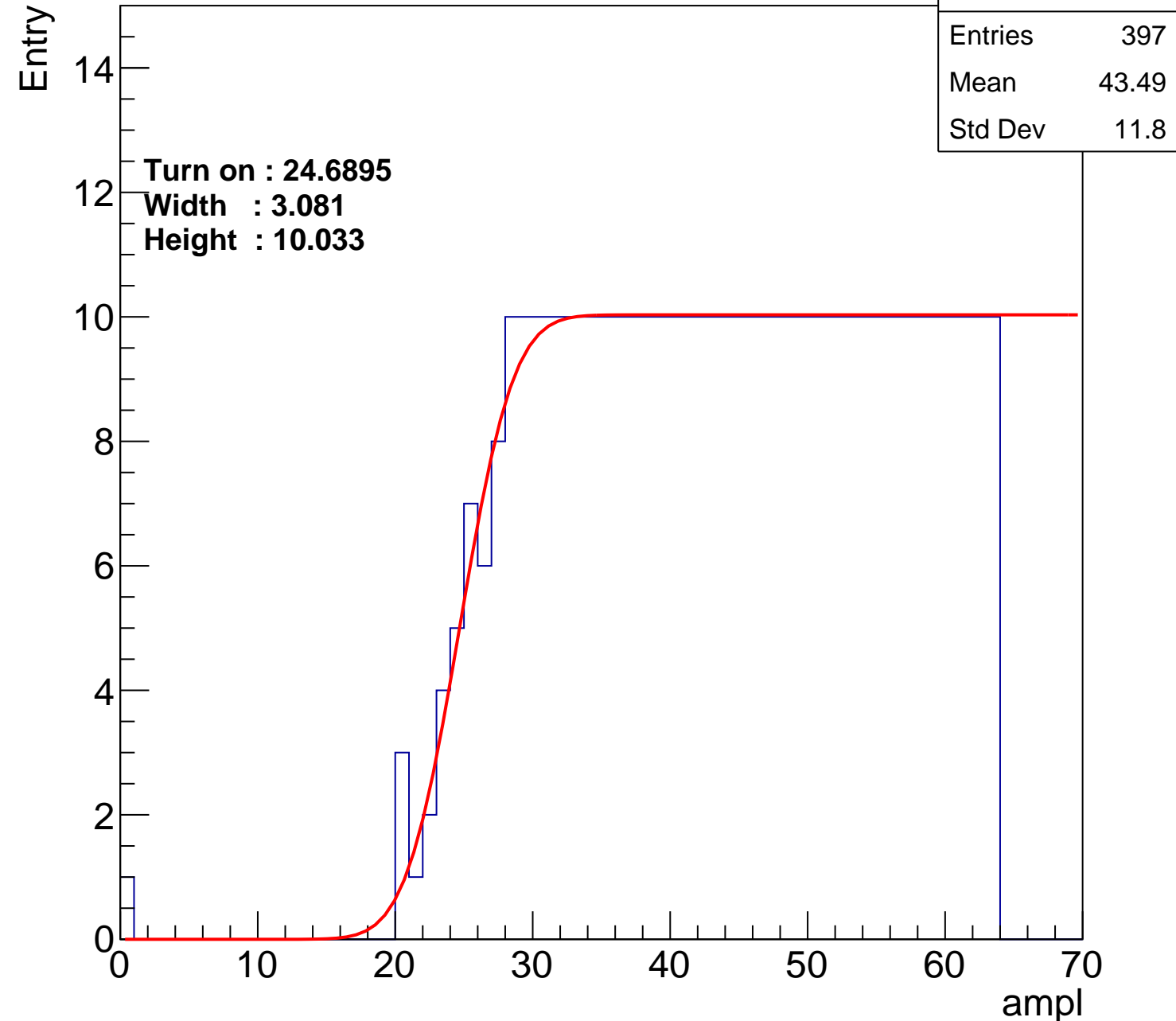
Width : 3.081

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

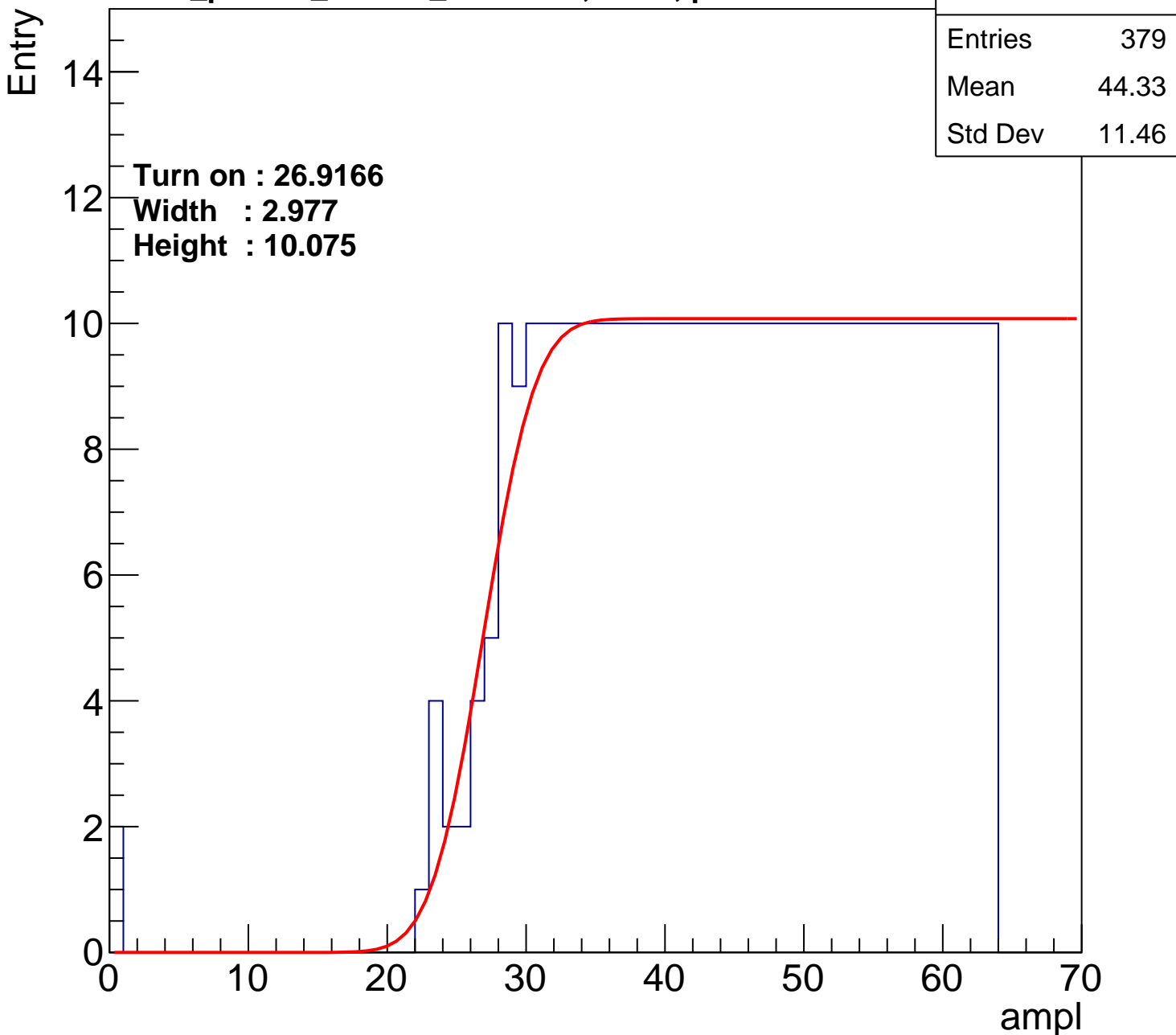
Entries	379
Mean	44.33
Std Dev	11.46

Mean	44.33
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Std Dev	11.46
---------	-------

Width : 2.977

Height : 10.075



B1L000S, U23-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.92
Std Dev	10.97

Turn on : 27.4622

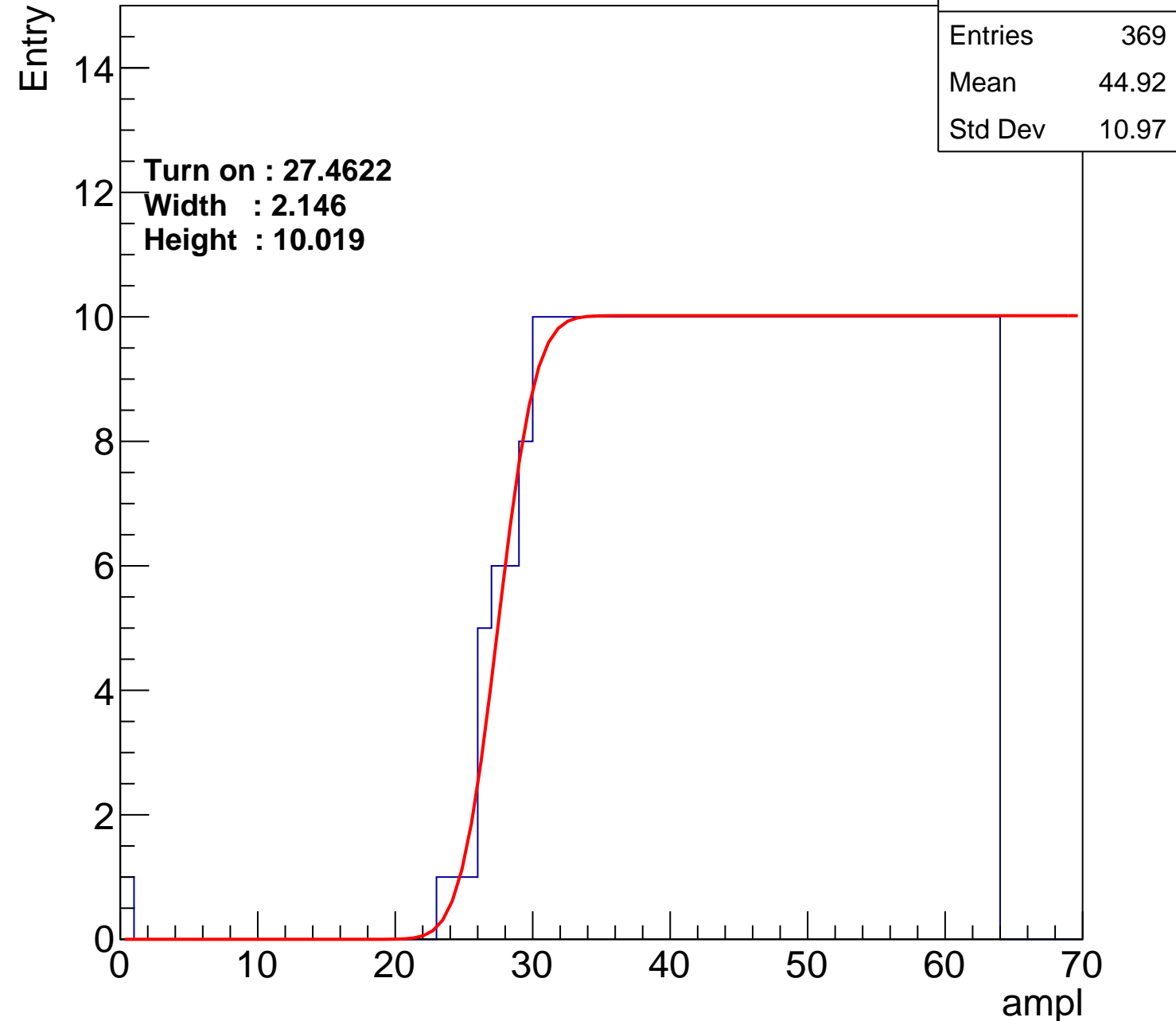
Width : 2.146

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.55
Std Dev	11.52

Turn on : 27.4873

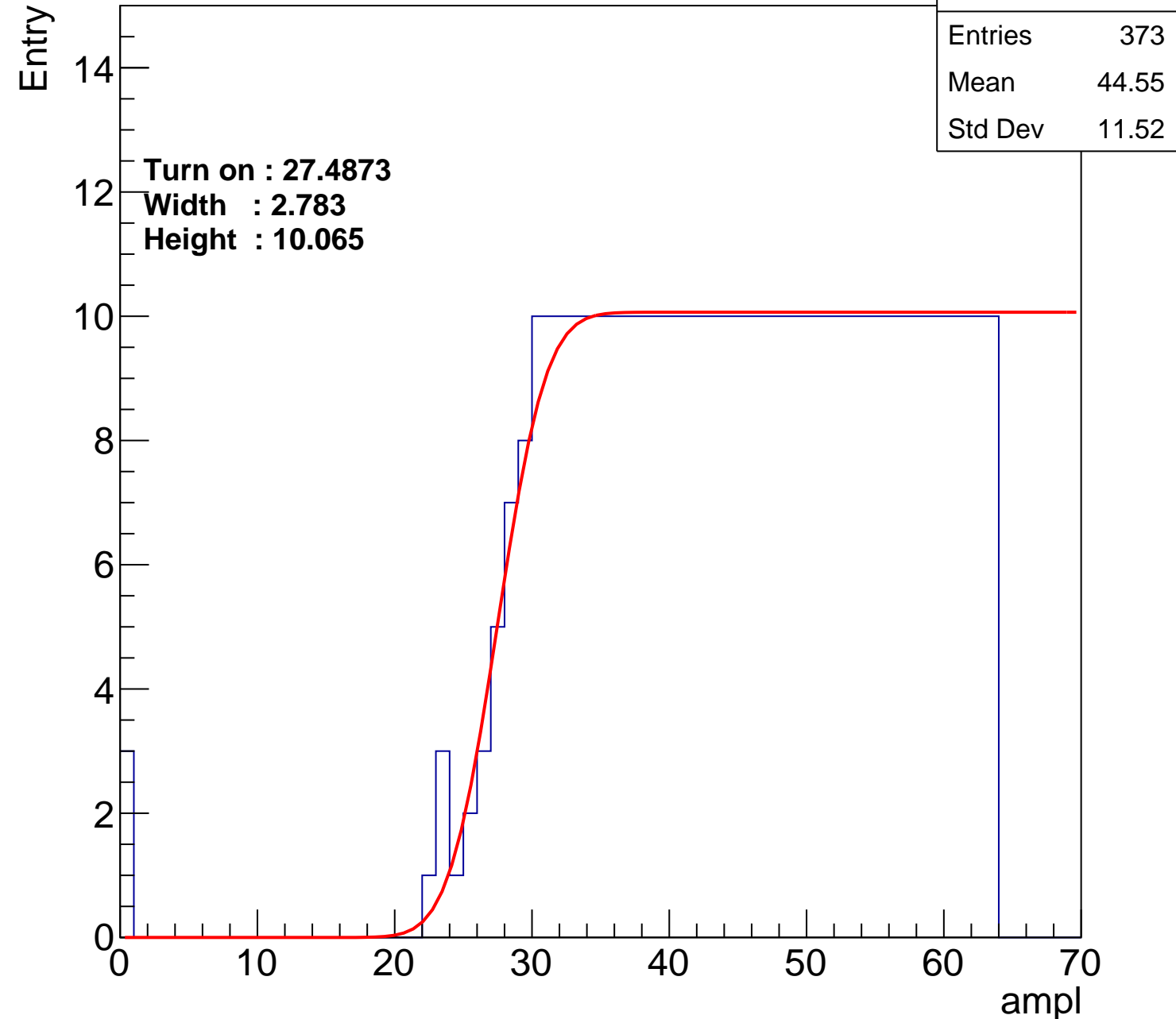
Width : 2.783

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch46

calib_packv5_042523_0143.root, FC#3, port B3

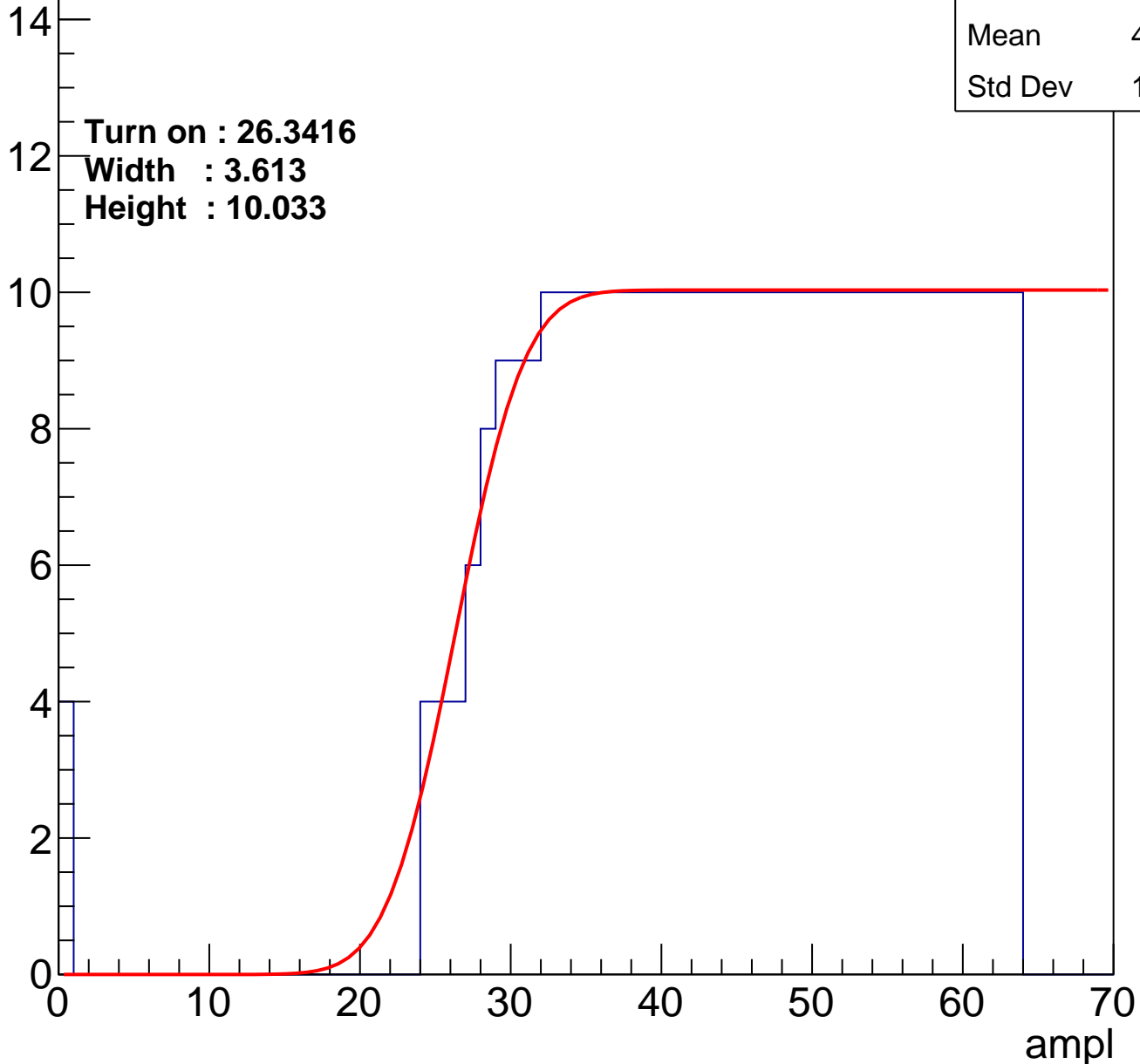
Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.3416

Width : 3.613

Height : 10.033

Entry



B1L000S, U23-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
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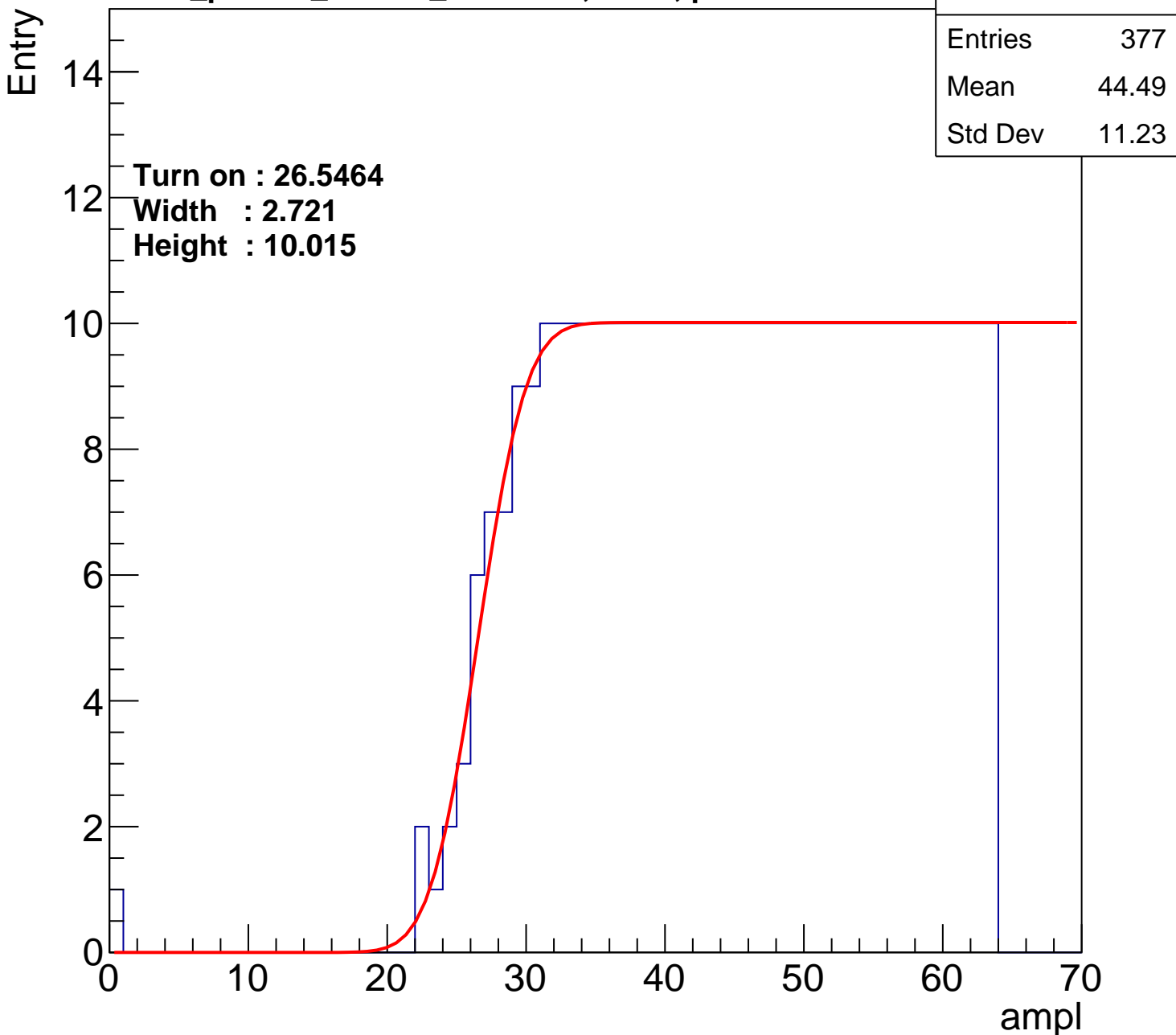
Mean	44.49
------	-------

Std Dev	11.23
---------	-------

Turn on : 26.5464

Width : 2.721

Height : 10.015



B1L000S, U23-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.47
Std Dev	11.52

Turn on : 26.4274

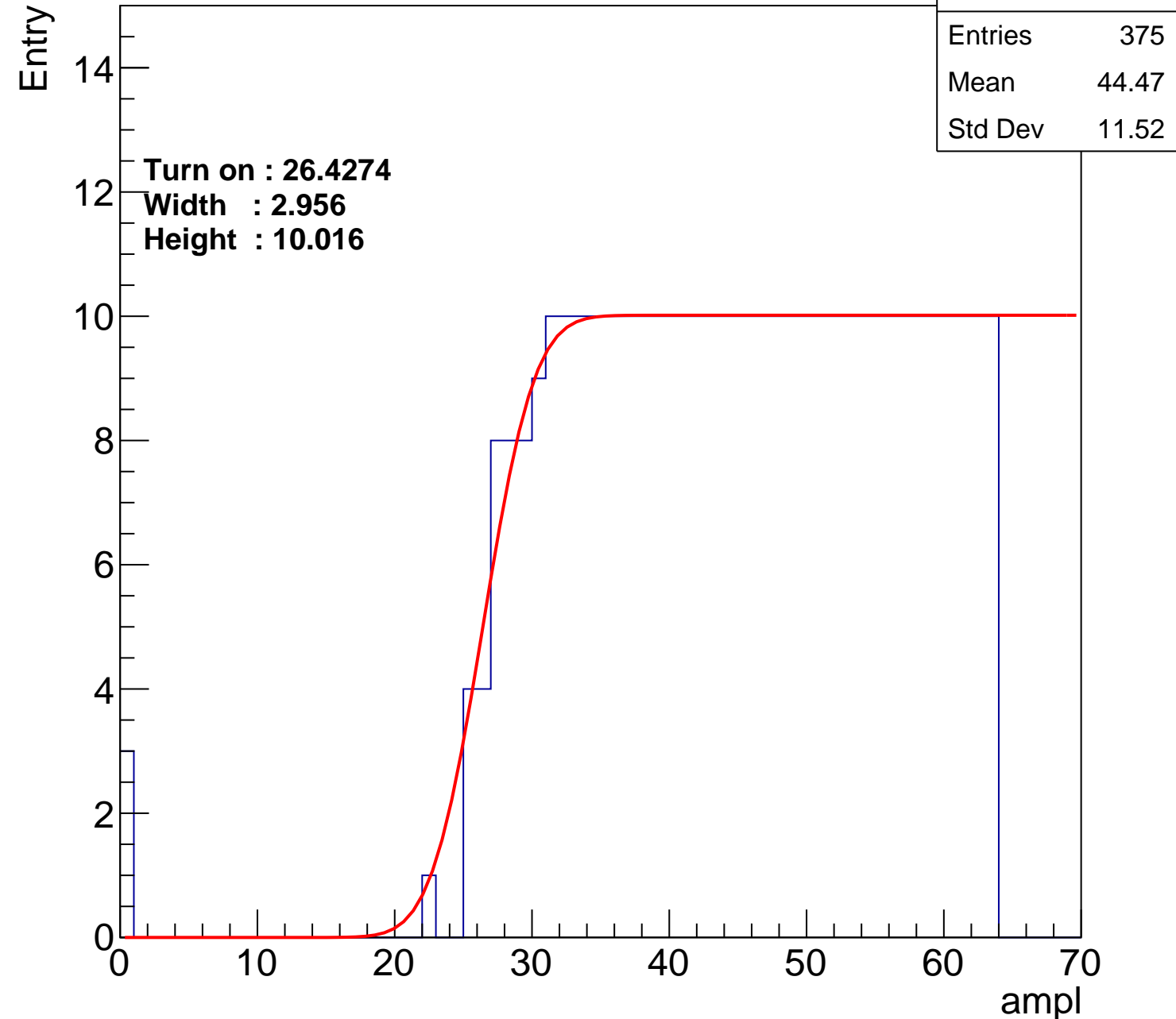
Width : 2.956

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch49

calib_packv5_042523_0143.root, FC#3, port B3

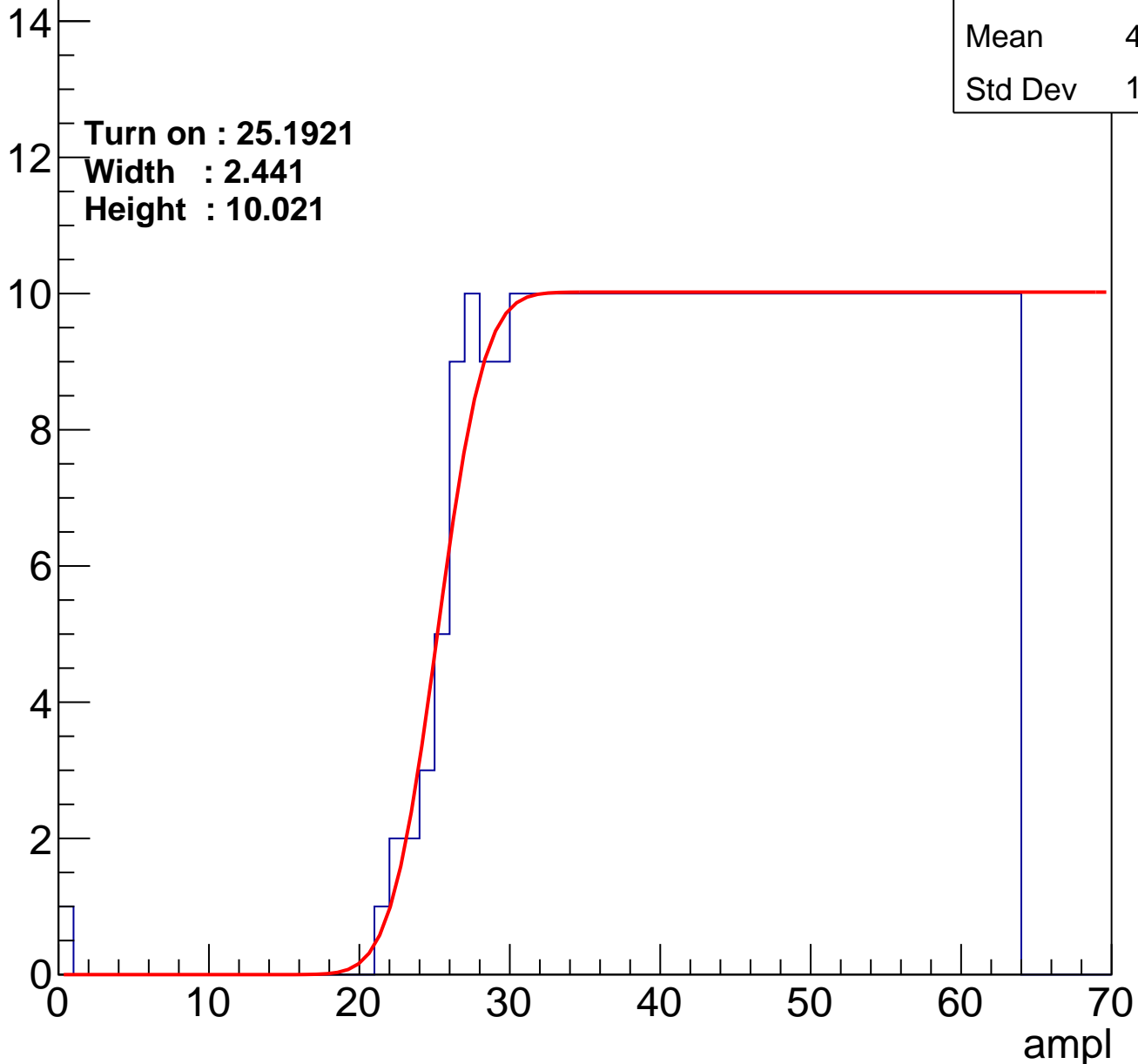
Entries	391
Mean	43.82
Std Dev	11.56

Turn on : 25.1921

Width : 2.441

Height : 10.021

Entry



B1L000S, U23-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.5906

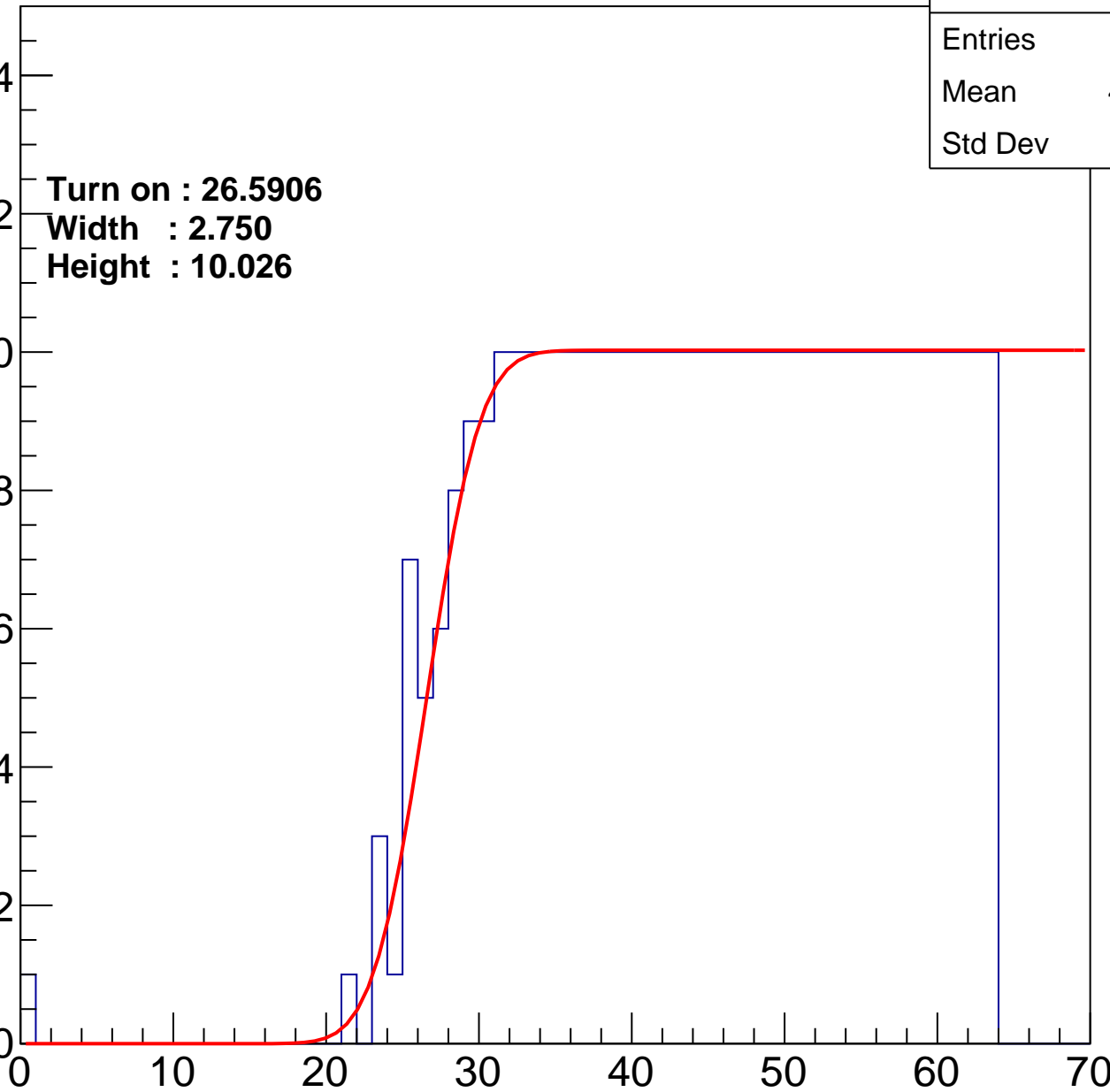
Width : 2.750

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.35

Turn on : 26.3980

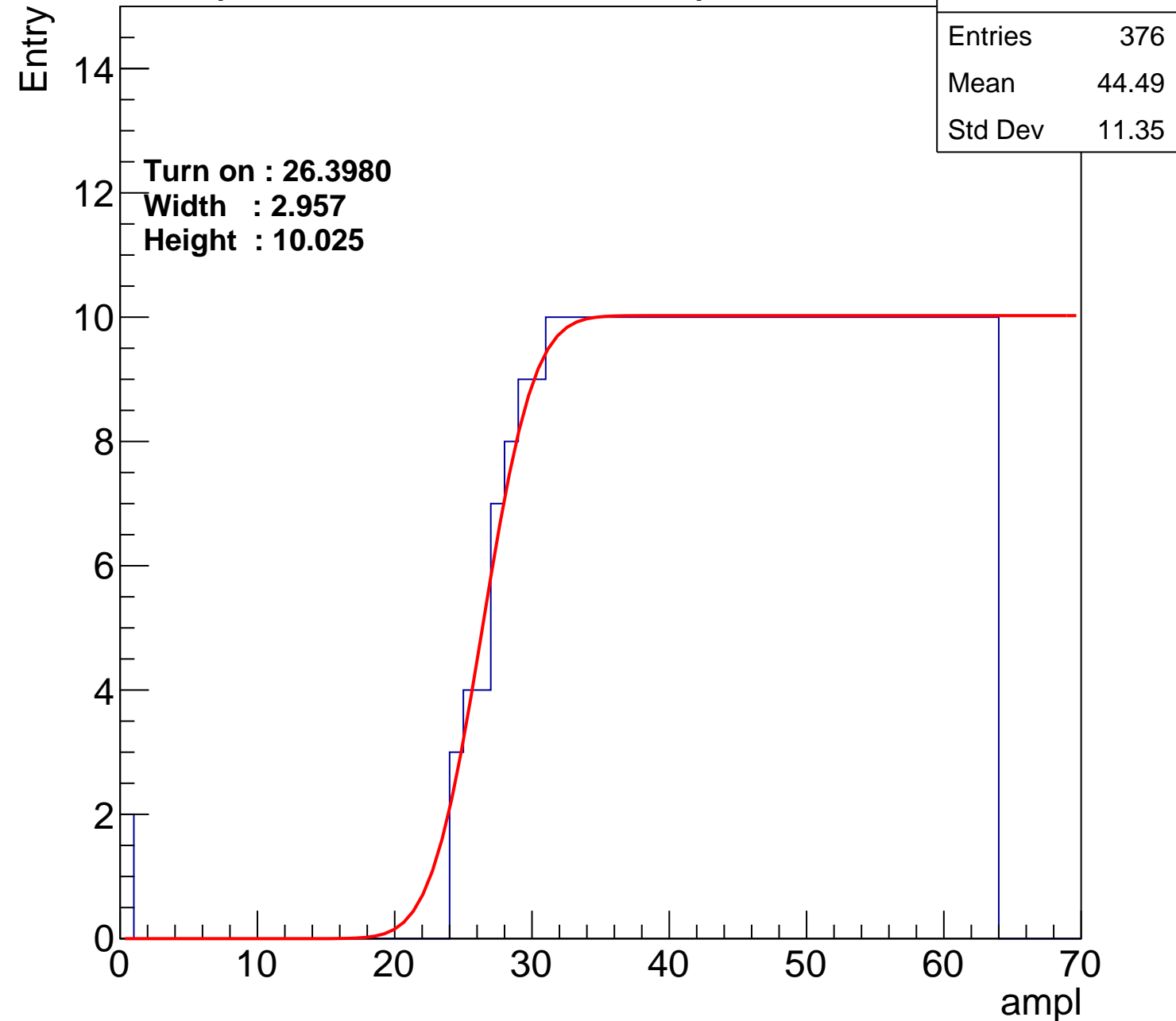
Width : 2.957

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.02
Std Dev	11.77

Turn on : 26.3010

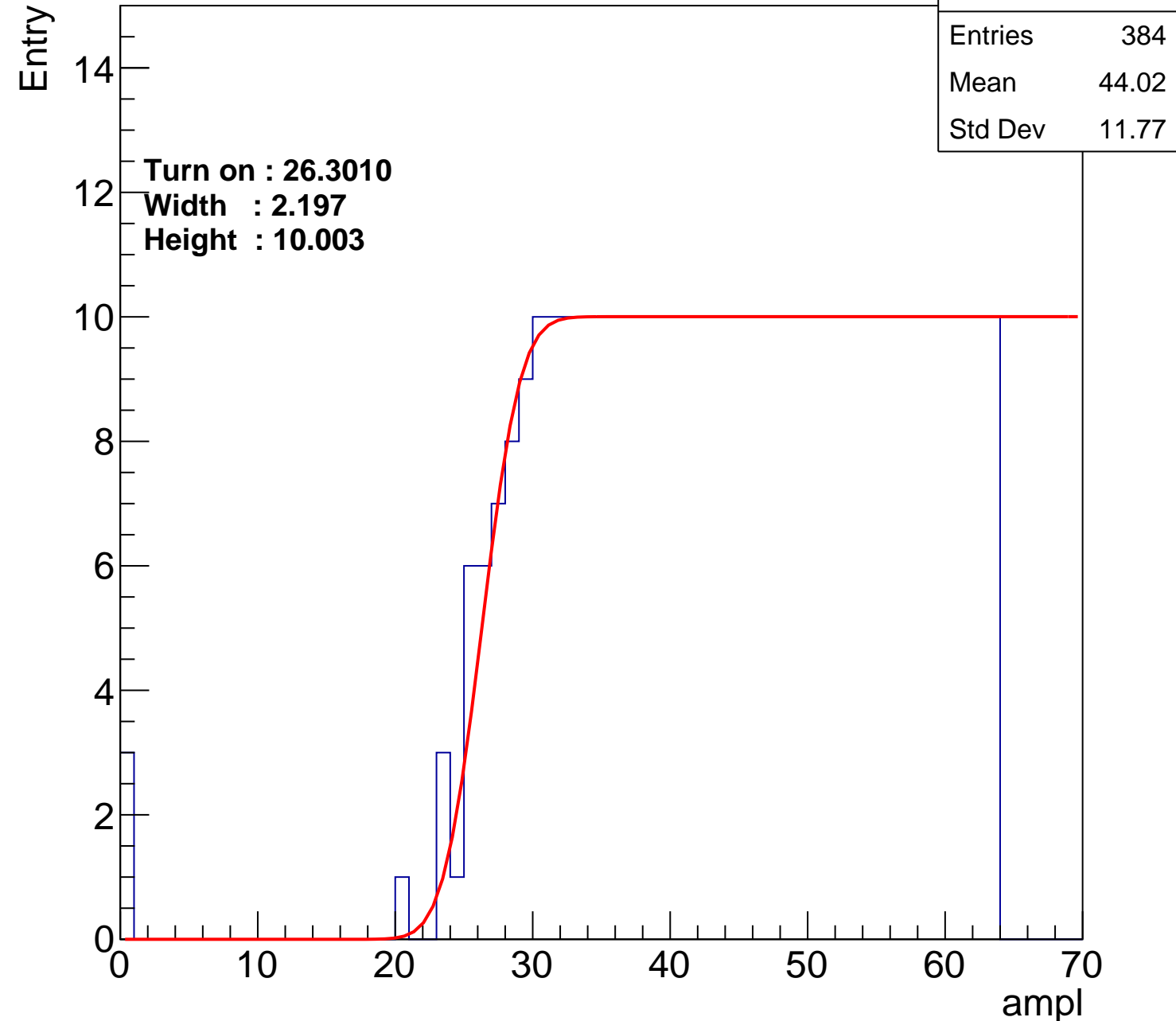
Width : 2.197

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.9298
Width : 1.487
Height : 9.959



B1L000S, U23-ch54

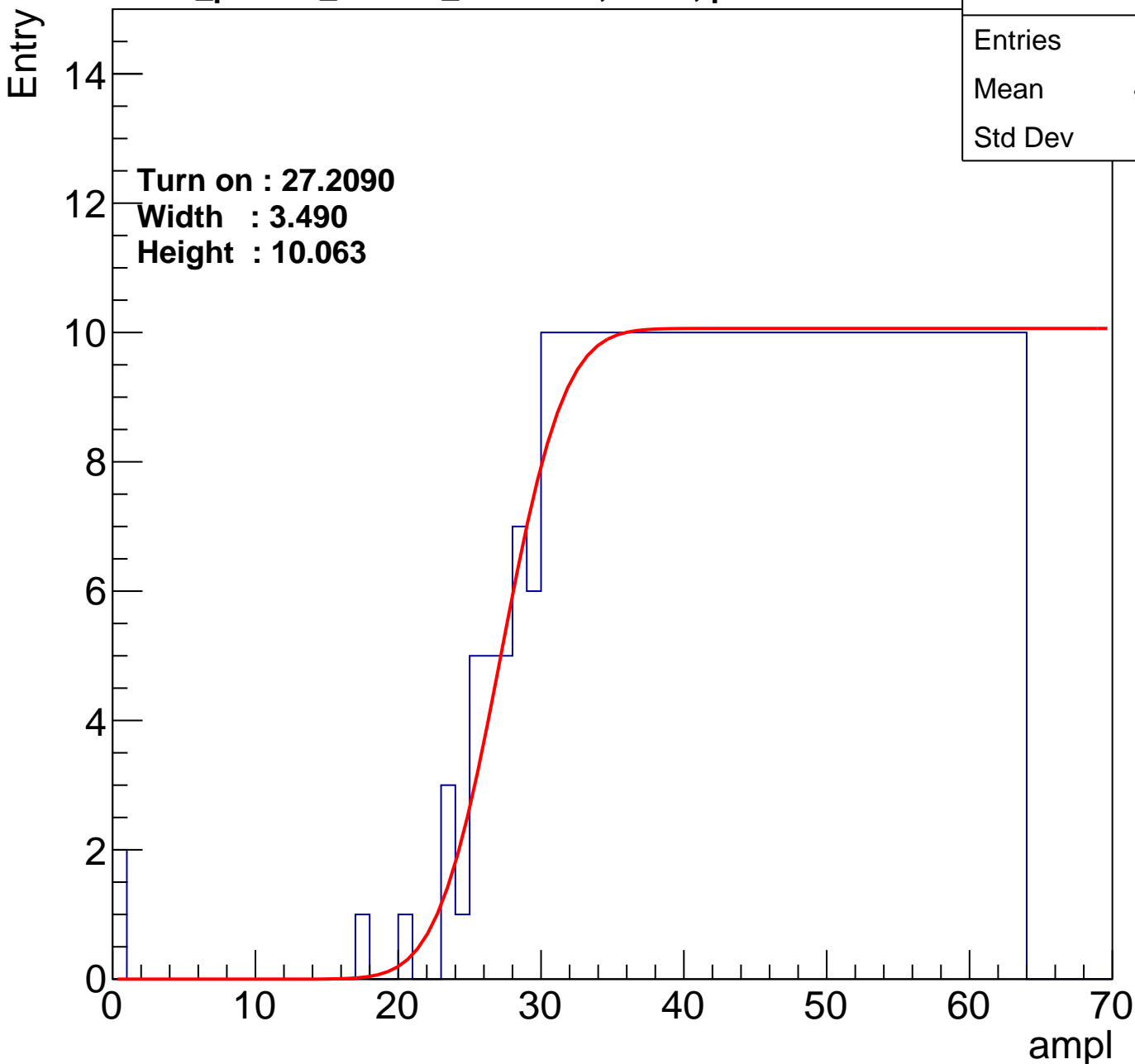
calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.41
Std Dev	11.5

Turn on : 27.2090

Width : 3.490

Height : 10.063



B1L000S, U23-ch55

calib_packv5_042523_0143.root, FC#3, port B3

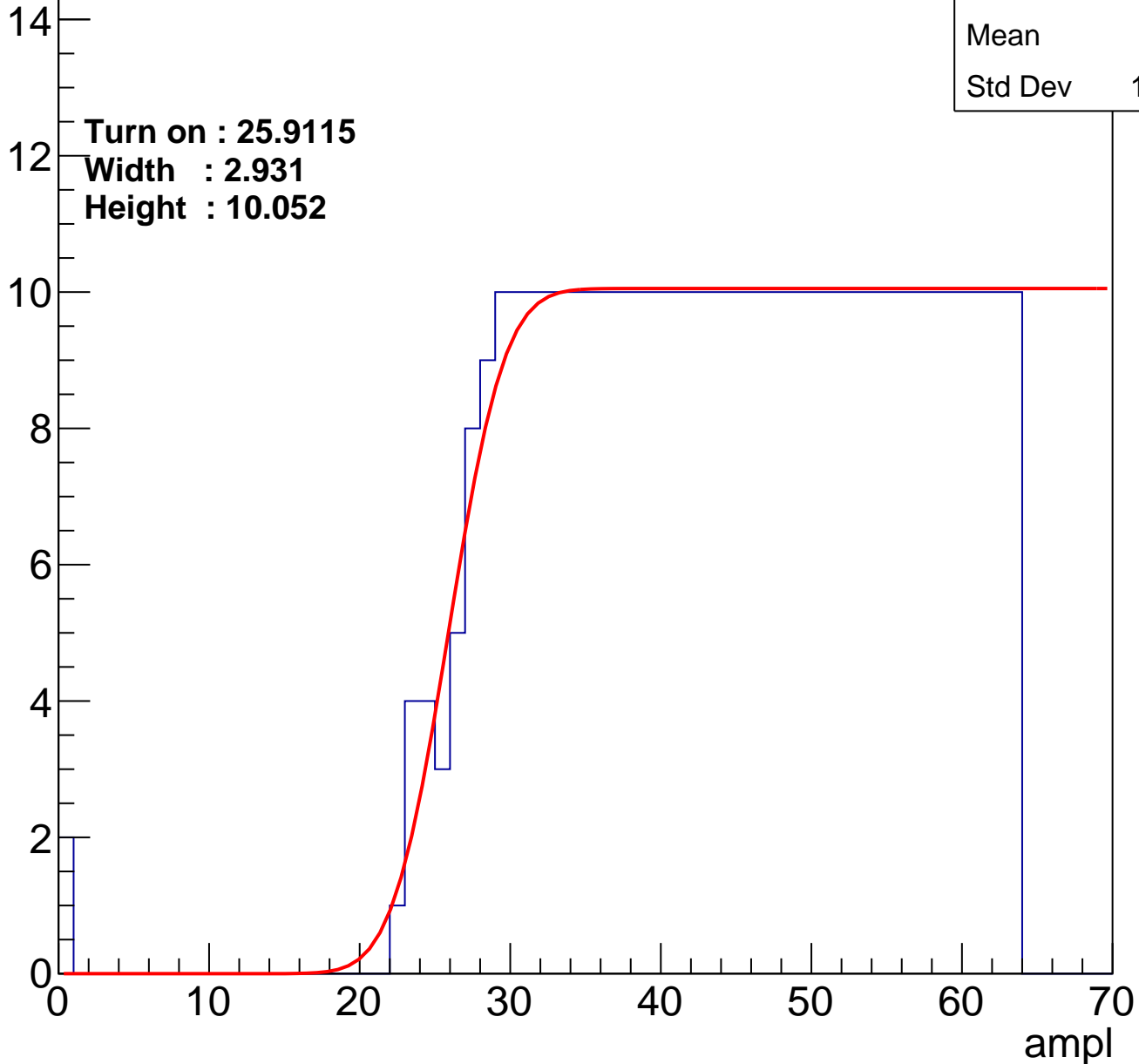
Entries	386
Mean	44
Std Dev	11.62

Turn on : 25.9115

Width : 2.931

Height : 10.052

Entry



B1L000S, U23-ch56

calib_packv5_042523_0143.root, FC#3, port B3

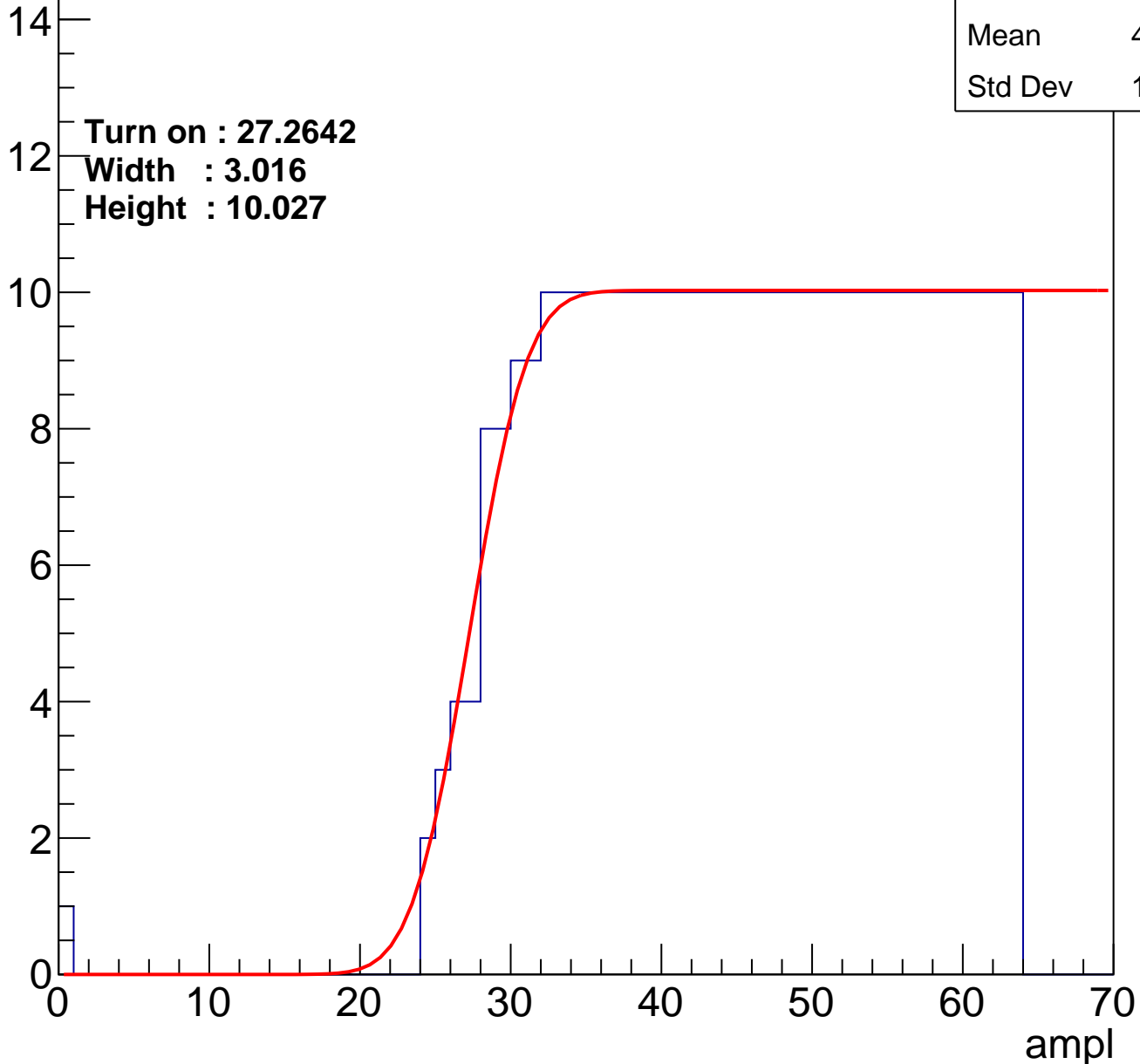
Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.2642

Width : 3.016

Height : 10.027

Entry



B1L000S, U23-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.78
Std Dev	12.17

Turn on : 25.9475

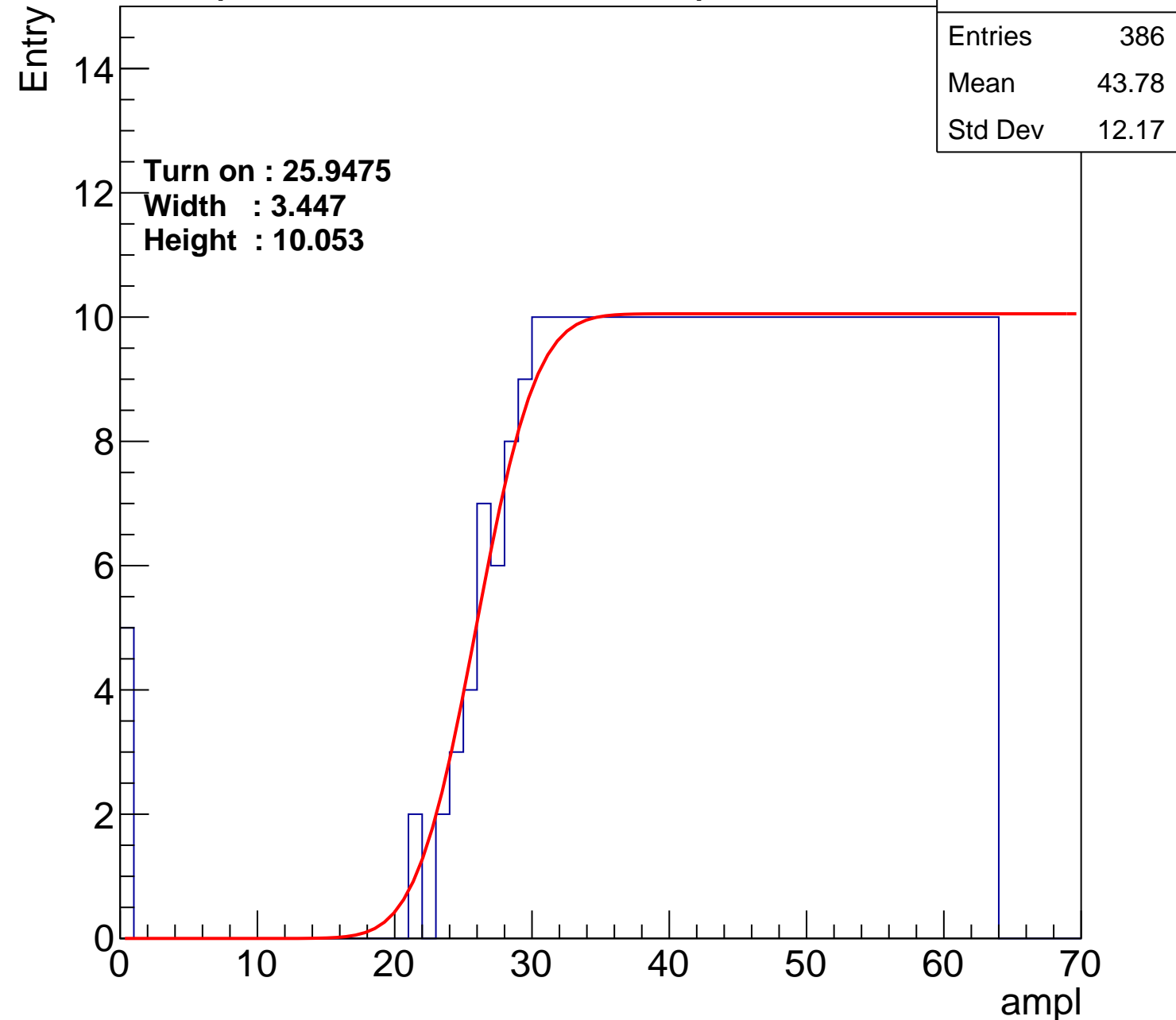
Width : 3.447

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.97
Std Dev	11.68

Turn on : 26.1625

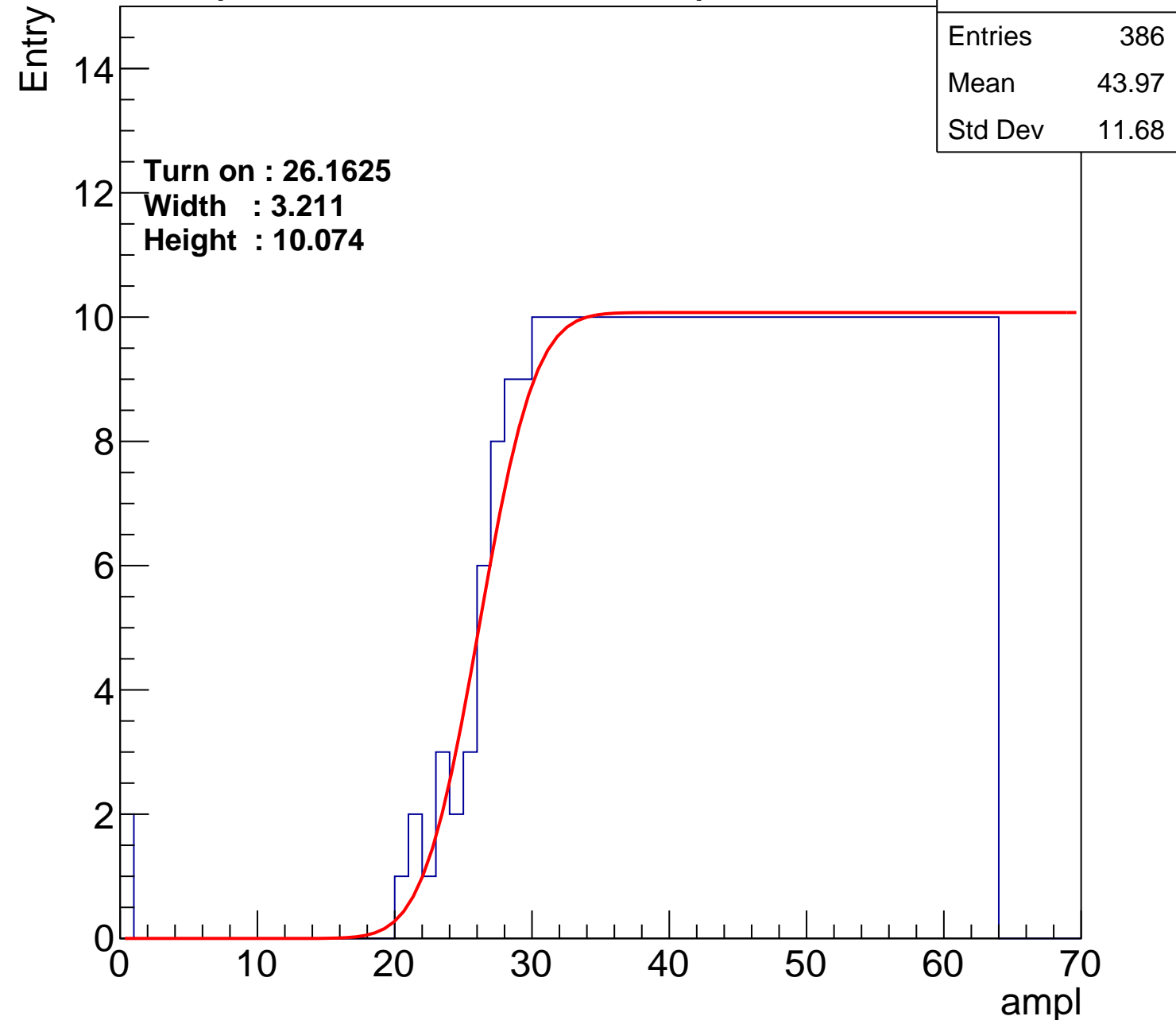
Width : 3.211

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.37
Std Dev	11.9

Turn on : 27.1373

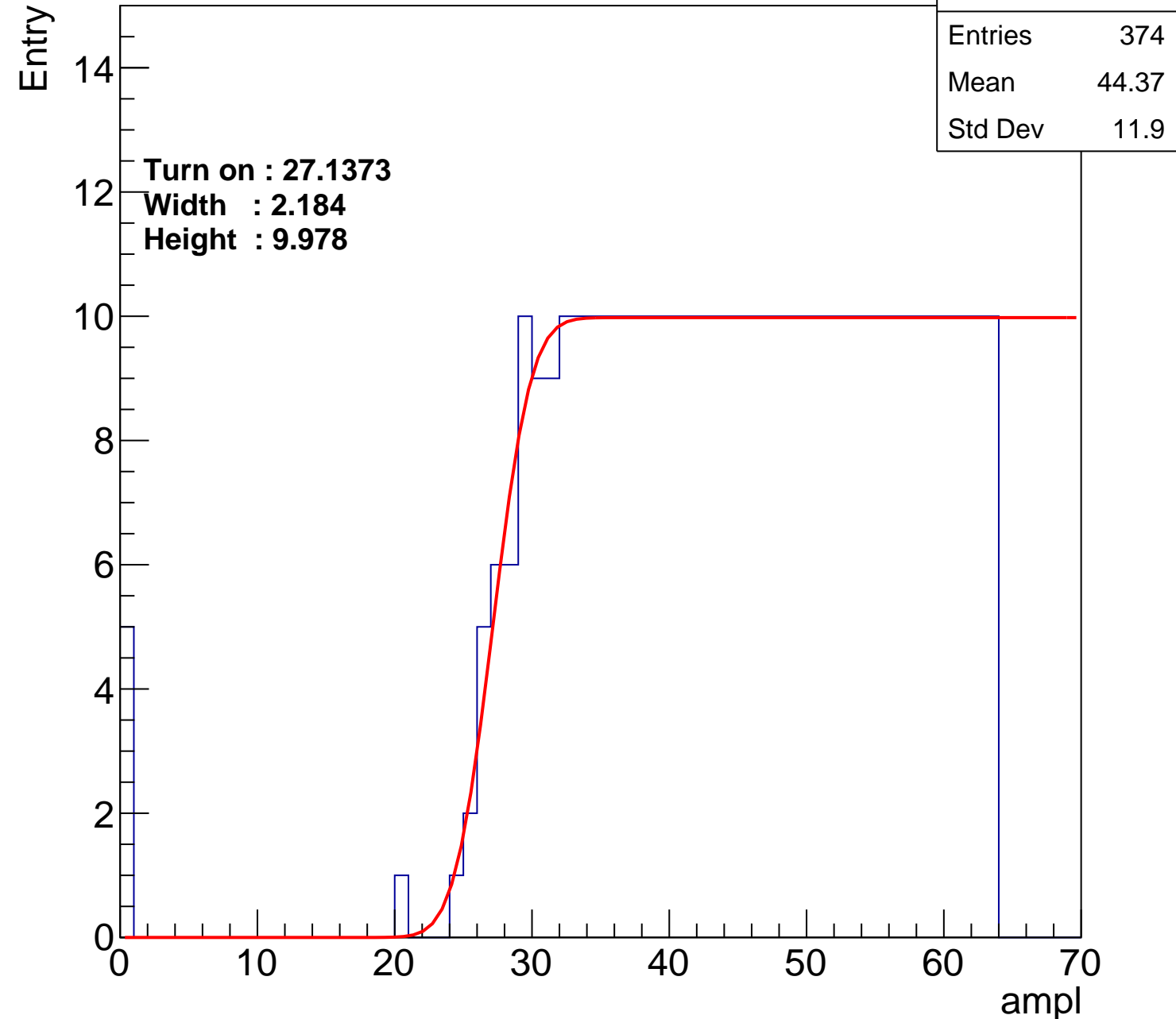
Width : 2.184

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch60

calib_packv5_042523_0143.root, FC#3, port B3

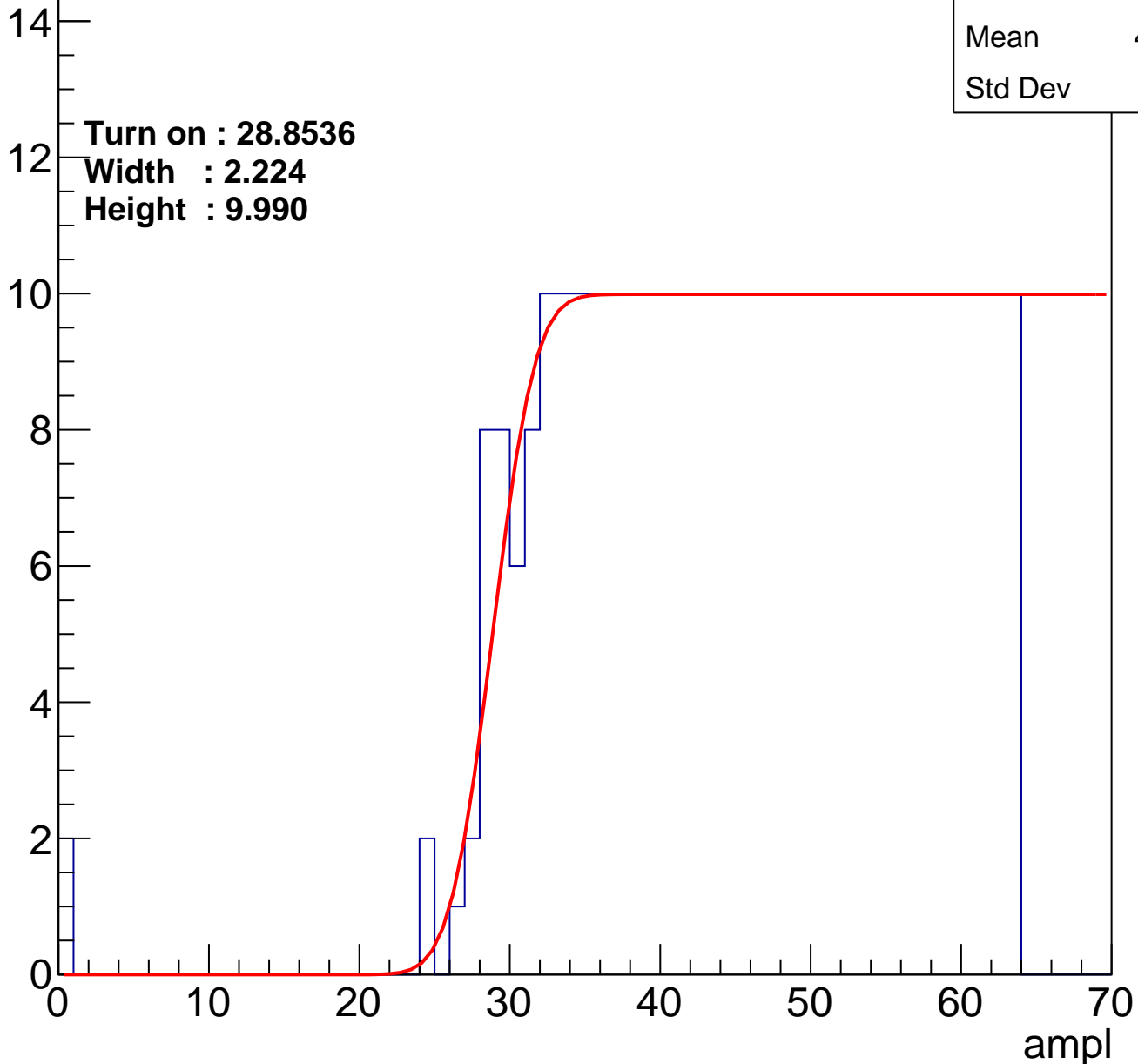
Entries	357
Mean	45.41
Std Dev	10.9

Turn on : 28.8536

Width : 2.224

Height : 9.990

Entry



B1L000S, U23-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 25.9242

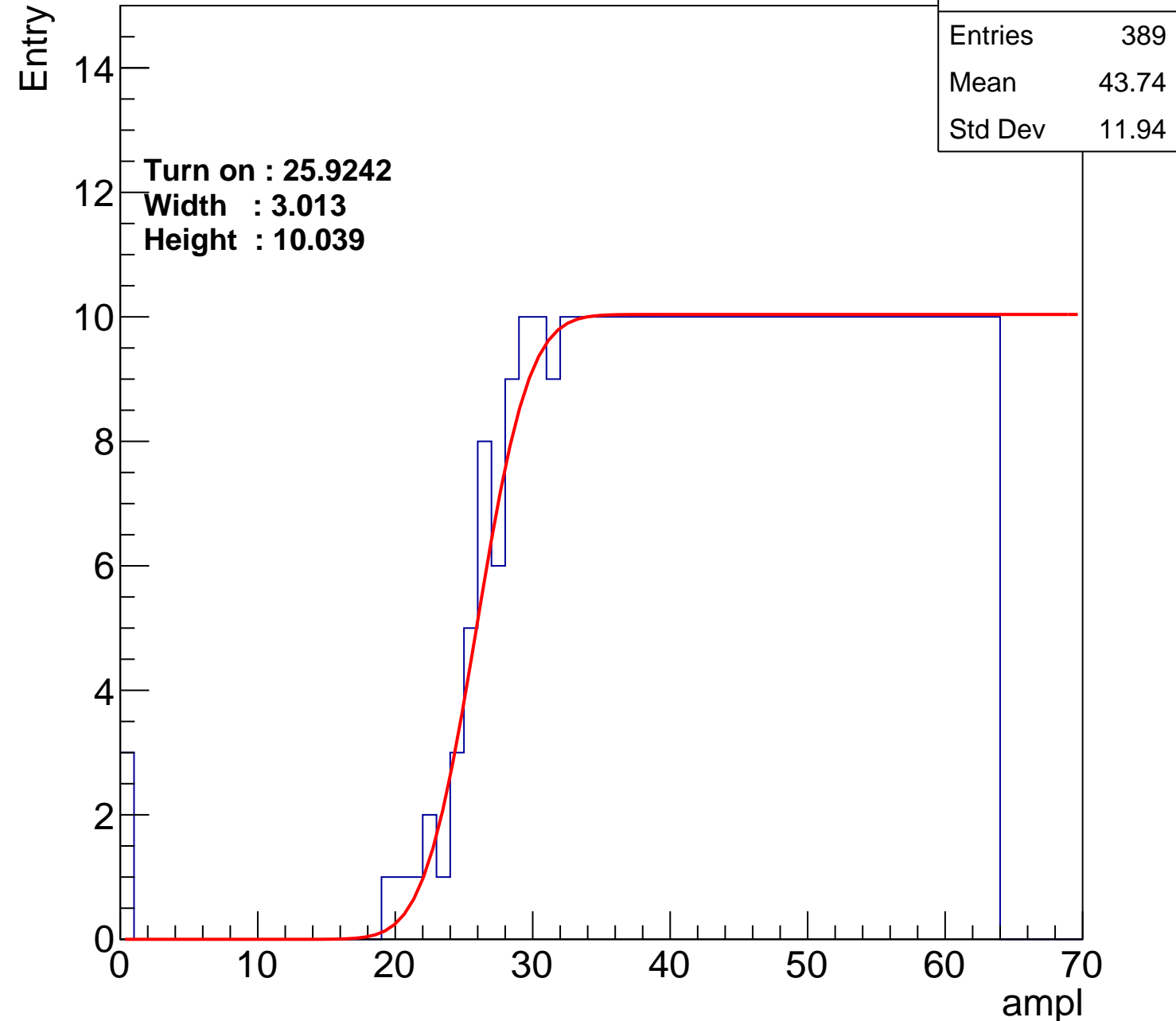
Width : 3.013

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.35
Std Dev	11.97

Turn on : 24.3630

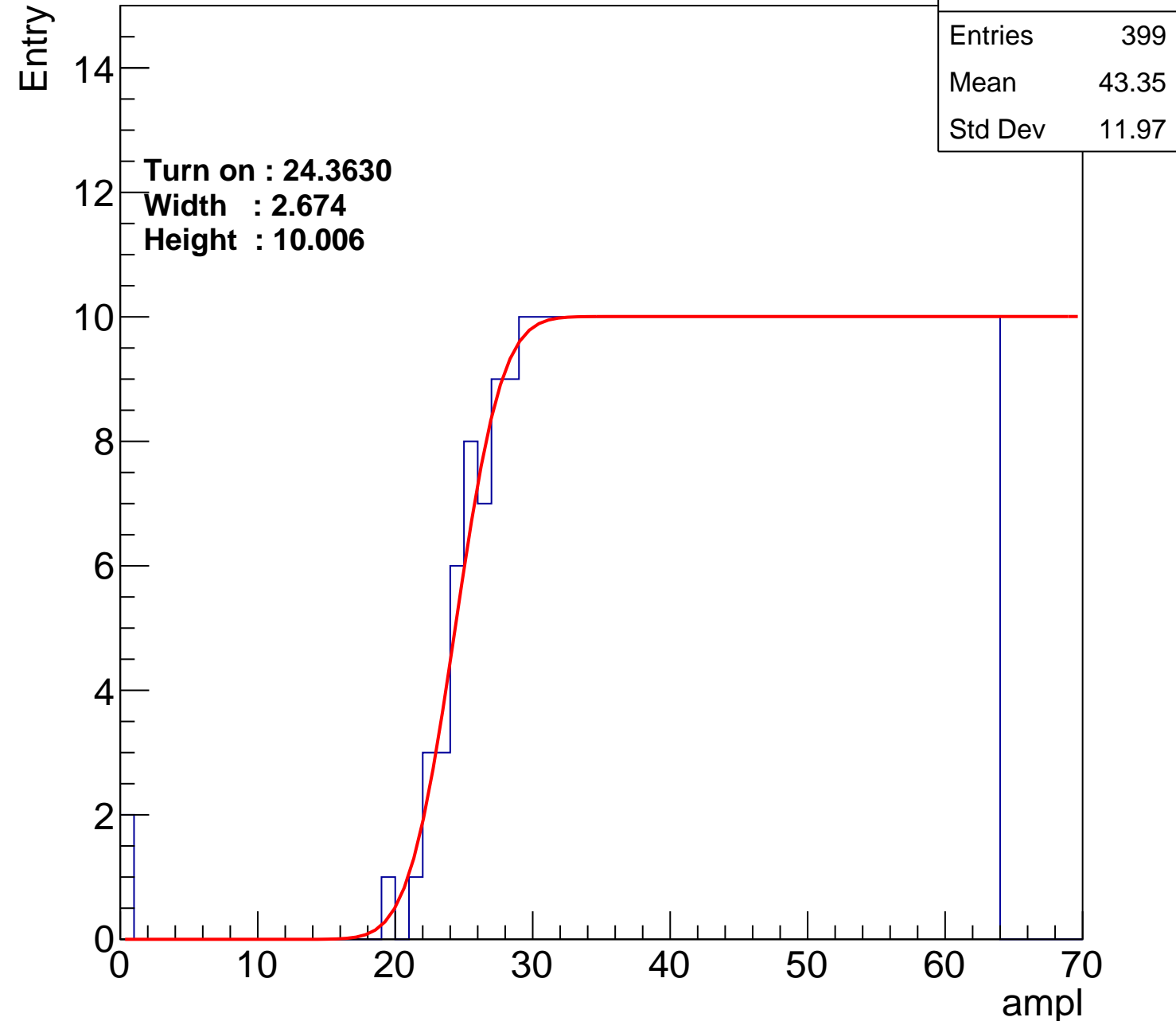
Width : 2.674

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.0274

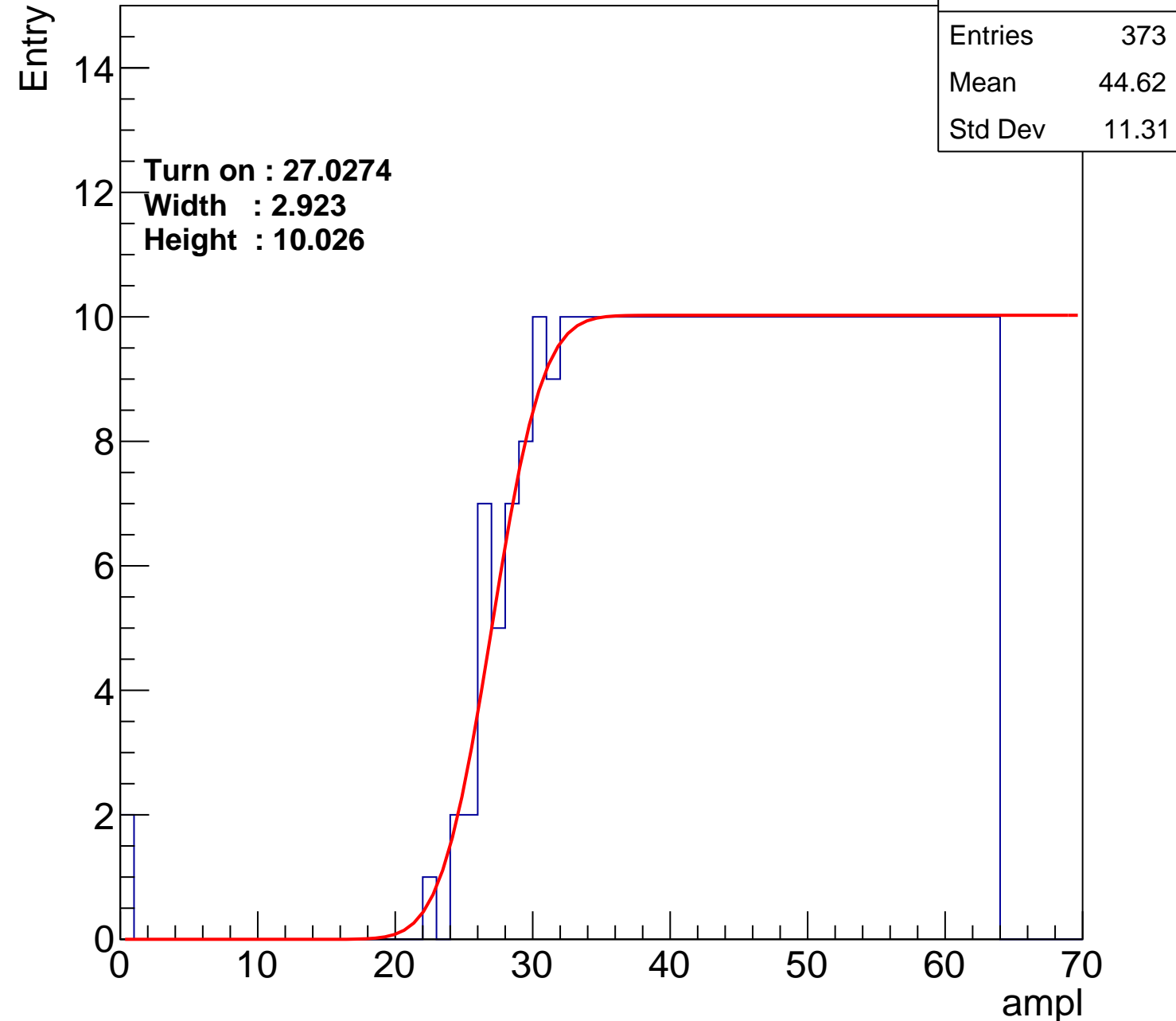
Width : 2.923

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch64

calib_packv5_042523_0143.root, FC#3, port B3

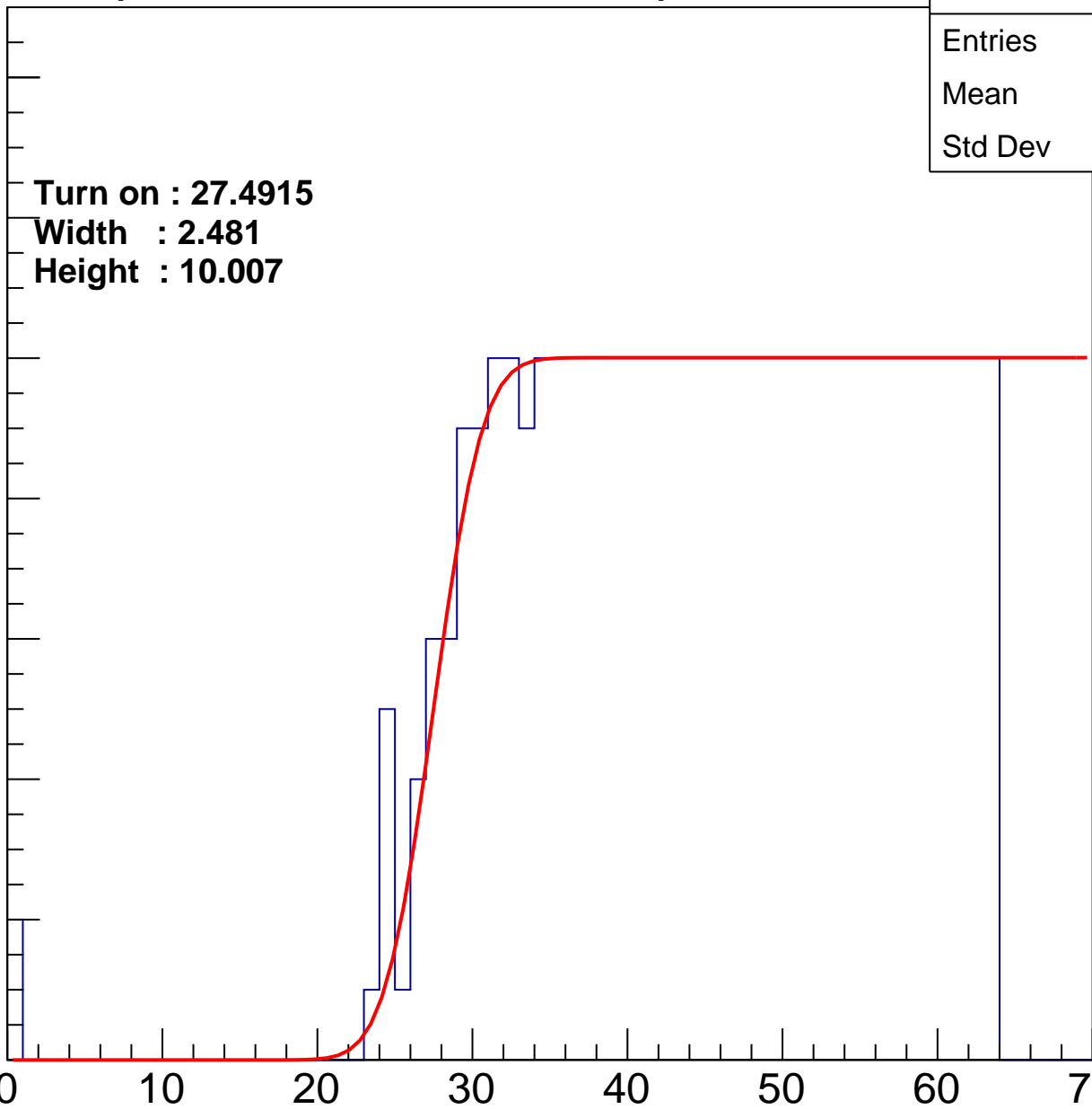
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4915
Width : 2.481
Height : 10.007

Entries	372
Mean	44.65
Std Dev	11.32

ampl



B1L000S, U23-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.54
Std Dev	11.21

Turn on : 26.7599

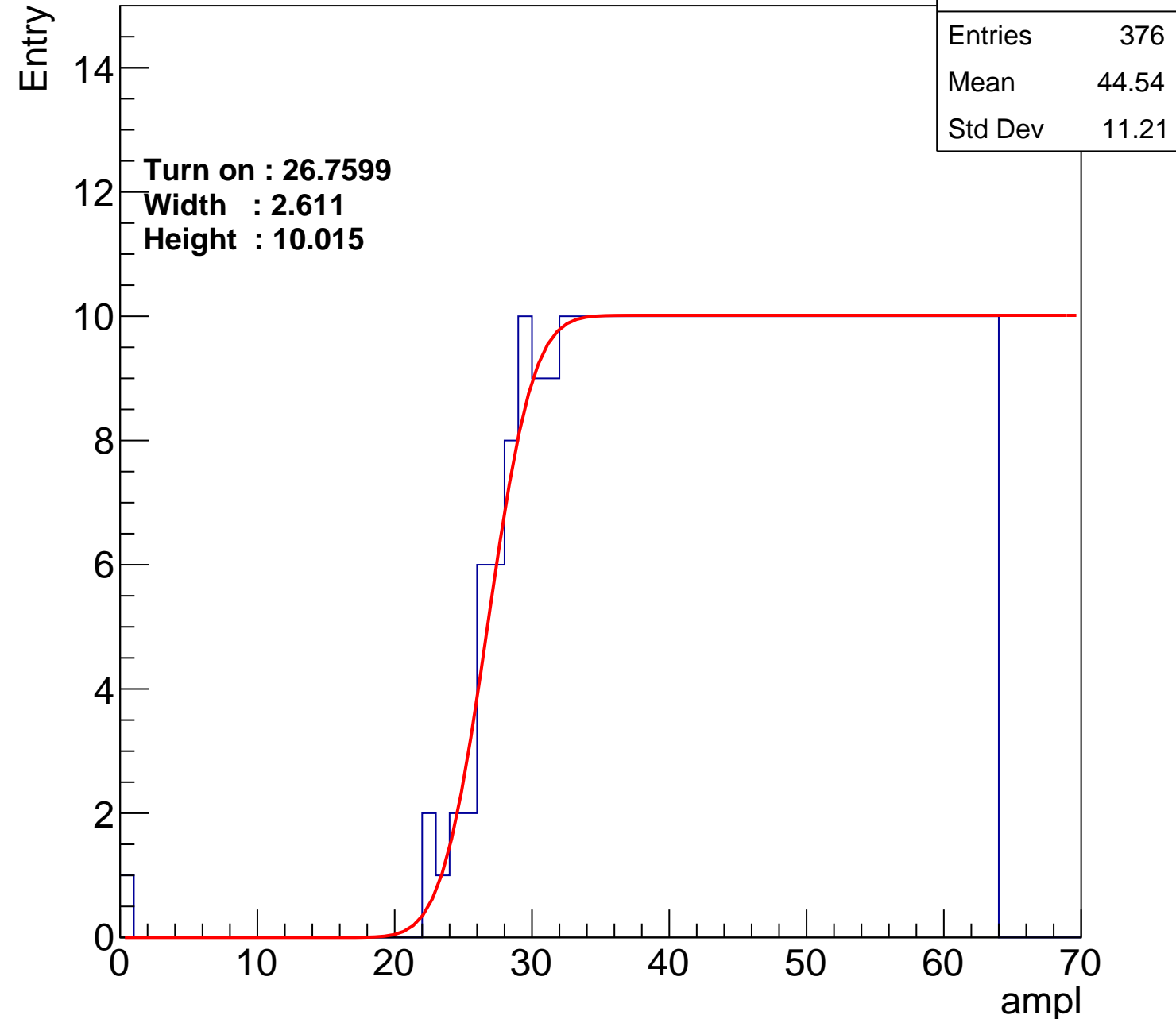
Width : 2.611

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch66

calib_packv5_042523_0143.root, FC#3, port B3

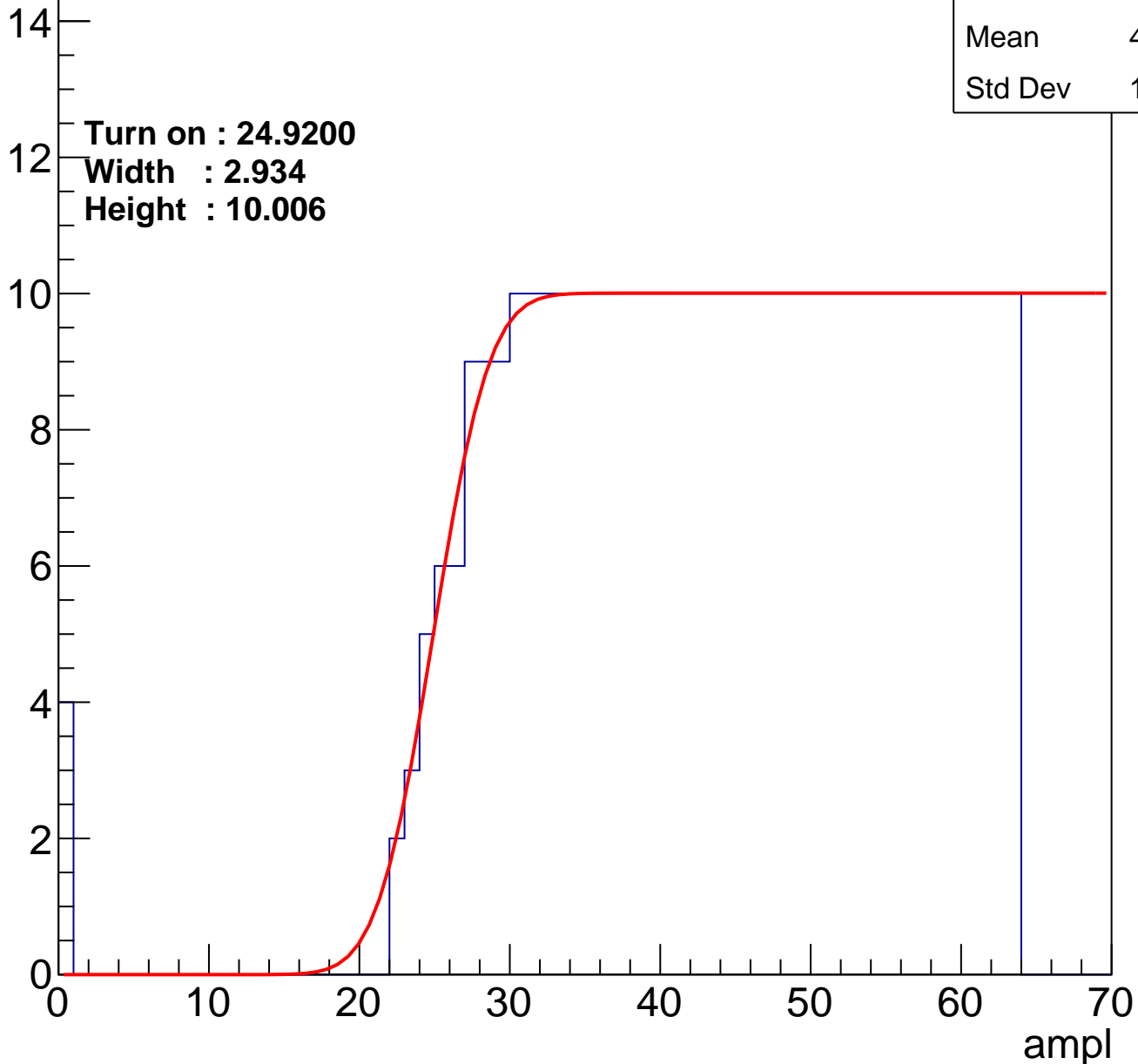
Entries	393
Mean	43.52
Std Dev	12.13

Turn on : 24.9200

Width : 2.934

Height : 10.006

Entry



B1L000S, U23-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.15
Std Dev	11.85

Turn on : 26.7469

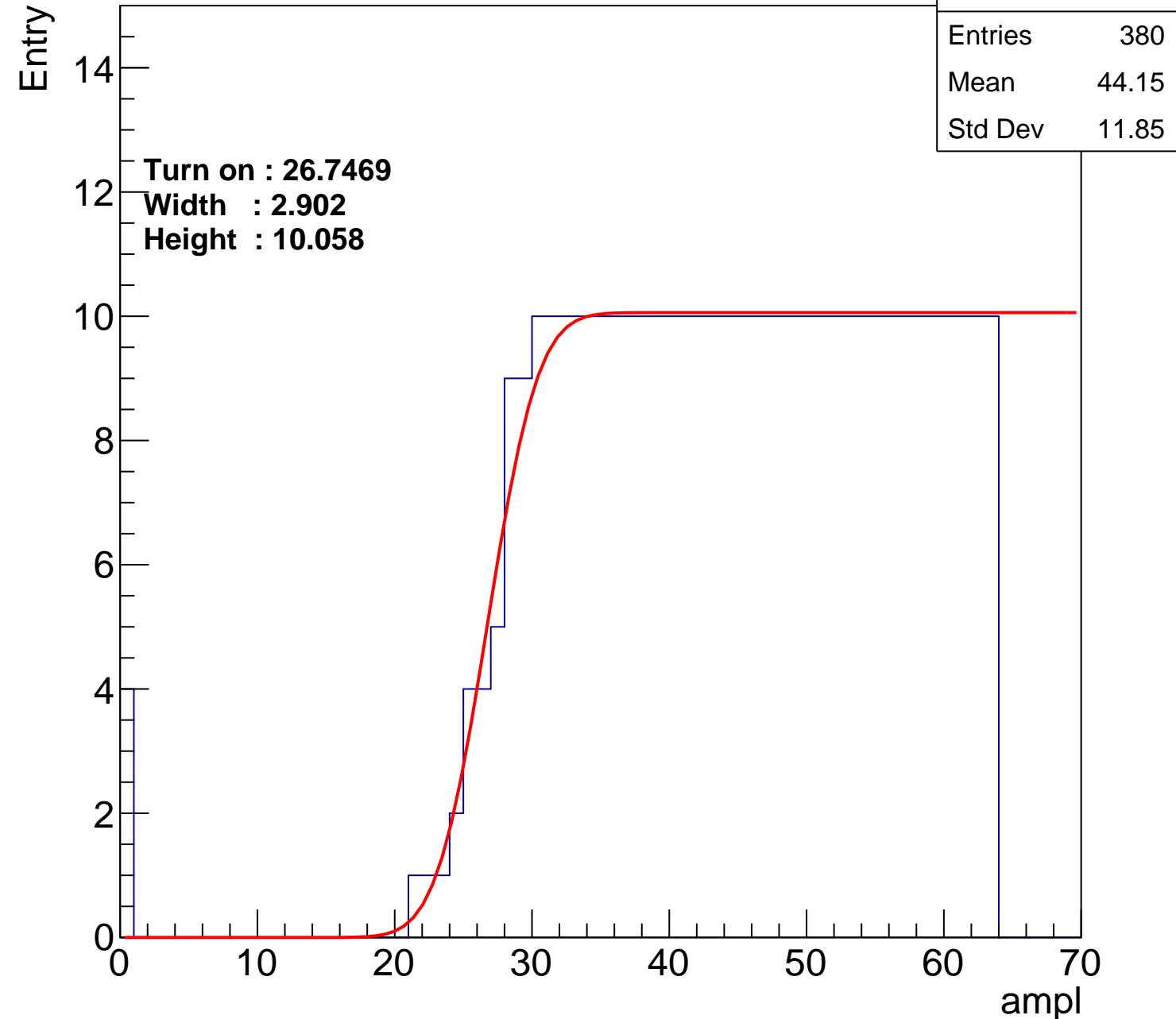
Width : 2.902

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch68

calib_packv5_042523_0143.root, FC#3, port B3

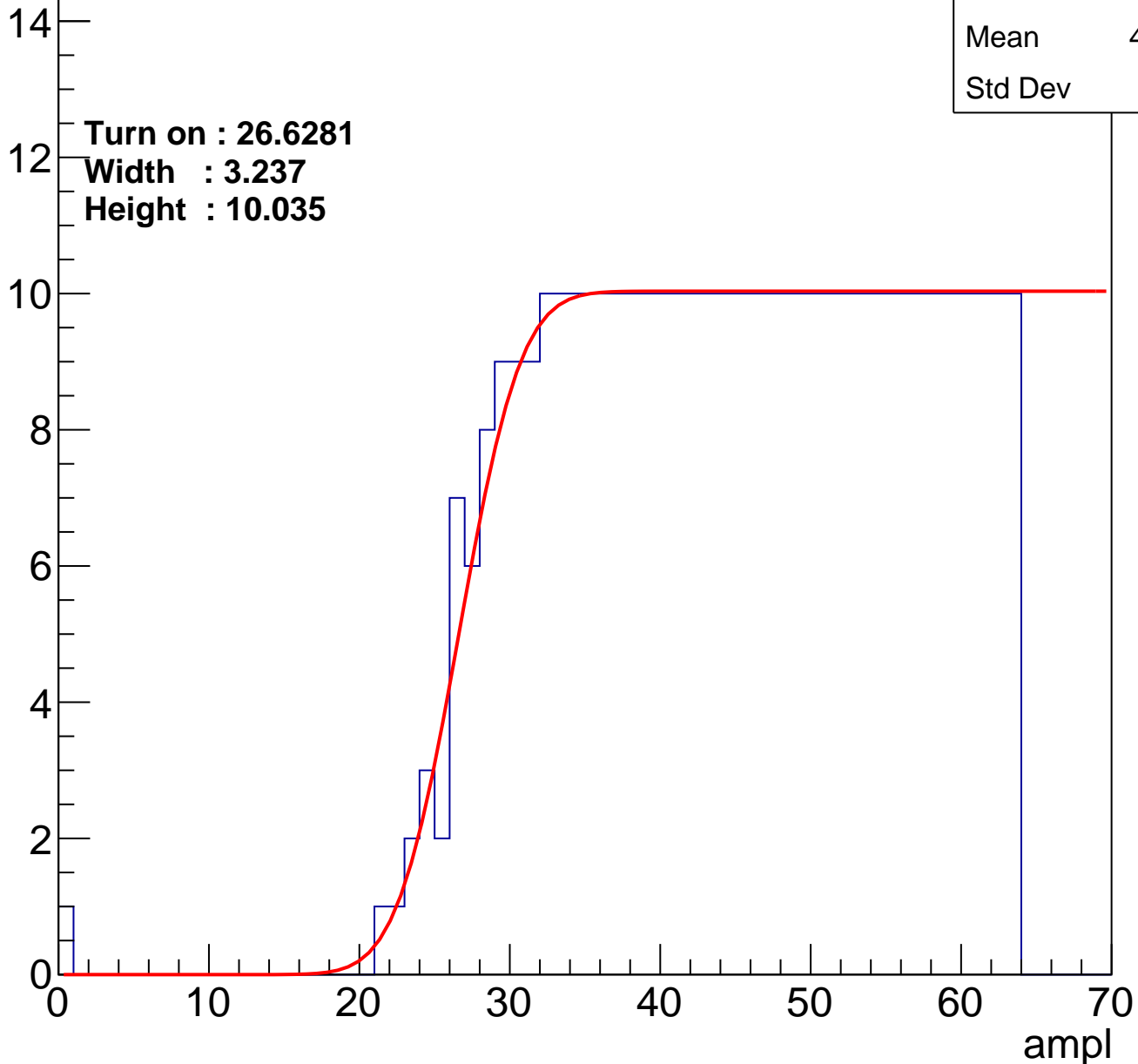
Entries	378
Mean	44.42
Std Dev	11.3

Turn on : 26.6281

Width : 3.237

Height : 10.035

Entry



B1L000S, U23-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.8397

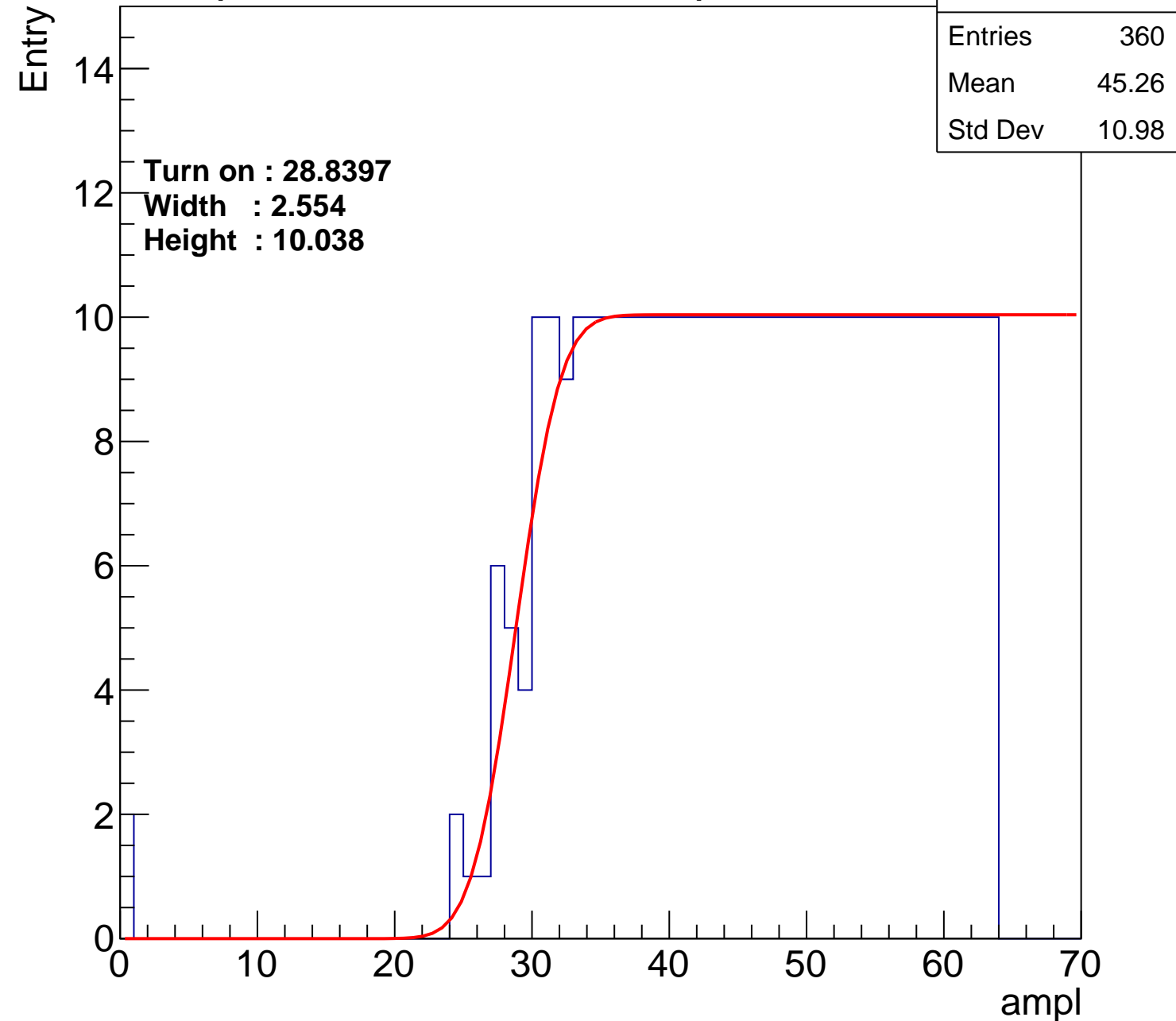
Width : 2.554

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.45
Std Dev	11.23

Turn on : 26.2846

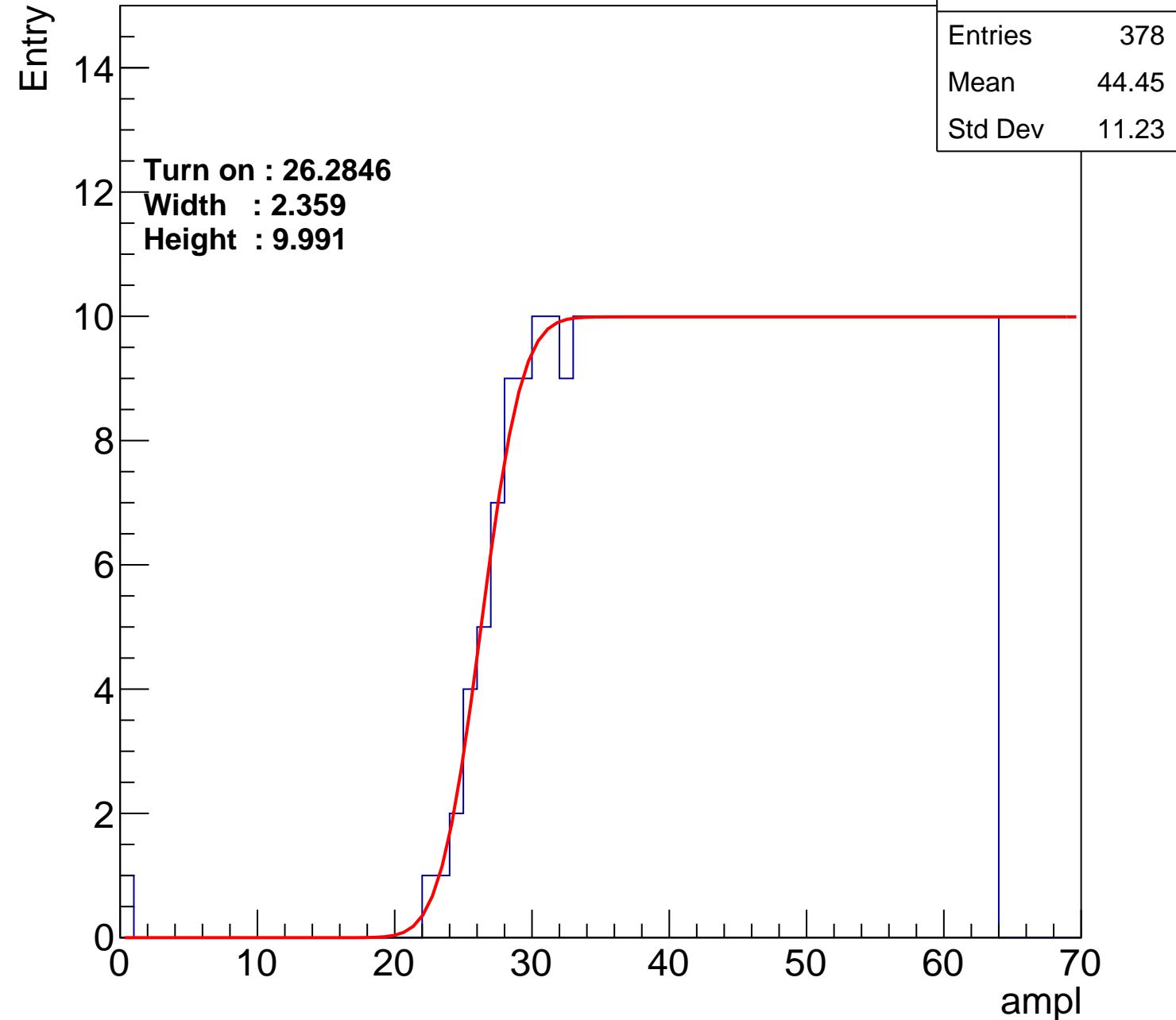
Width : 2.359

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 26.7930

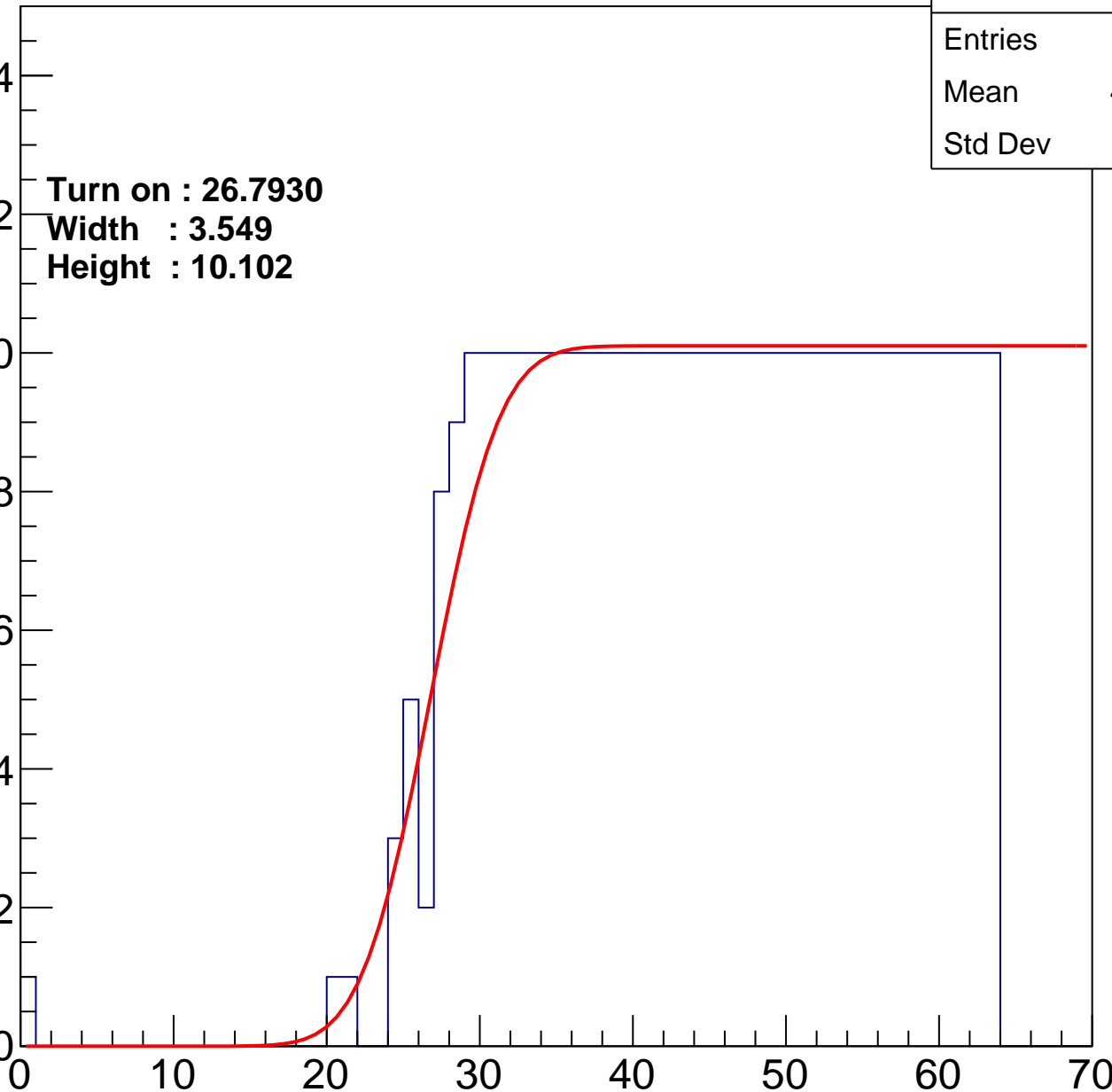
Width : 3.549

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.47
Std Dev	11.2

Turn on : 26.4372

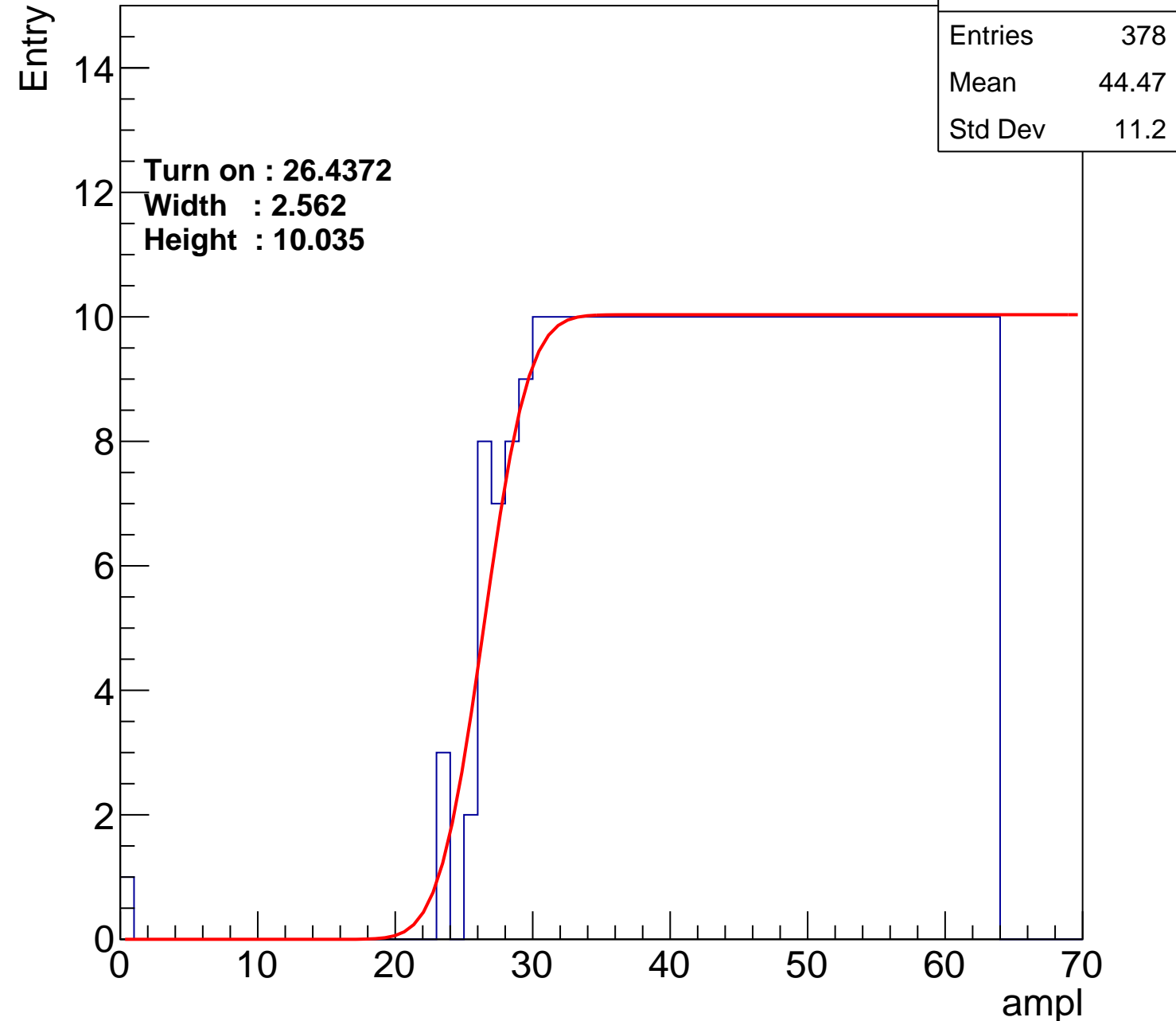
Width : 2.562

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.07
Std Dev	11.86

Turn on : 26.5040

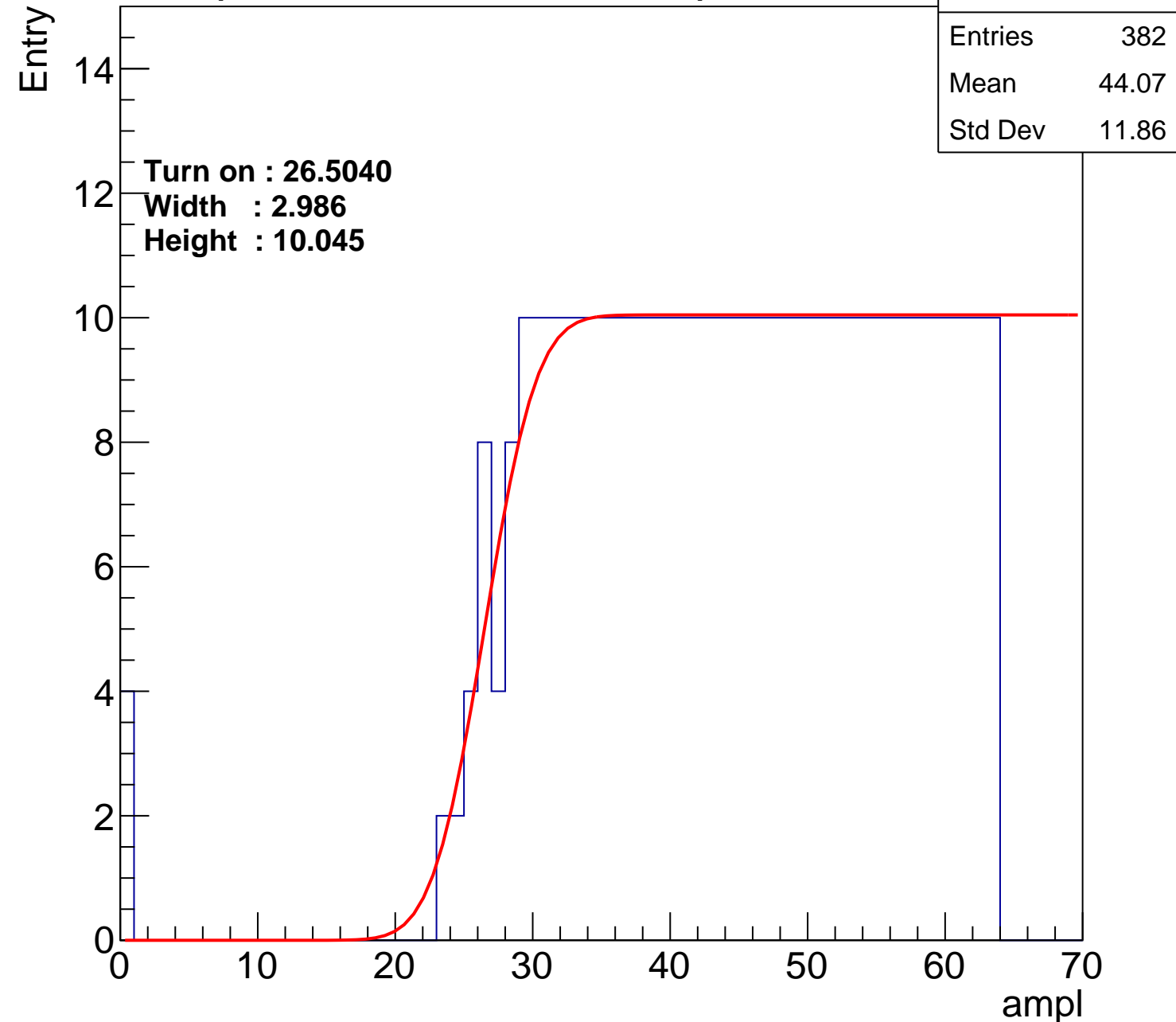
Width : 2.986

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44
Std Dev	11.61

Turn on : 25.4284

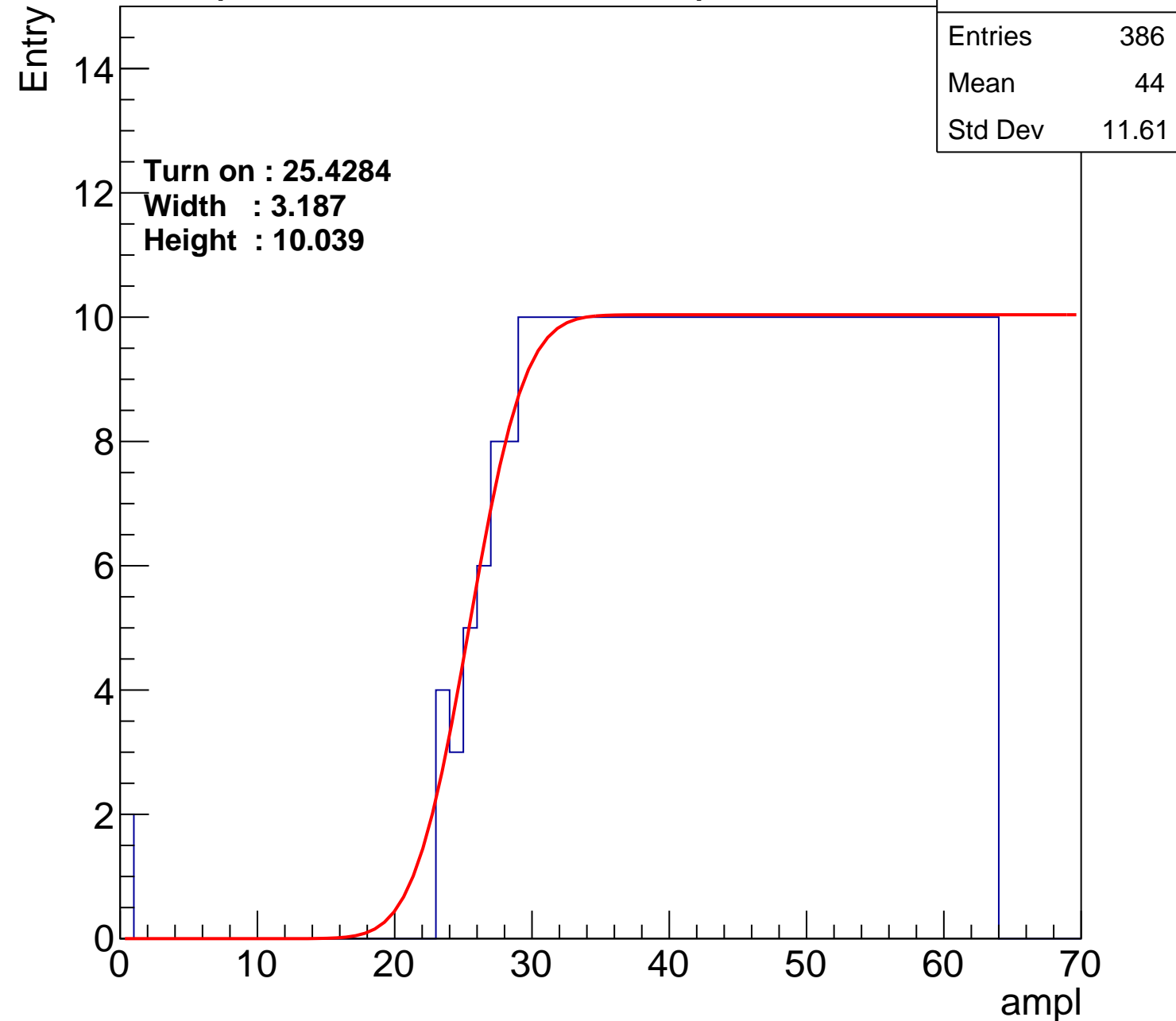
Width : 3.187

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch75

calib_packv5_042523_0143.root, FC#3, port B3

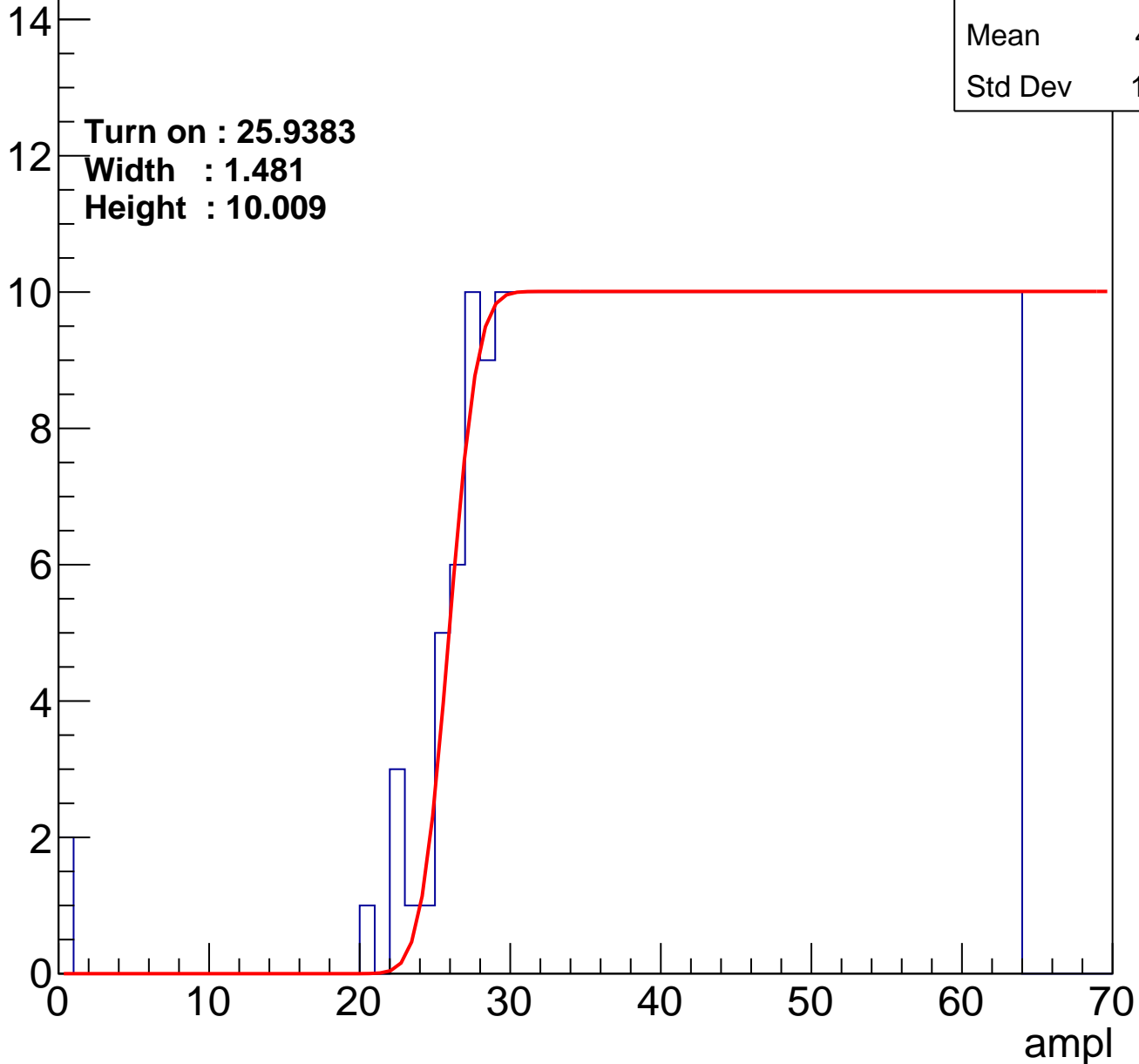
Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.9383

Width : 1.481

Height : 10.009

Entry



B1L000S, U23-ch76

calib_packv5_042523_0143.root, FC#3, port B3

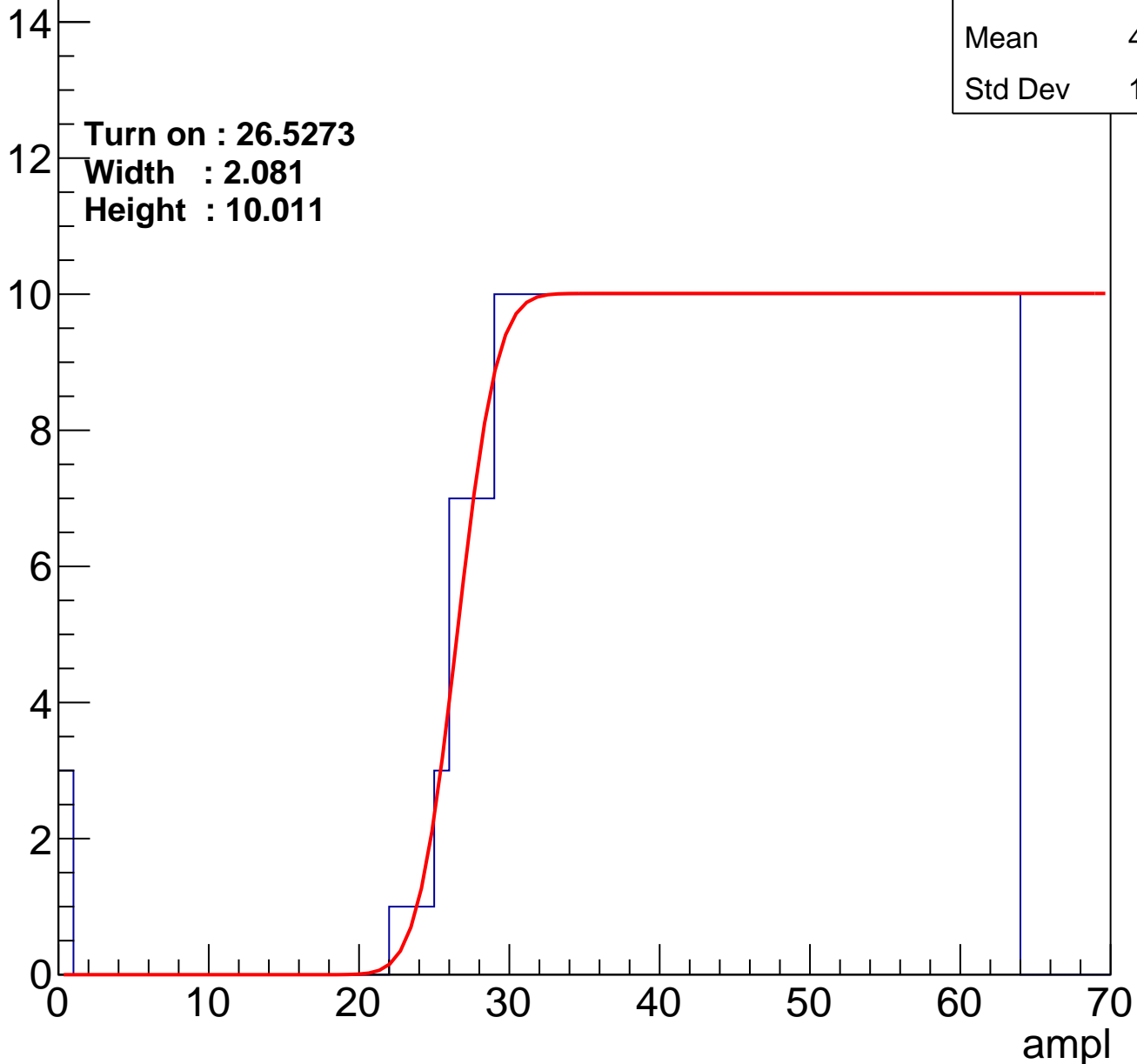
Entries	380
Mean	44.24
Std Dev	11.63

Turn on : 26.5273

Width : 2.081

Height : 10.011

Entry



B1L000S, U23-ch77

calib_packv5_042523_0143.root, FC#3, port B3

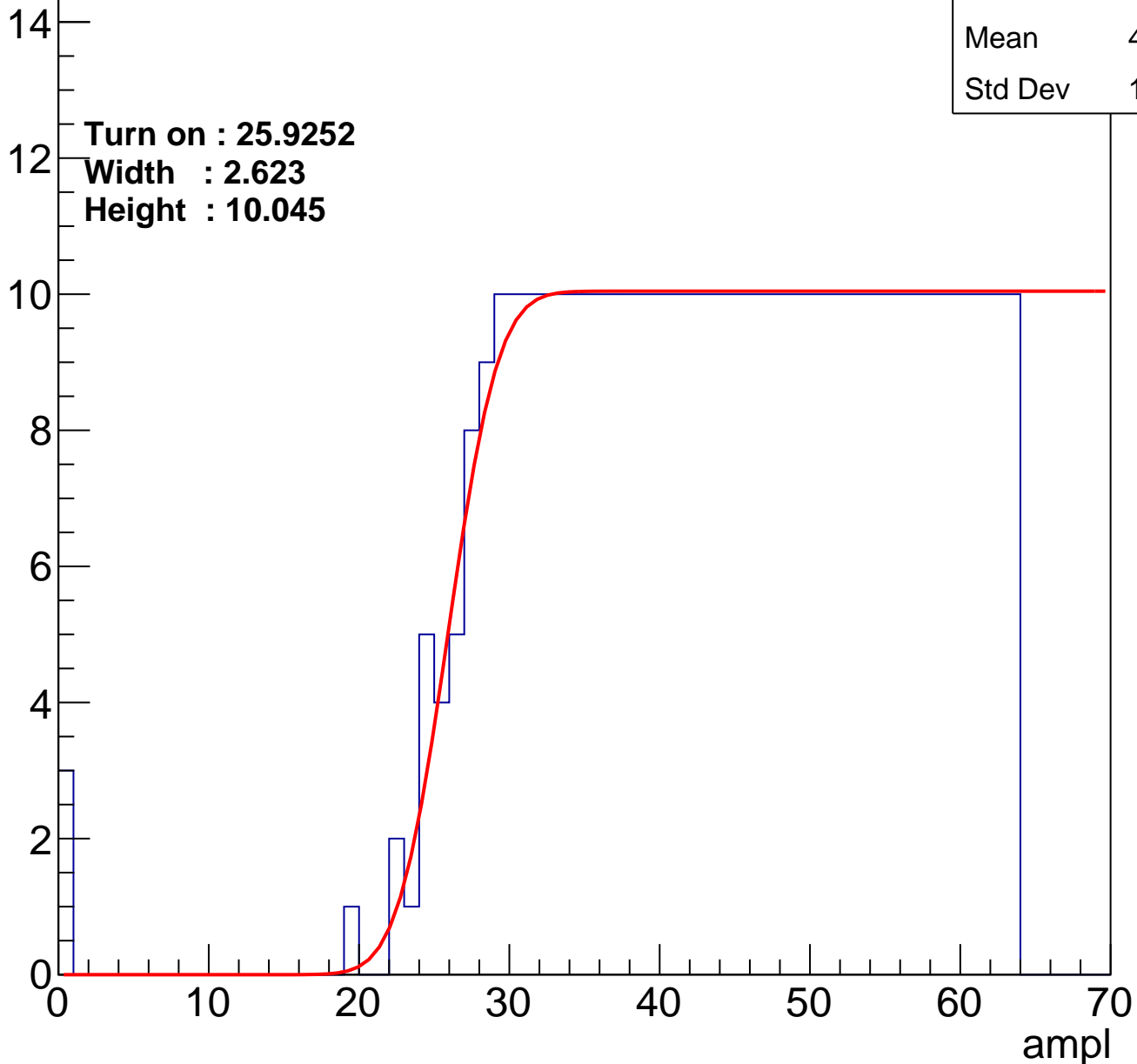
Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 25.9252

Width : 2.623

Height : 10.045

Entry



B1L000S, U23-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.3
Std Dev	11.58

Turn on : 26.4911

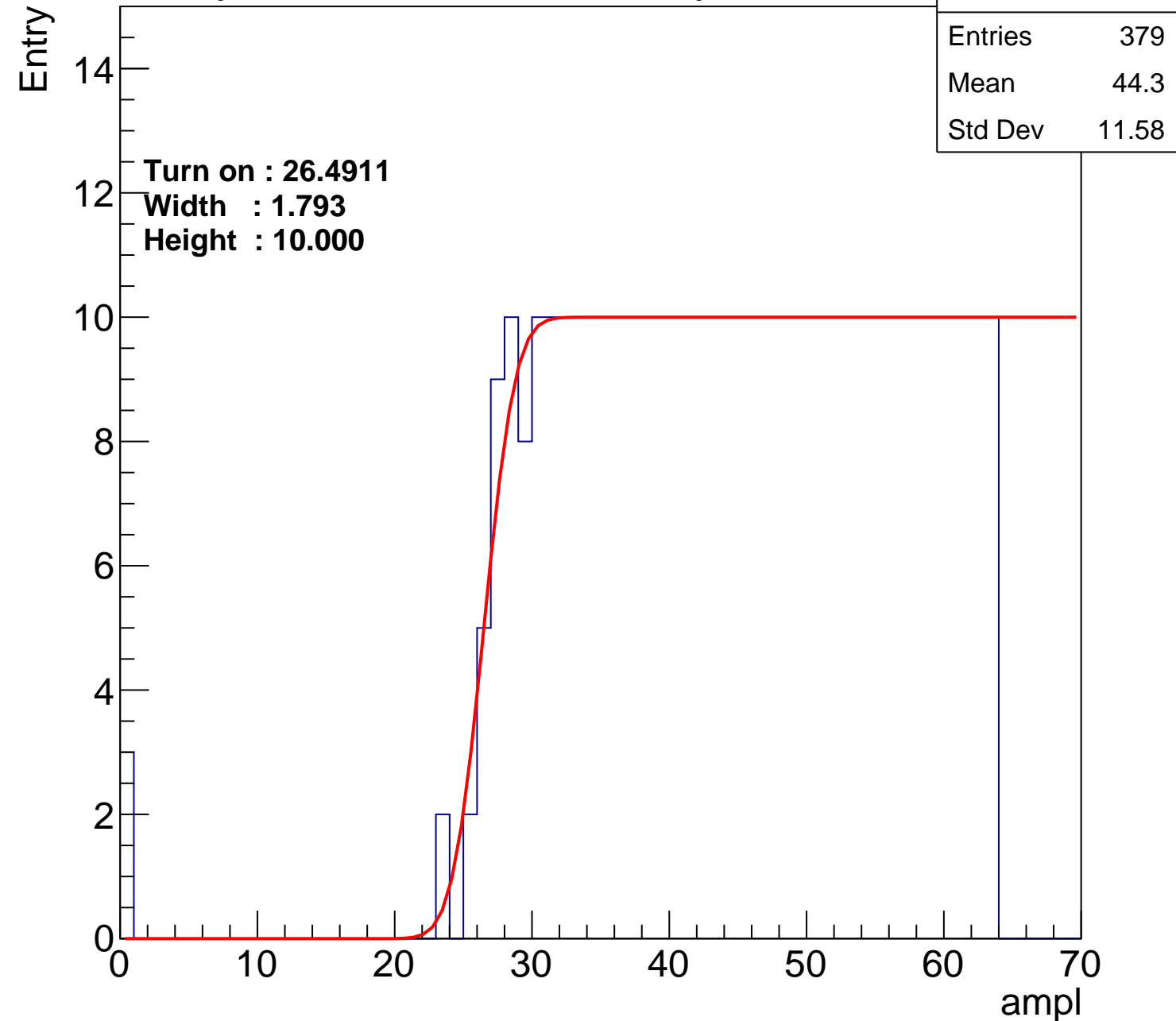
Width : 1.793

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.2970

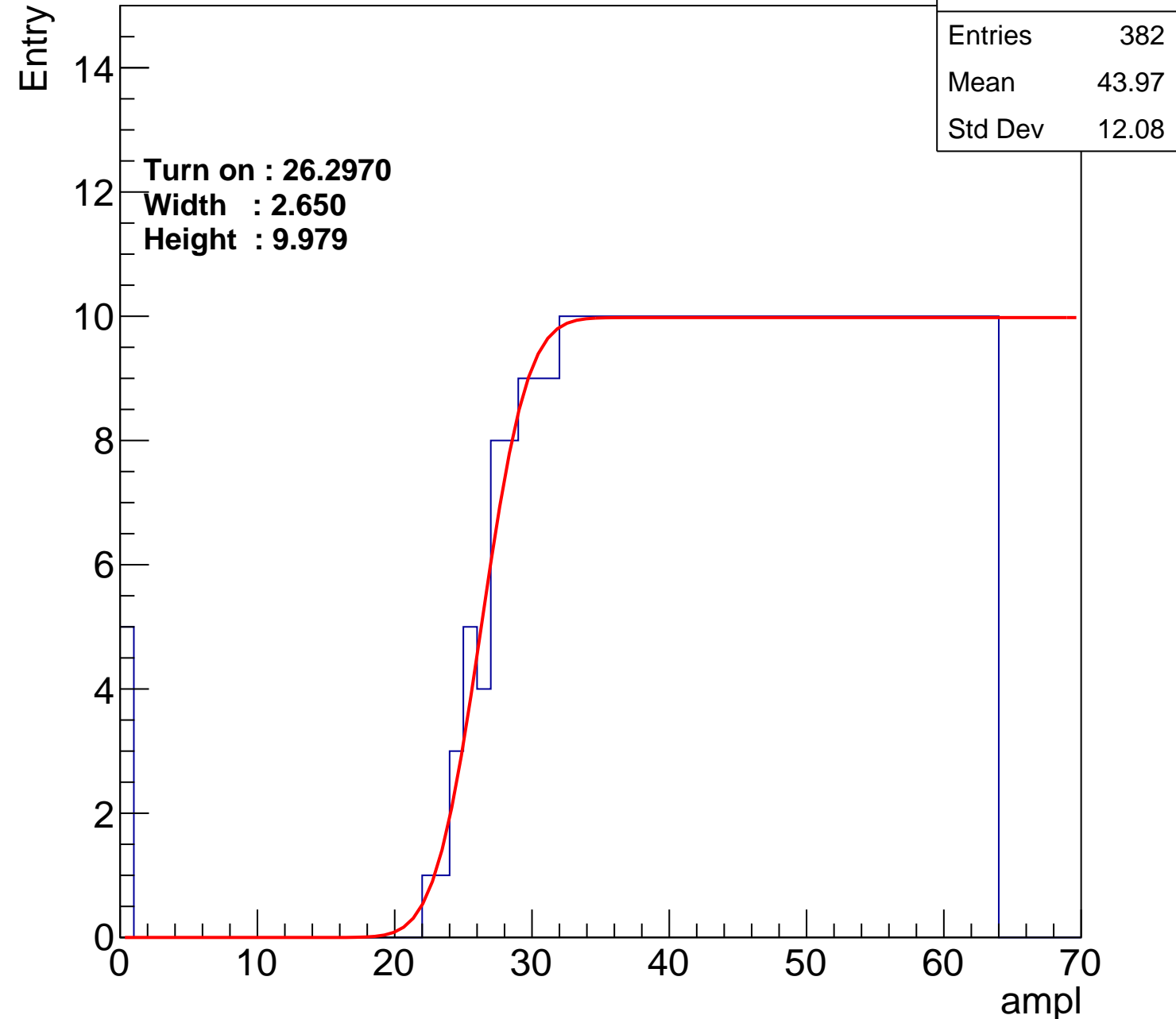
Width : 2.650

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 27.3912

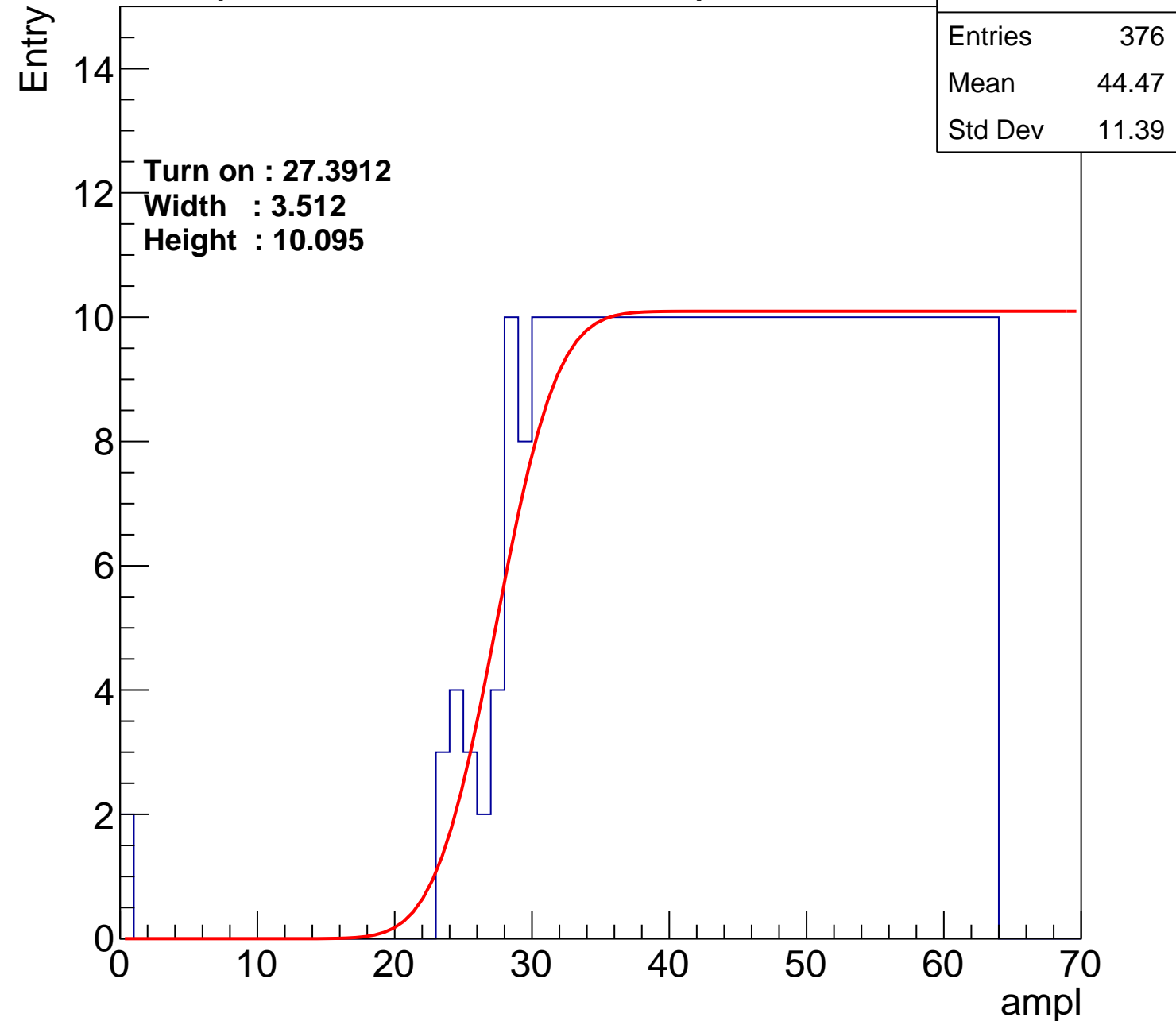
Width : 3.512

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch81

calib_packv5_042523_0143.root, FC#3, port B3

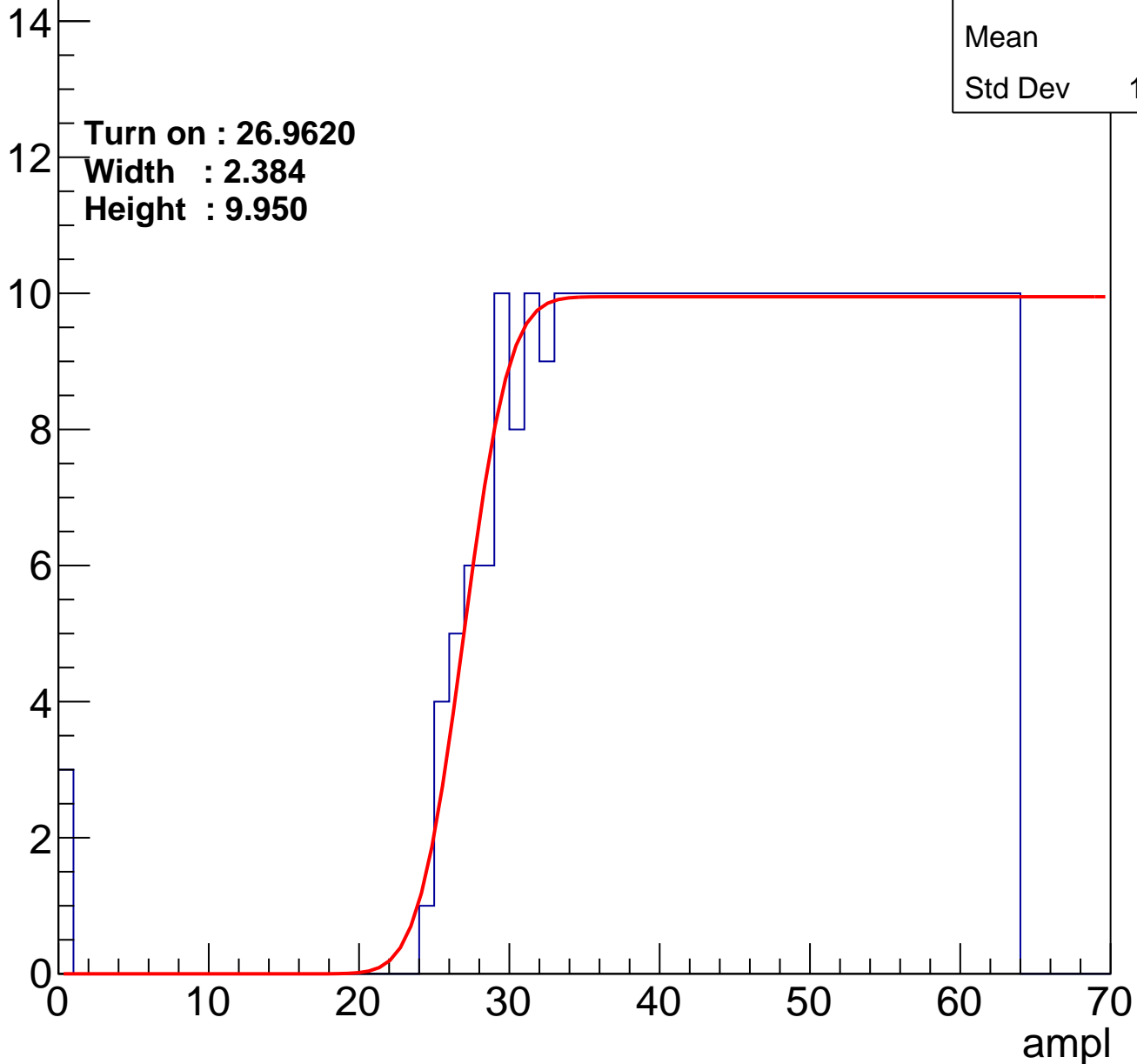
Entries	372
Mean	44.6
Std Dev	11.47

Turn on : 26.9620

Width : 2.384

Height : 9.950

Entry



B1L000S, U23-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 25.9980

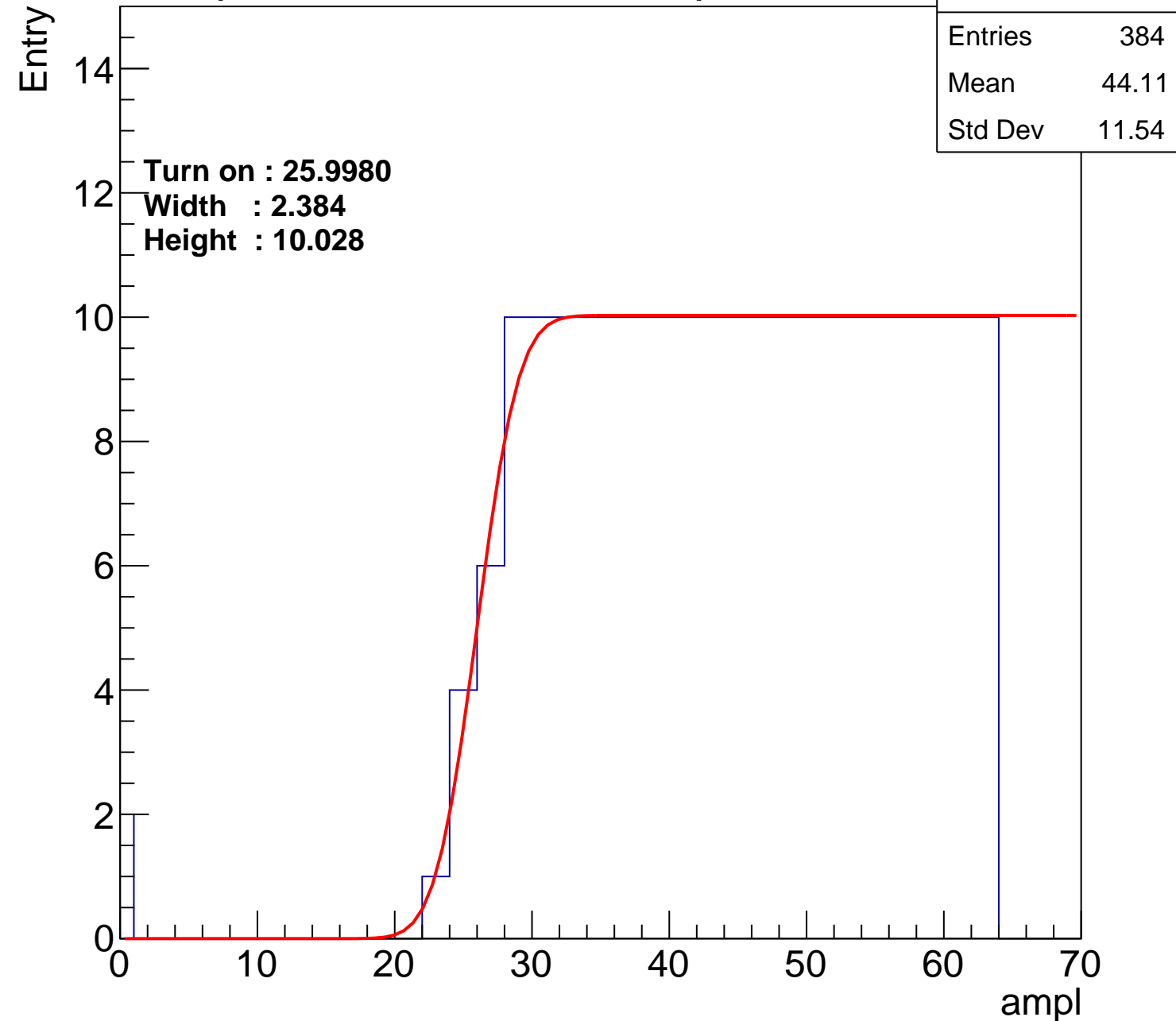
Width : 2.384

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch83

calib_packv5_042523_0143.root, FC#3, port B3

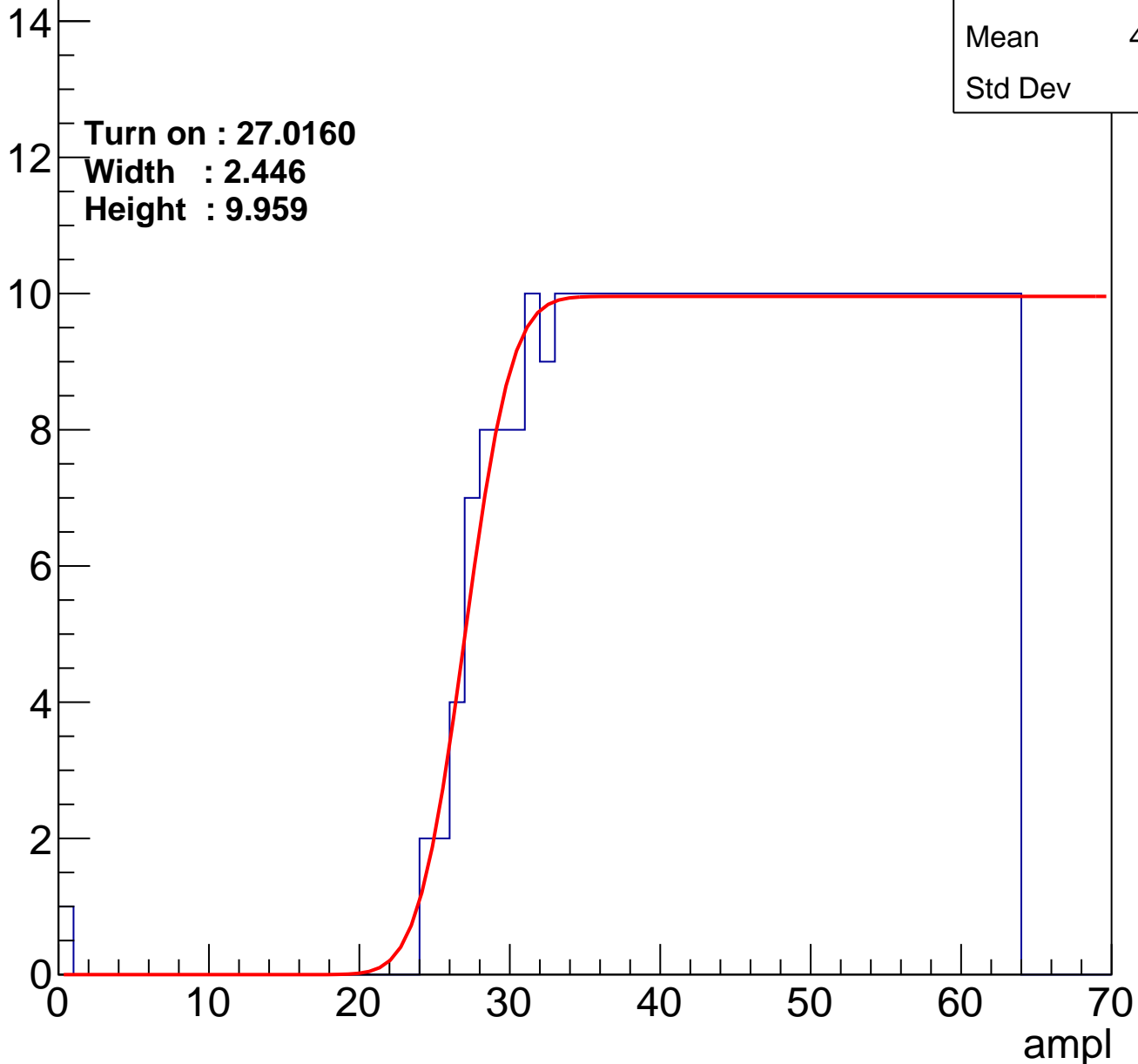
Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.0160

Width : 2.446

Height : 9.959

Entry



B1L000S, U23-ch84

calib_packv5_042523_0143.root, FC#3, port B3

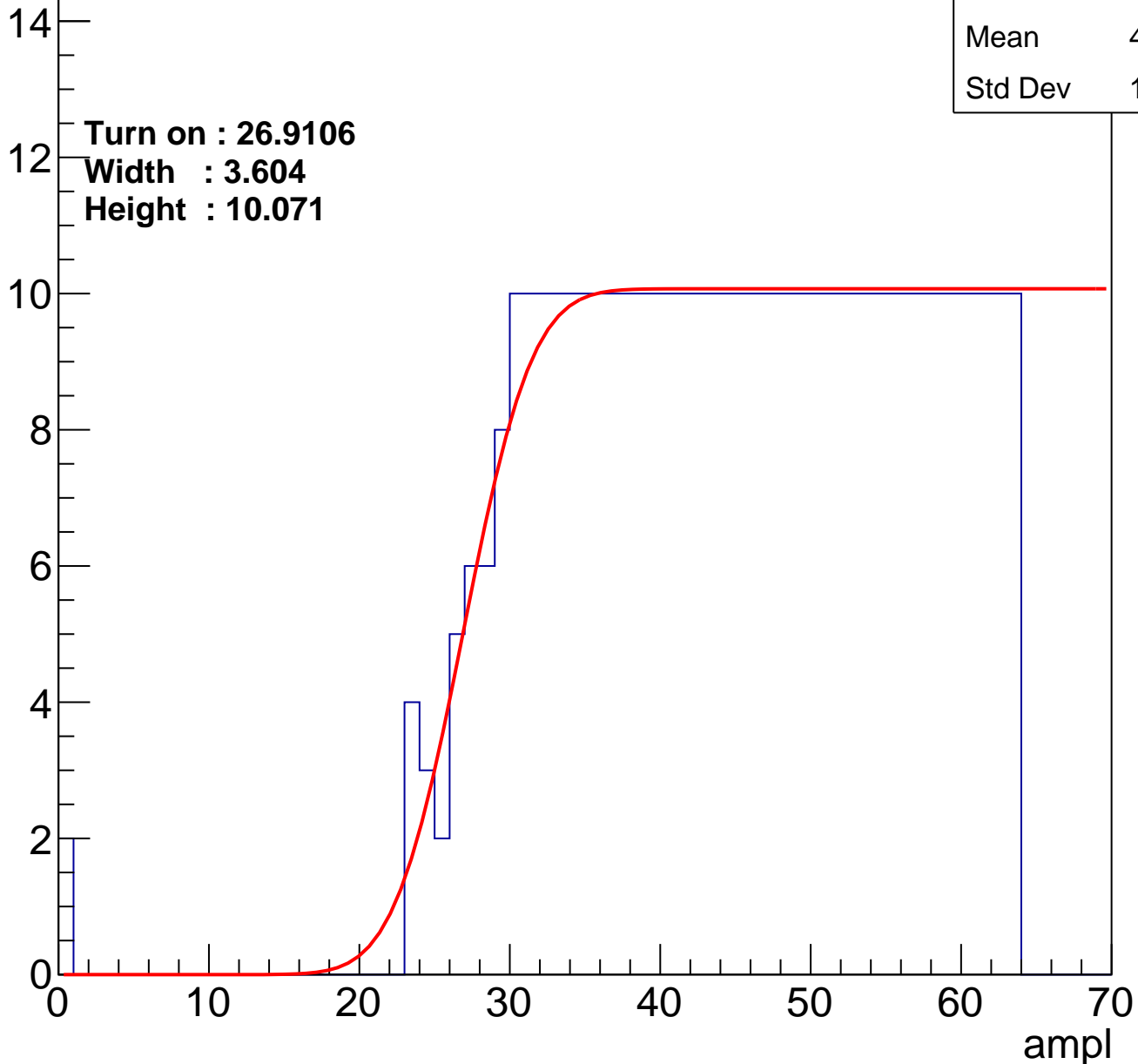
Entries	376
Mean	44.46
Std Dev	11.42

Turn on : 26.9106

Width : 3.604

Height : 10.071

Entry



B1L000S, U23-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.5076

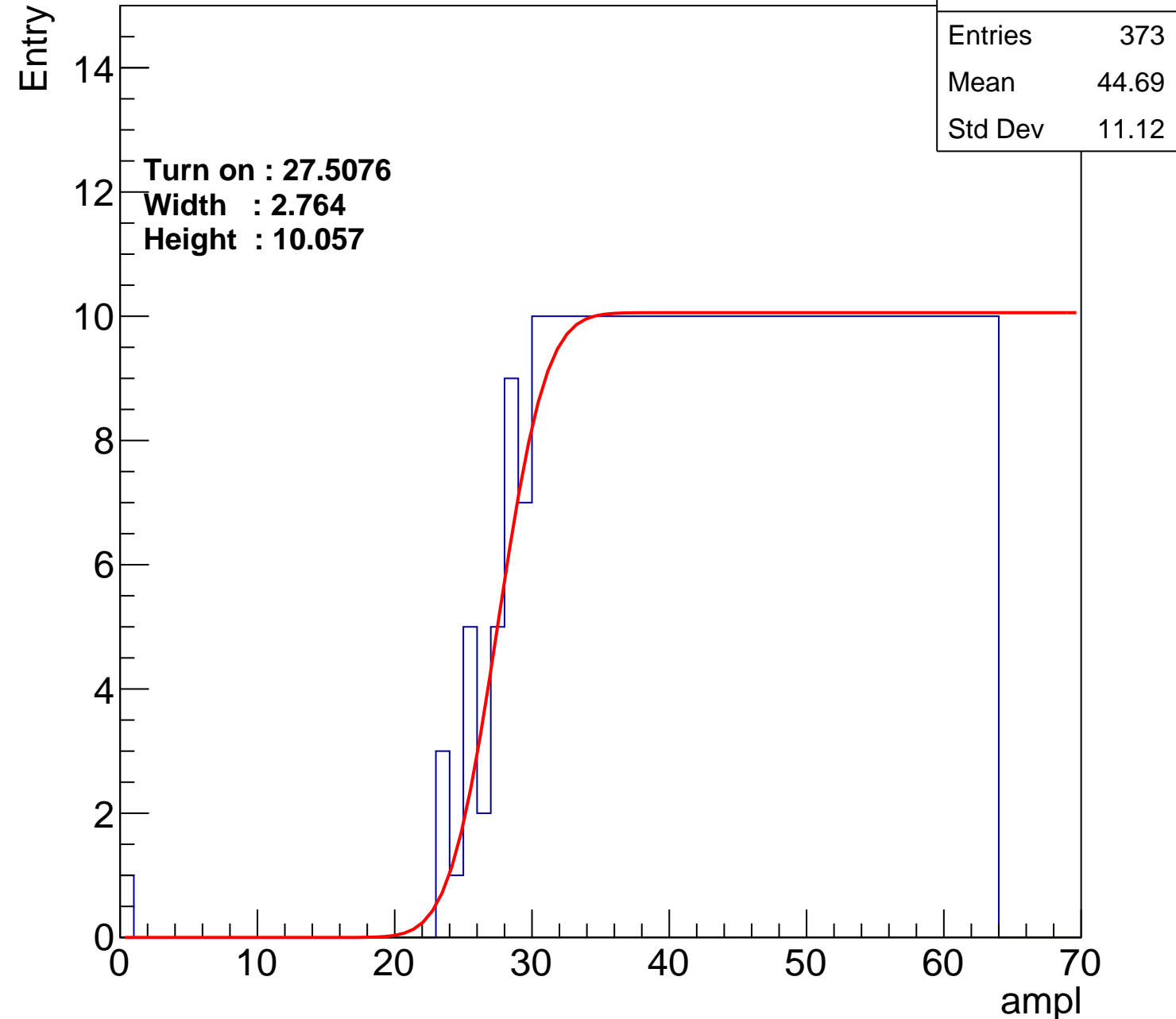
Width : 2.764

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.87
Std Dev	11.89

Turn on : 25.9858

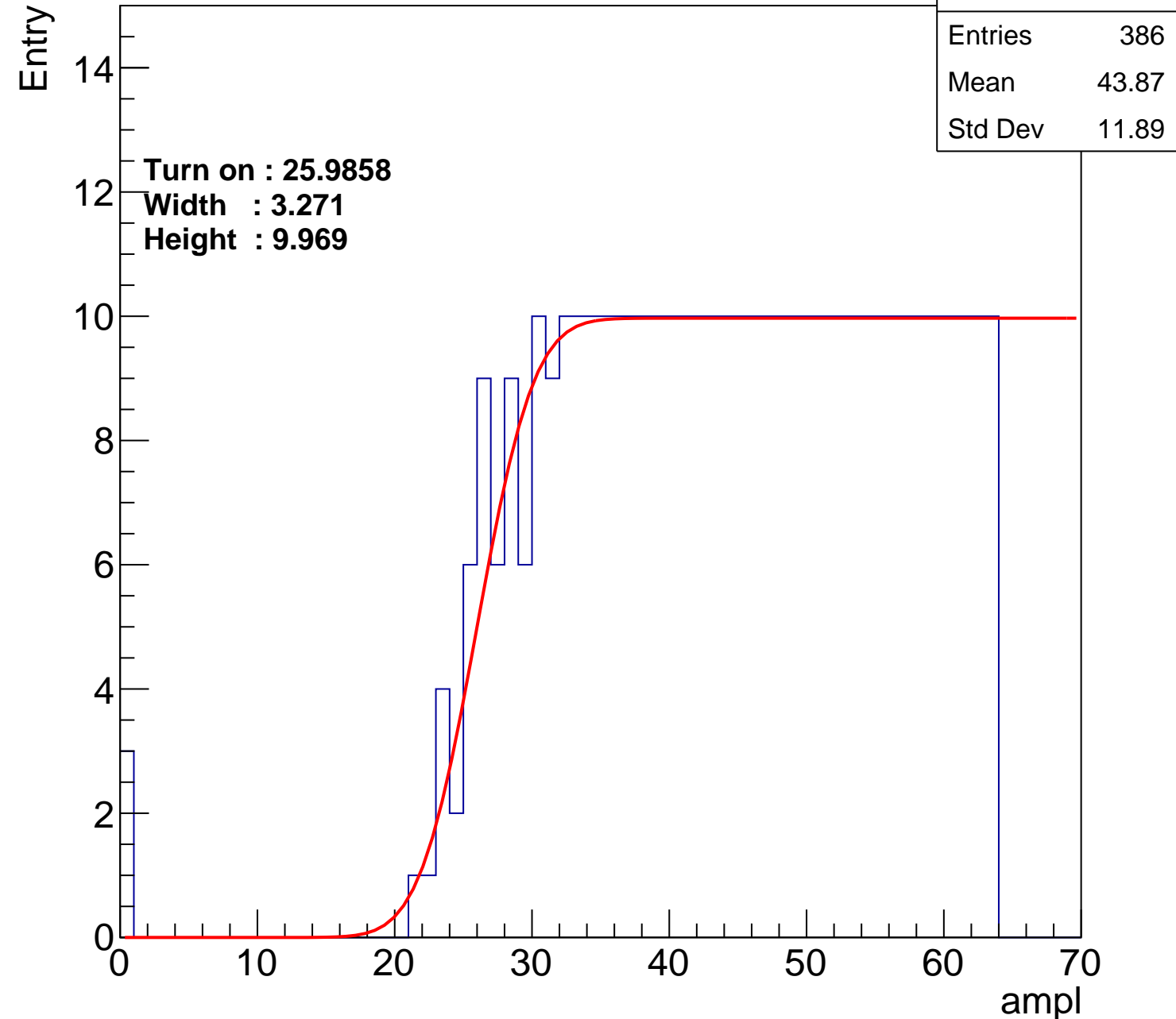
Width : 3.271

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.4513

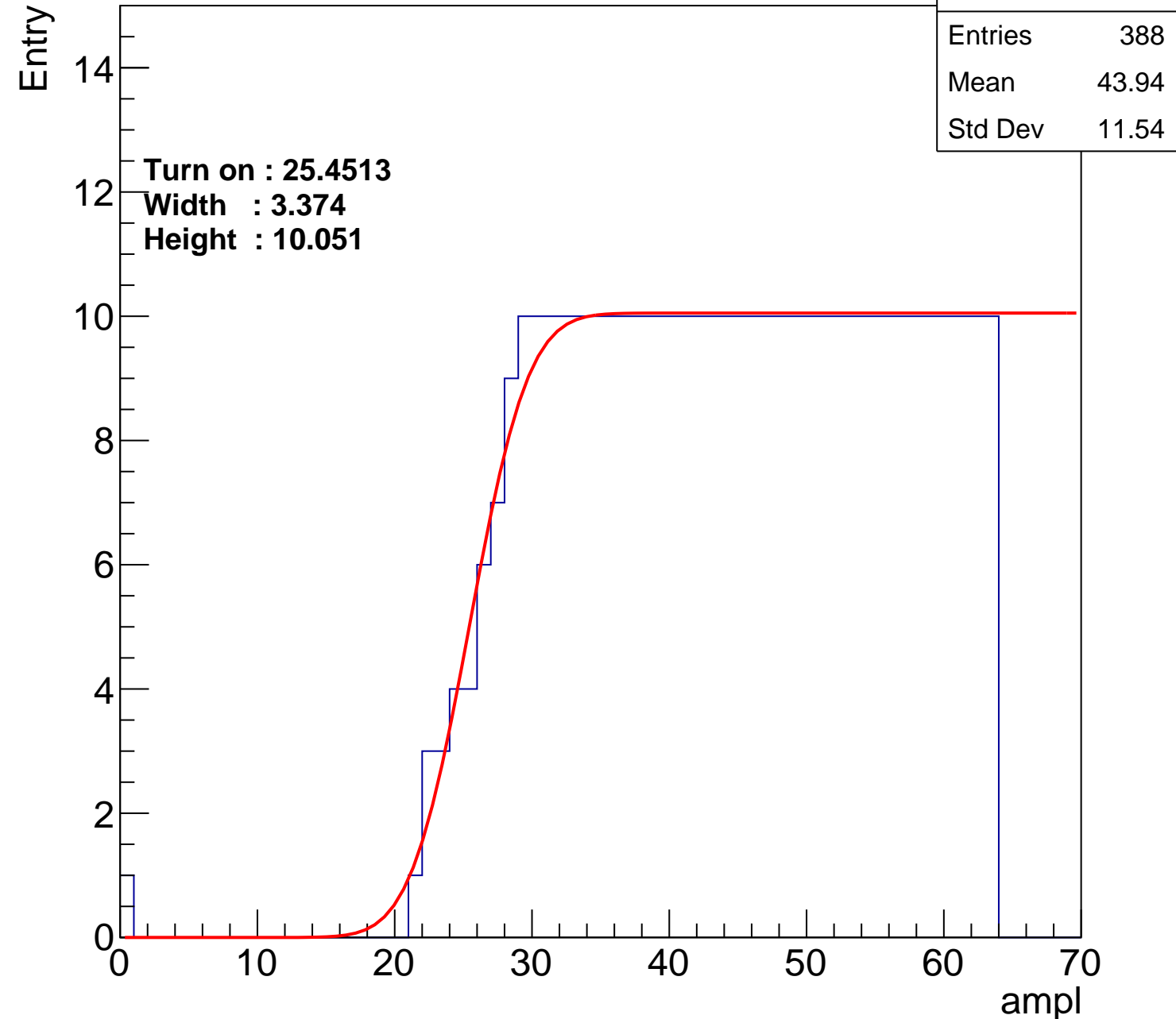
Width : 3.374

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.53
Std Dev	11.21

Turn on : 26.9835

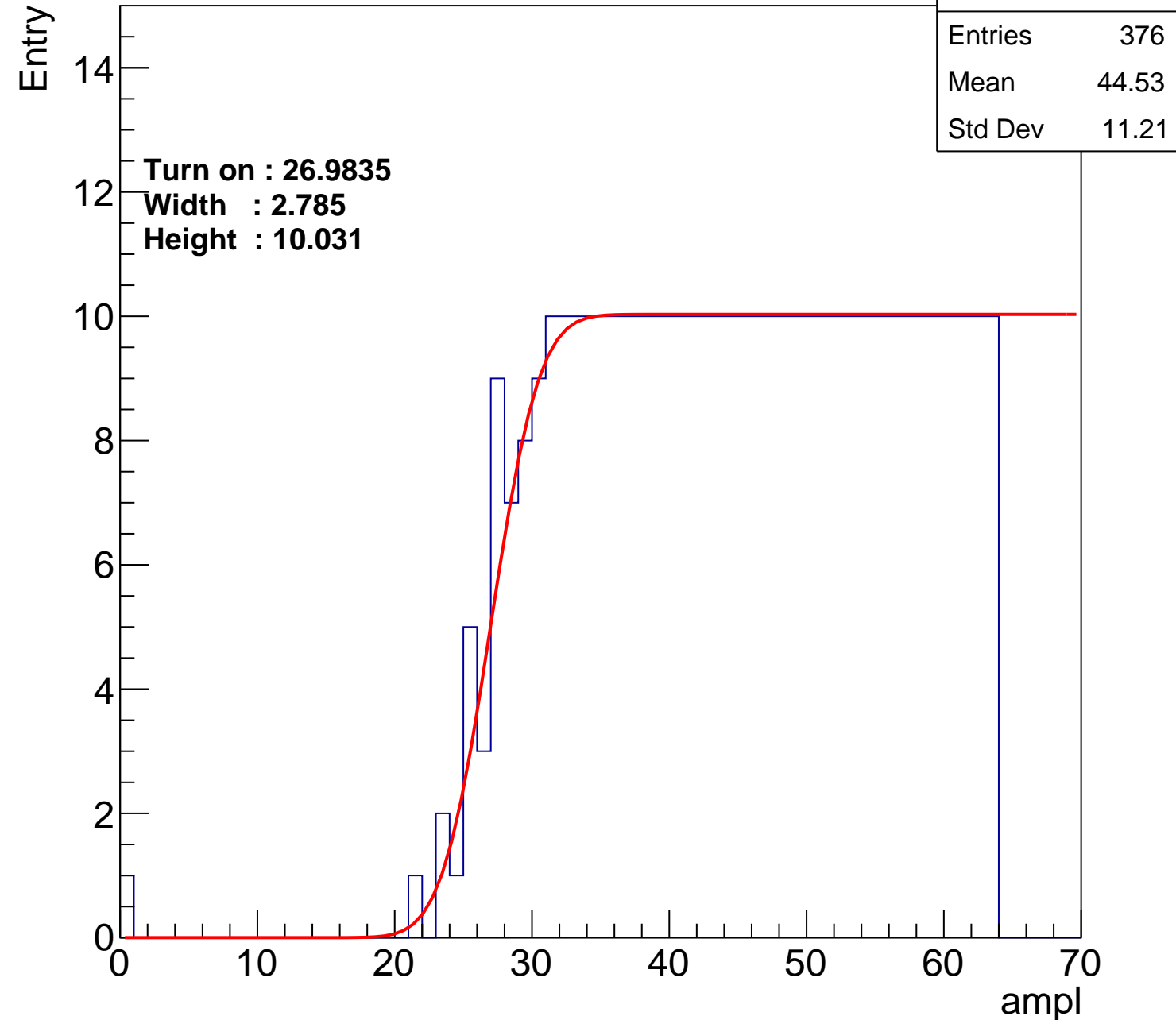
Width : 2.785

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch89

calib_packv5_042523_0143.root, FC#3, port B3

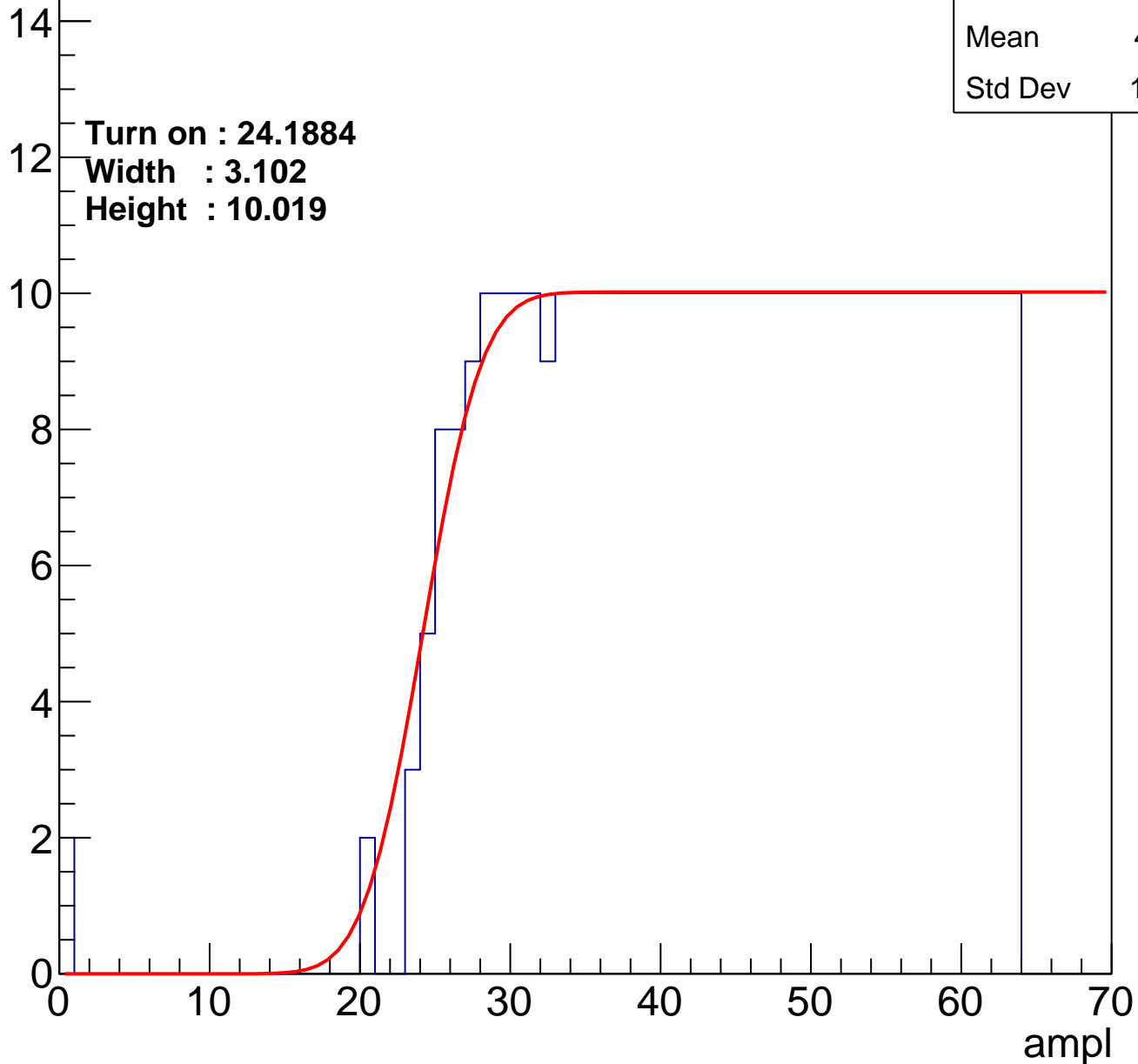
Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.1884

Width : 3.102

Height : 10.019

Entry



B1L000S, U23-ch90

calib_packv5_042523_0143.root, FC#3, port B3

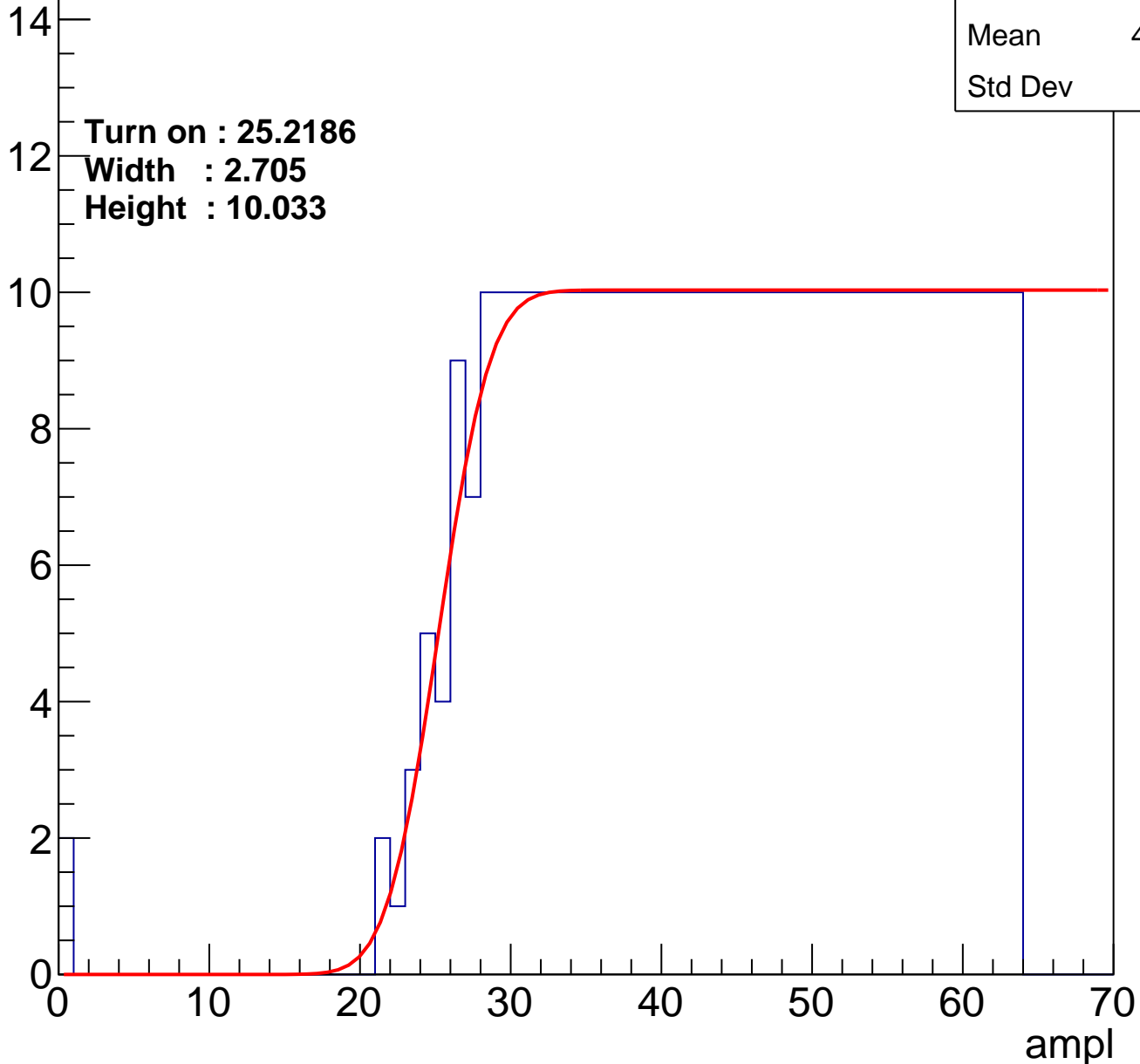
Entries	393
Mean	43.65
Std Dev	11.8

Turn on : 25.2186

Width : 2.705

Height : 10.033

Entry



B1L000S, U23-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.07
Std Dev	10.98

Turn on : 28.0396

Width : 3.859

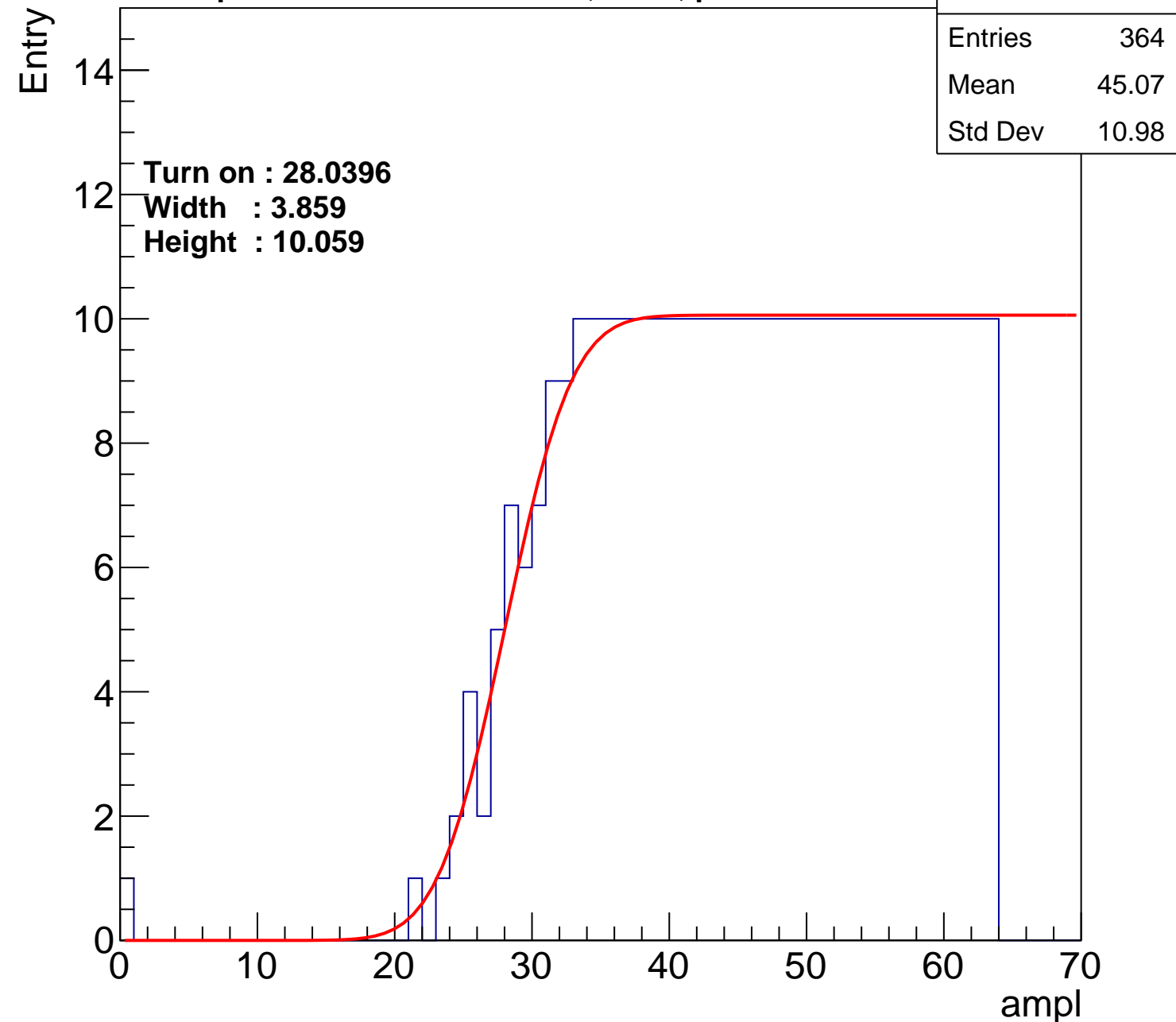
Height : 10.059

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L000S, U23-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 26.9043

Width : 3.256

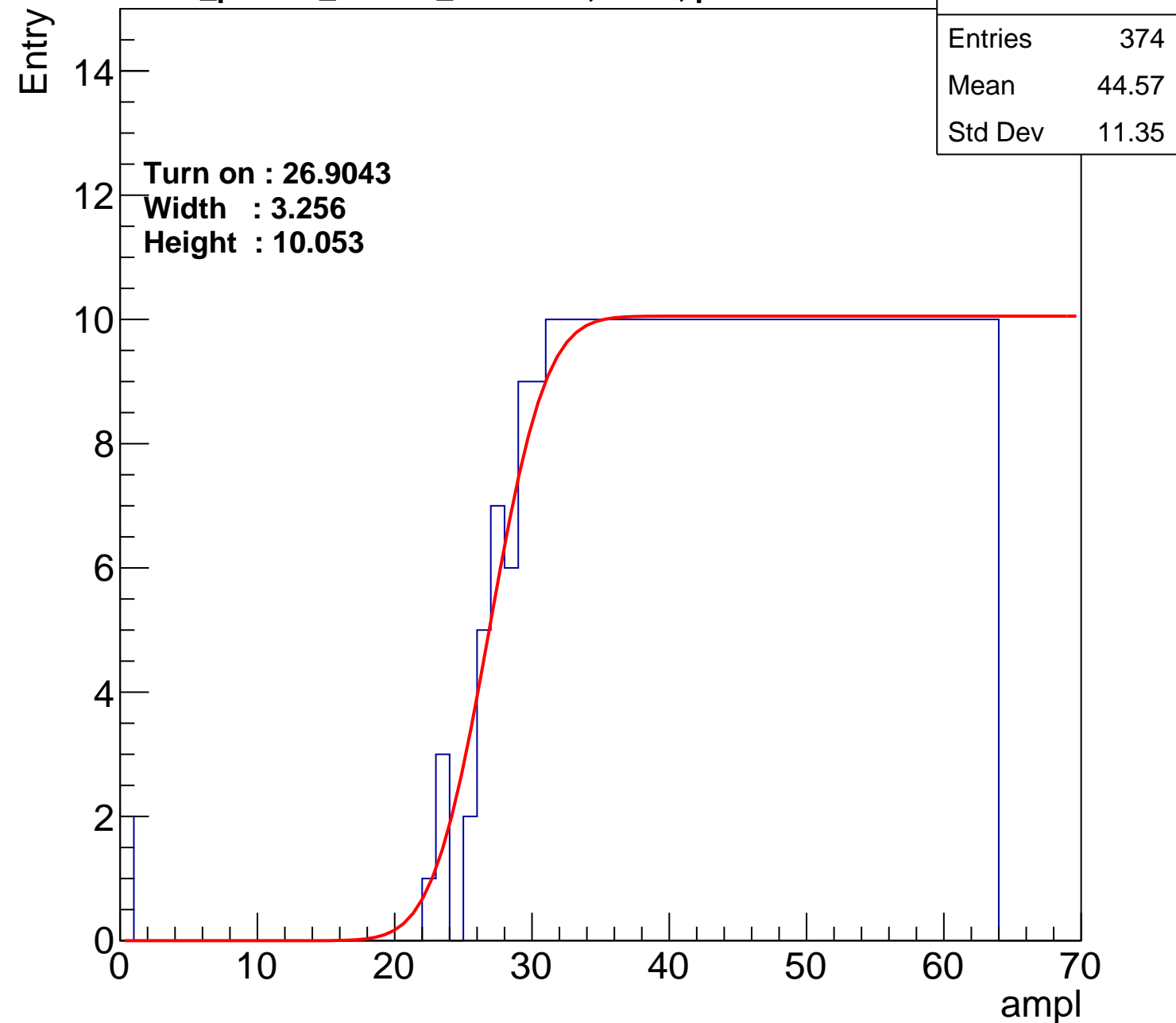
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U23-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.0275

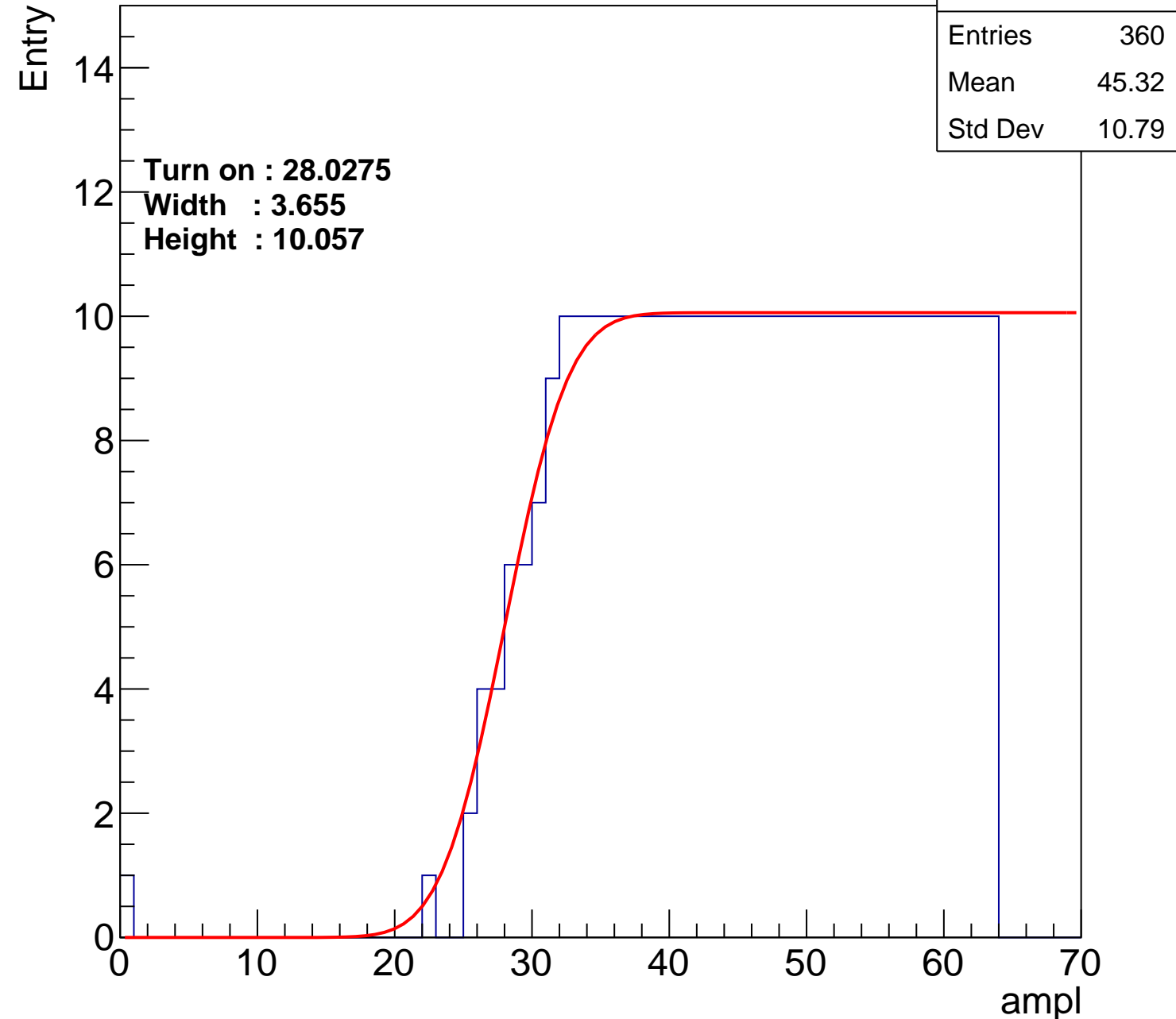
Width : 3.655

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.9563

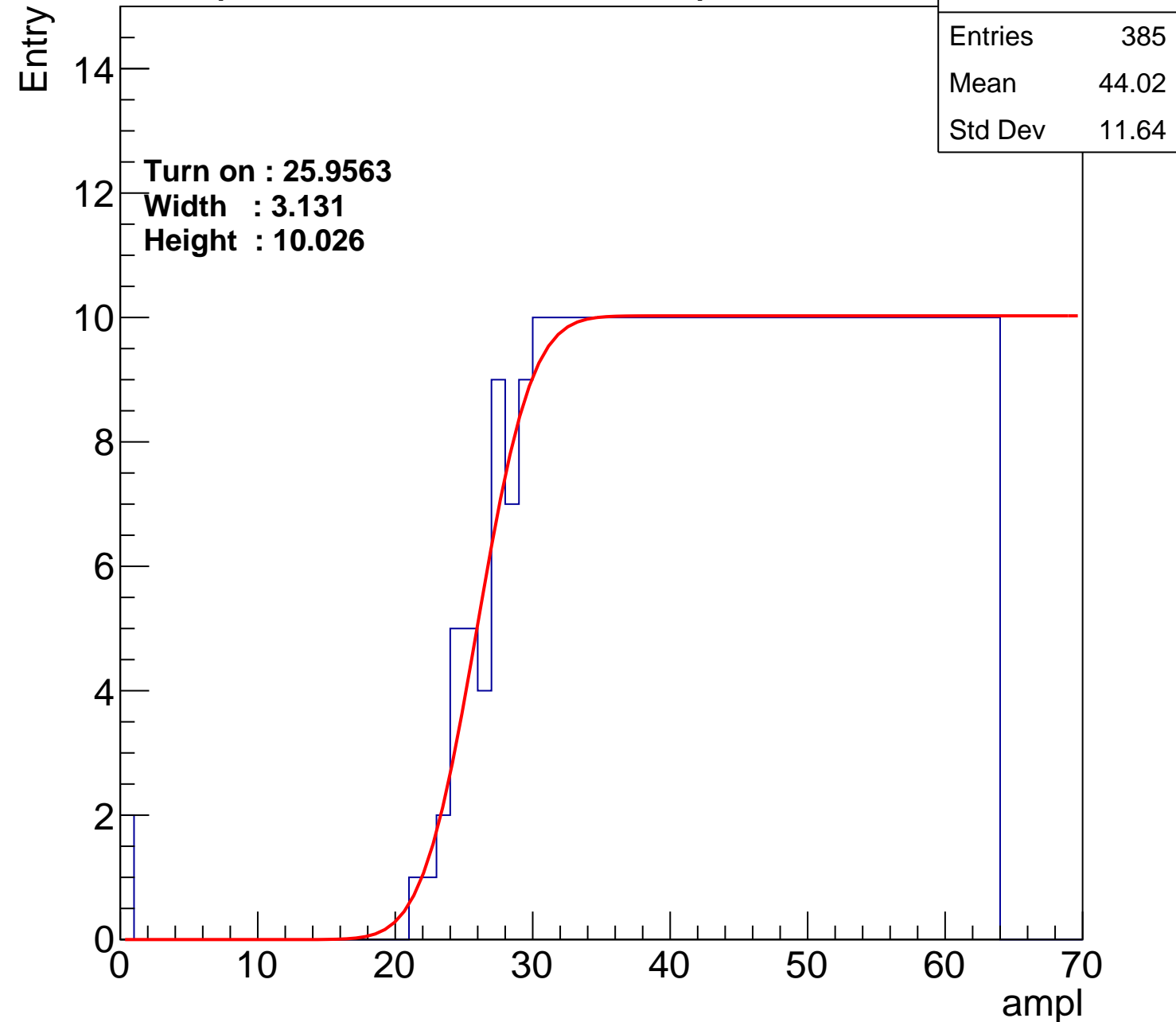
Width : 3.131

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.88
Std Dev	11.04

Turn on : 27.4601

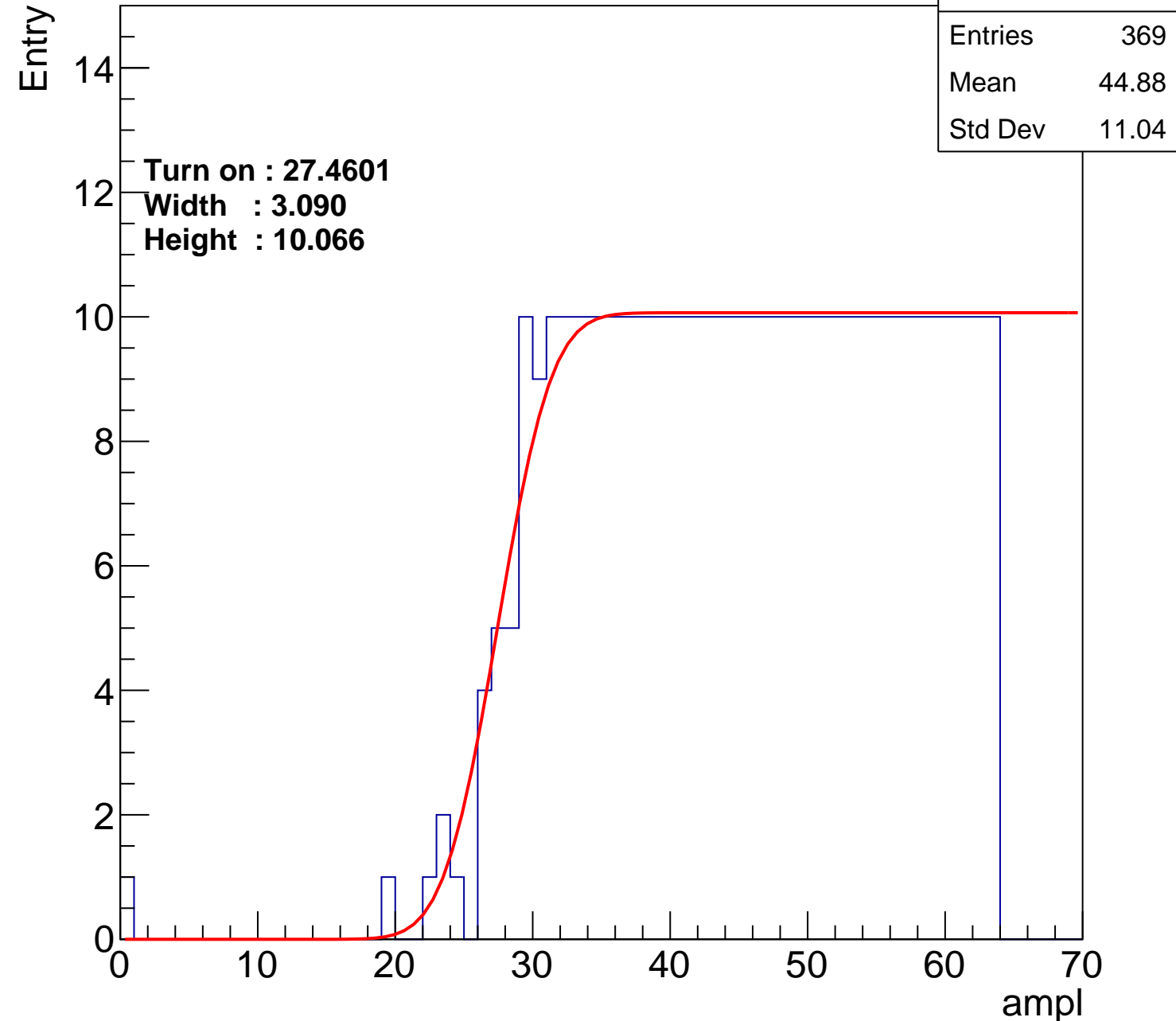
Width : 3.090

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.8
Std Dev	11.06

Turn on : 27.6315

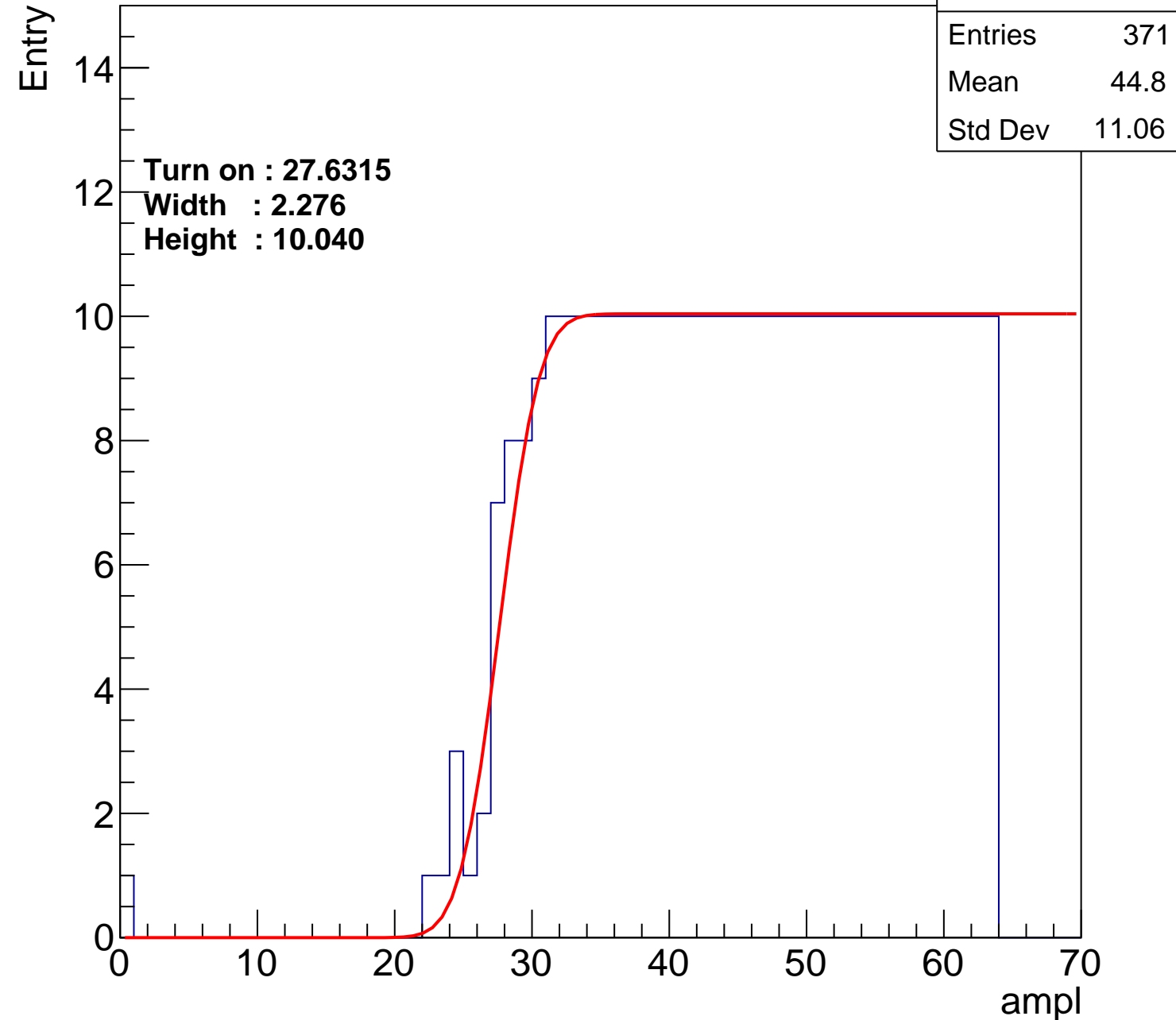
Width : 2.276

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.29
Std Dev	11.85

Turn on : 24.1162

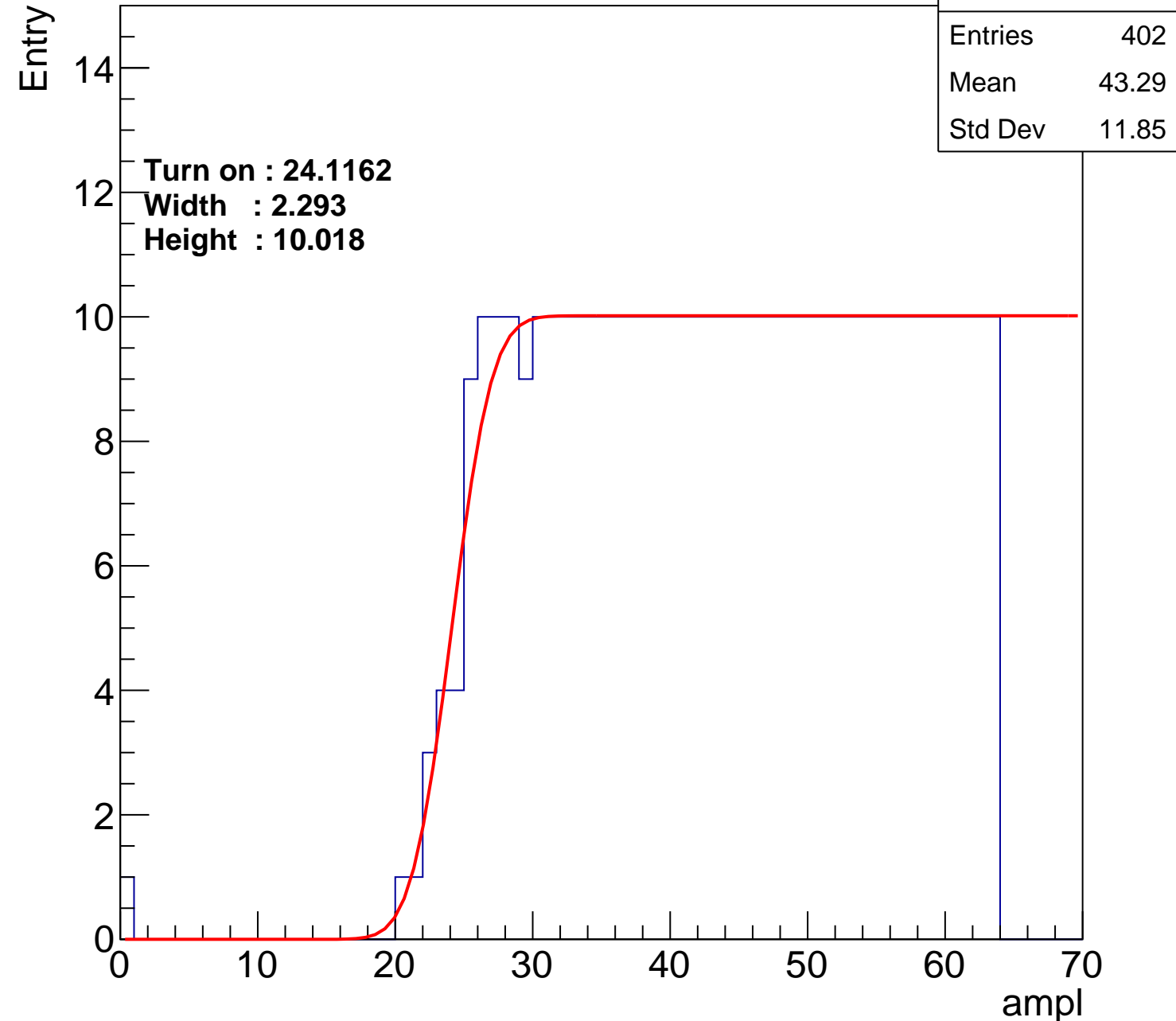
Width : 2.293

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch98

calib_packv5_042523_0143.root, FC#3, port B3

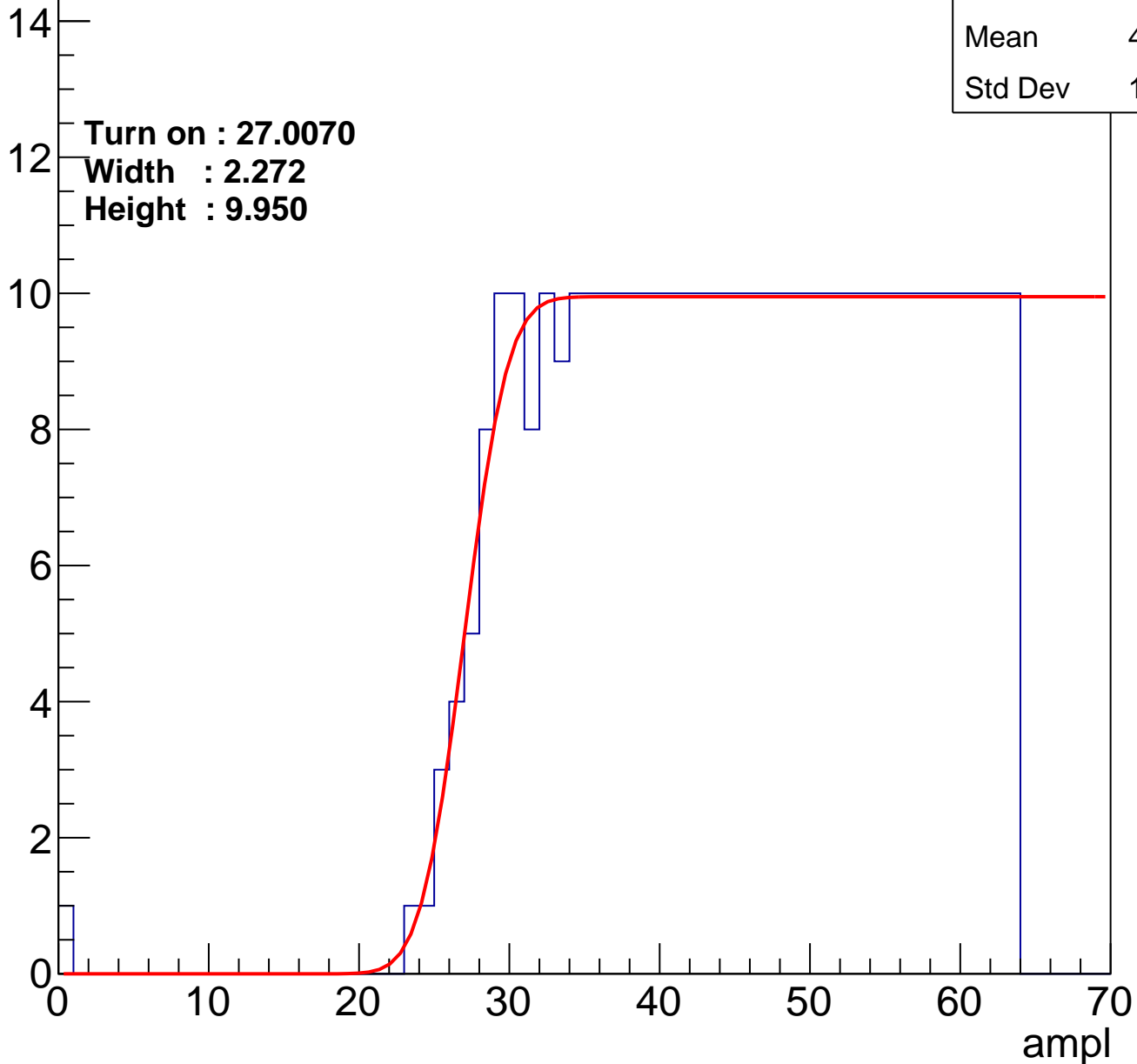
Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 27.0070

Width : 2.272

Height : 9.950

Entry



B1L000S, U23-ch99

calib_packv5_042523_0143.root, FC#3, port B3

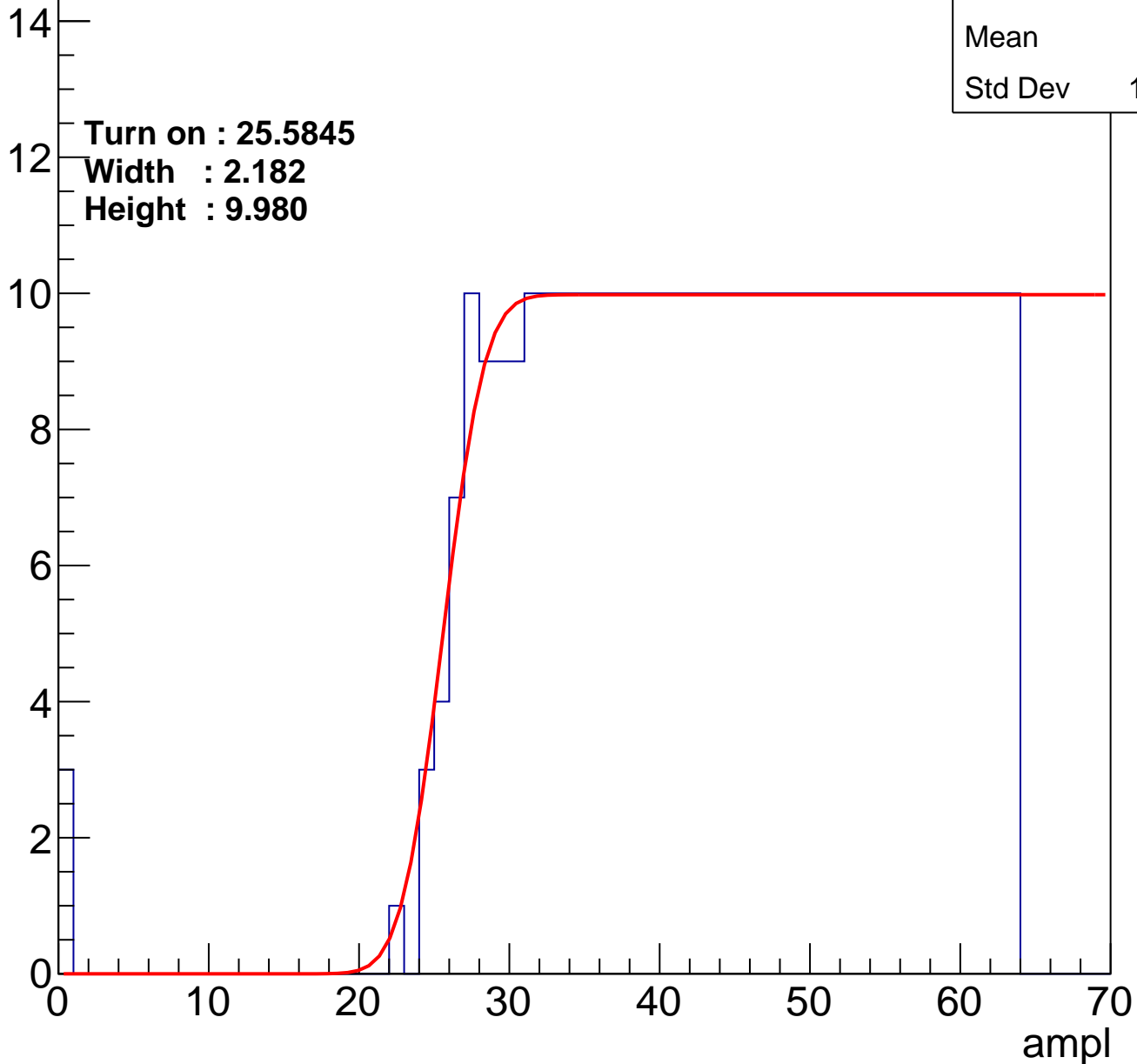
Entries	385
Mean	44
Std Dev	11.74

Turn on : 25.5845

Width : 2.182

Height : 9.980

Entry



B1L000S, U23-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.7630

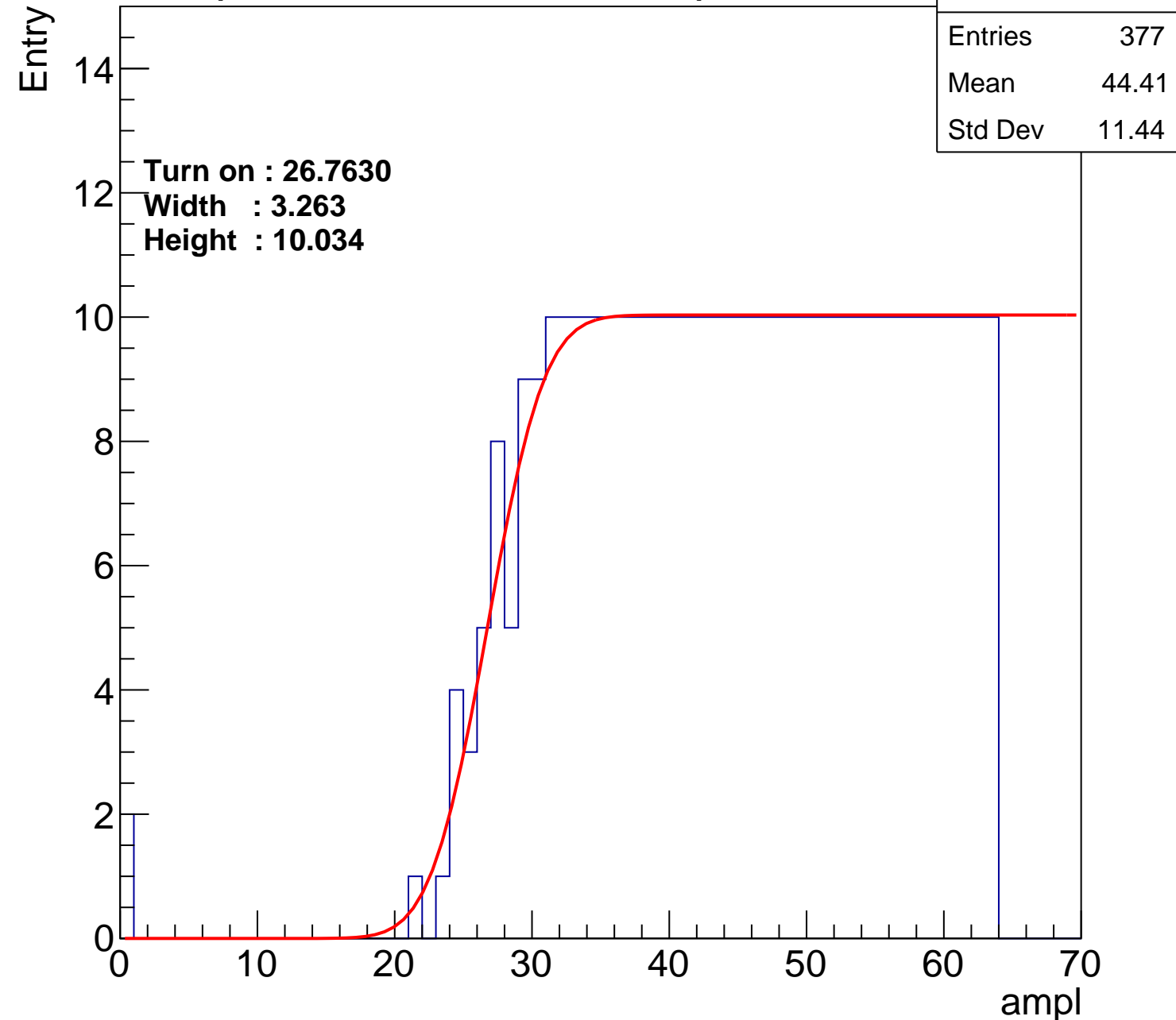
Width : 3.263

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.66
Std Dev	11.78

Turn on : 25.0981

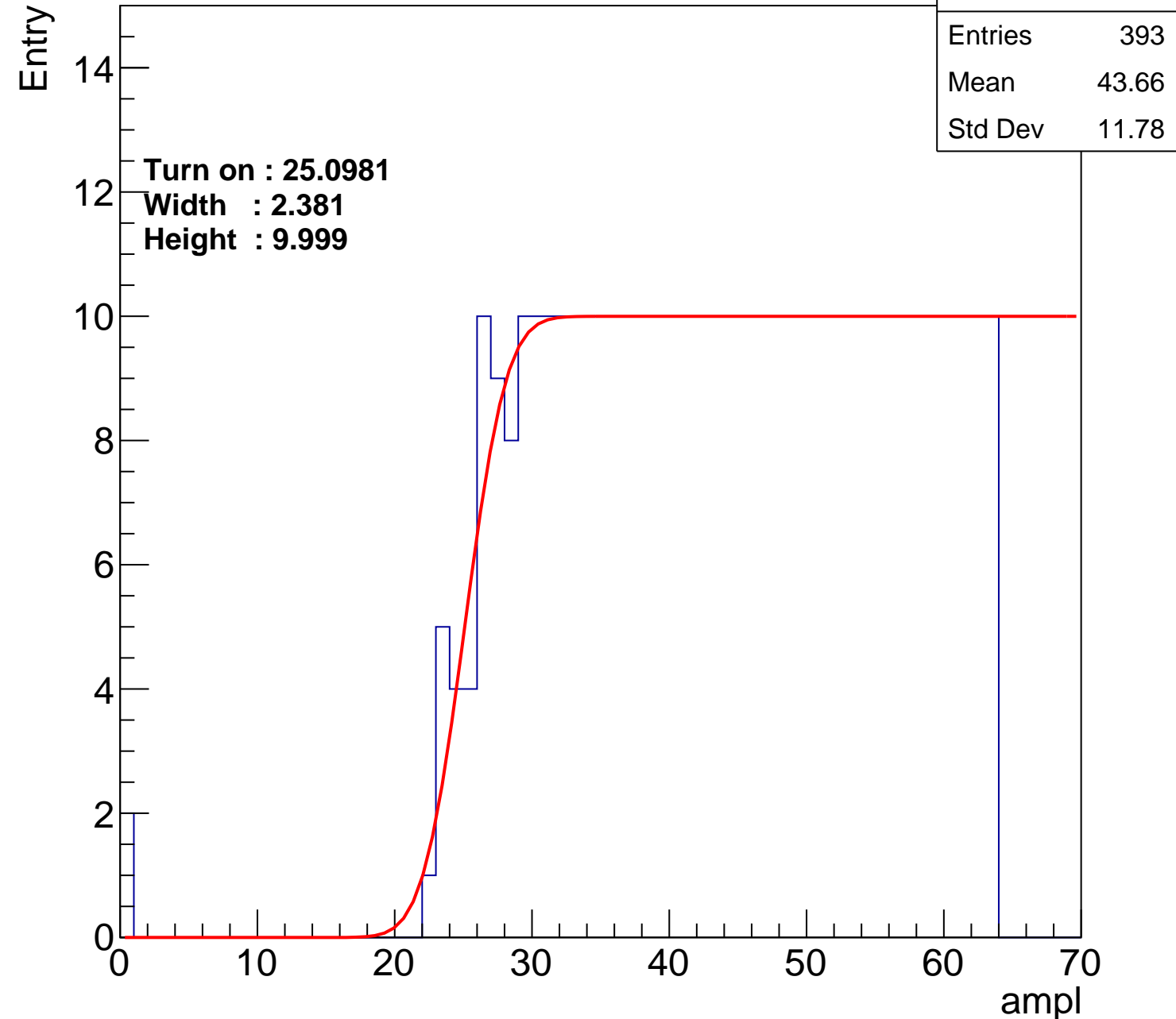
Width : 2.381

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch102

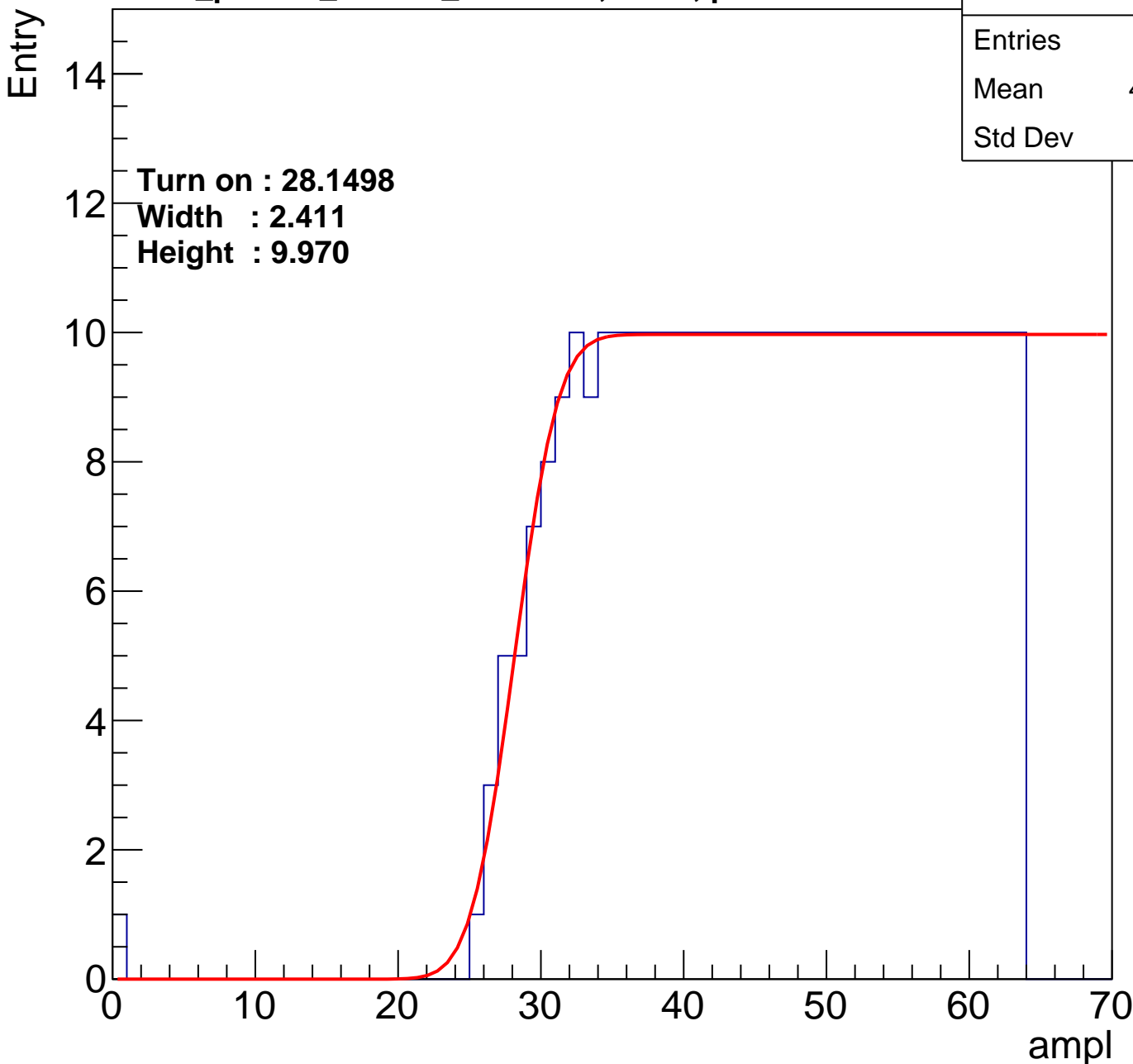
calib_packv5_042523_0143.root, FC#3, port B3

Entries	358
Mean	45.44
Std Dev	10.7

Turn on : 28.1498

Width : 2.411

Height : 9.970



B1L000S, U23-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.38
Std Dev	11.97

Turn on : 24.0657

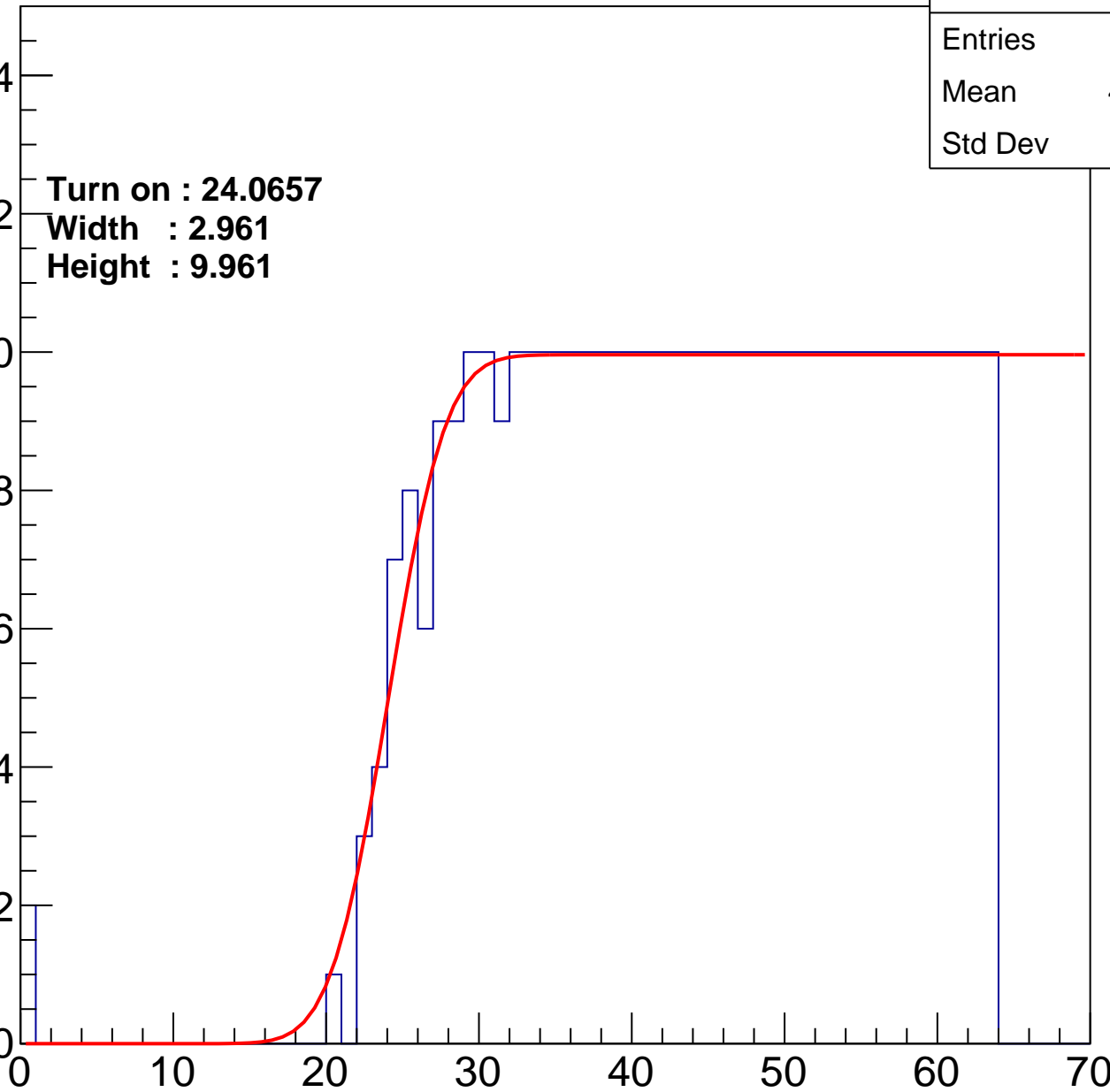
Width : 2.961

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.93
Std Dev	11.8

Turn on : 25.5049

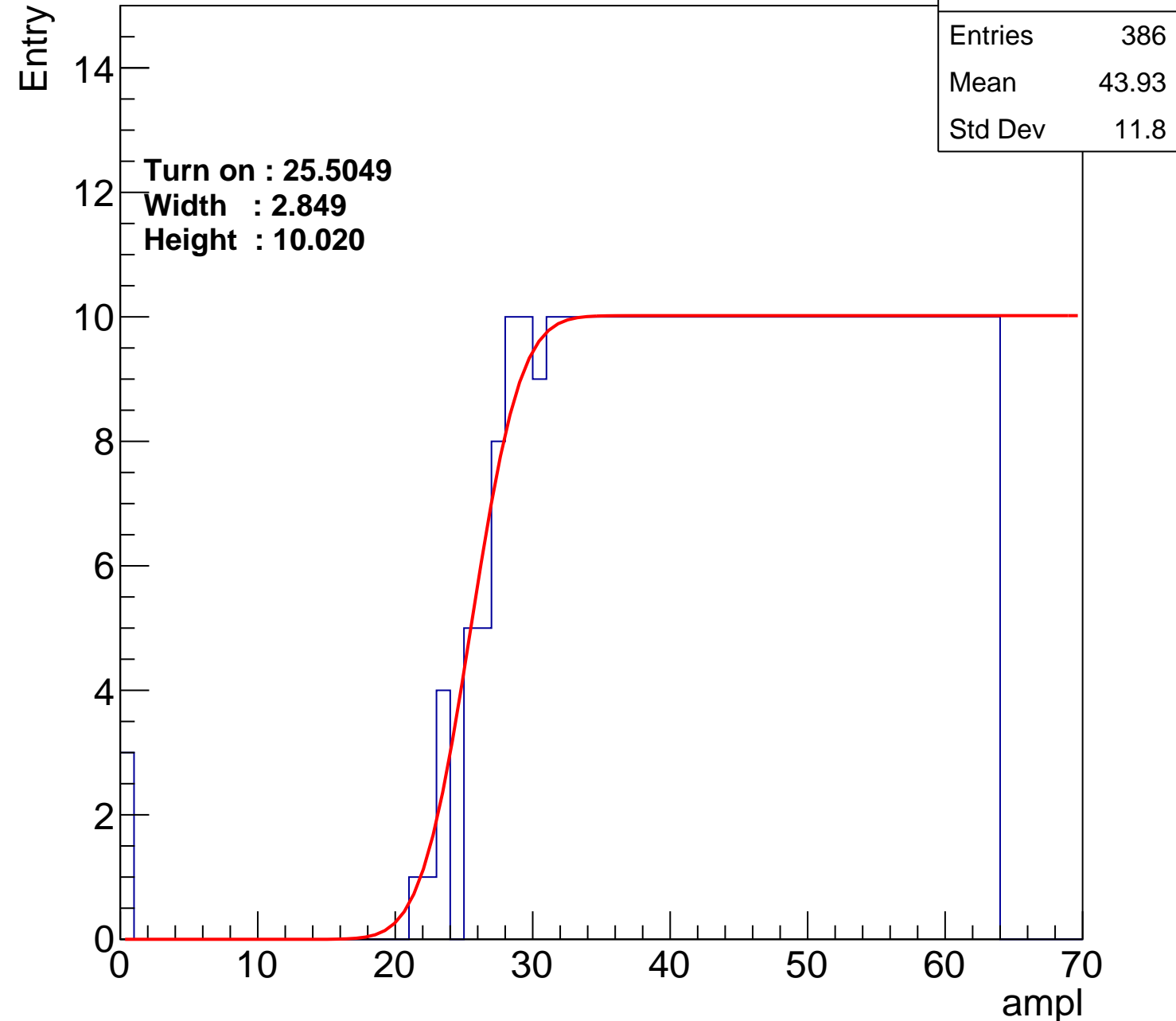
Width : 2.849

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch105

calib_packv5_042523_0143.root, FC#3, port B3

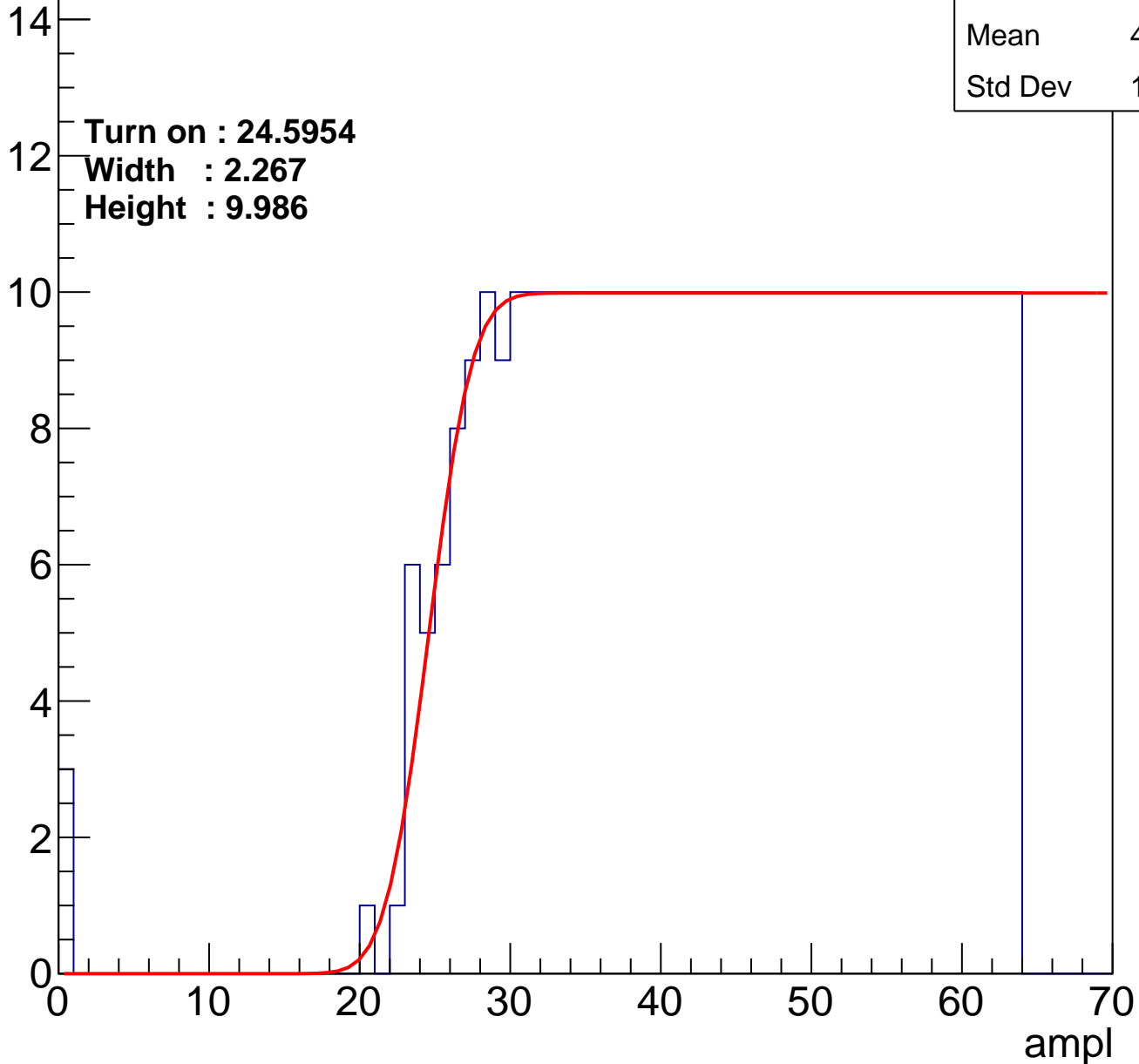
Entries	398
Mean	43.35
Std Dev	12.09

Turn on : 24.5954

Width : 2.267

Height : 9.986

Entry



B1L000S, U23-ch106

calib_packv5_042523_0143.root, FC#3, port B3

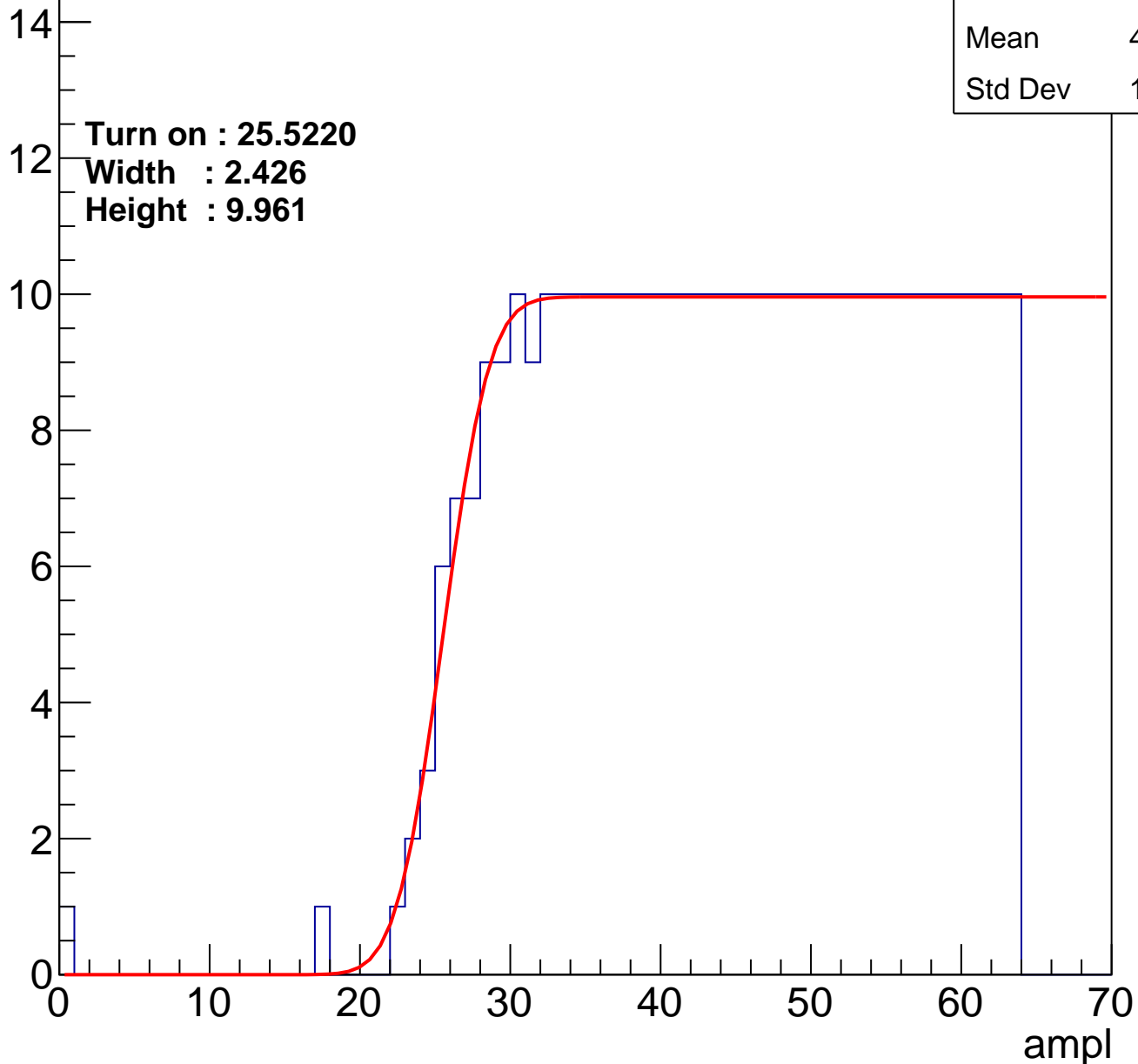
Entries	385
Mean	44.08
Std Dev	11.48

Turn on : 25.5220

Width : 2.426

Height : 9.961

Entry



B1L000S, U23-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.94
Std Dev	11.83

Turn on : 25.8477

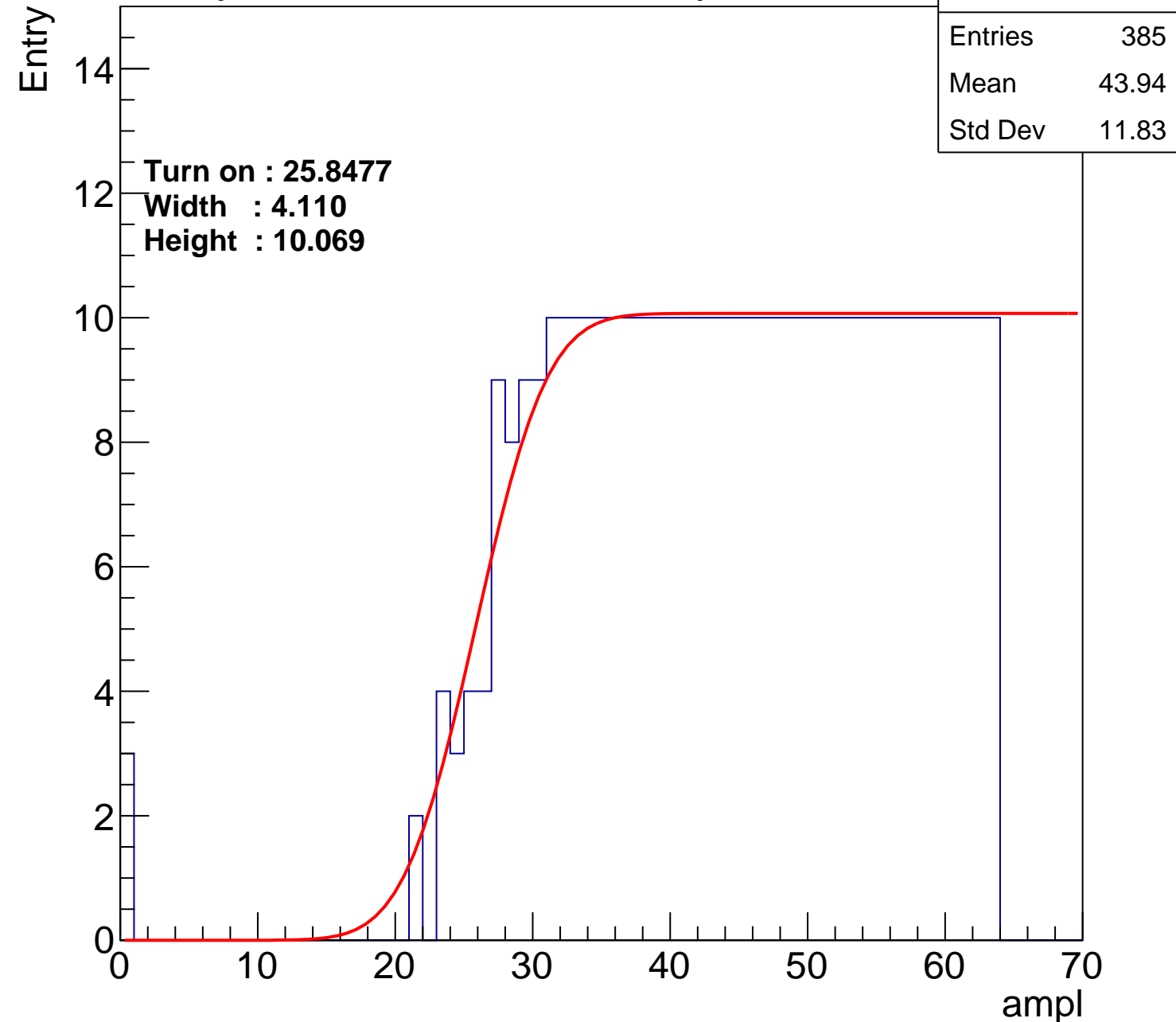
Width : 4.110

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch108

calib_packv5_042523_0143.root, FC#3, port B3

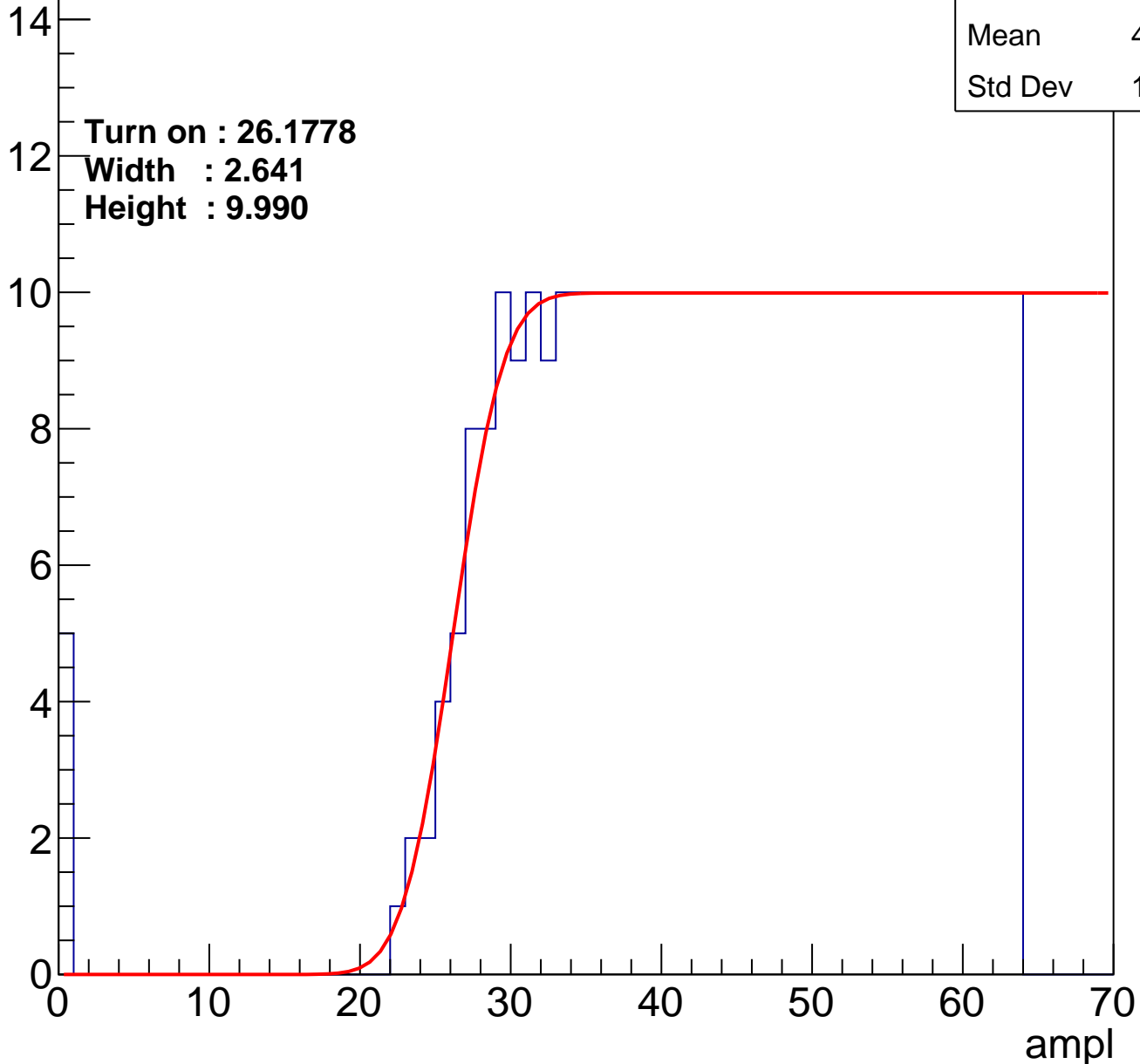
Entries	383
Mean	43.93
Std Dev	12.09

Turn on : 26.1778

Width : 2.641

Height : 9.990

Entry



B1L000S, U23-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.2508

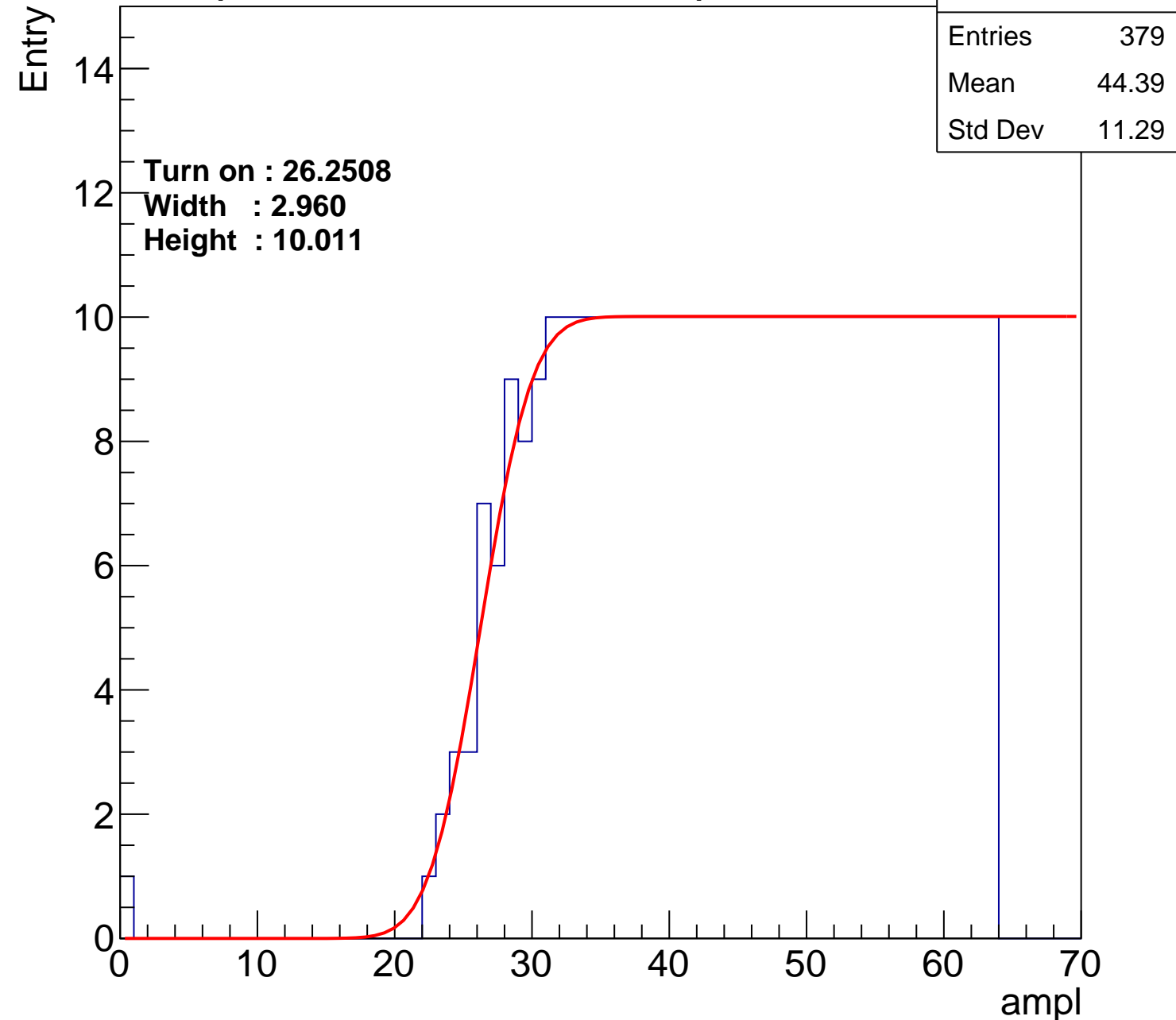
Width : 2.960

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch110

calib_packv5_042523_0143.root, FC#3, port B3

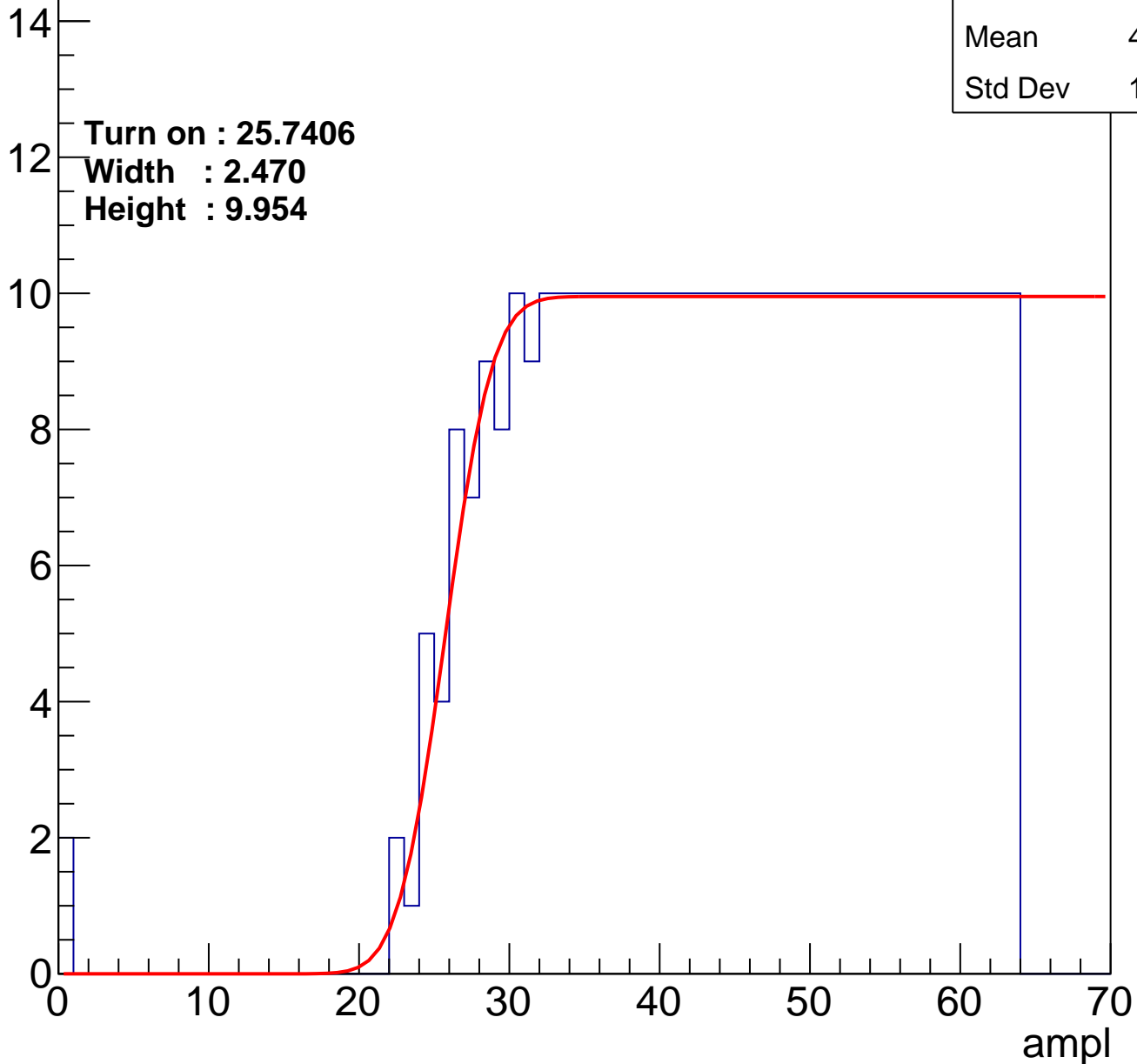
Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.7406

Width : 2.470

Height : 9.954

Entry



B1L000S, U23-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.28
Std Dev	11.55

Turn on : 26.8737

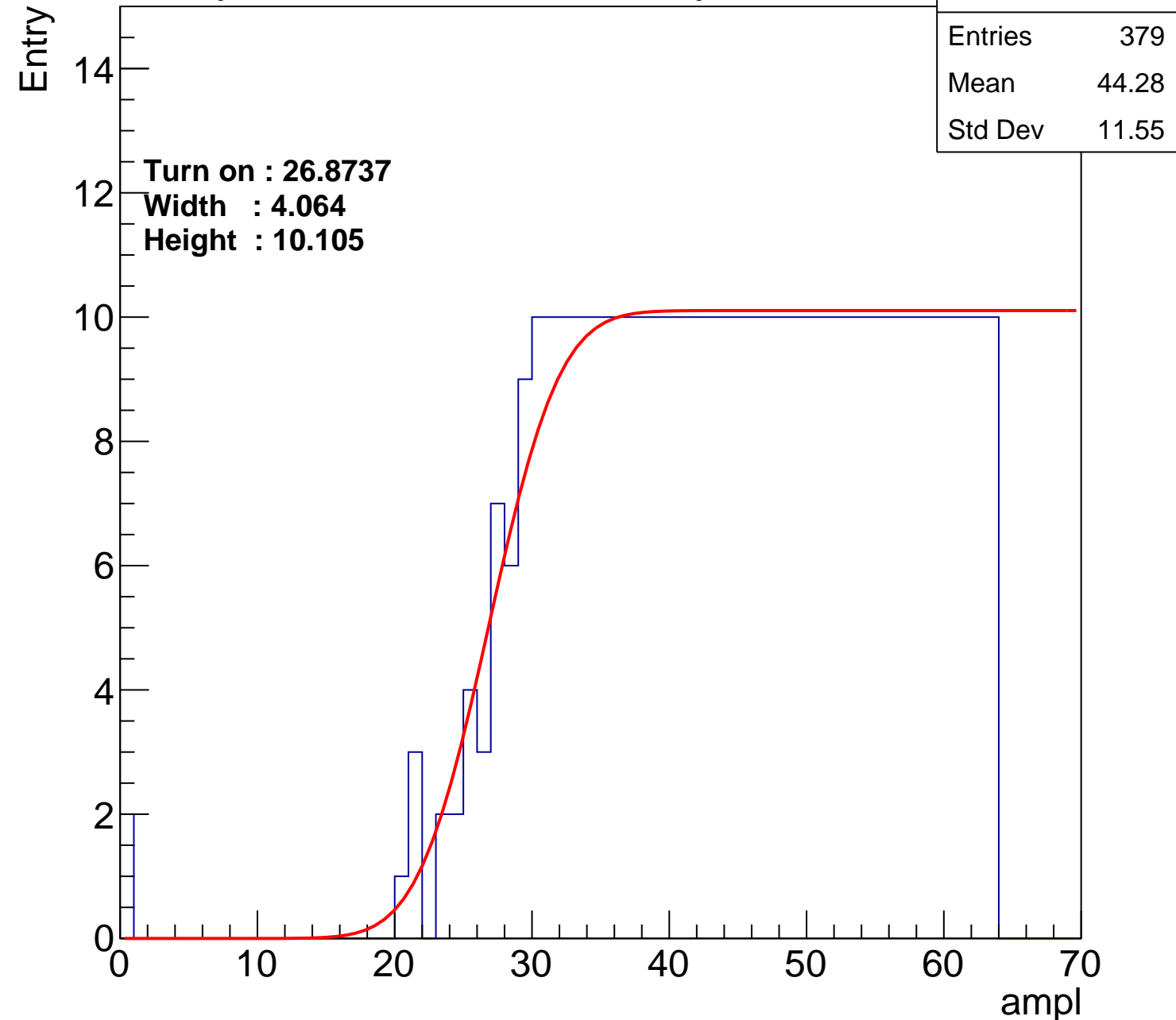
Width : 4.064

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch112

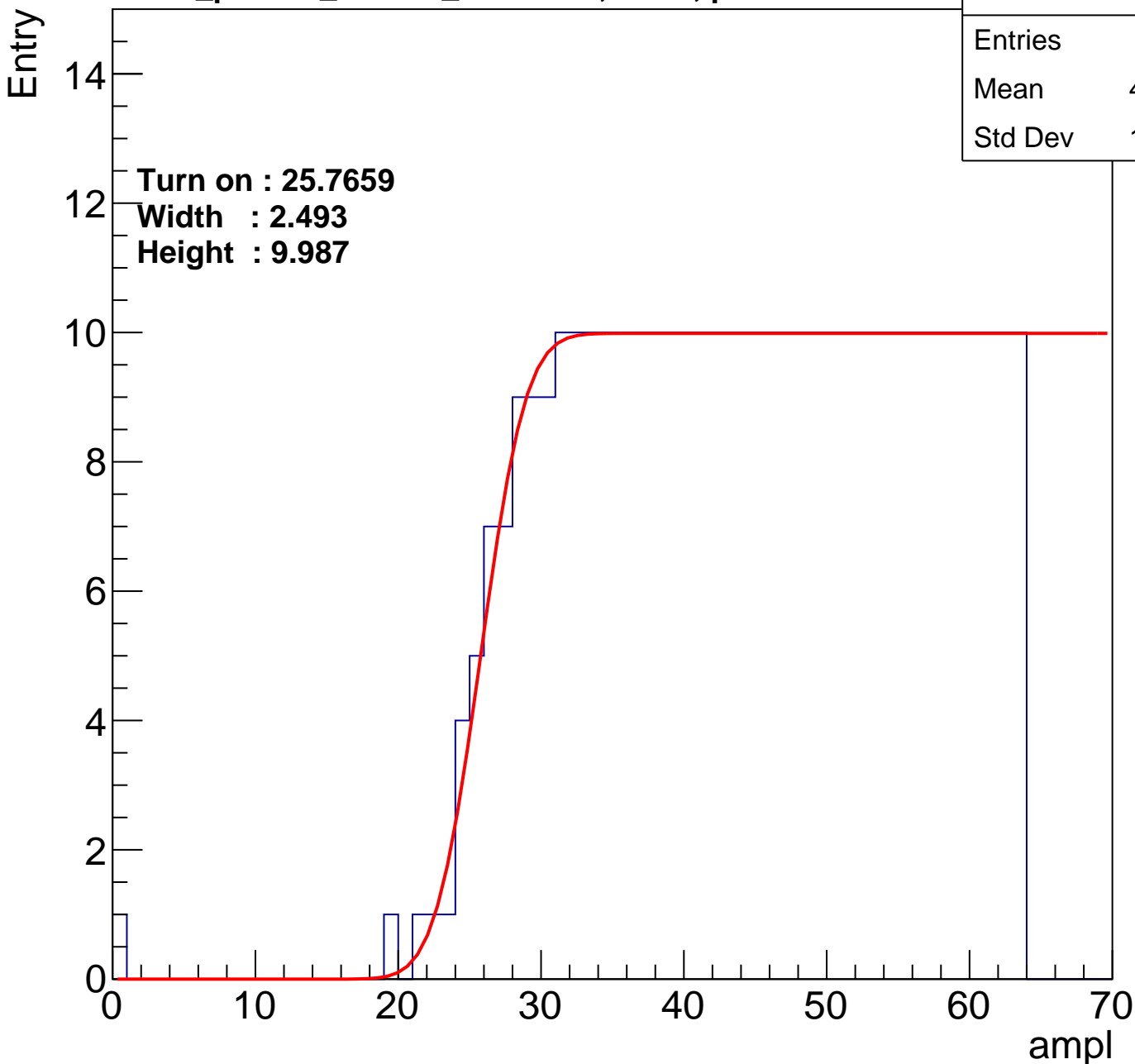
calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.7659

Width : 2.493

Height : 9.987



B1L000S, U23-ch113

calib_packv5_042523_0143.root, FC#3, port B3

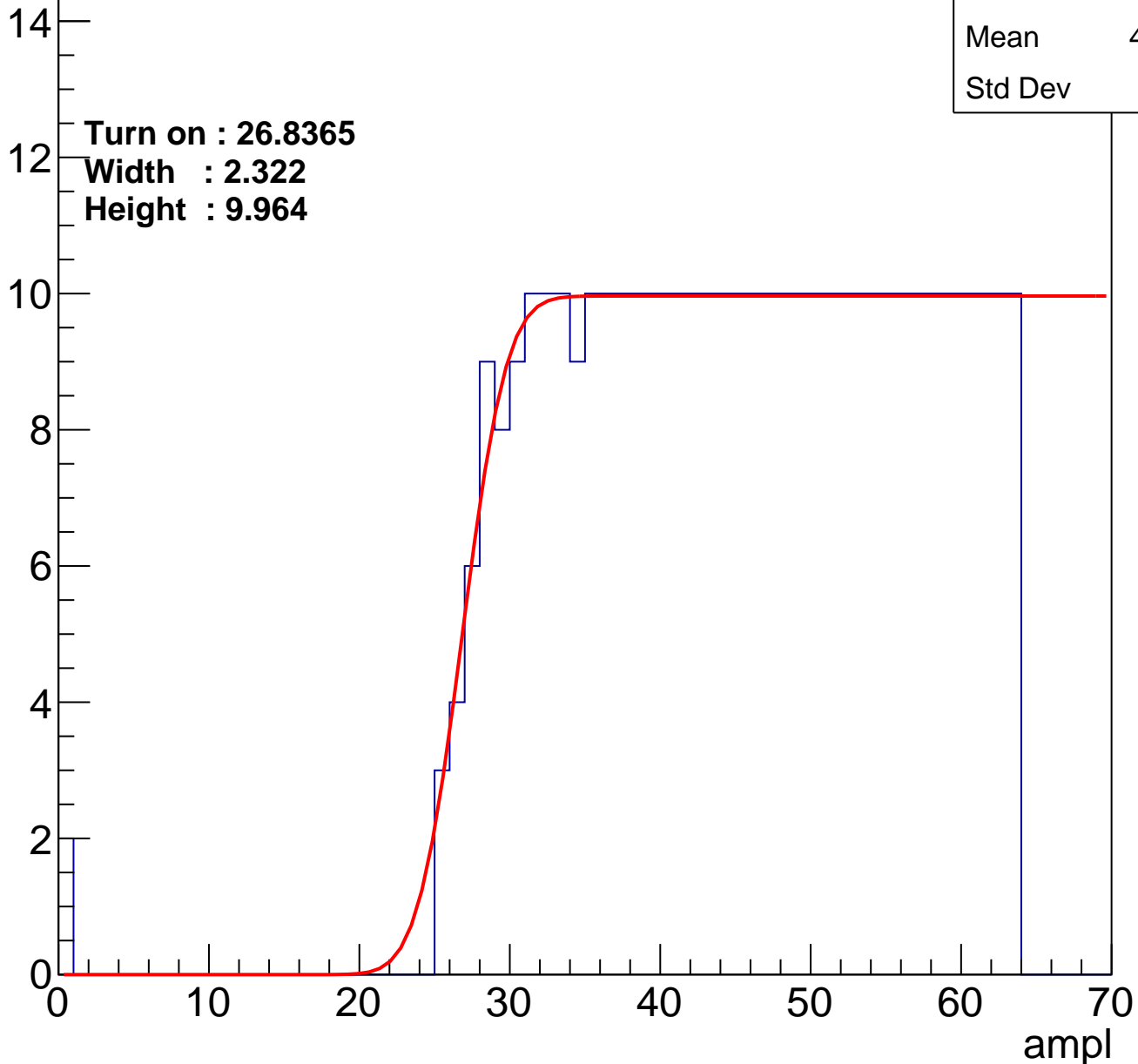
Entries	370
Mean	44.79
Std Dev	11.2

Turn on : 26.8365

Width : 2.322

Height : 9.964

Entry



B1L000S, U23-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.24
Std Dev	12.26

Turn on : 24.5500

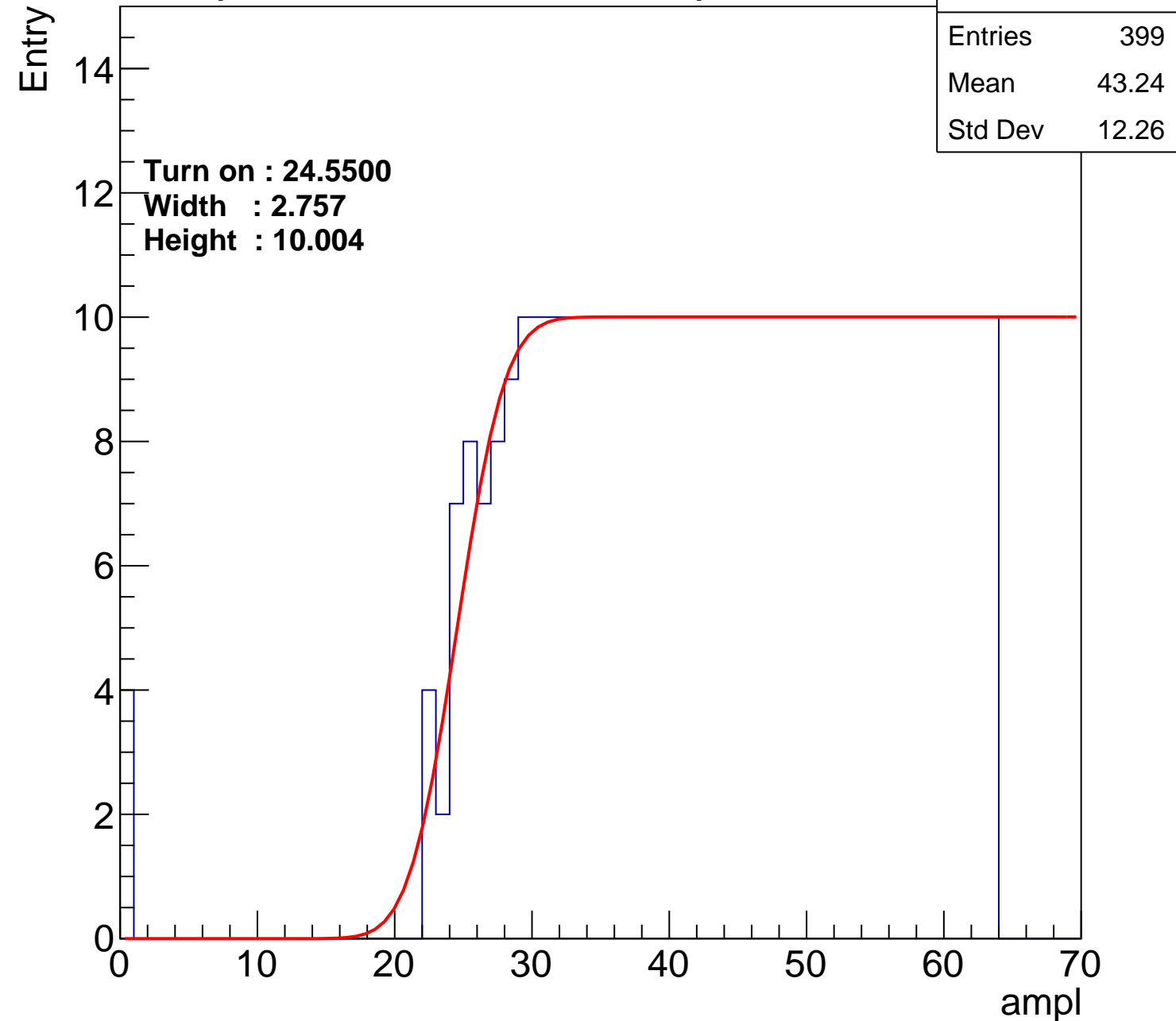
Width : 2.757

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.2299

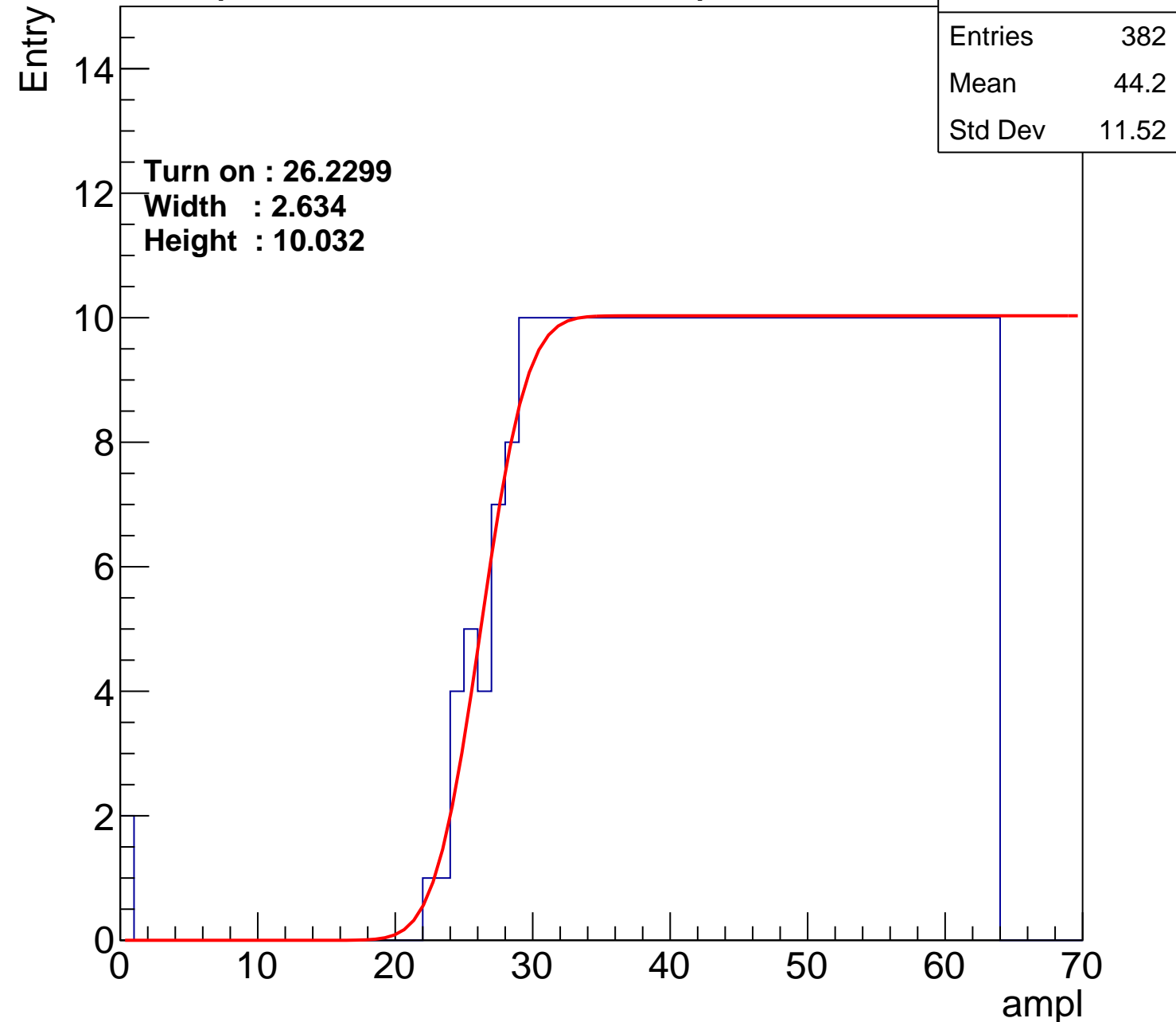
Width : 2.634

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch116

calib_packv5_042523_0143.root, FC#3, port B3

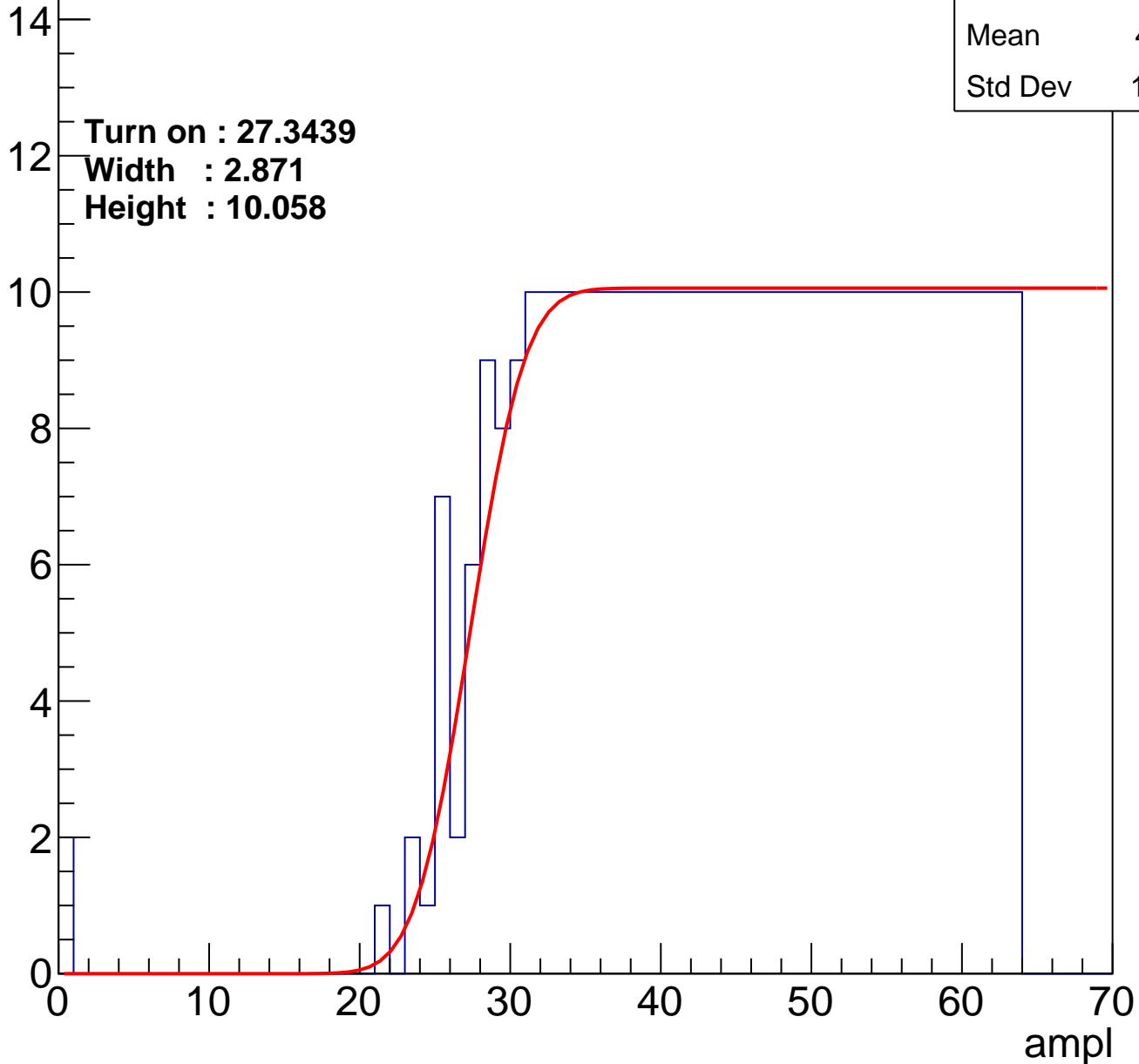
Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 27.3439

Width : 2.871

Height : 10.058

Entry



B1L000S, U23-ch117

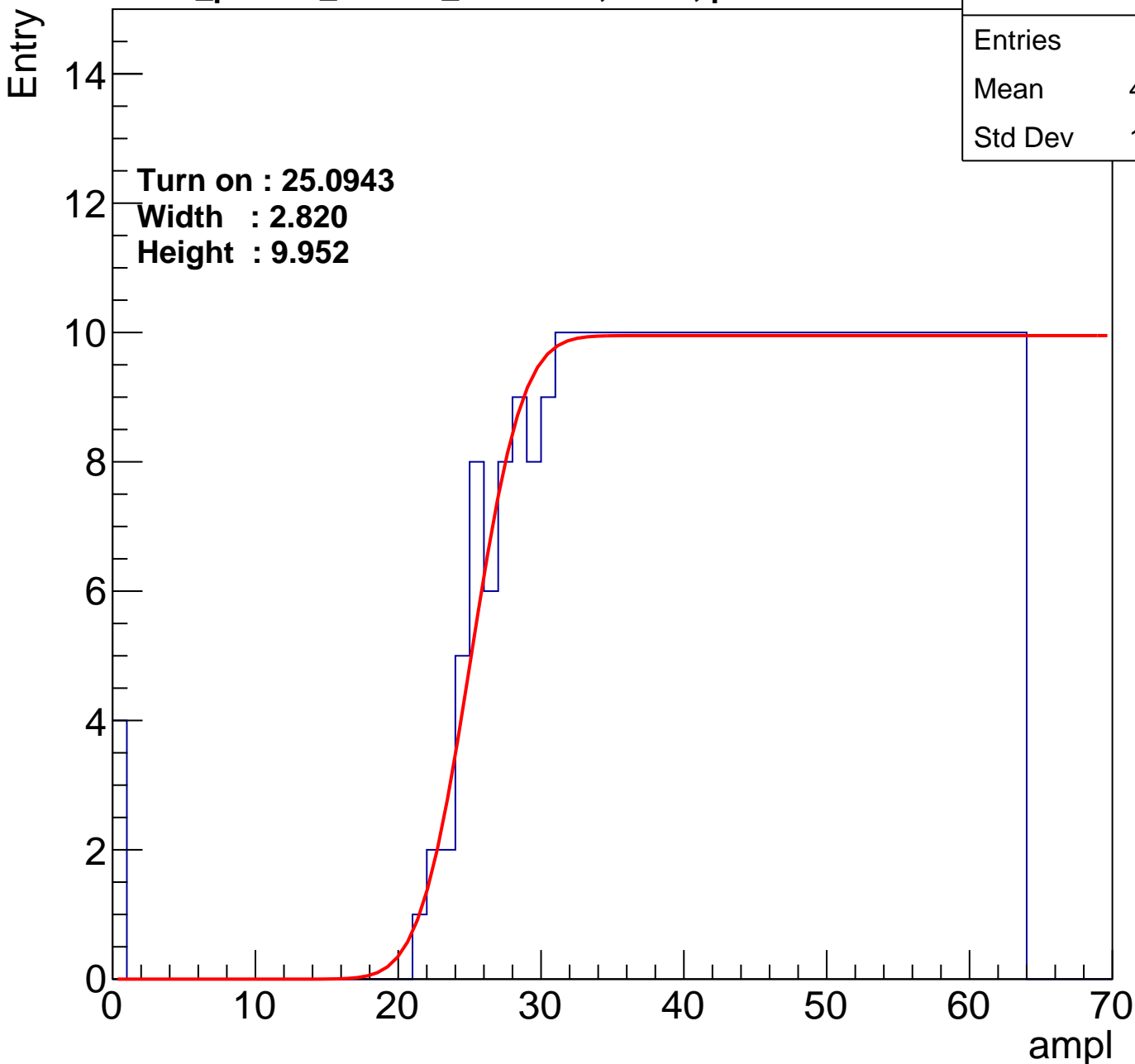
calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.54
Std Dev	12.16

Turn on : 25.0943

Width : 2.820

Height : 9.952



B1L000S, U23-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.2832

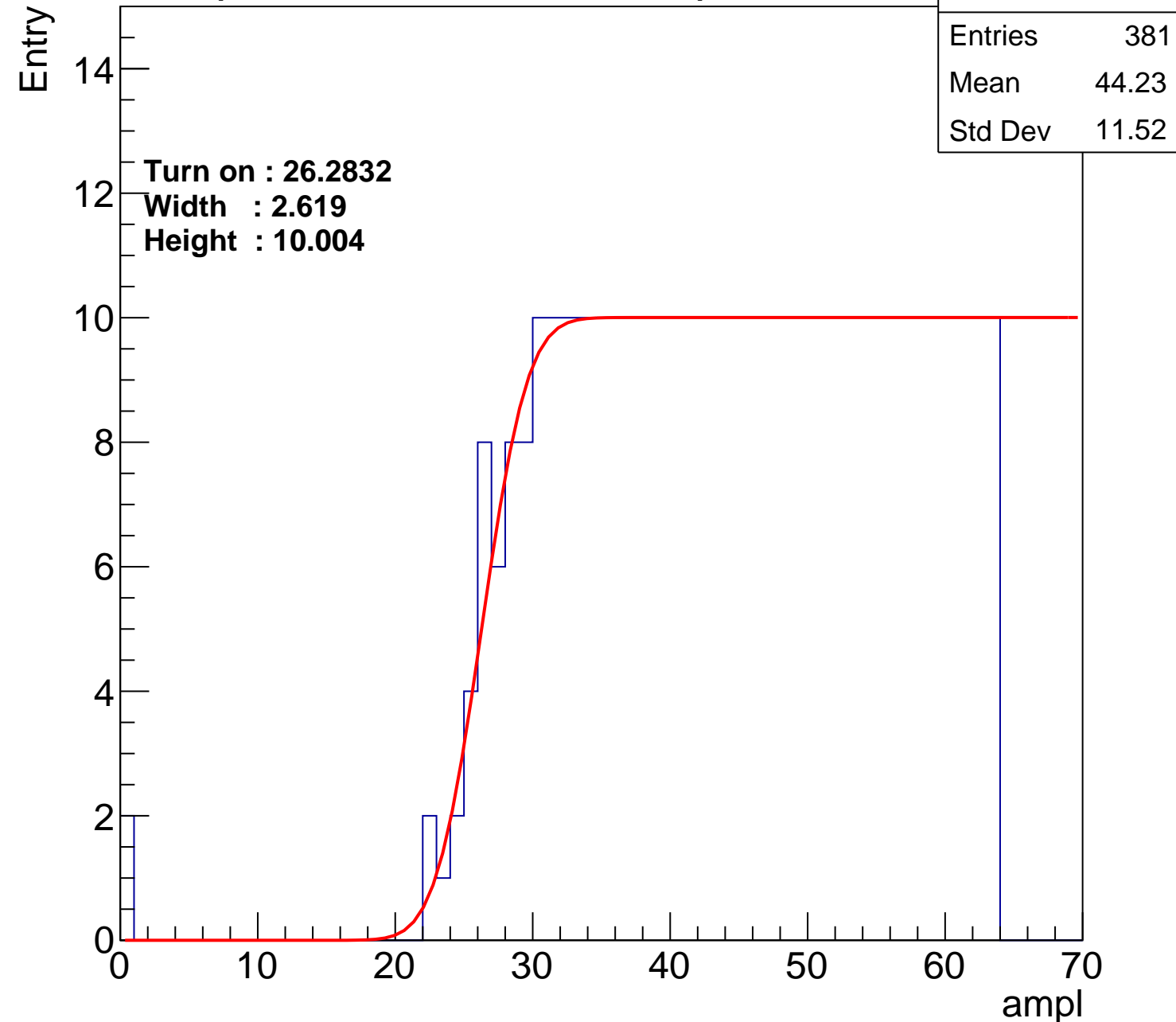
Width : 2.619

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.01
Std Dev	11.96

Turn on : 26.3147

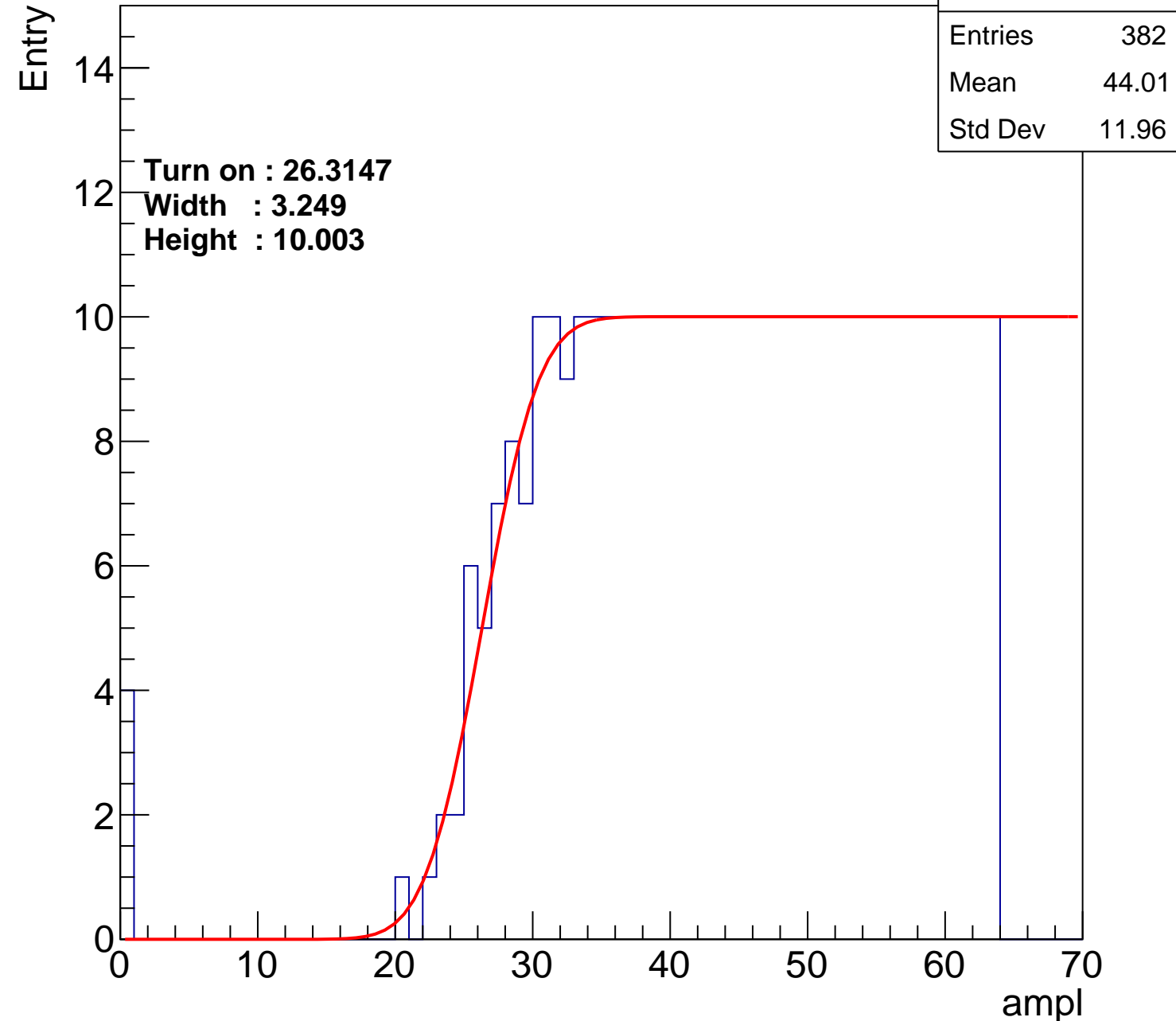
Width : 3.249

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 25.7642

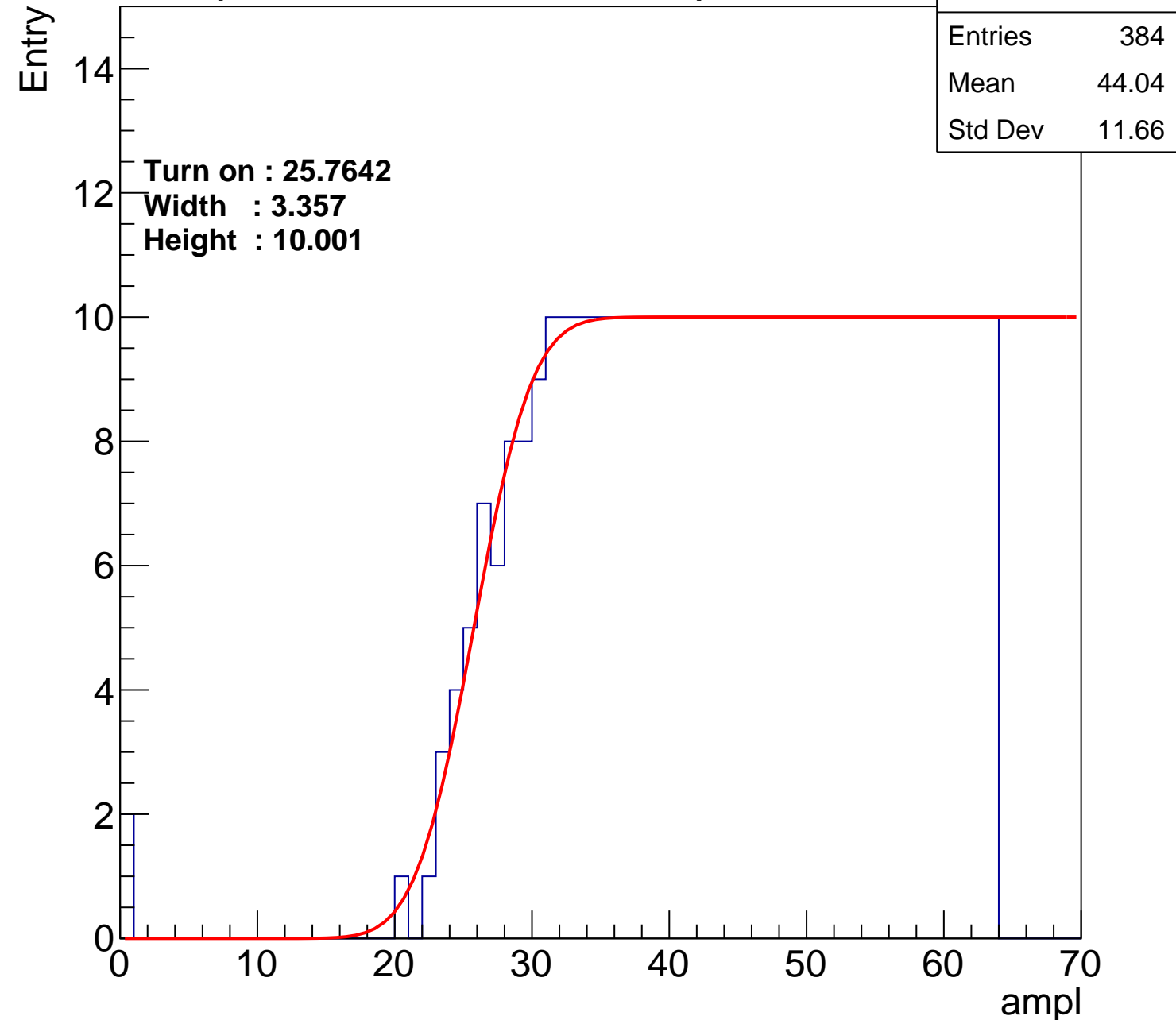
Width : 3.357

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.4844

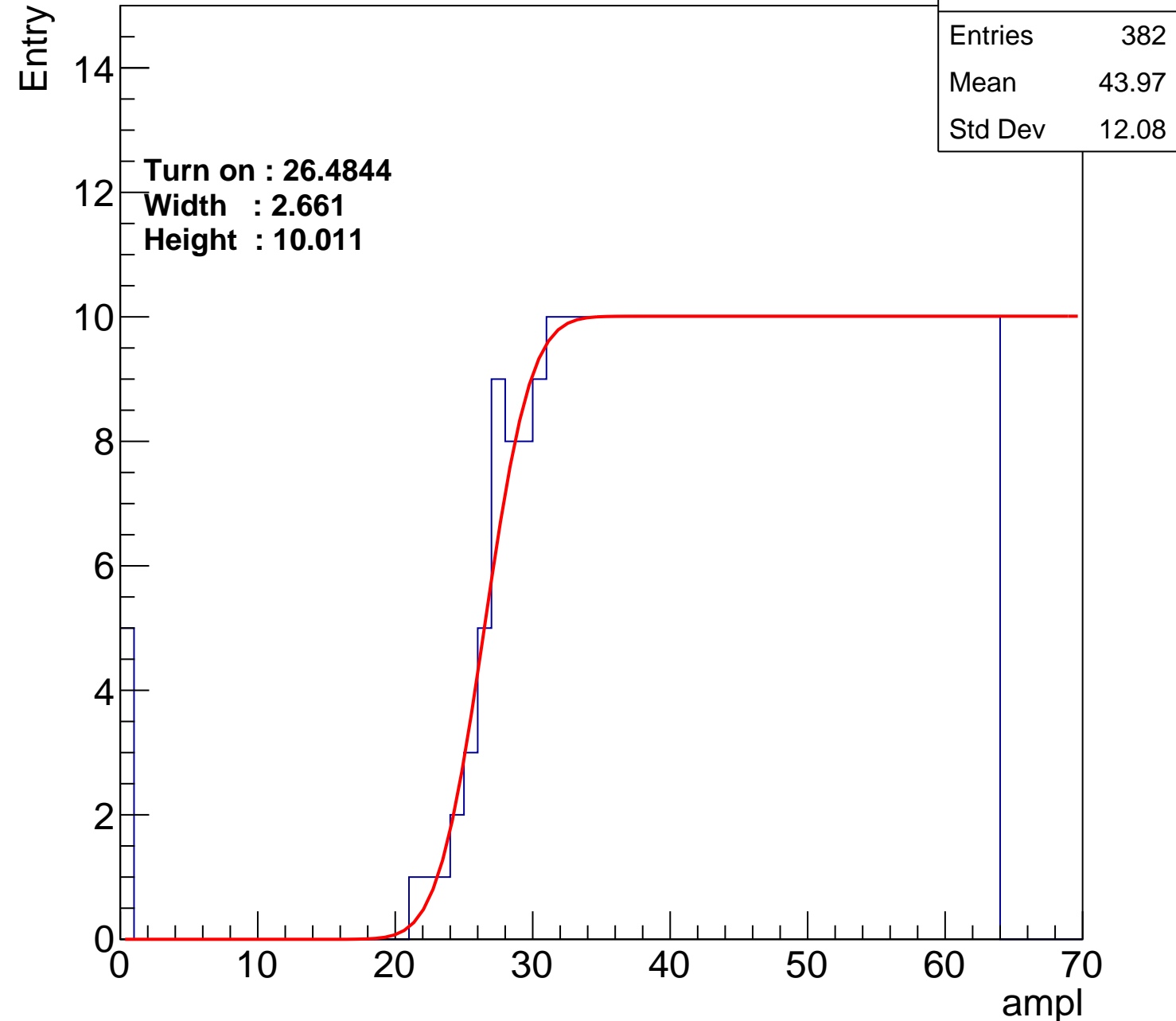
Width : 2.661

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch122

calib_packv5_042523_0143.root, FC#3, port B3

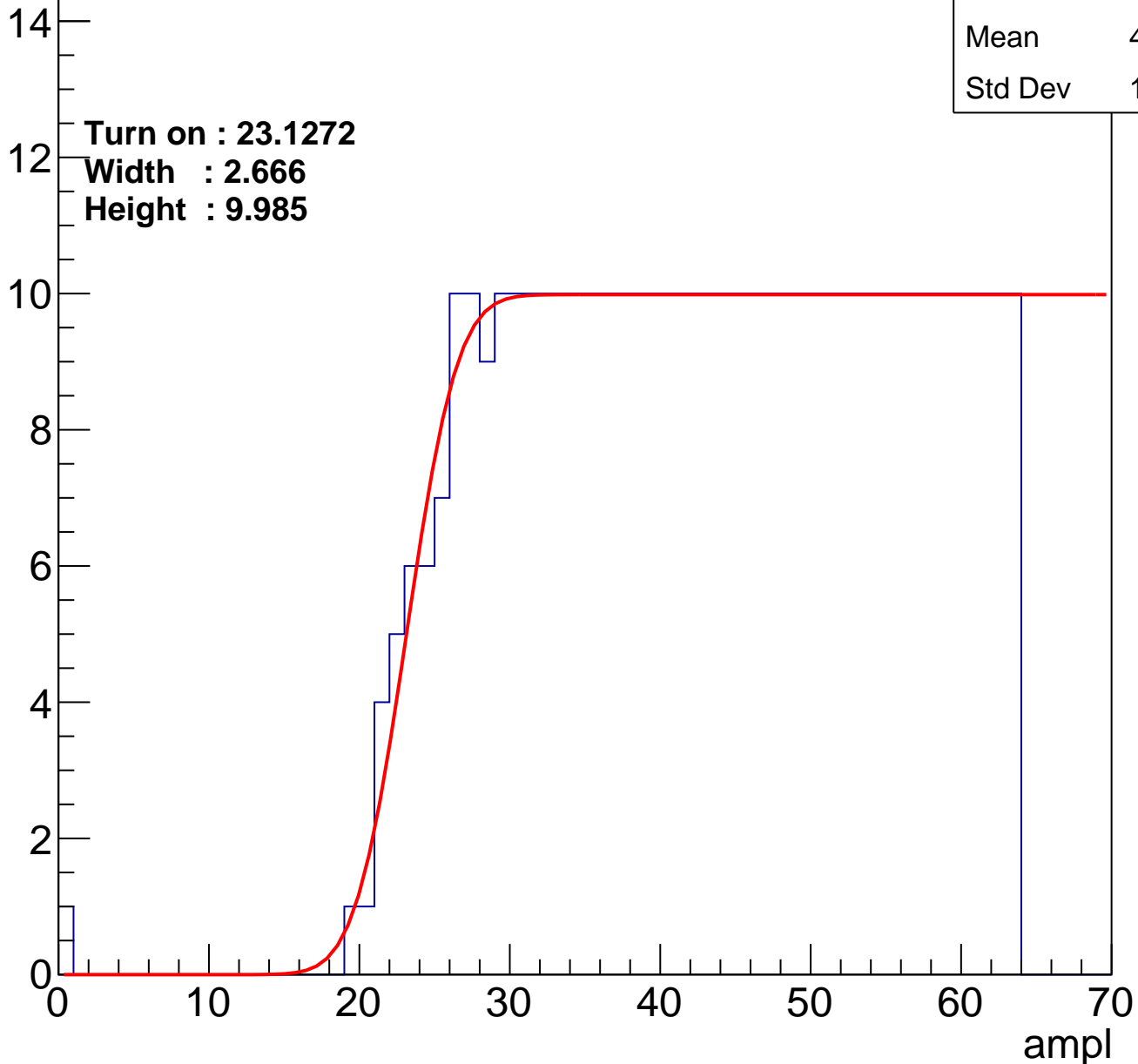
Entries	410
Mean	42.86
Std Dev	12.12

Turn on : 23.1272

Width : 2.666

Height : 9.985

Entry



B1L000S, U23-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.95
Std Dev	10.96

Turn on : 27.6583

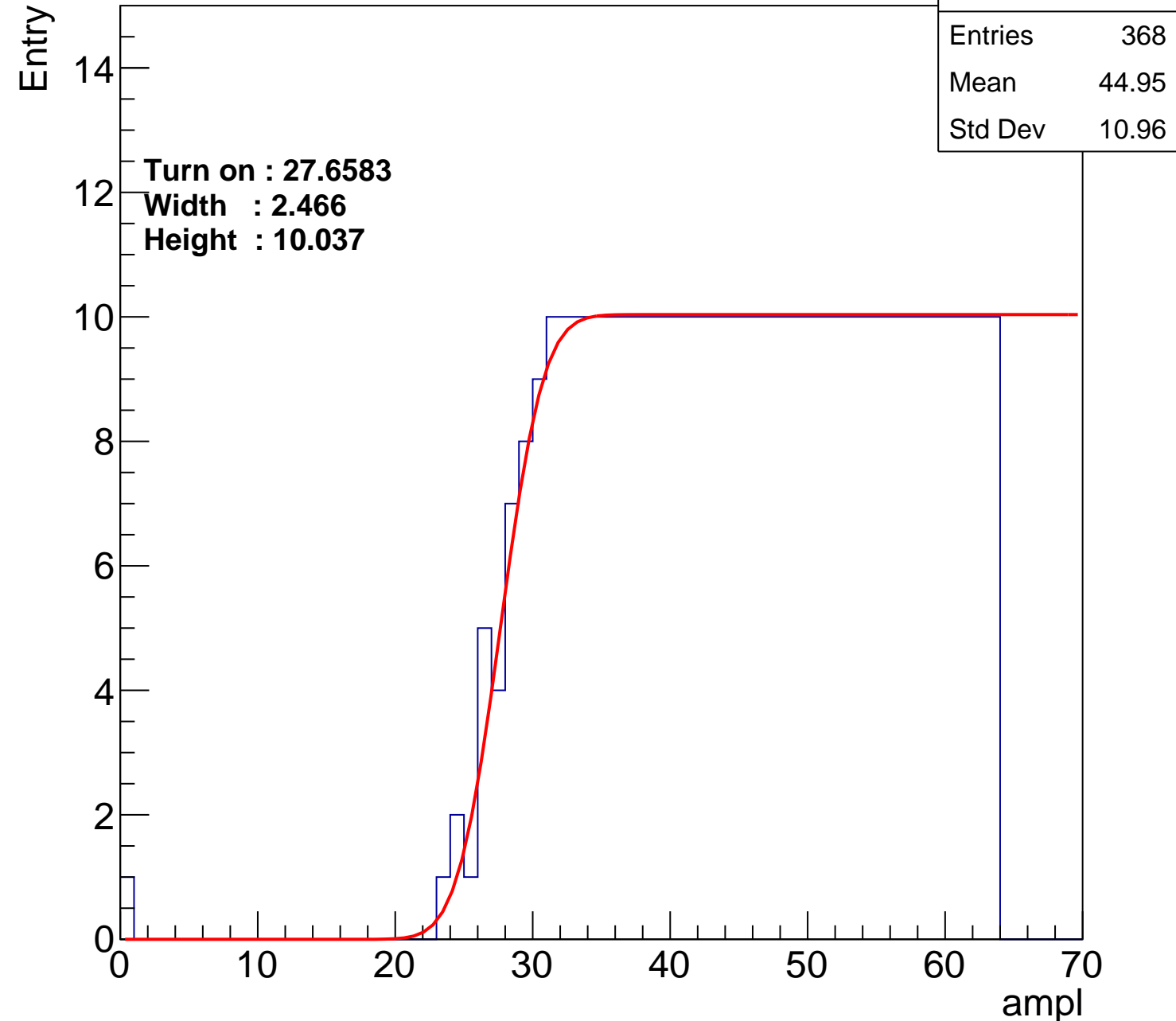
Width : 2.466

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.4
Std Dev	11.57

Turn on : 26.5989

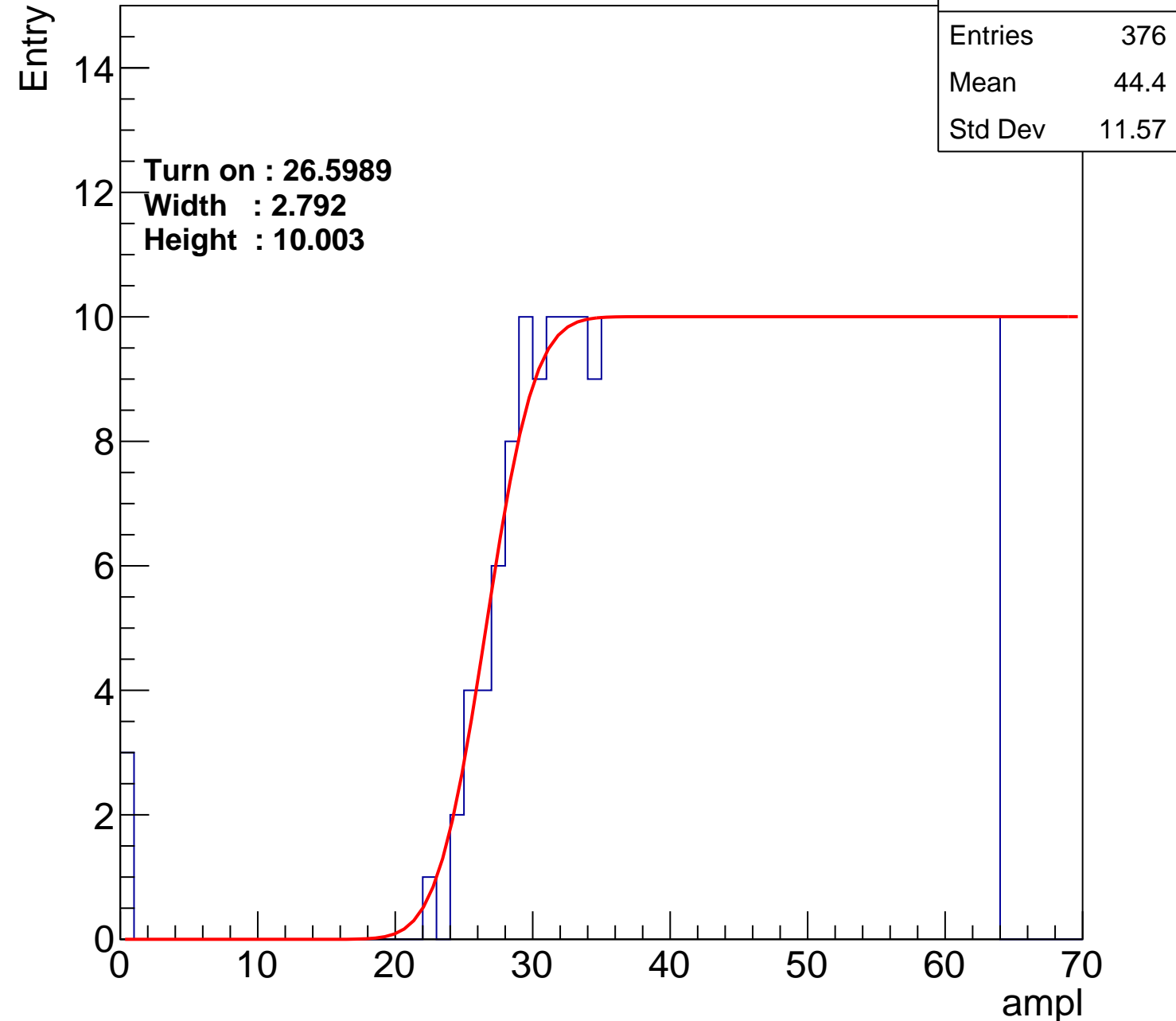
Width : 2.792

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch125

calib_packv5_042523_0143.root, FC#3, port B3

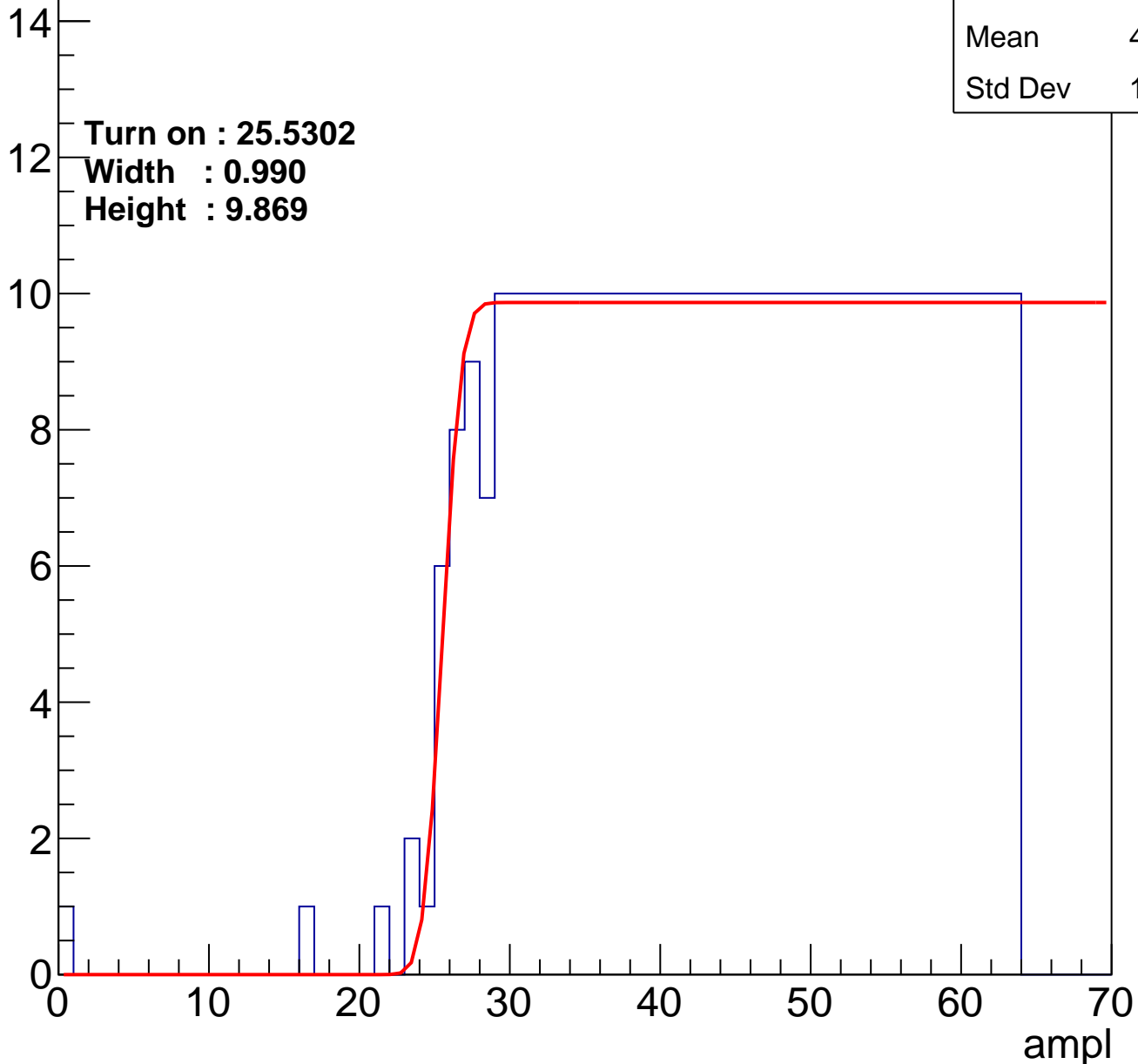
Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.5302

Width : 0.990

Height : 9.869

Entry



B1L000S, U23-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.0313

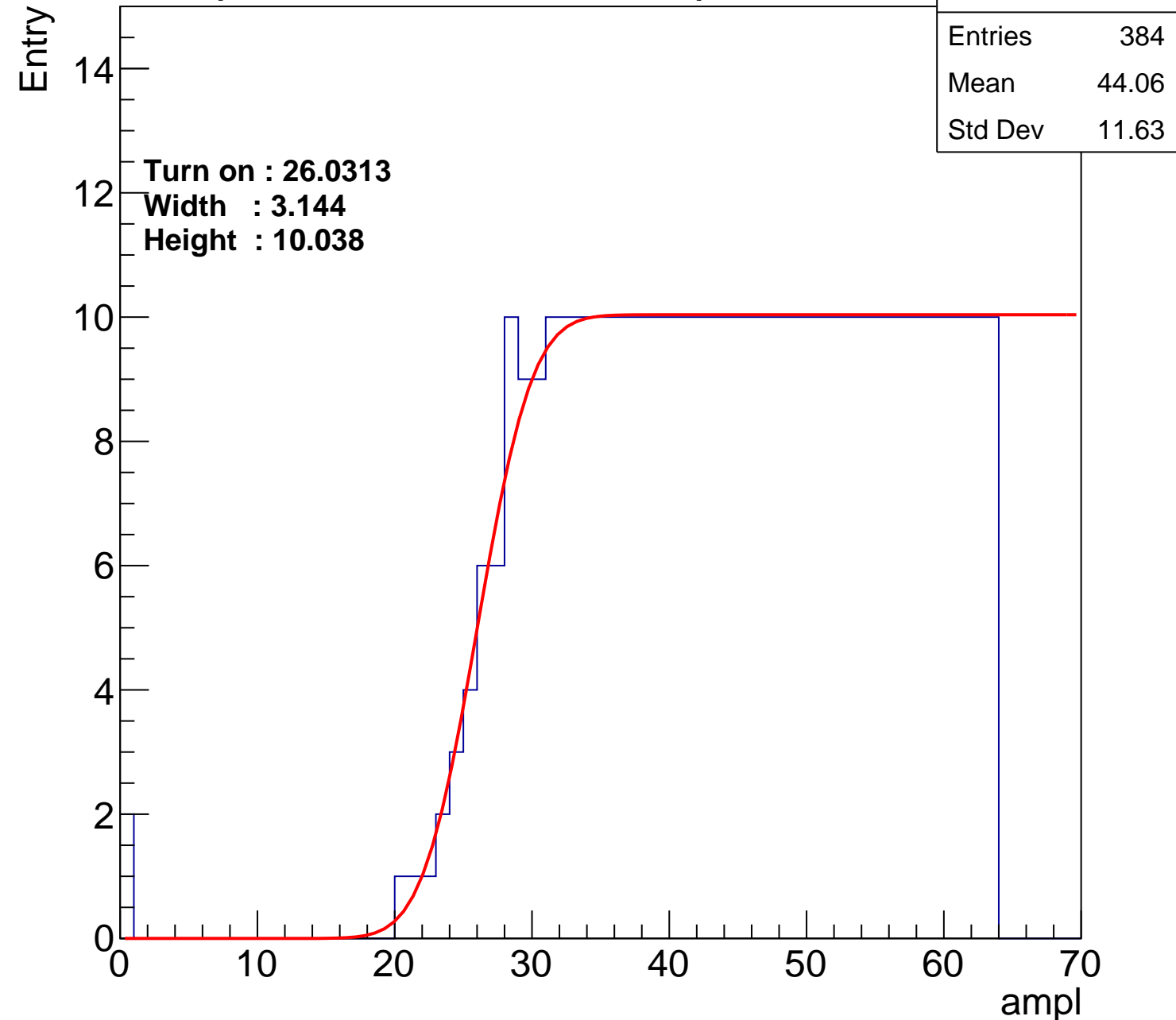
Width : 3.144

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.79
Std Dev	11.79

Turn on : 25.4032

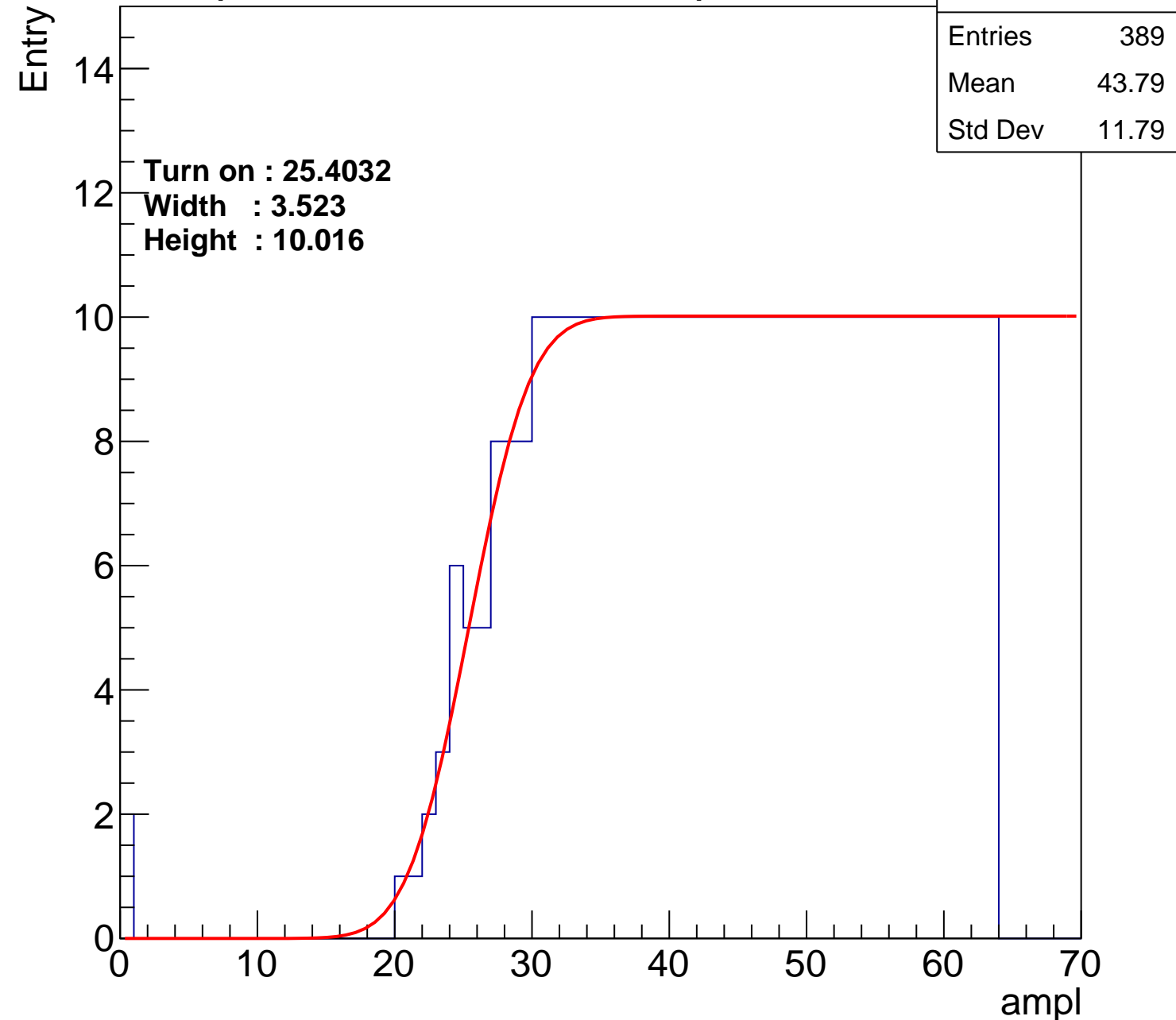
Width : 3.523

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U23-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.4032

Width : 3.523

Height : 10.016

Entries	389
Mean	43.79
Std Dev	11.79

