

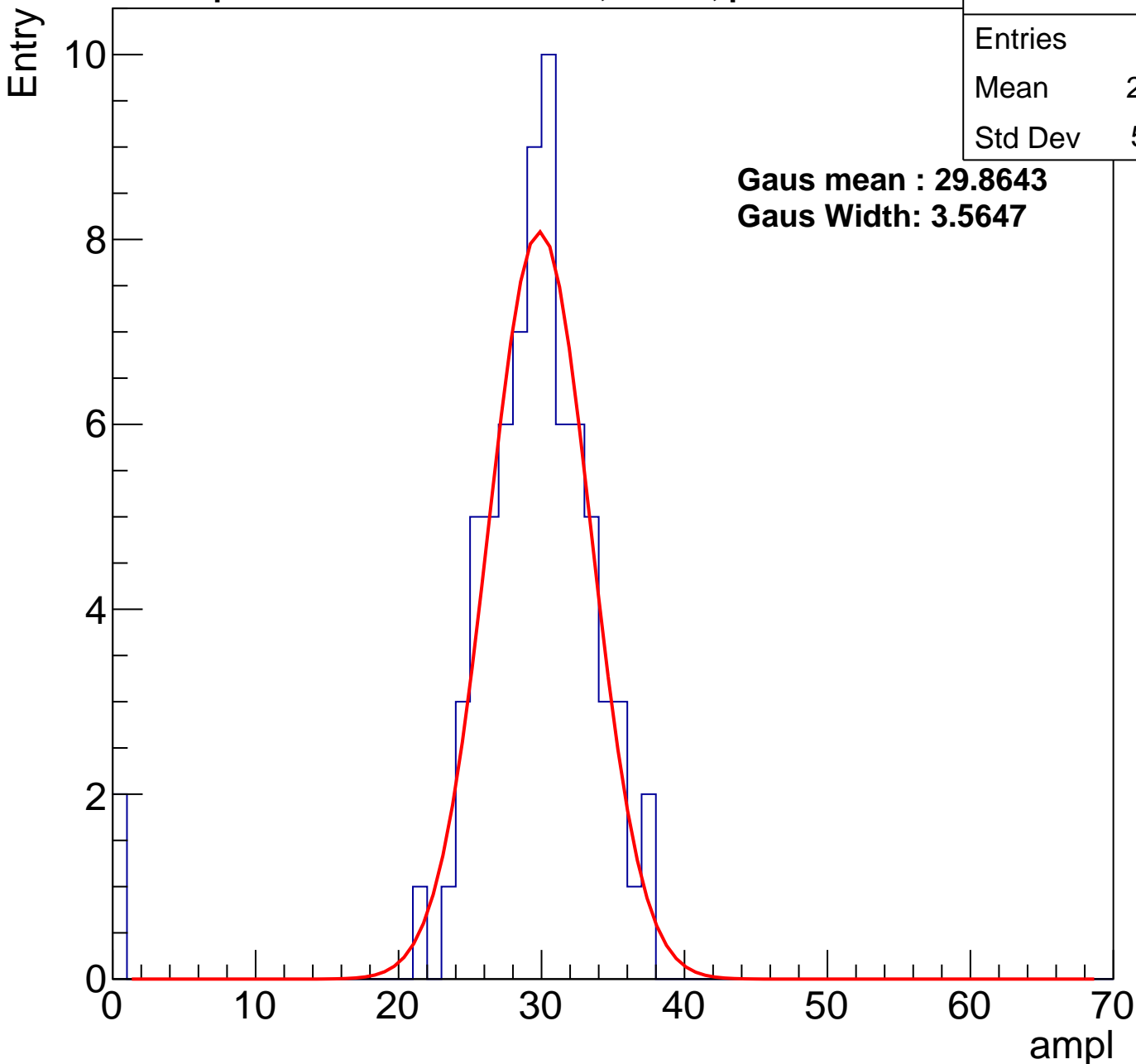
B1L003S, U16-ch0, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	28.67
Std Dev	5.811

Gaus mean : 29.8643

Gaus Width: 3.5647



B1L003S, U16-ch0, adc1

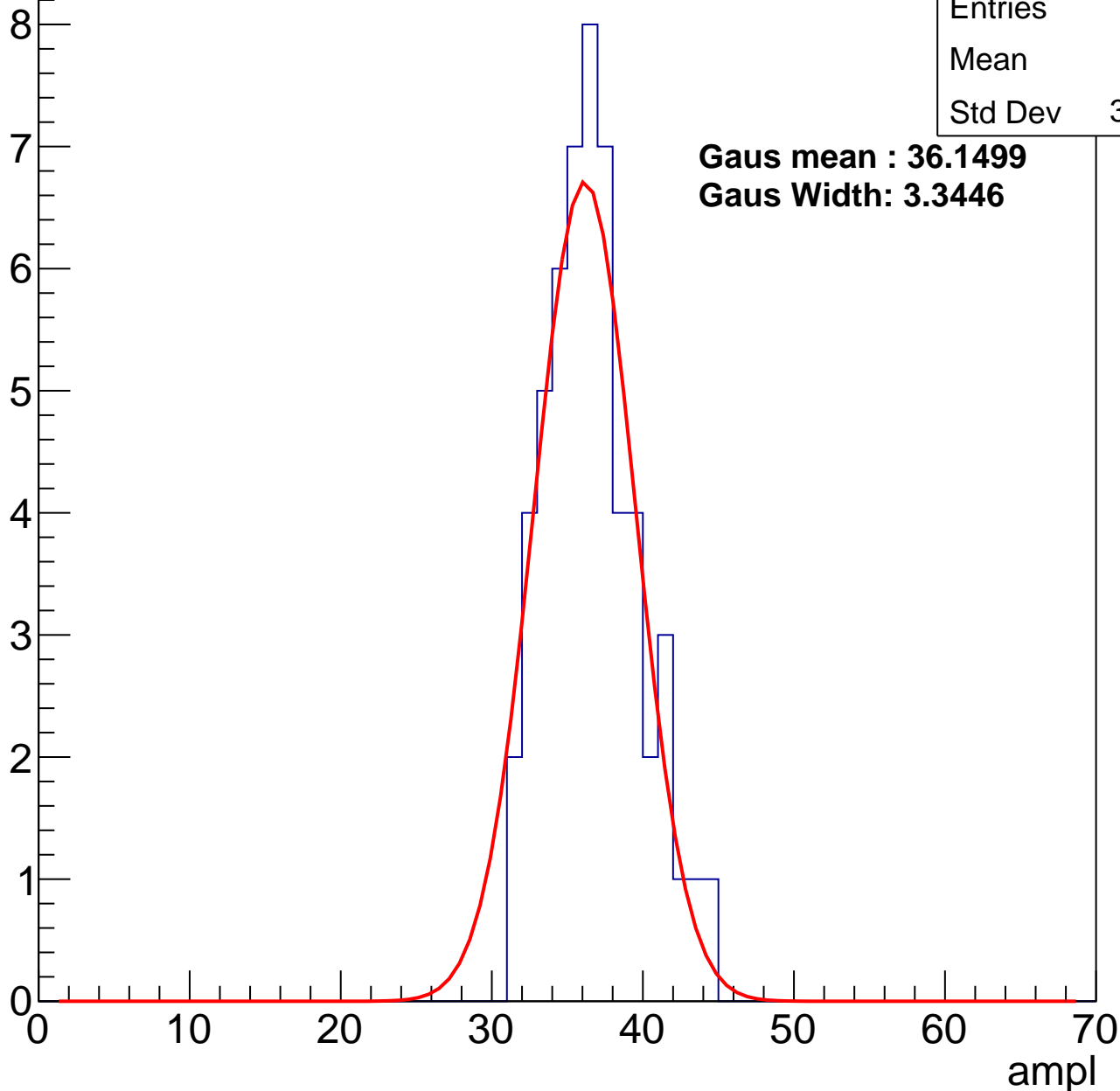
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.2
Std Dev	3.024

Gaus mean : 36.1499

Gaus Width: 3.3446



B1L003S, U16-ch0, adc2

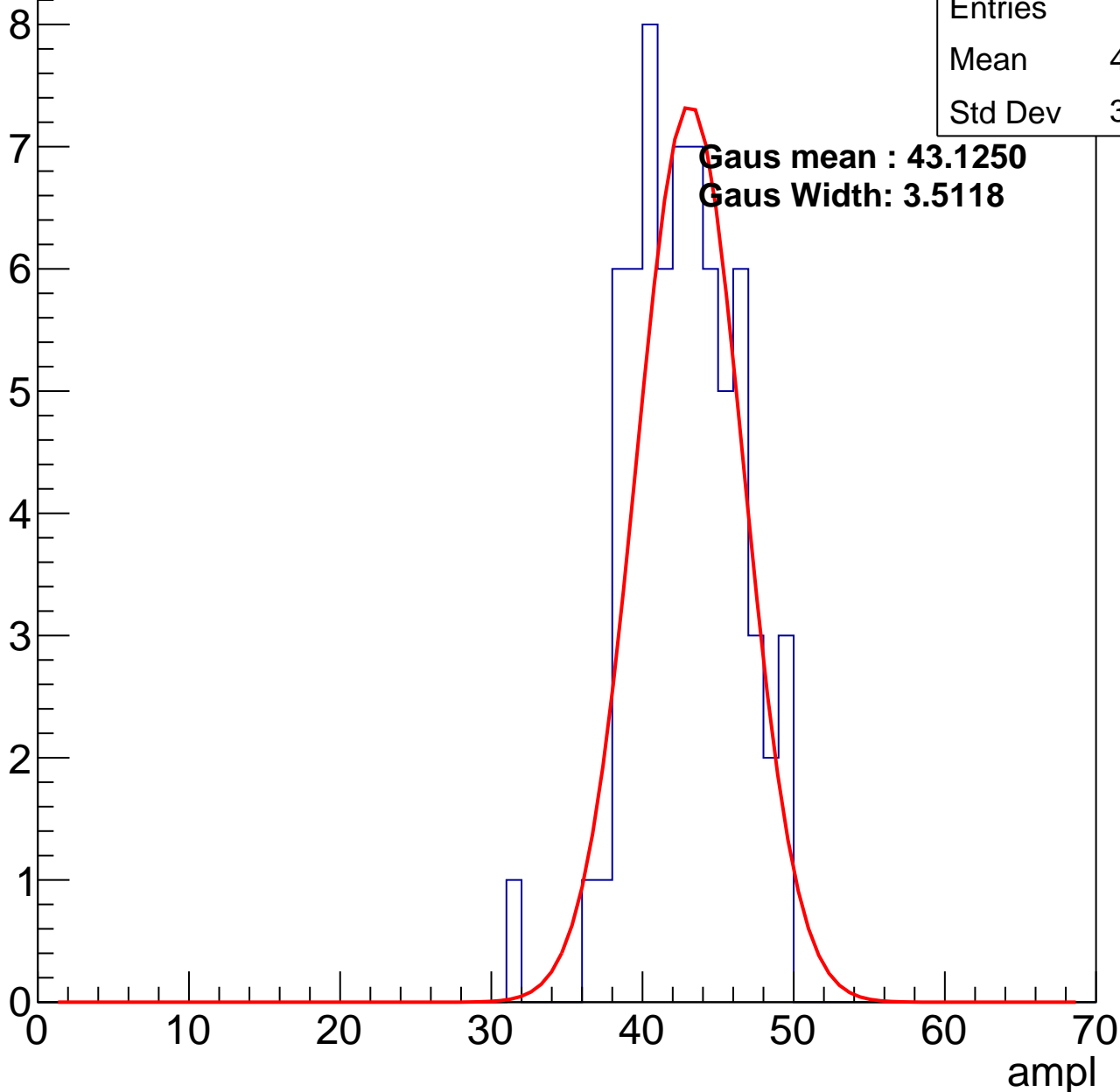
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.29
Std Dev	3.494

Gaus mean : 43.1250

Gaus Width: 3.5118

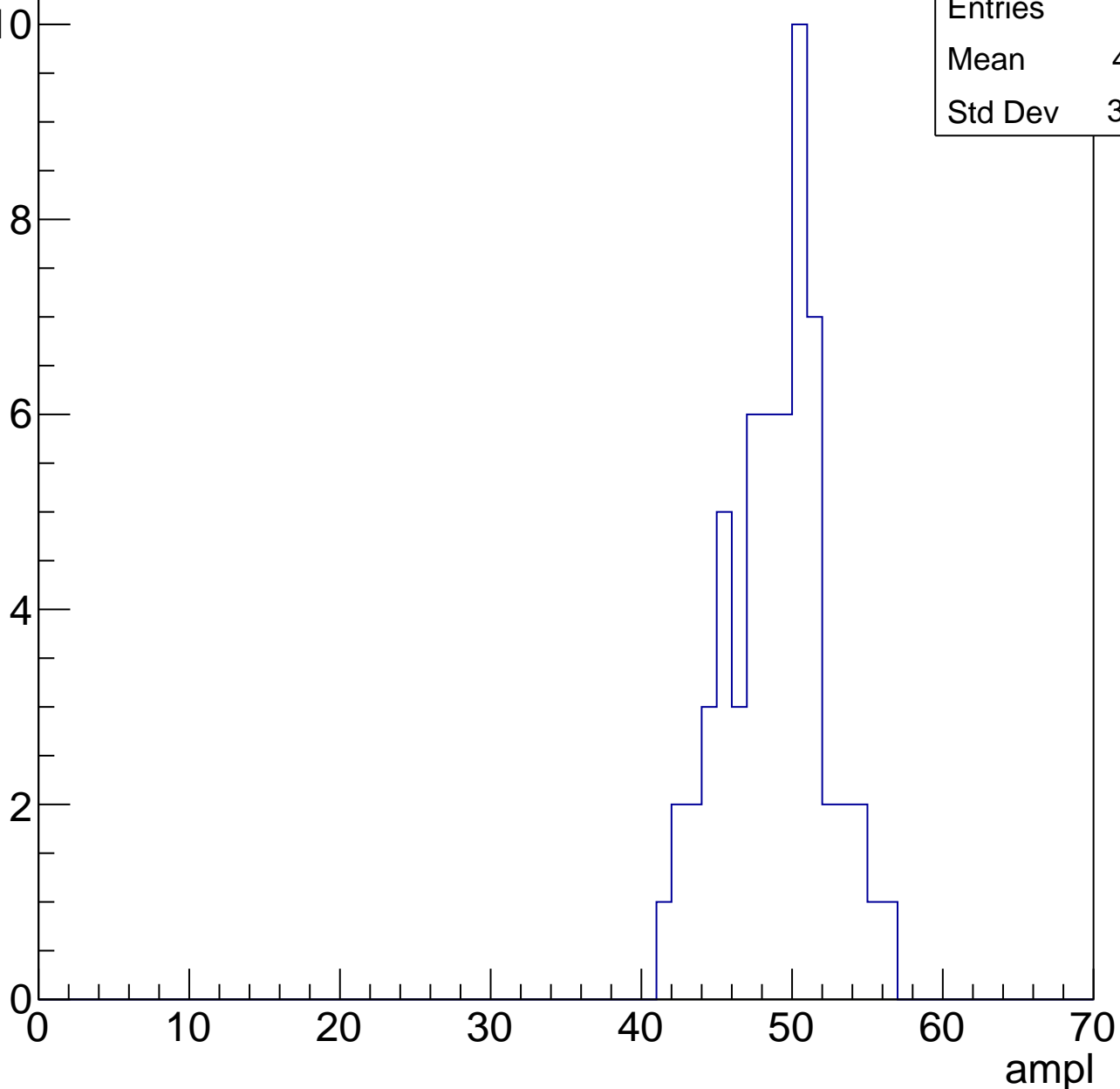


B1L003S, U16-ch0, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.41
Std Dev	3.315

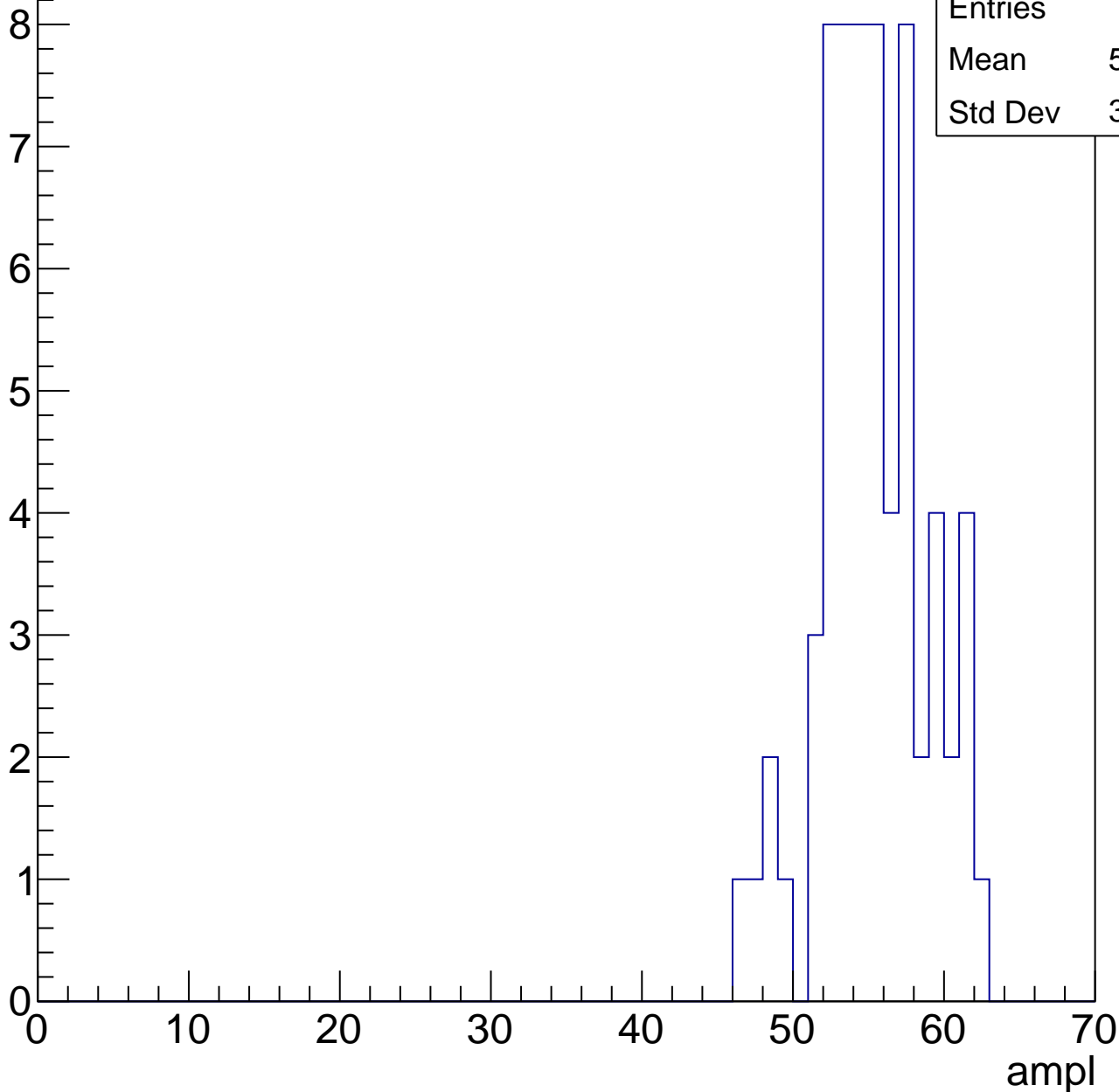


B1L003S, U16-ch0, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.78
Std Dev	3.502

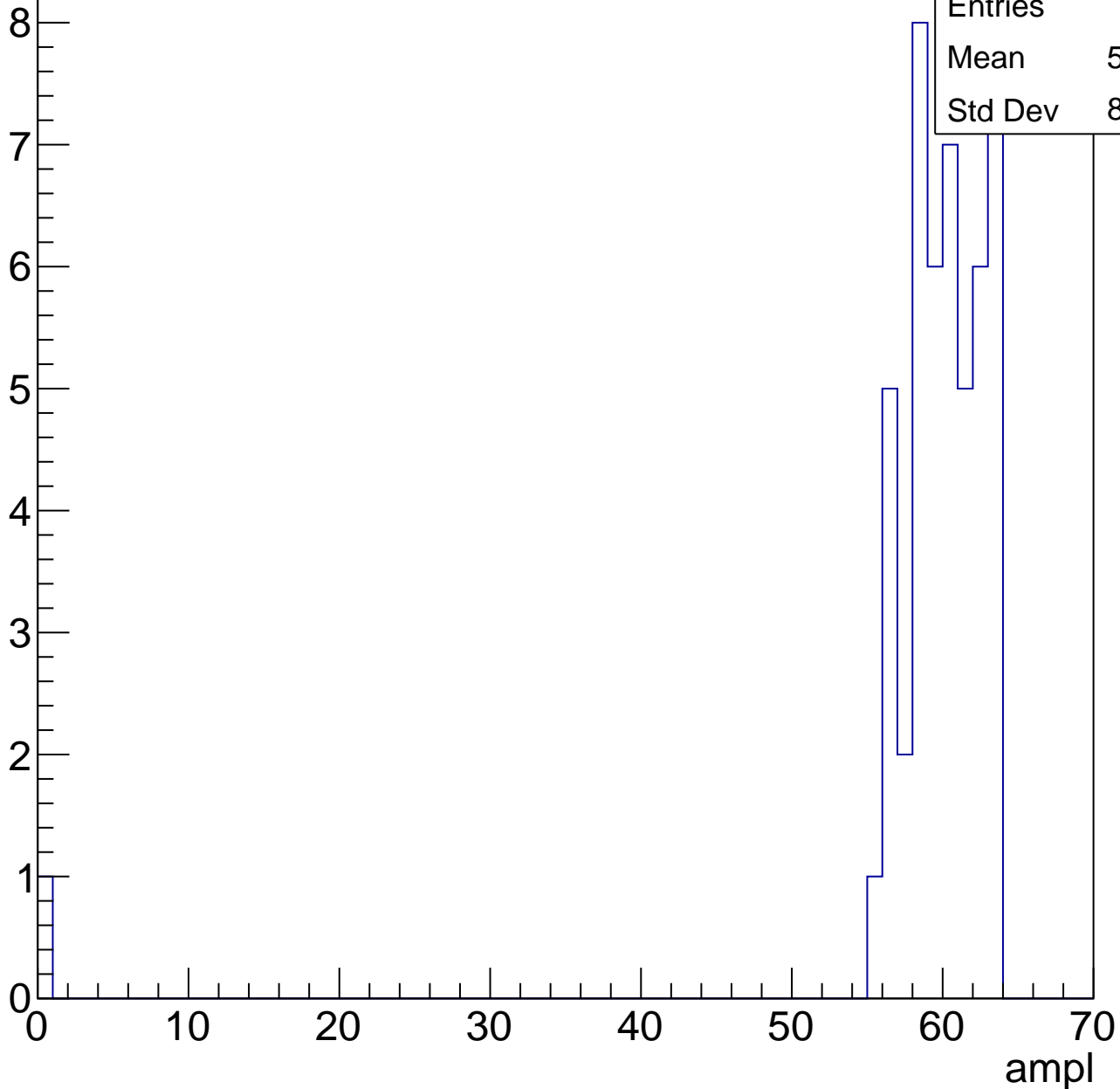


B1L003S, U16-ch0, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.53
Std Dev	8.755



B1L003S, U16-ch0, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch0, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch1, adc0

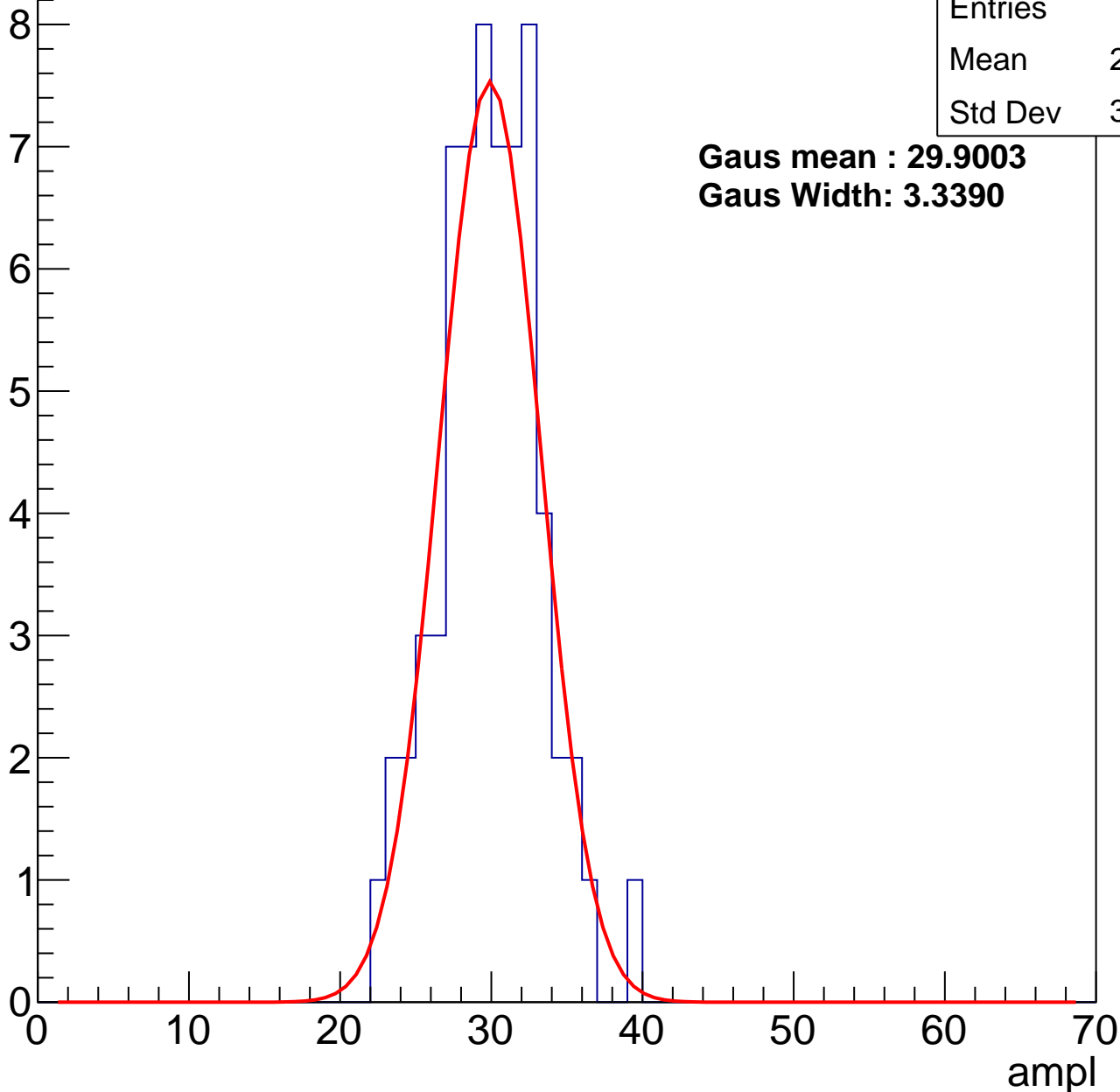
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.45
Std Dev	3.305

Gaus mean : 29.9003

Gaus Width: 3.3390



B1L003S, U16-ch1, adc1

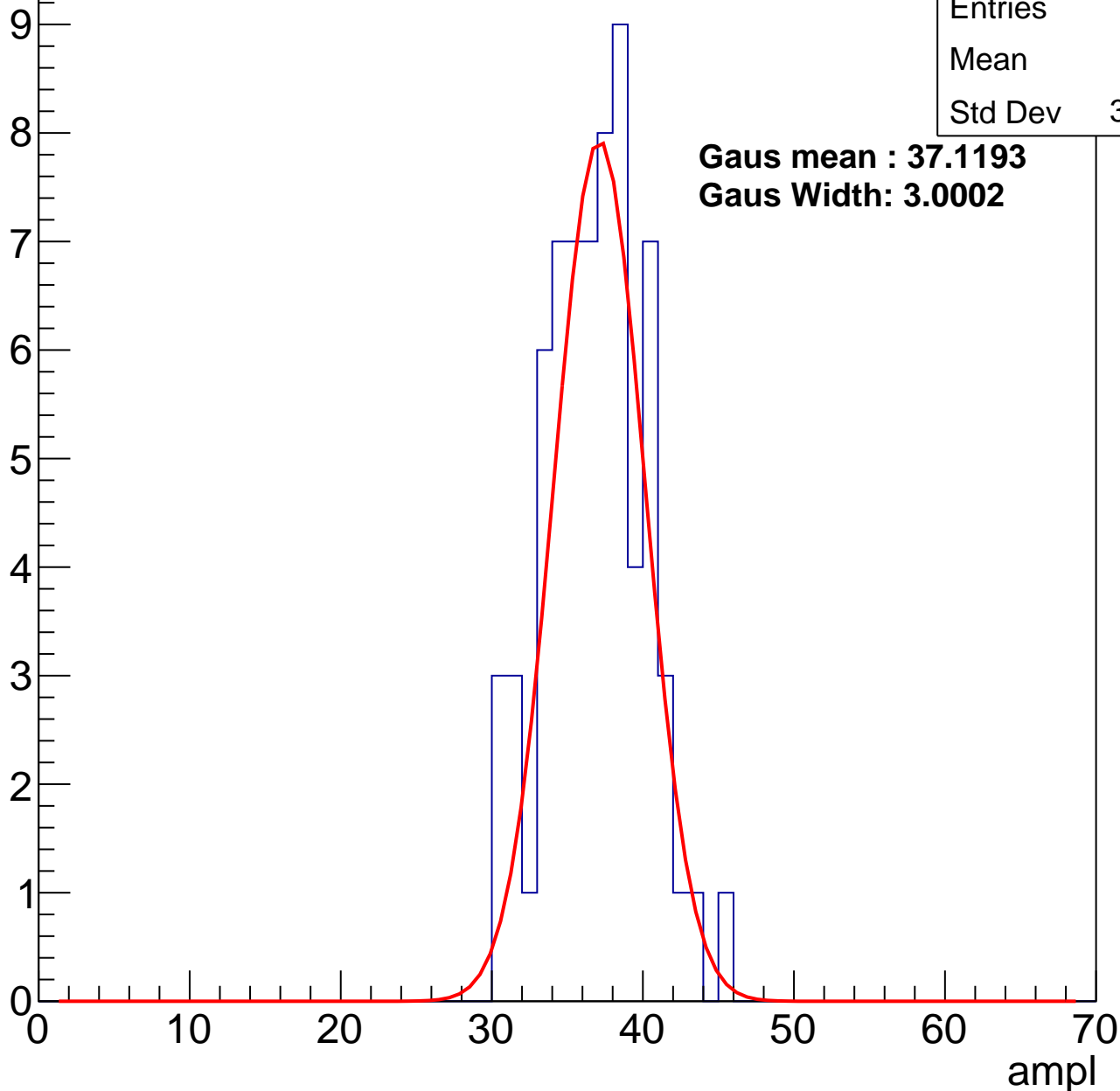
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.4
Std Dev	3.232

Gaus mean : 37.1193

Gaus Width: 3.0002



B1L003S, U16-ch1, adc2

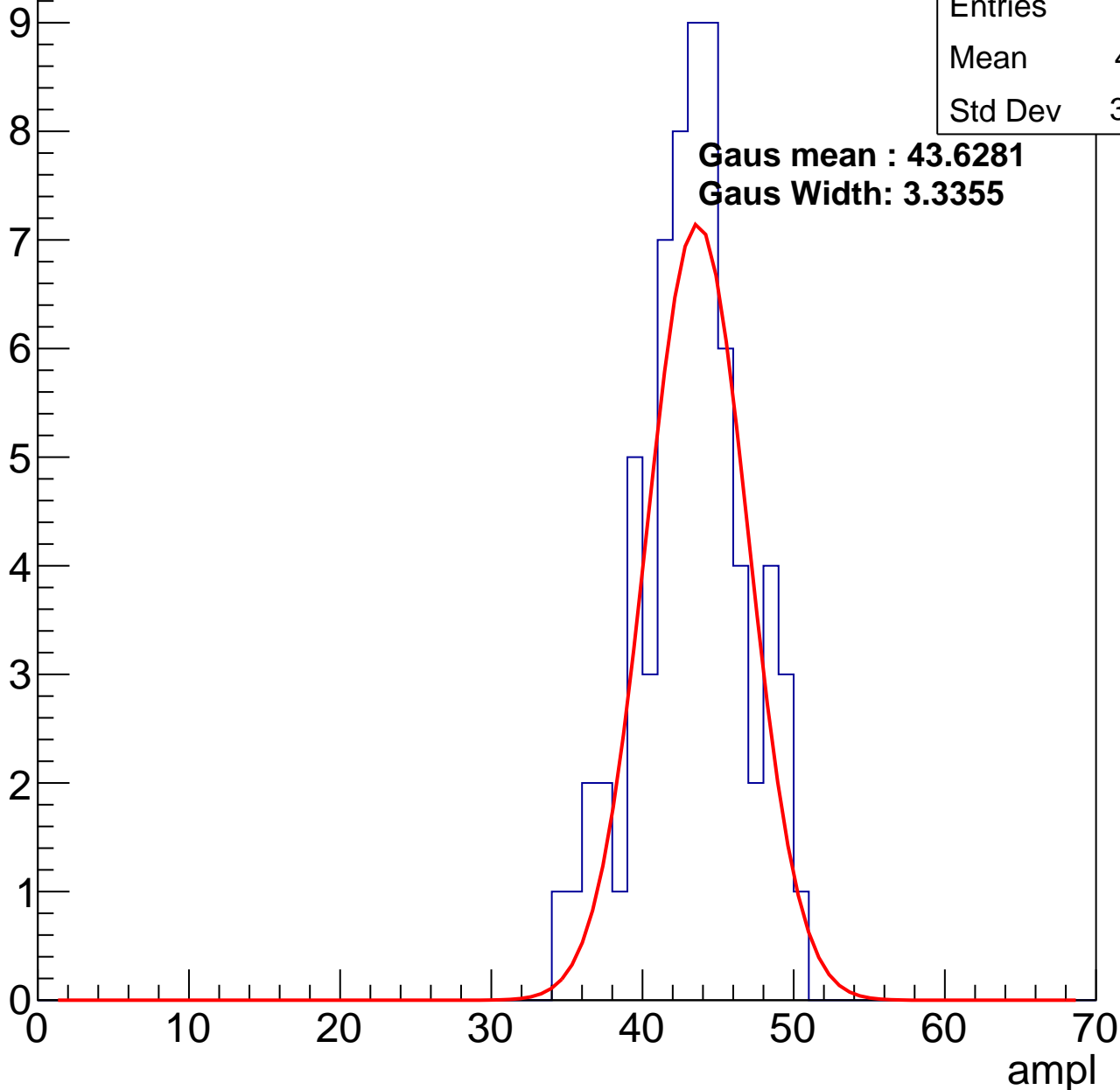
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.81
Std Dev	3.512

Gaus mean : 43.6281

Gaus Width: 3.3355

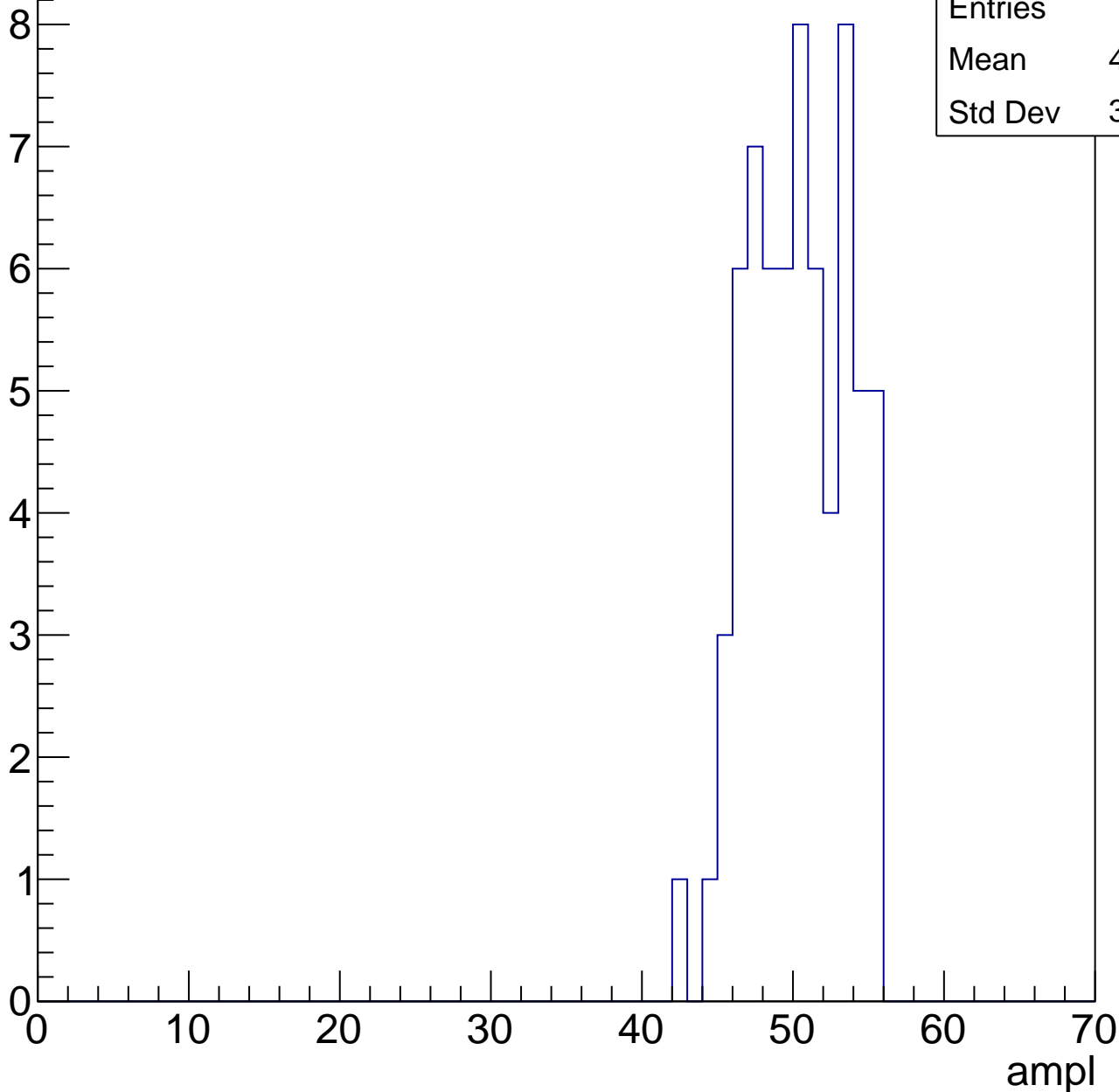


B1L003S, U16-ch1, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.86
Std Dev	3.167

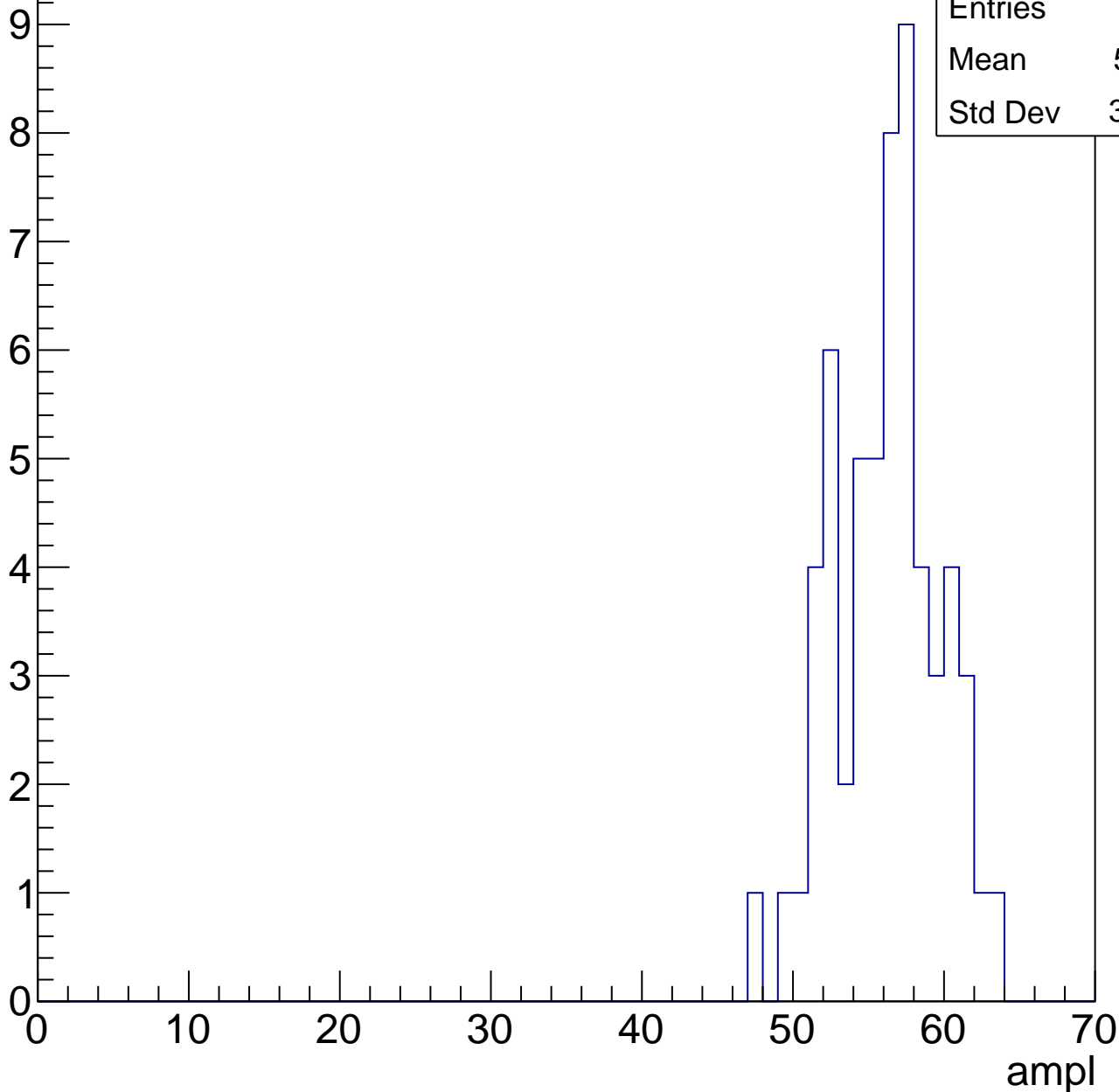


B1L003S, U16-ch1, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

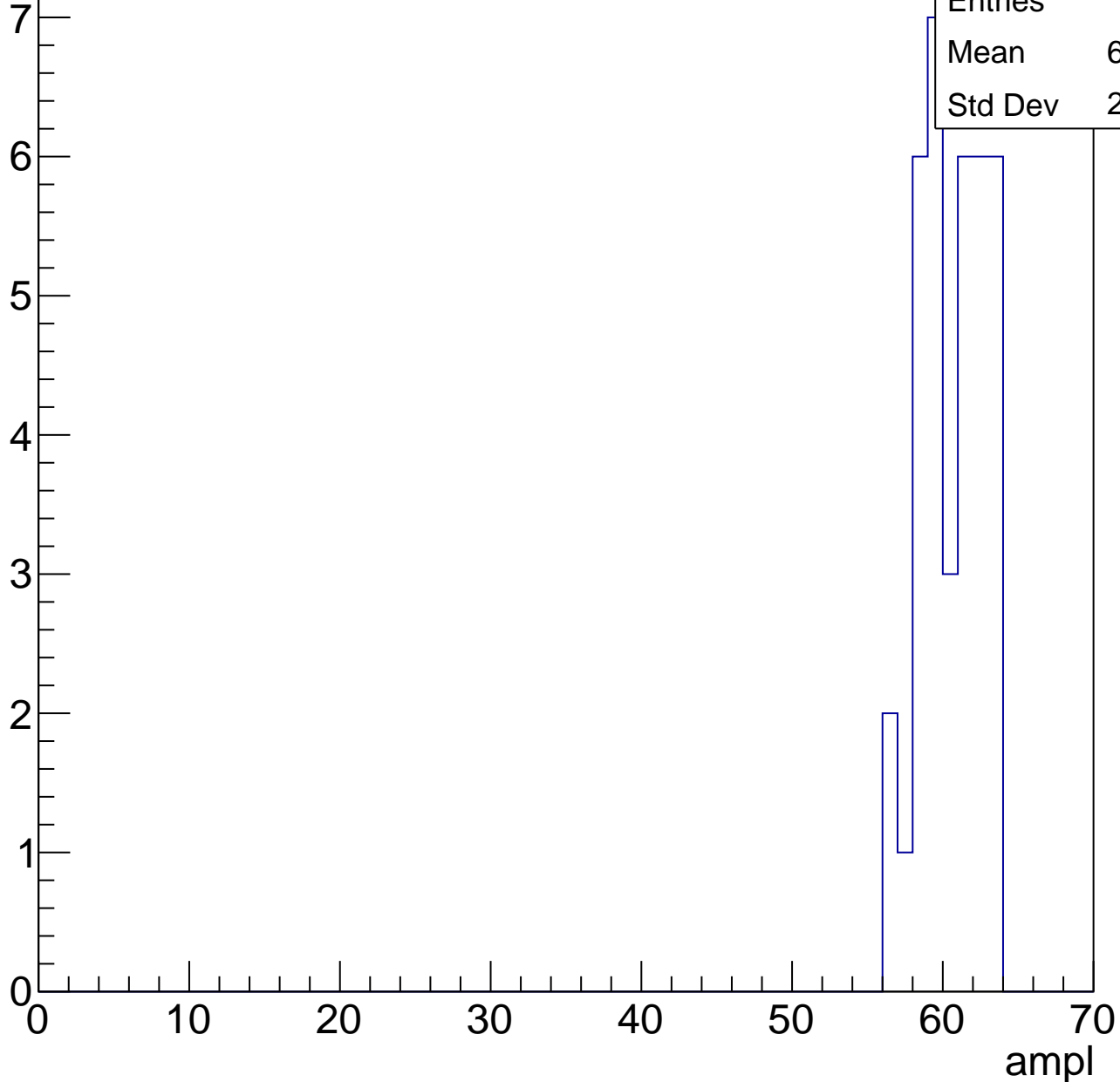
Entries	58
Mean	55.71
Std Dev	3.414



B1L003S, U16-ch1, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	37
Mean	60.16
Std Dev	2.047

B1L003S, U16-ch1, adc6

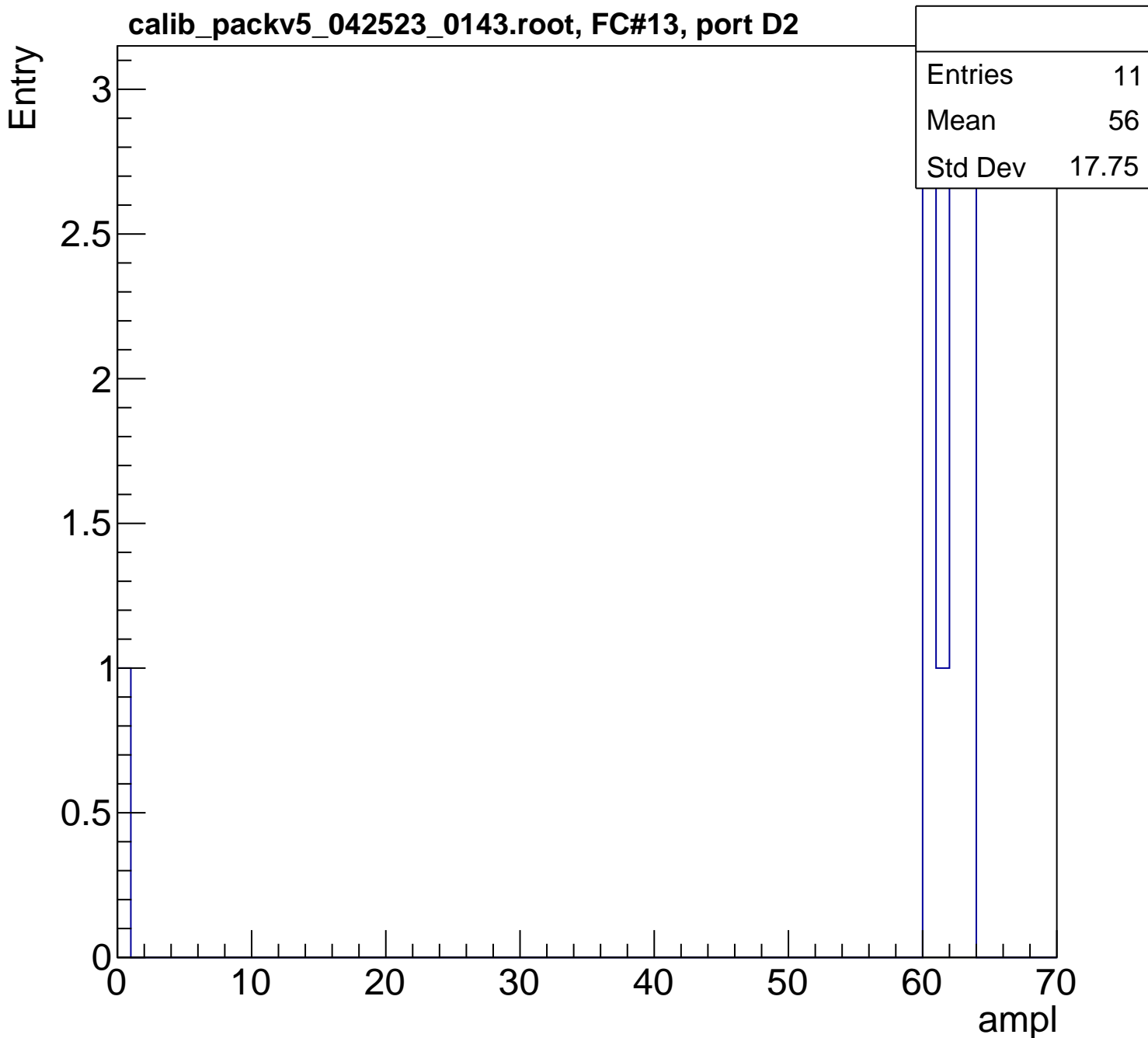
calib_packv5_042523_0143.root, FC#13, port D2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	56
Std Dev	17.75

ampl



B1L003S, U16-ch1, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch2, adc0

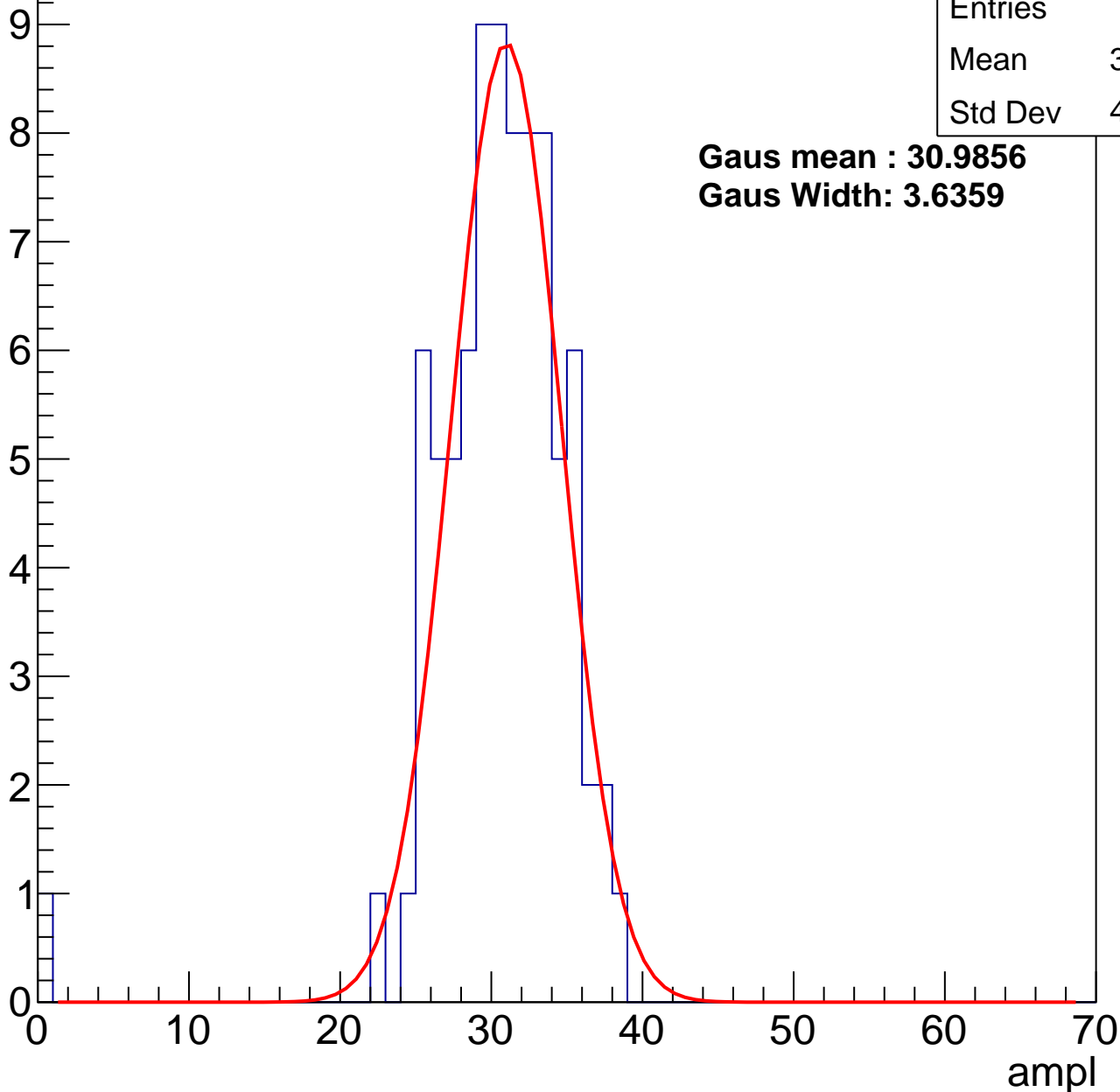
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	30.02
Std Dev	4.764

Gaus mean : 30.9856

Gaus Width: 3.6359



B1L003S, U16-ch2, adc1

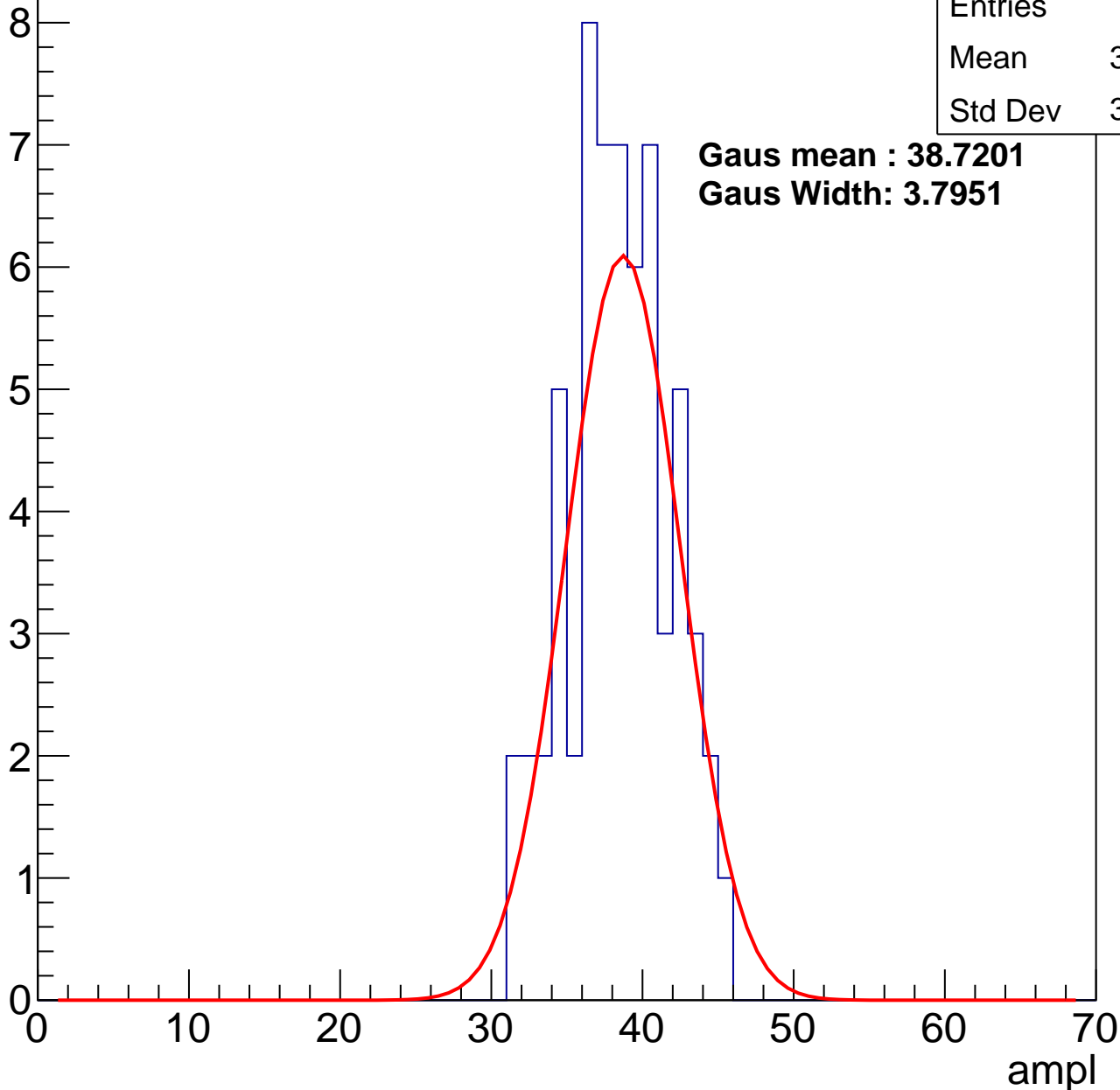
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.97
Std Dev	3.346

Gaus mean : 38.7201

Gaus Width: 3.7951



B1L003S, U16-ch2, adc2

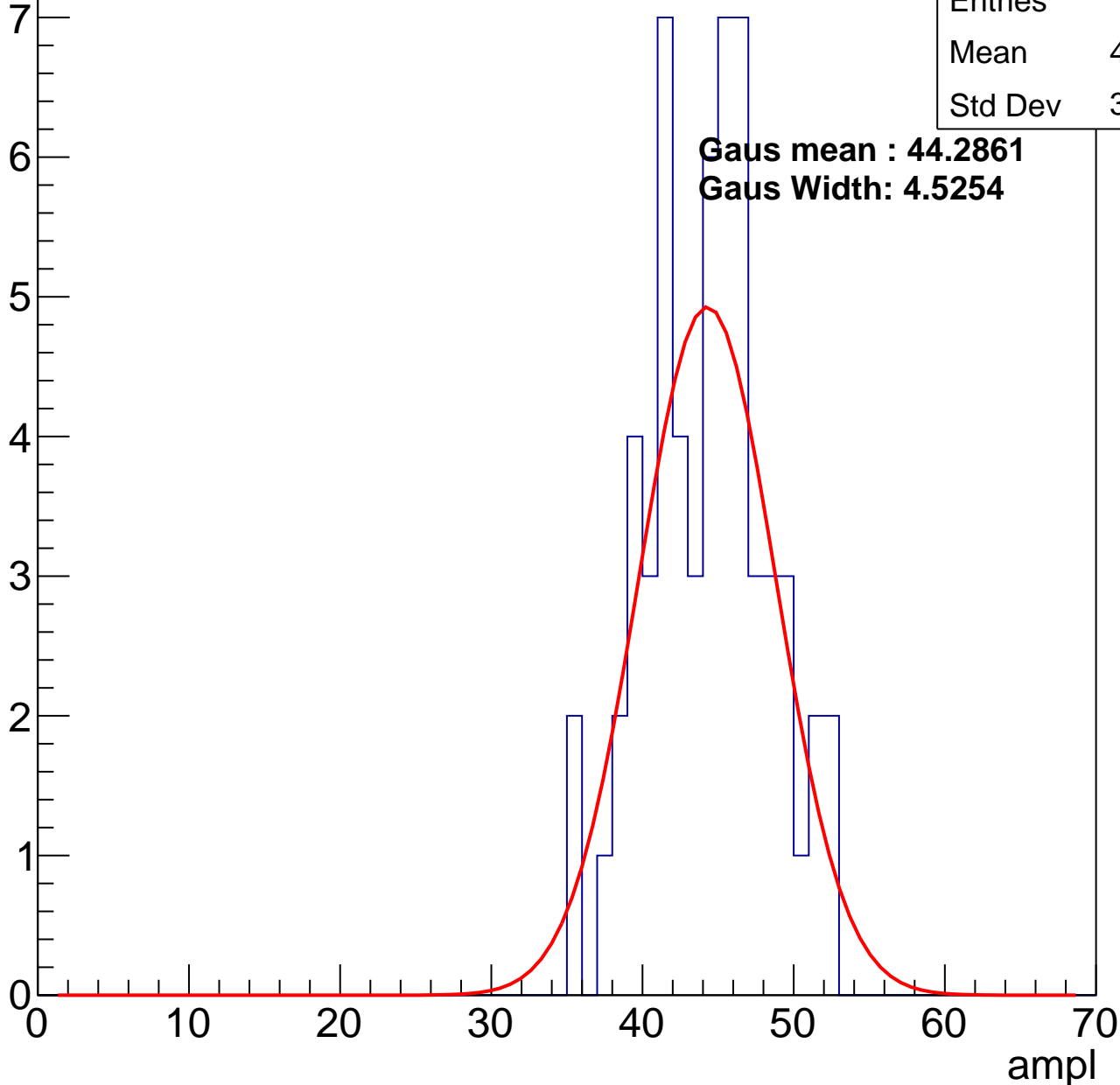
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.87
Std Dev	3.998

Gaus mean : 44.2861

Gaus Width: 4.5254

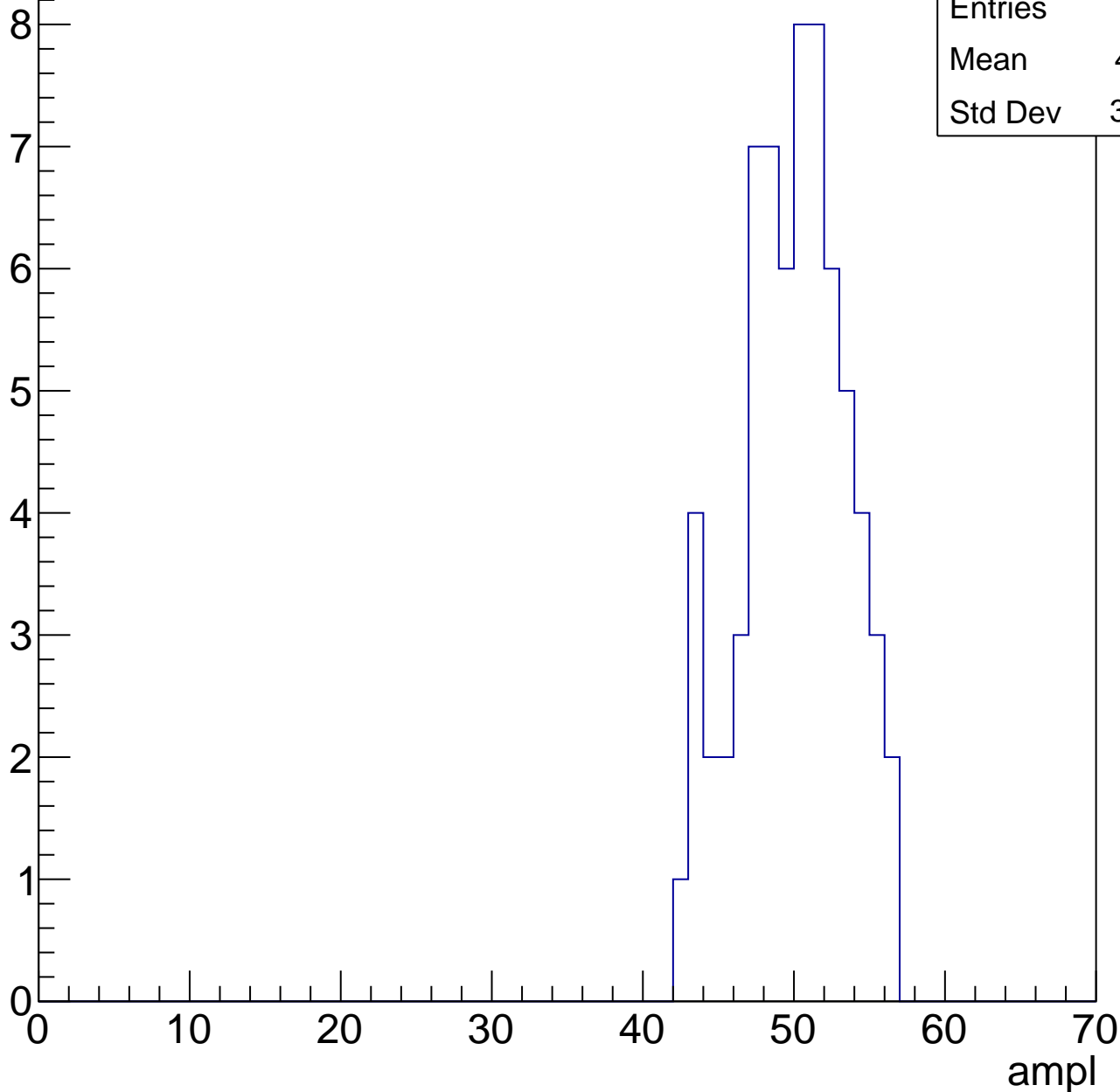


B1L003S, U16-ch2, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.51
Std Dev	3.428

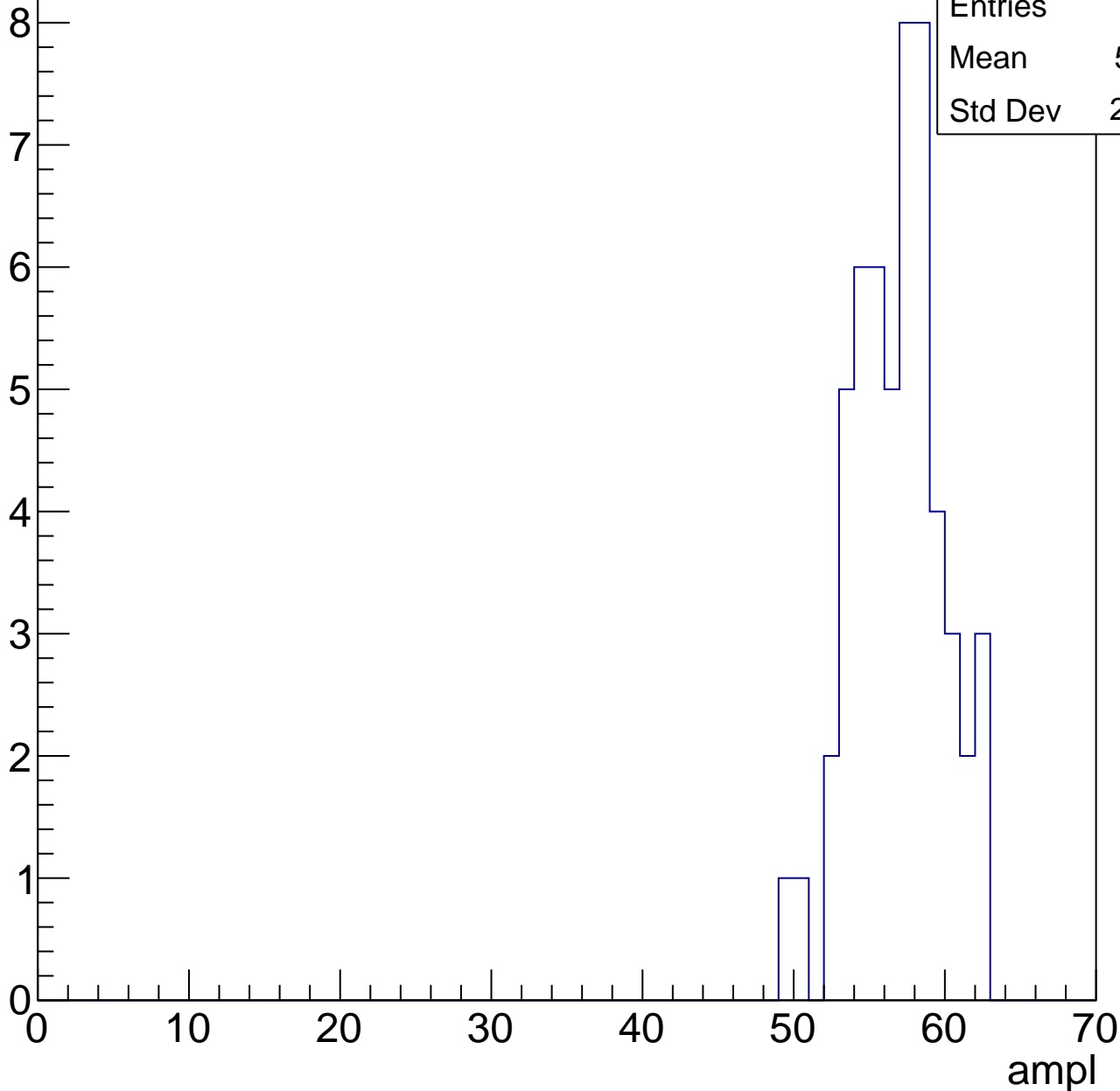


B1L003S, U16-ch2, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.41
Std Dev	2.935

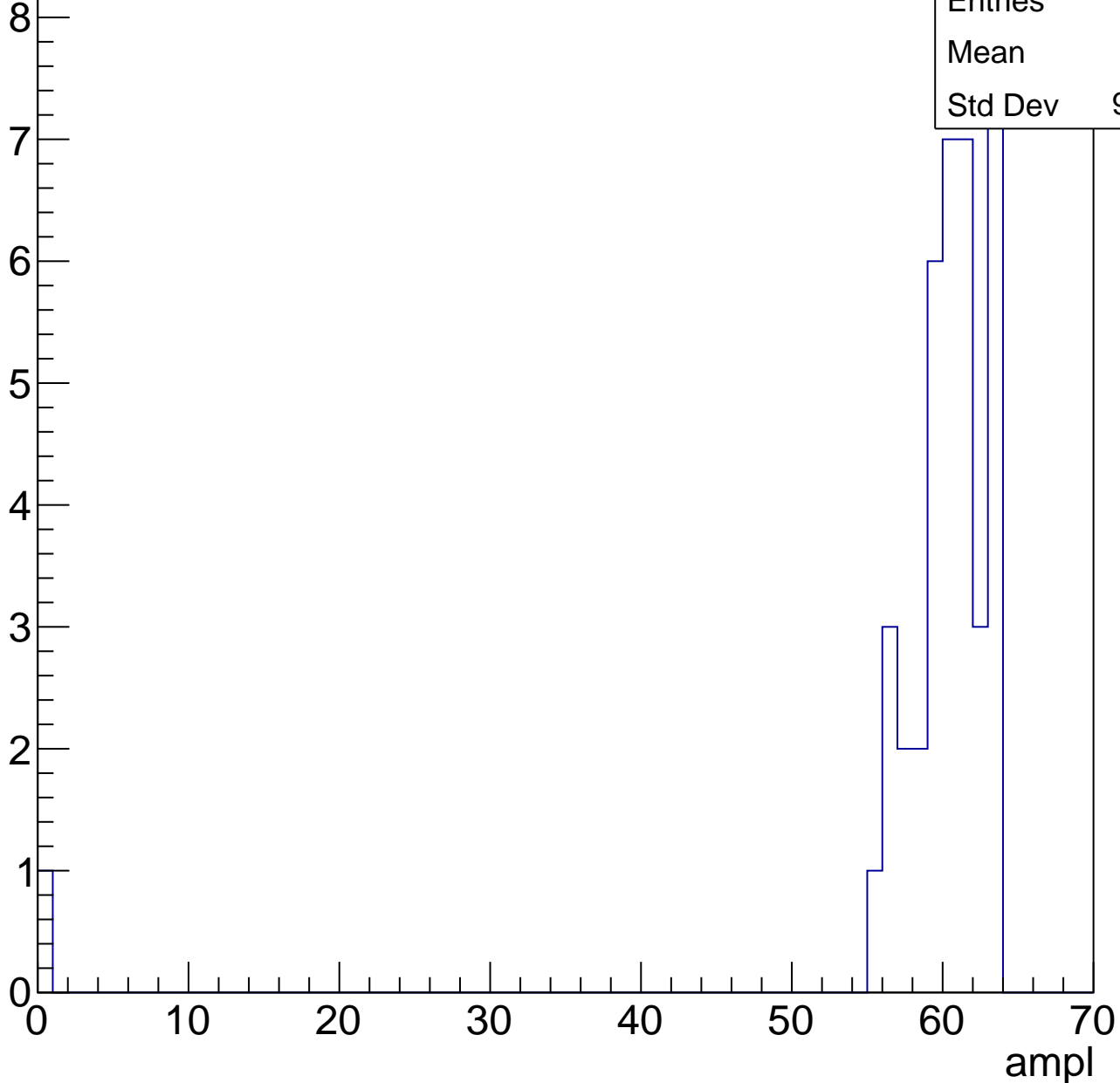


B1L003S, U16-ch2, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	58.6
Std Dev	9.641



B1L003S, U16-ch2, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch2, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch3, adc0

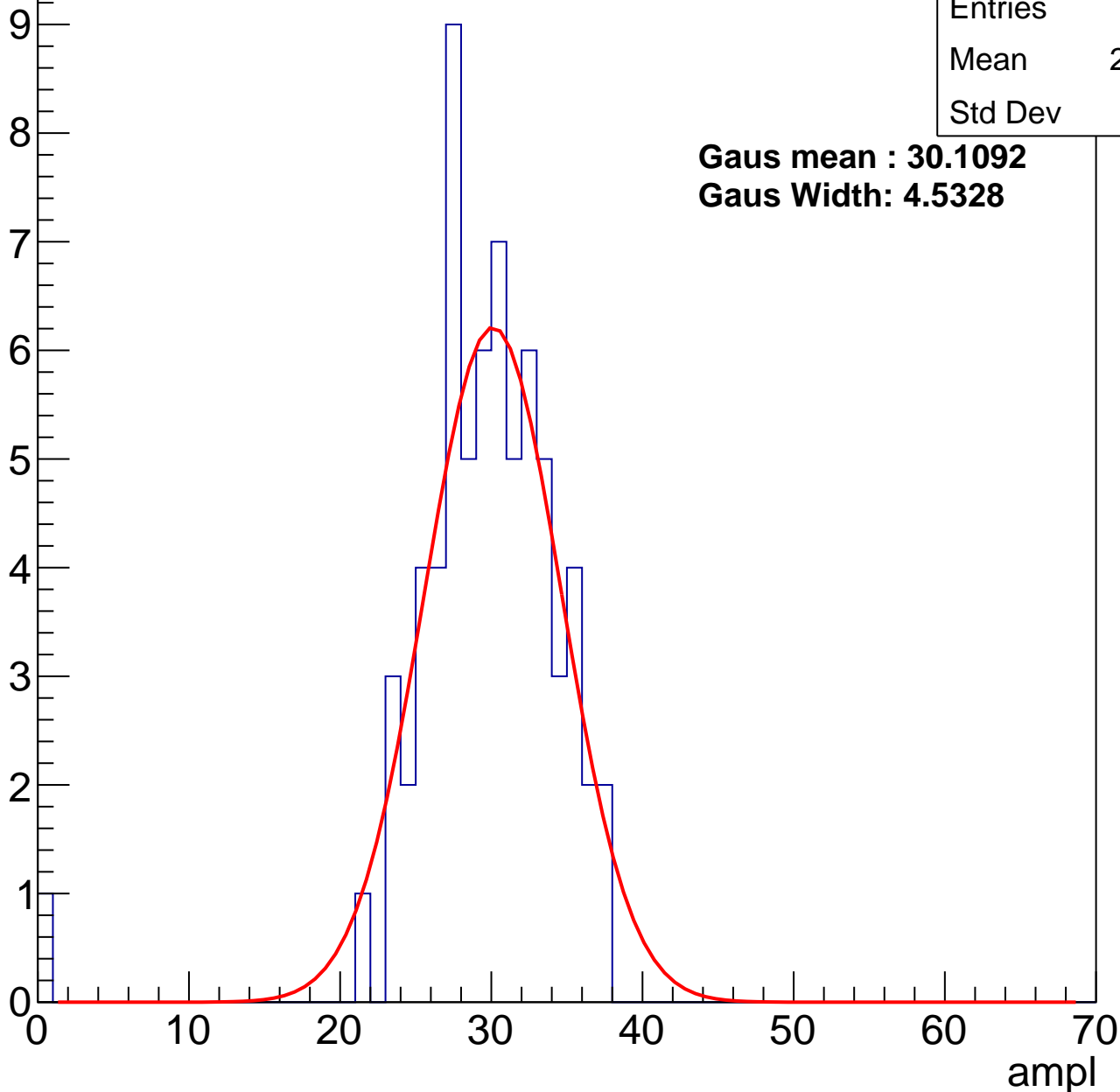
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.12
Std Dev	5.12

Gaus mean : 30.1092

Gaus Width: 4.5328



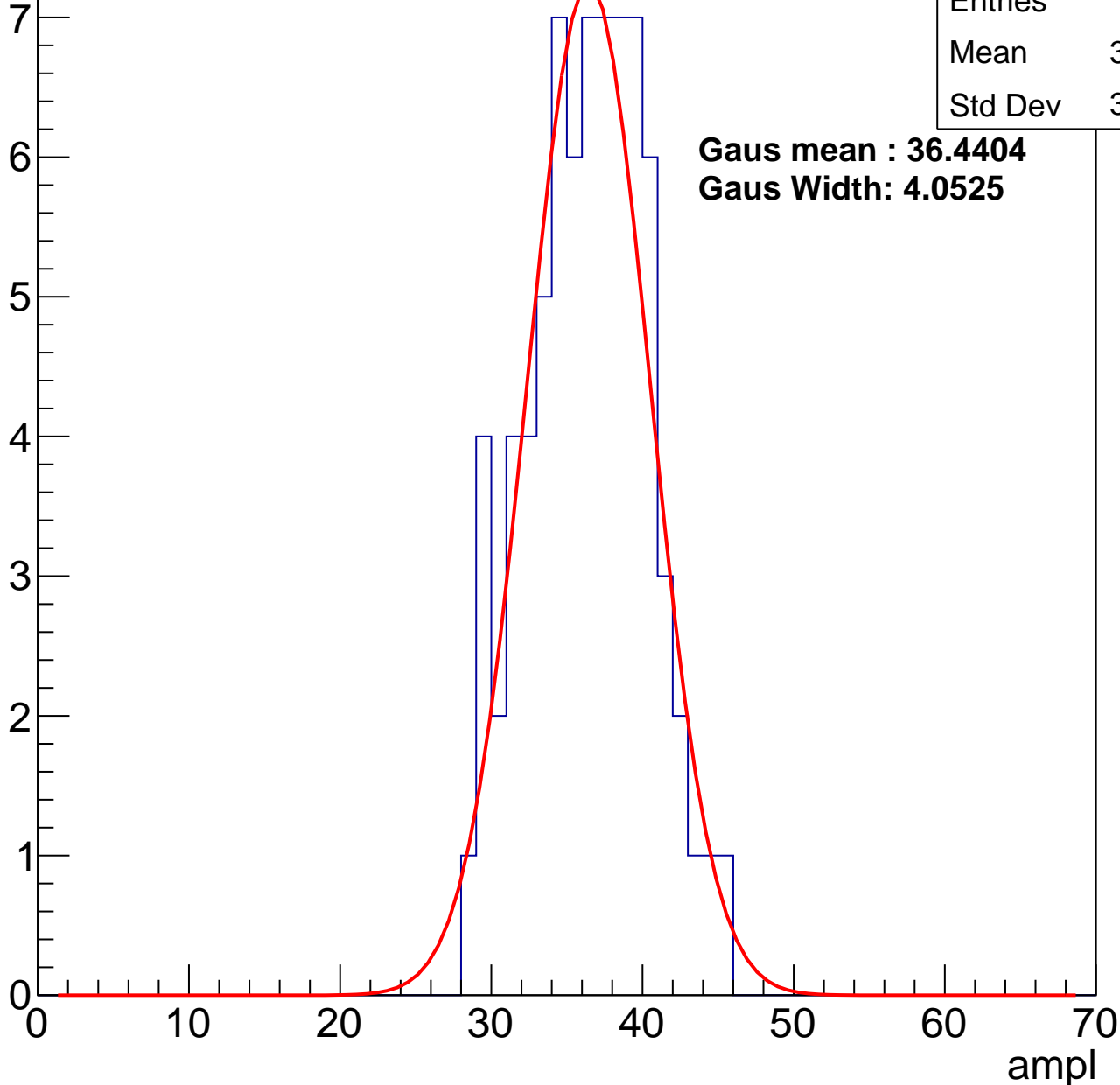
B1L003S, U16-ch3, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	35.97
Std Dev	3.847

Gaus mean : 36.4404
Gaus Width: 4.0525



B1L003S, U16-ch3, adc2

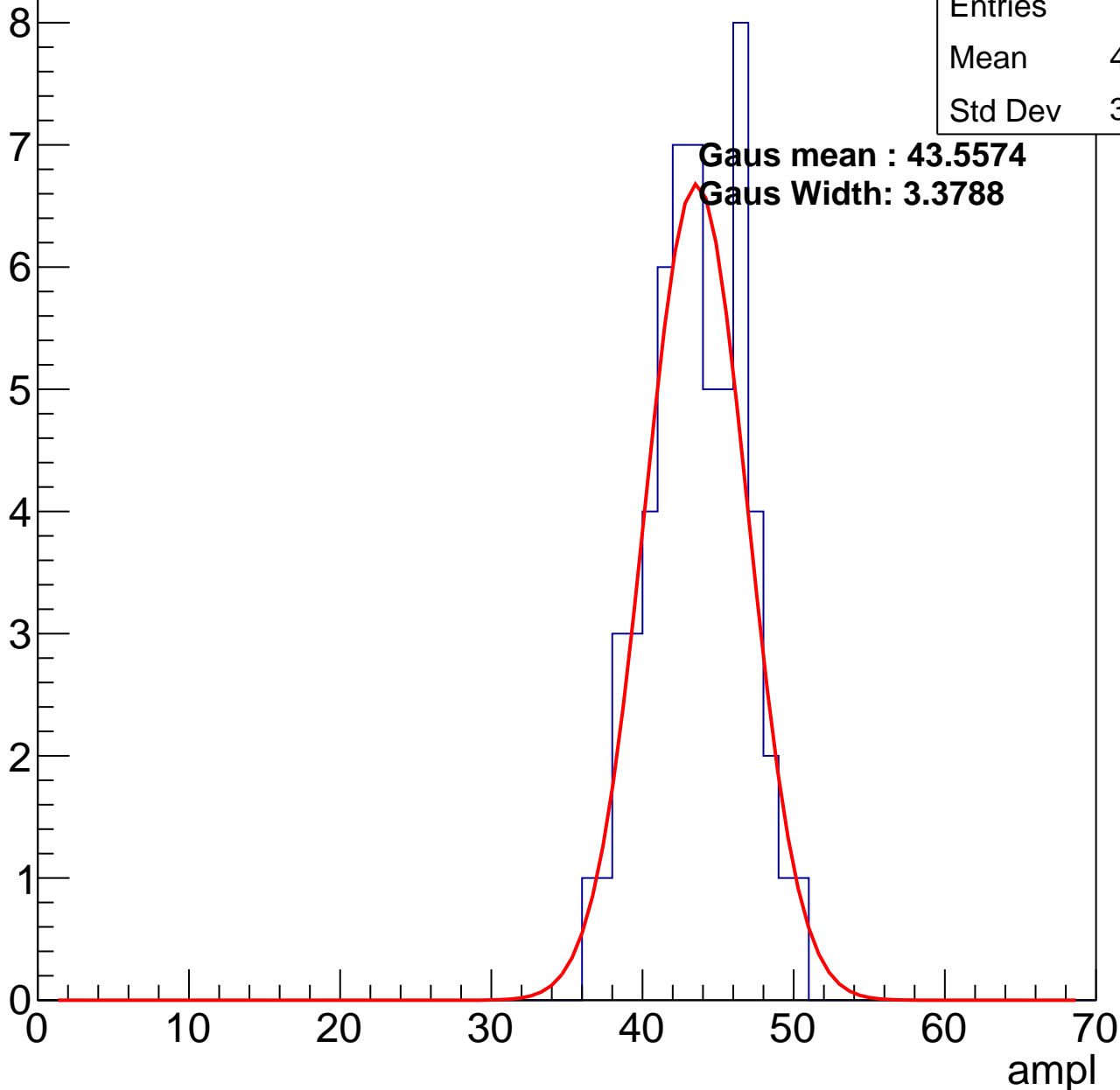
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.12
Std Dev	3.135

Gaus mean : 43.5574

Gaus Width: 3.3788

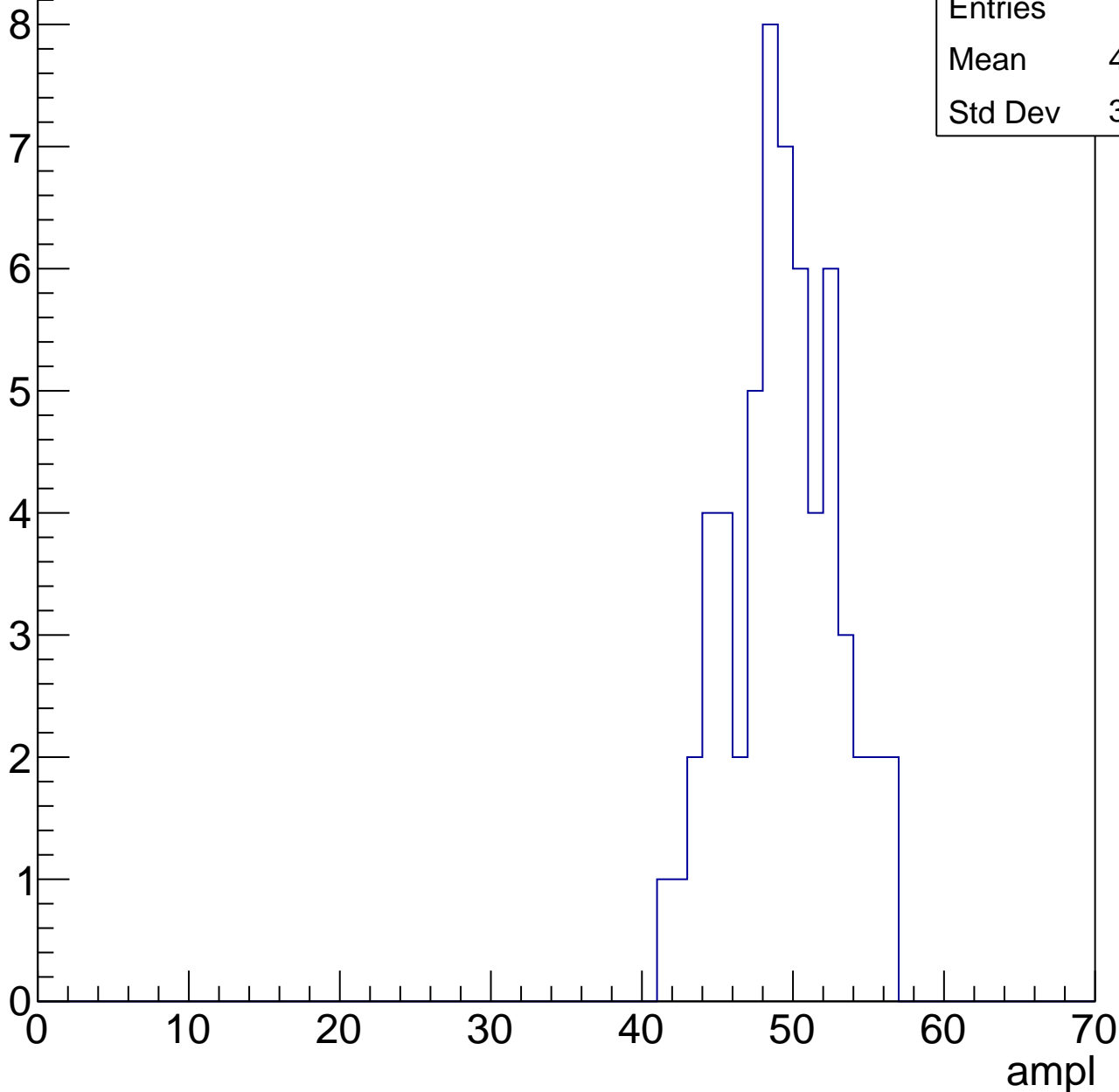


B1L003S, U16-ch3, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.88
Std Dev	3.537

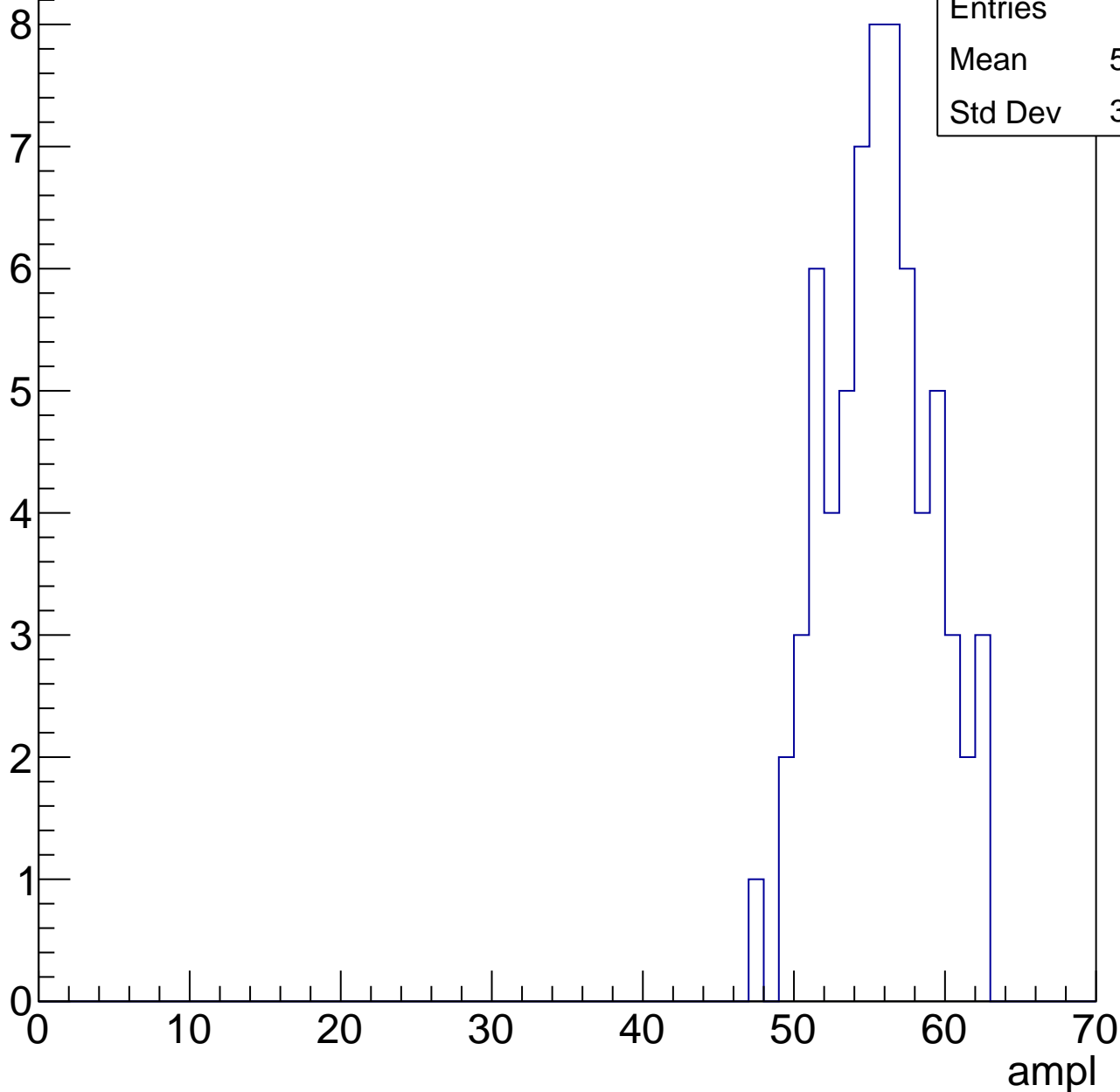


B1L003S, U16-ch3, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.18
Std Dev	3.477

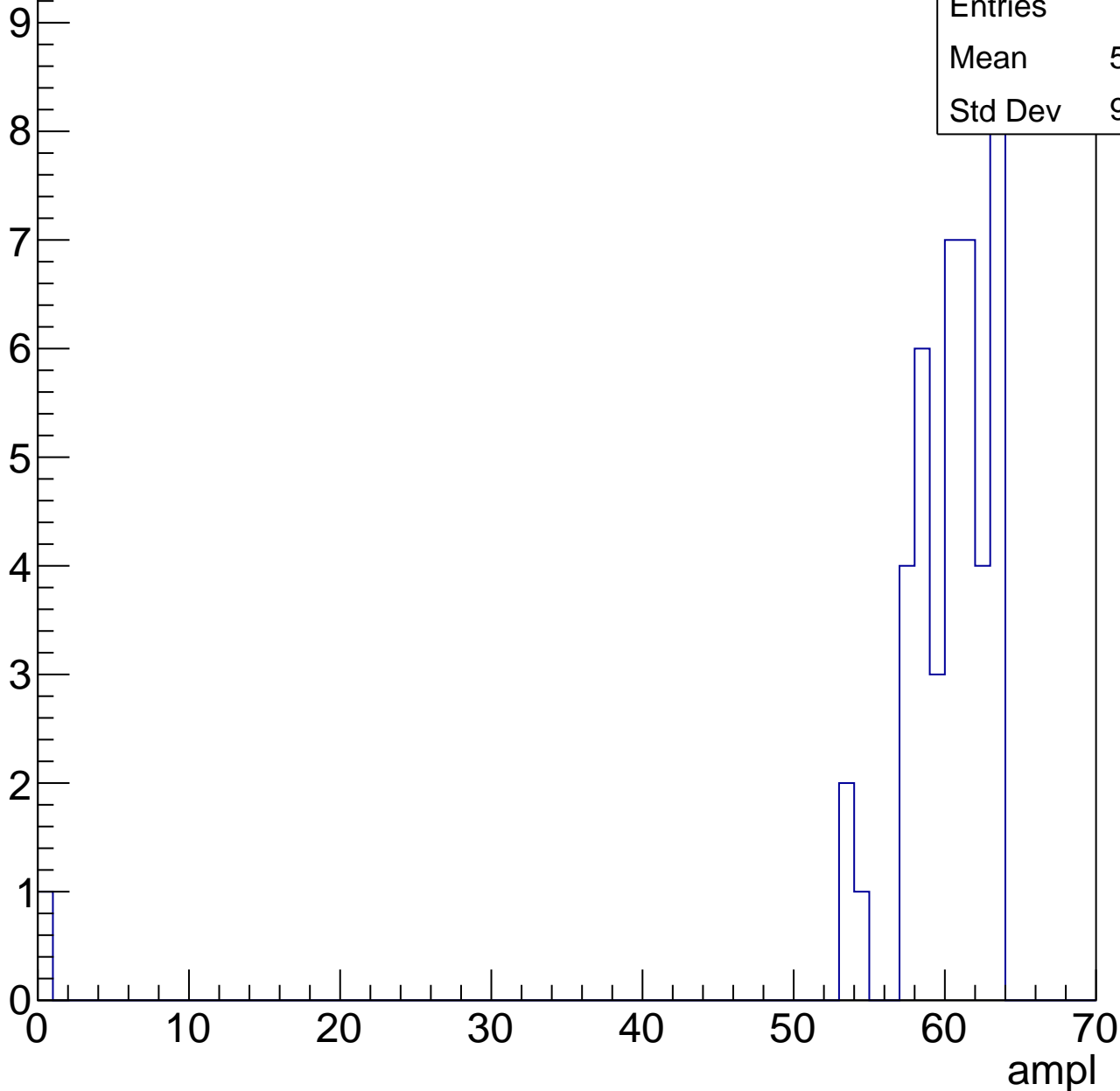


B1L003S, U16-ch3, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

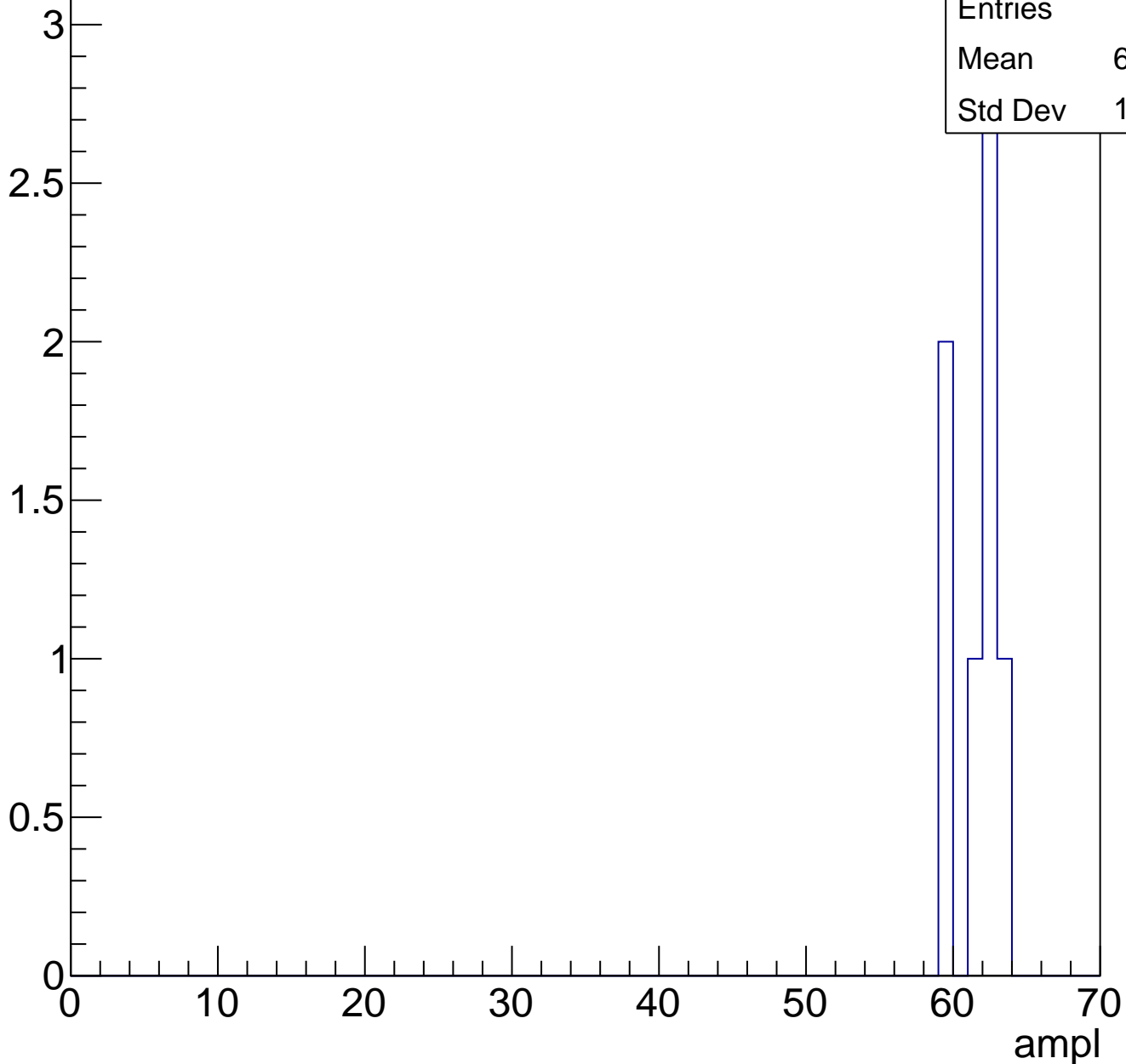
Entries	44
Mean	58.52
Std Dev	9.299



B1L003S, U16-ch3, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch3, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch4, adc0

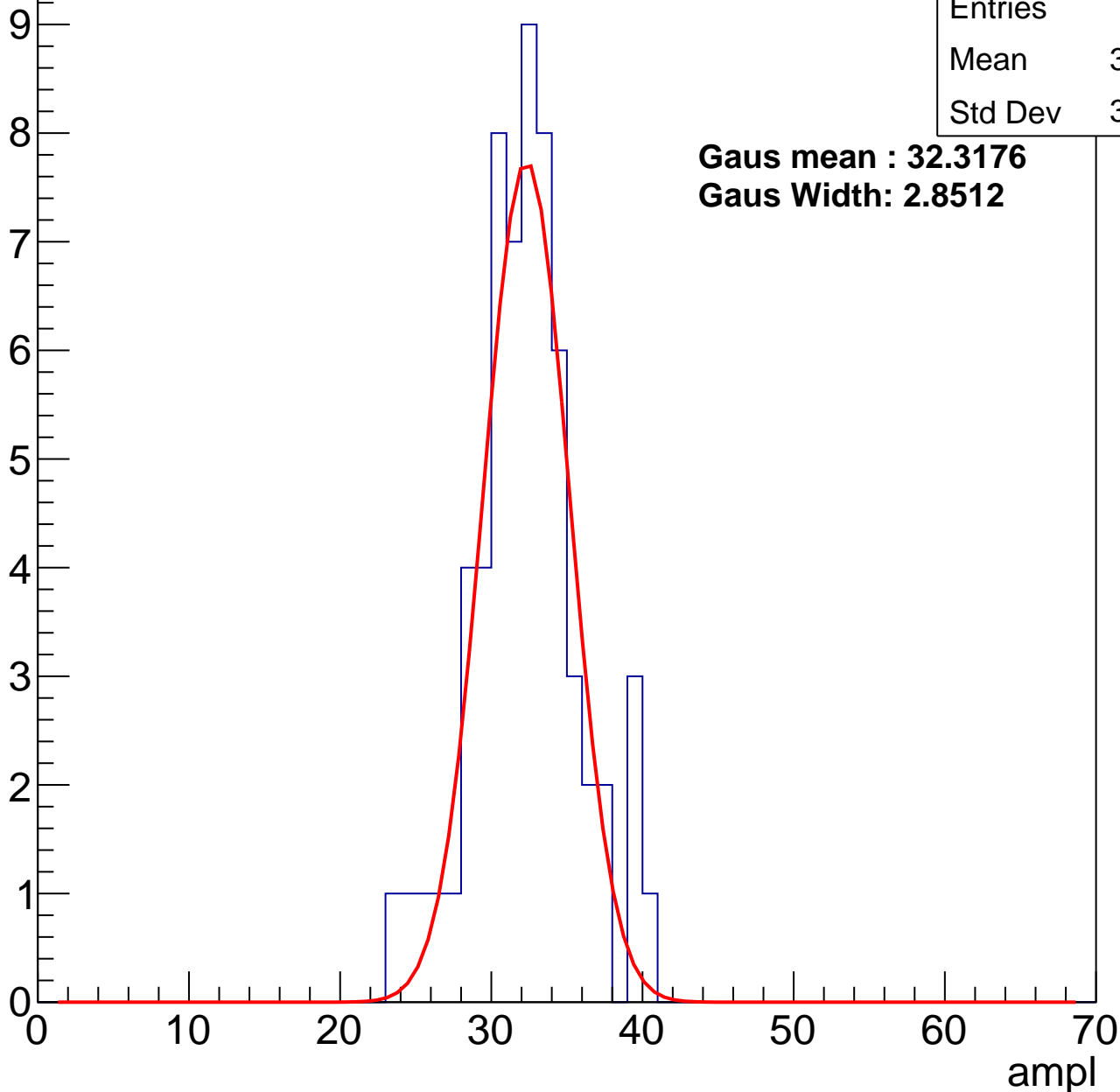
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	31.84
Std Dev	3.465

Gaus mean : 32.3176

Gaus Width: 2.8512



B1L003S, U16-ch4, adc1

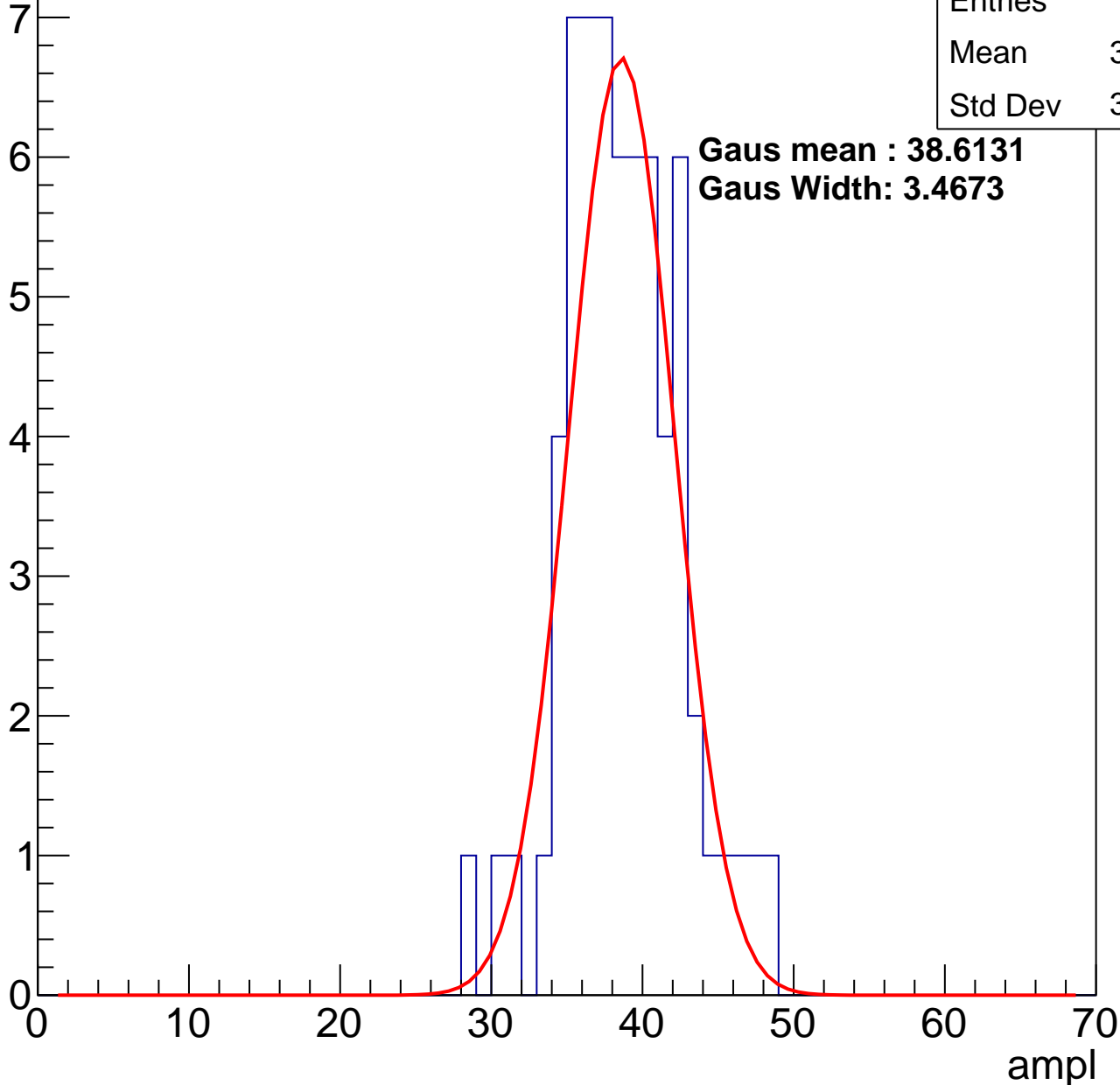
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	38.25
Std Dev	3.828

Gaus mean : 38.6131

Gaus Width: 3.4673

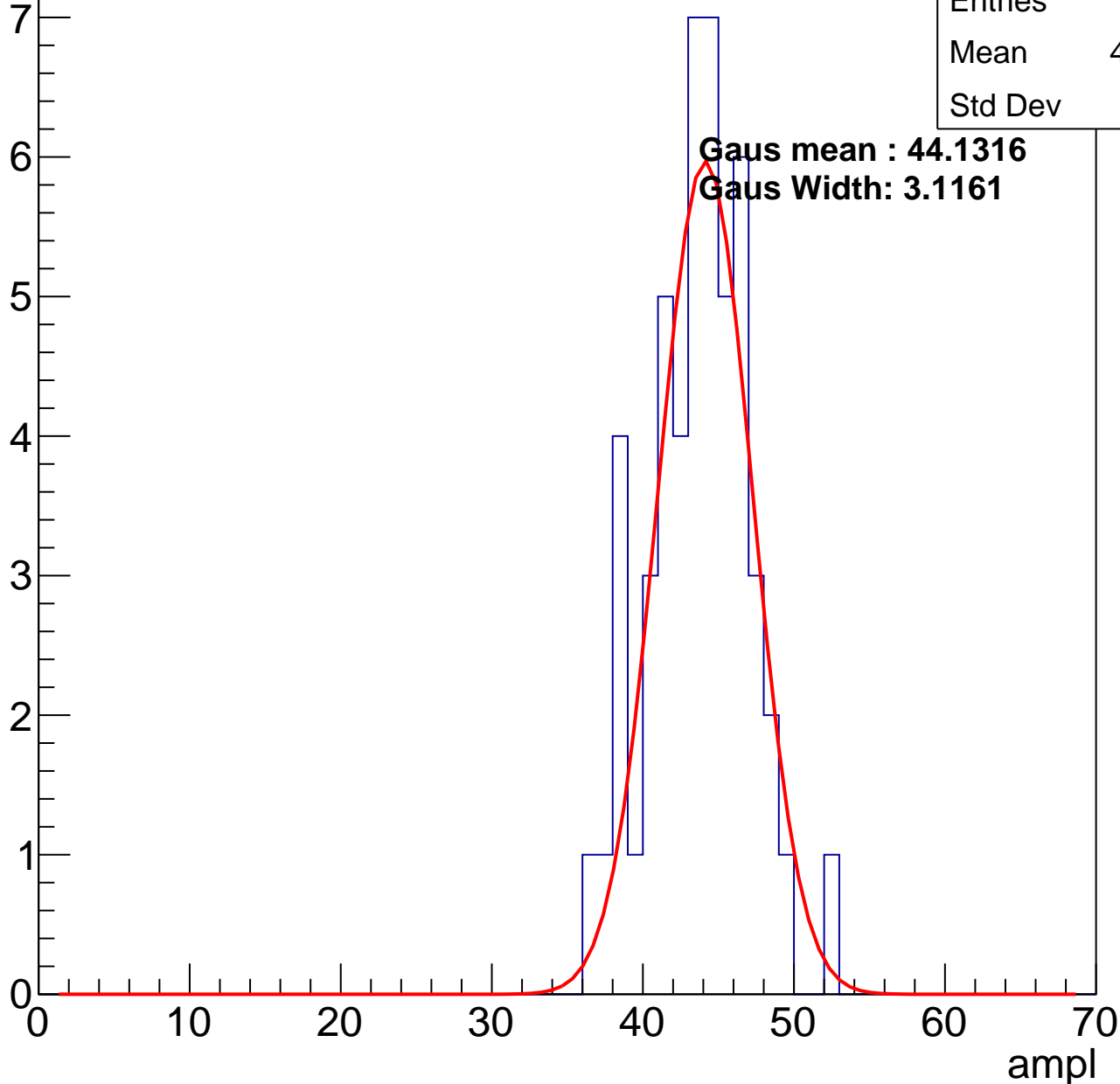


B1L003S, U16-ch4, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.24
Std Dev	3.27

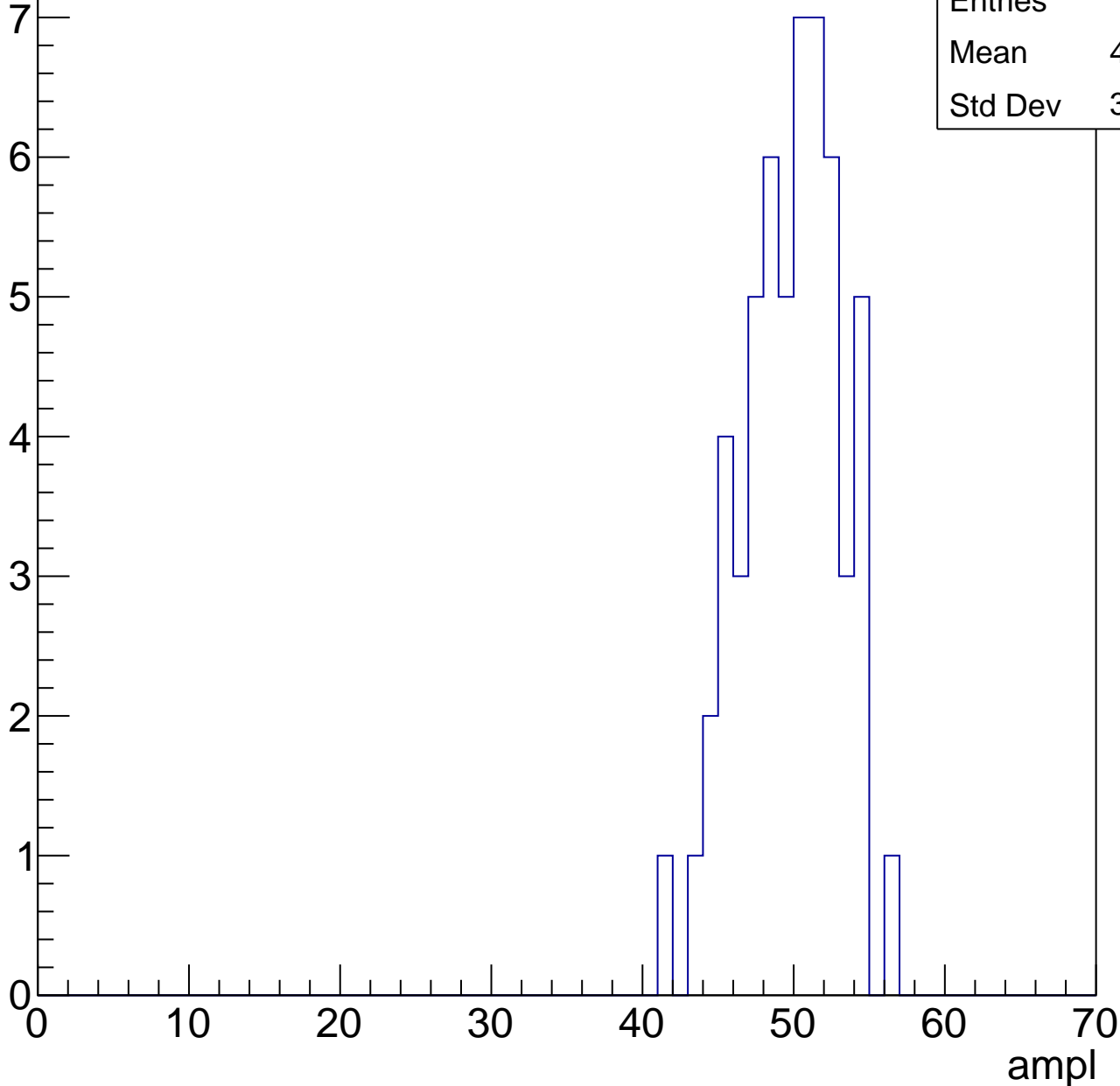


B1L003S, U16-ch4, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	49.32
Std Dev	3.197



B1L003S, U16-ch4, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	54.52
Std Dev	7.339

Entry

10

8

6

4

2

0

0

10

20

30

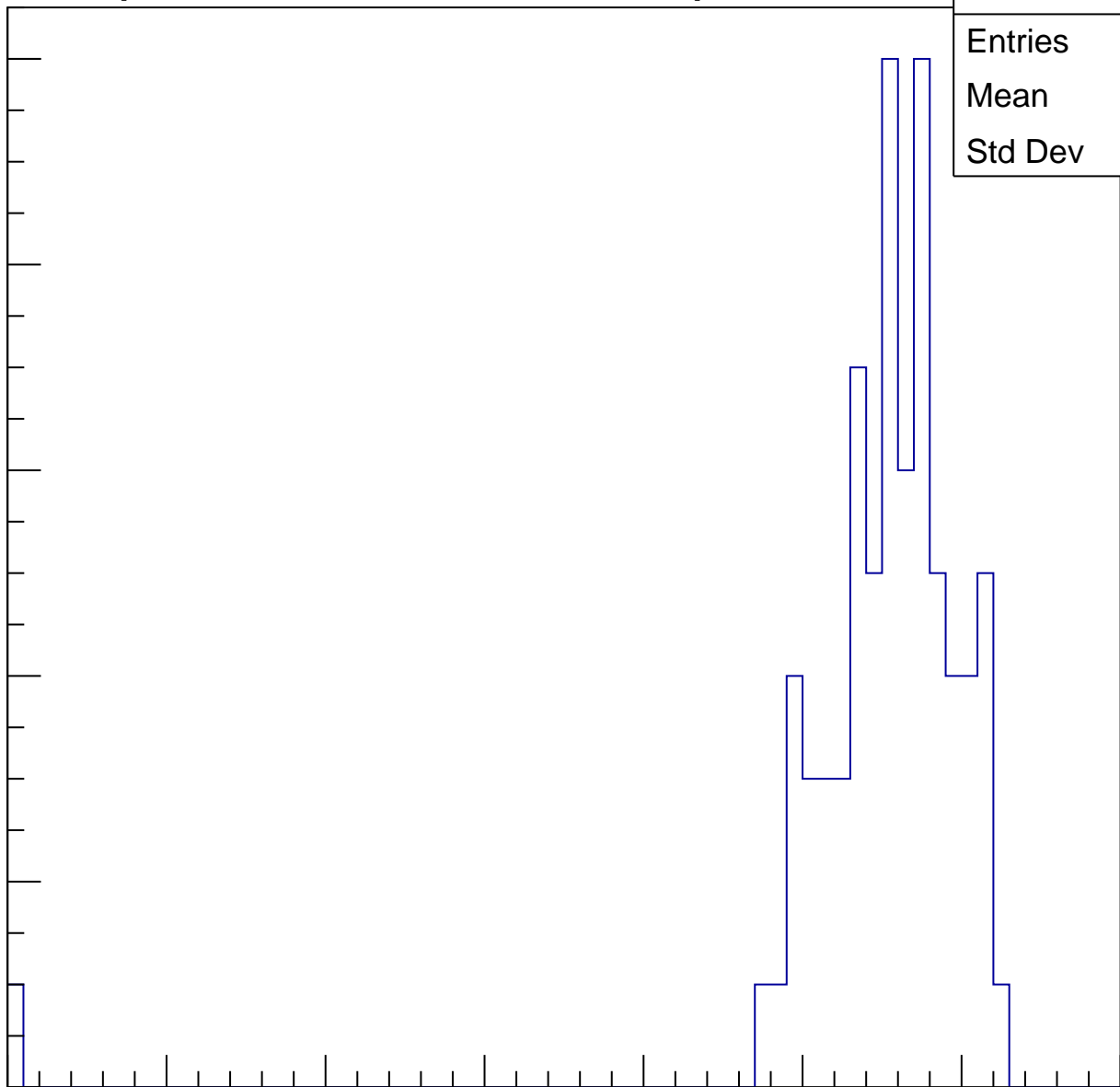
40

50

60

70

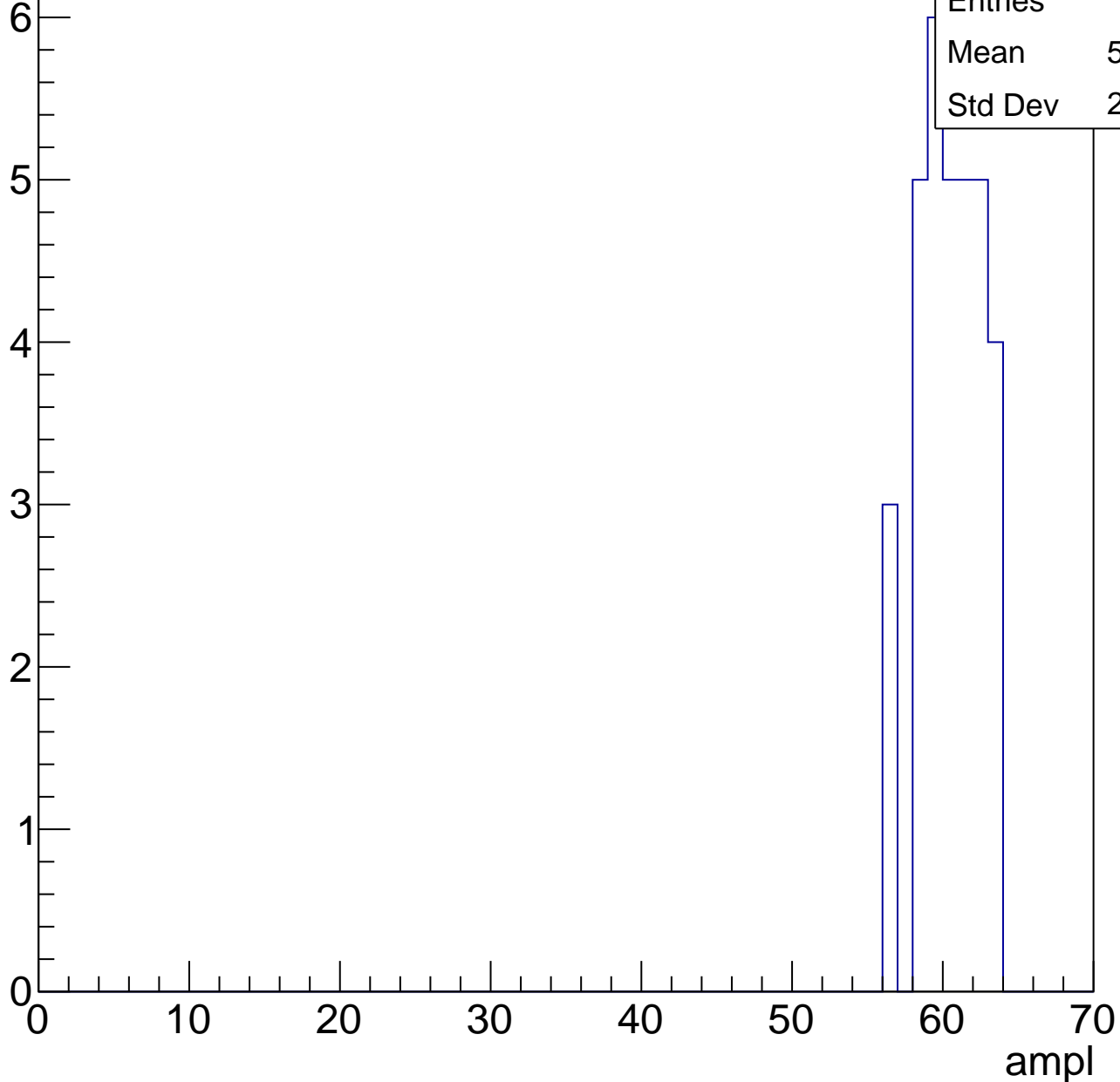
ampl



B1L003S, U16-ch4, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



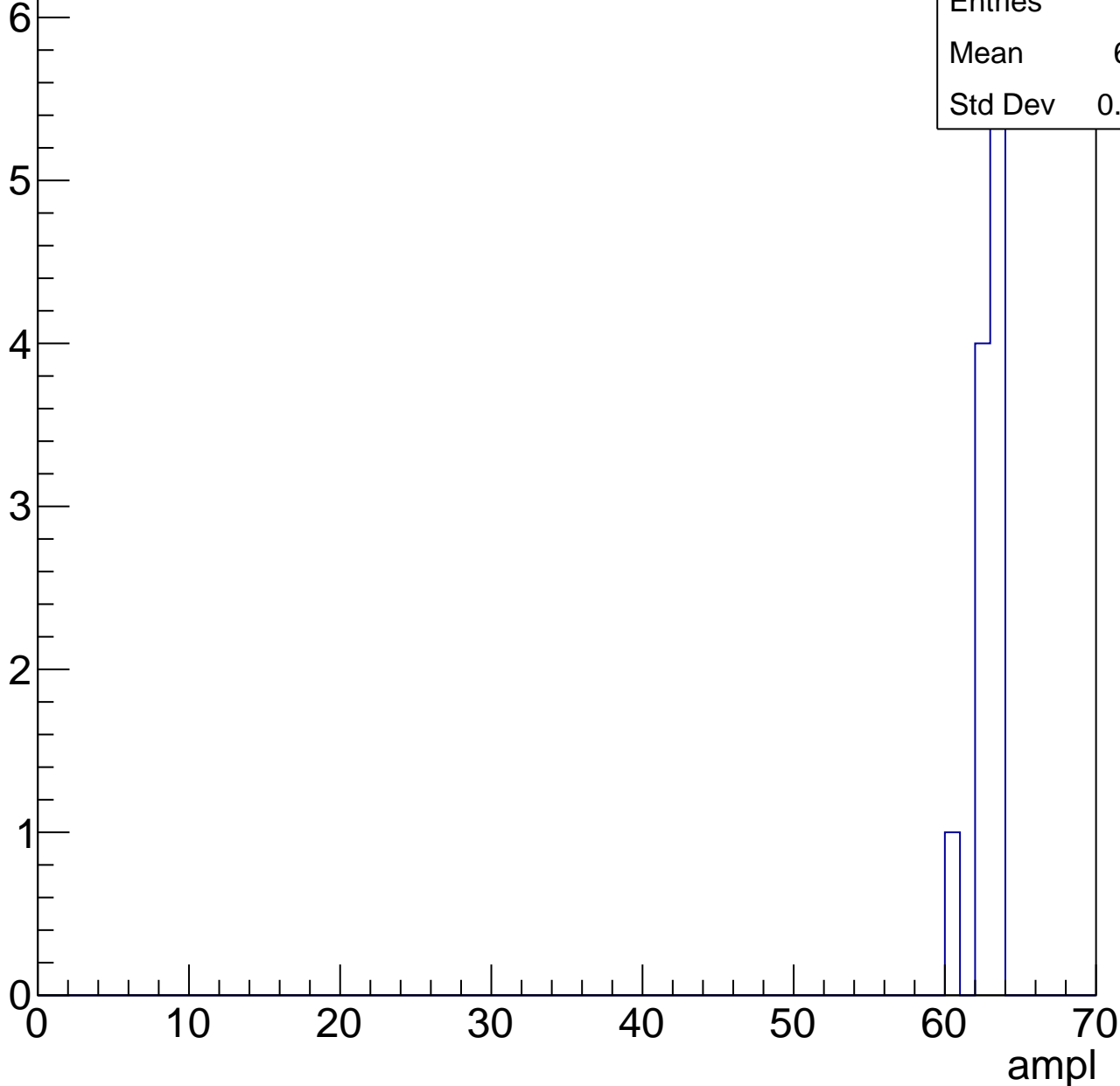
Entries	33
Mean	59.97
Std Dev	2.022

B1L003S, U16-ch4, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.36
Std Dev	0.8814



B1L003S, U16-ch4, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch5, adc0

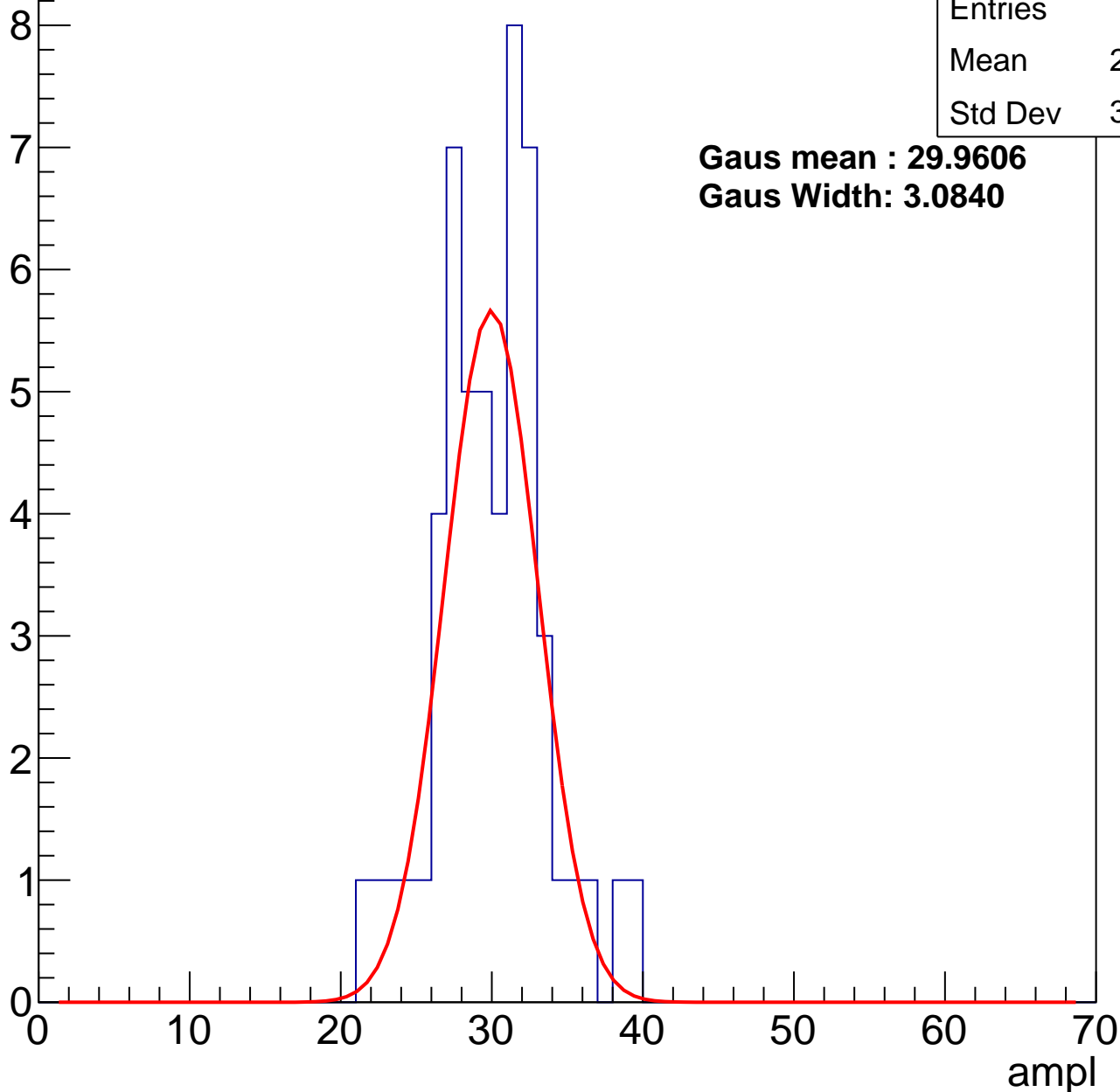
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	29.55
Std Dev	3.585

Gaus mean : 29.9606

Gaus Width: 3.0840



B1L003S, U16-ch5, adc1

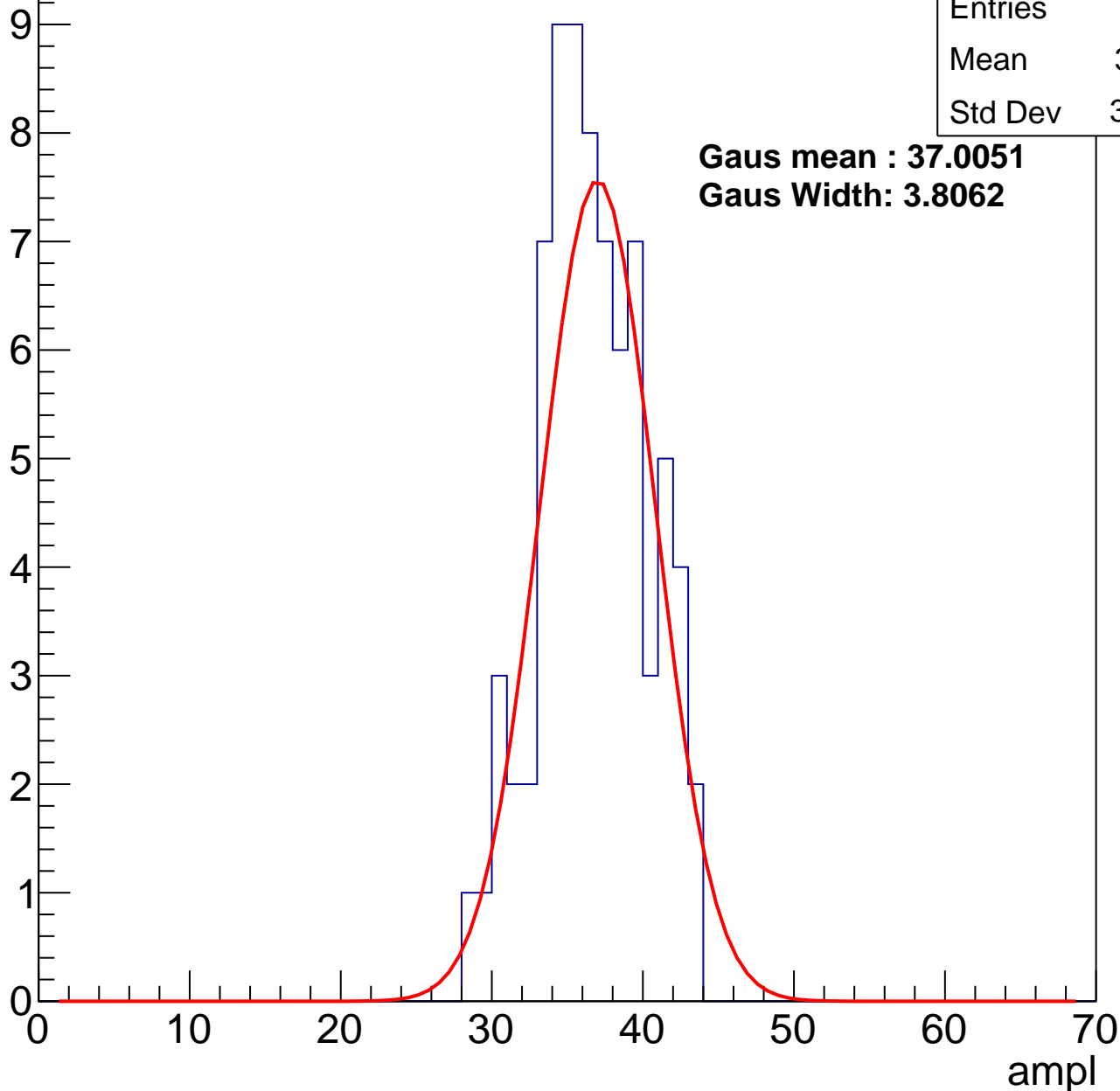
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	36.21
Std Dev	3.473

Gaus mean : 37.0051

Gaus Width: 3.8062



B1L003S, U16-ch5, adc2

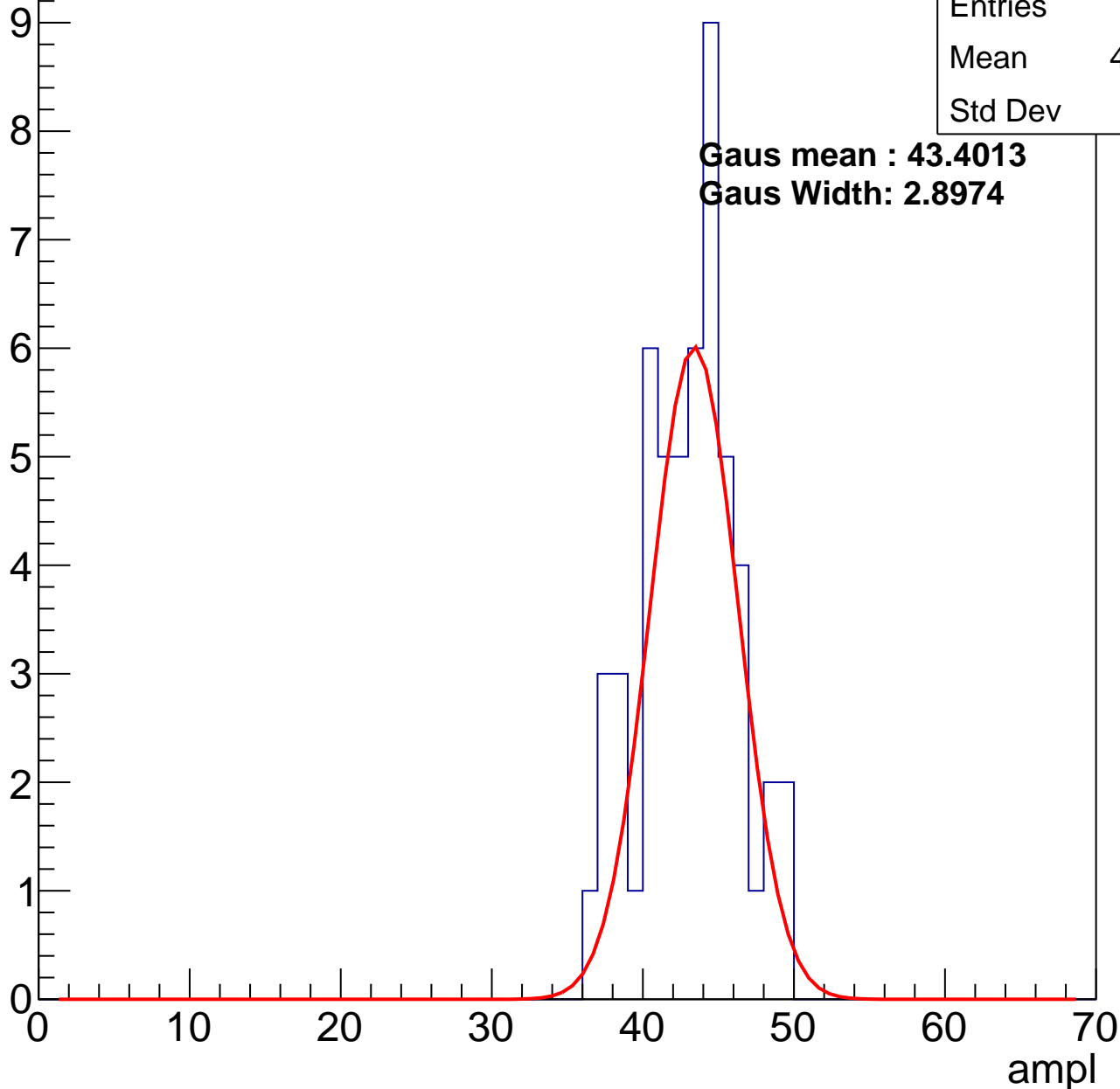
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.62
Std Dev	3.14

Gaus mean : 43.4013

Gaus Width: 2.8974

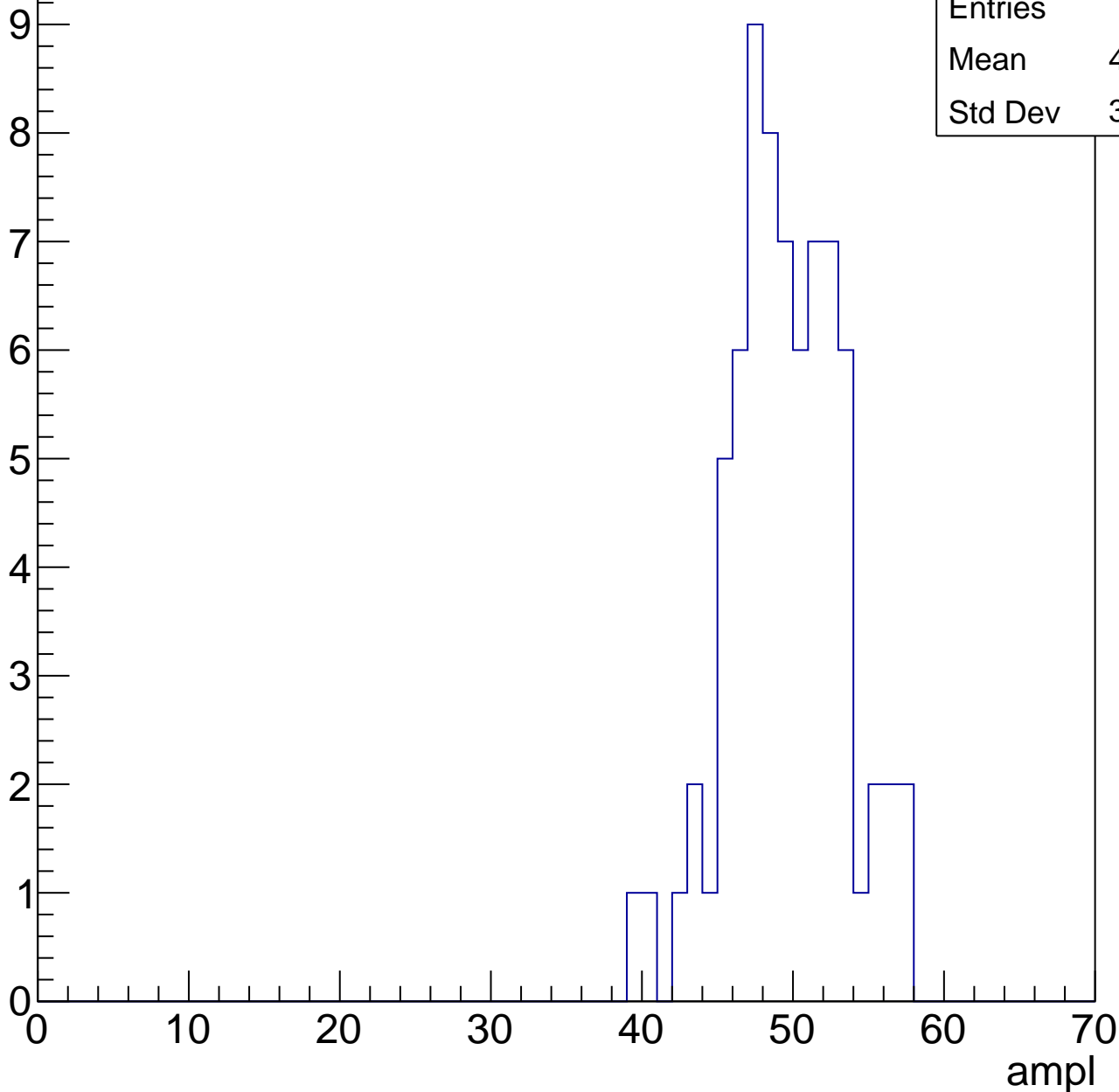


B1L003S, U16-ch5, adc3

calib_packv5_042523_0143.root, FC#13, port D2

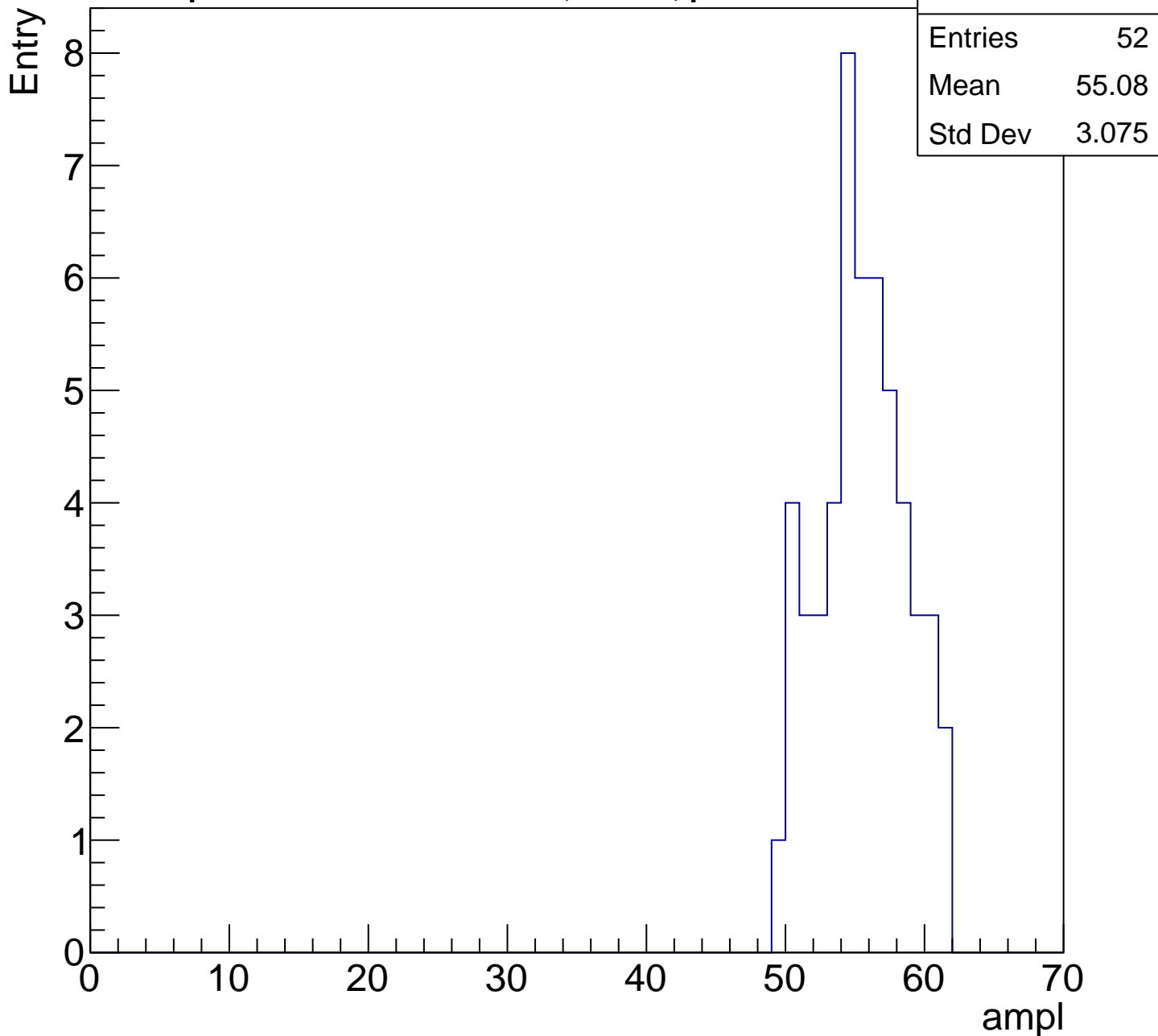
Entry

Entries	74
Mean	49.07
Std Dev	3.714



B1L003S, U16-ch5, adc4

calib_packv5_042523_0143.root, FC#13, port D2

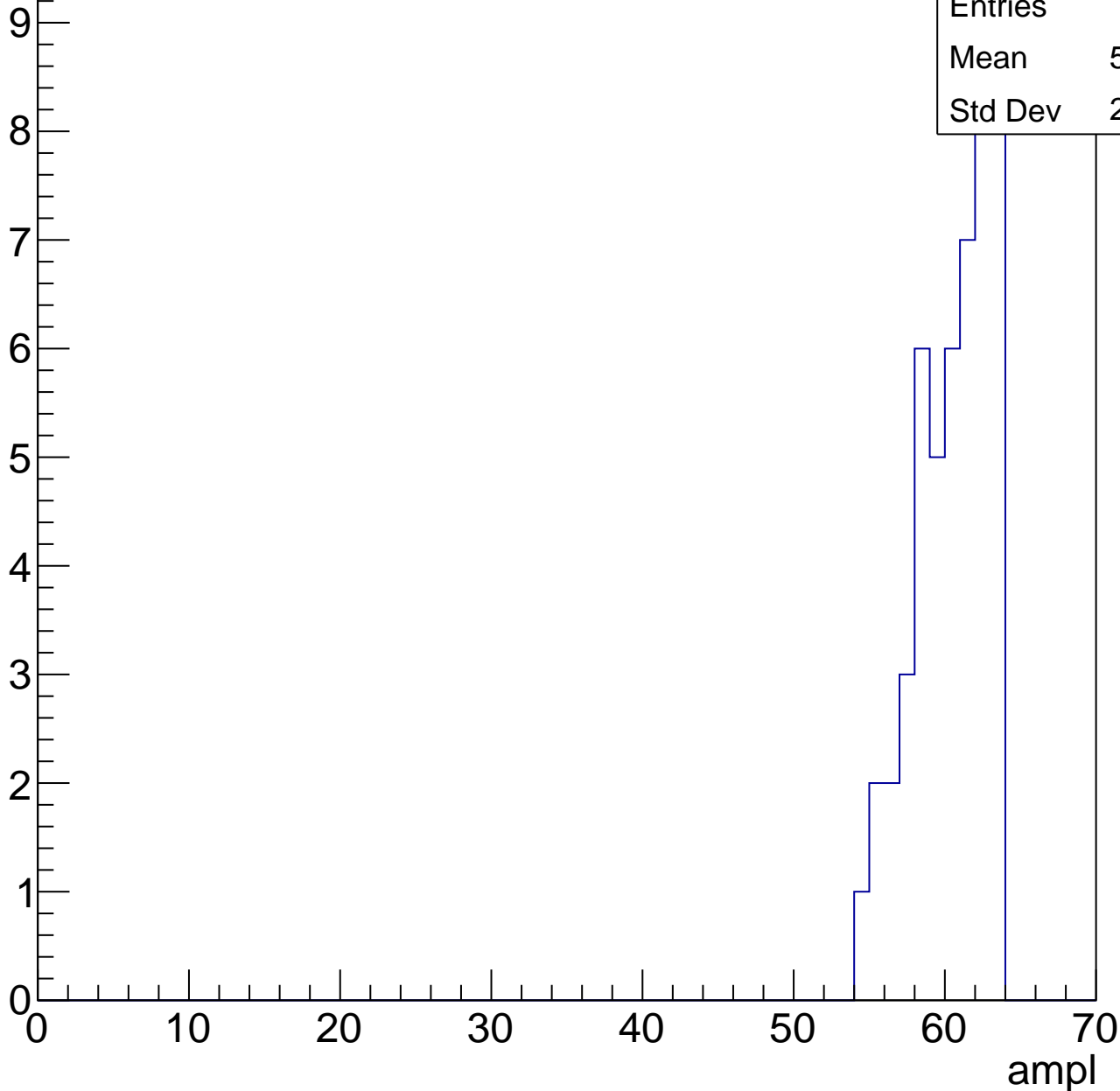


B1L003S, U16-ch5, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.98
Std Dev	2.428



B1L003S, U16-ch5, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	7
Mean	61
Std Dev	1.604

B1L003S, U16-ch5, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch6, adc0

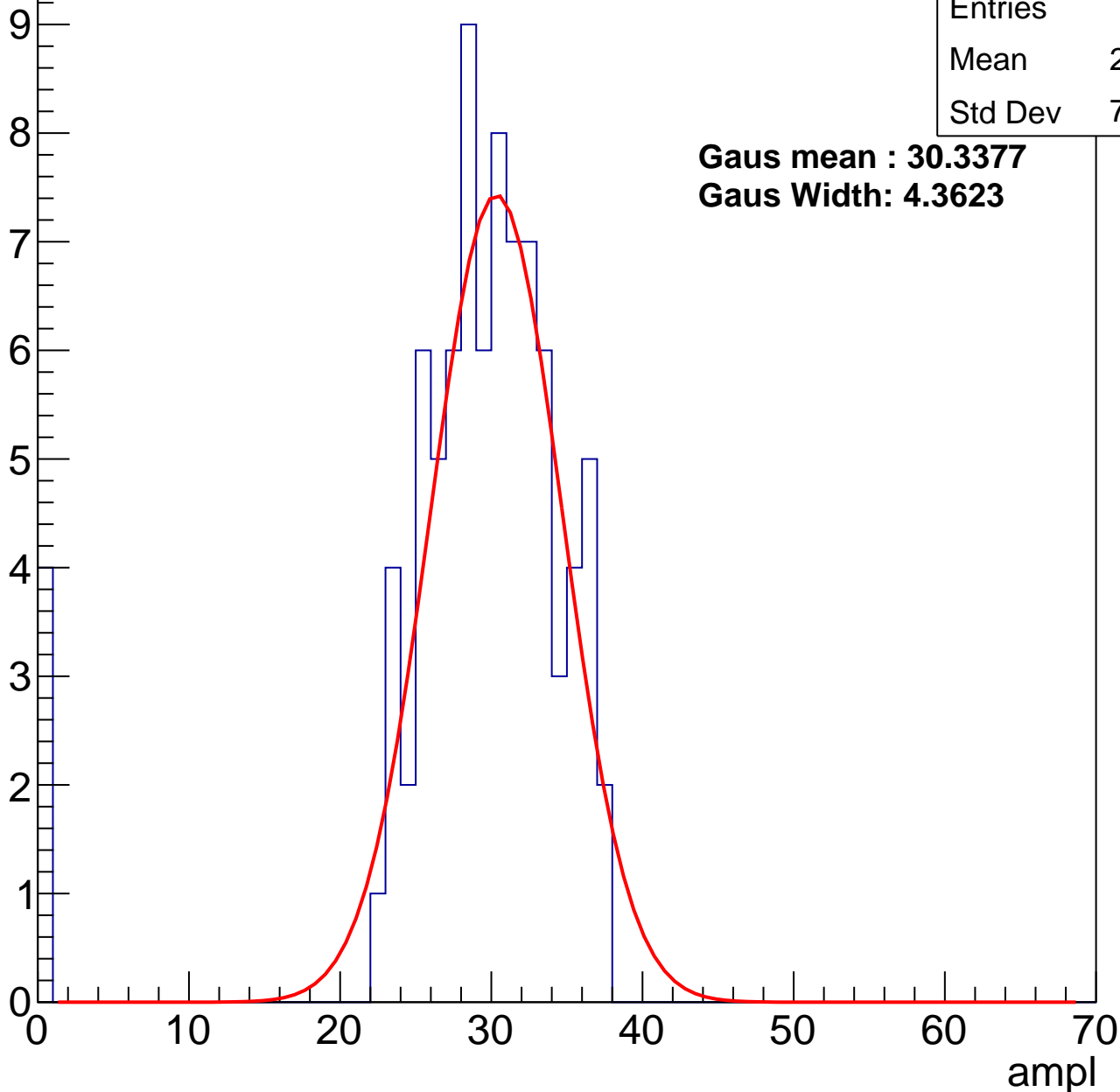
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	28.29
Std Dev	7.297

Gaus mean : 30.3377

Gaus Width: 4.3623



B1L003S, U16-ch6, adc1

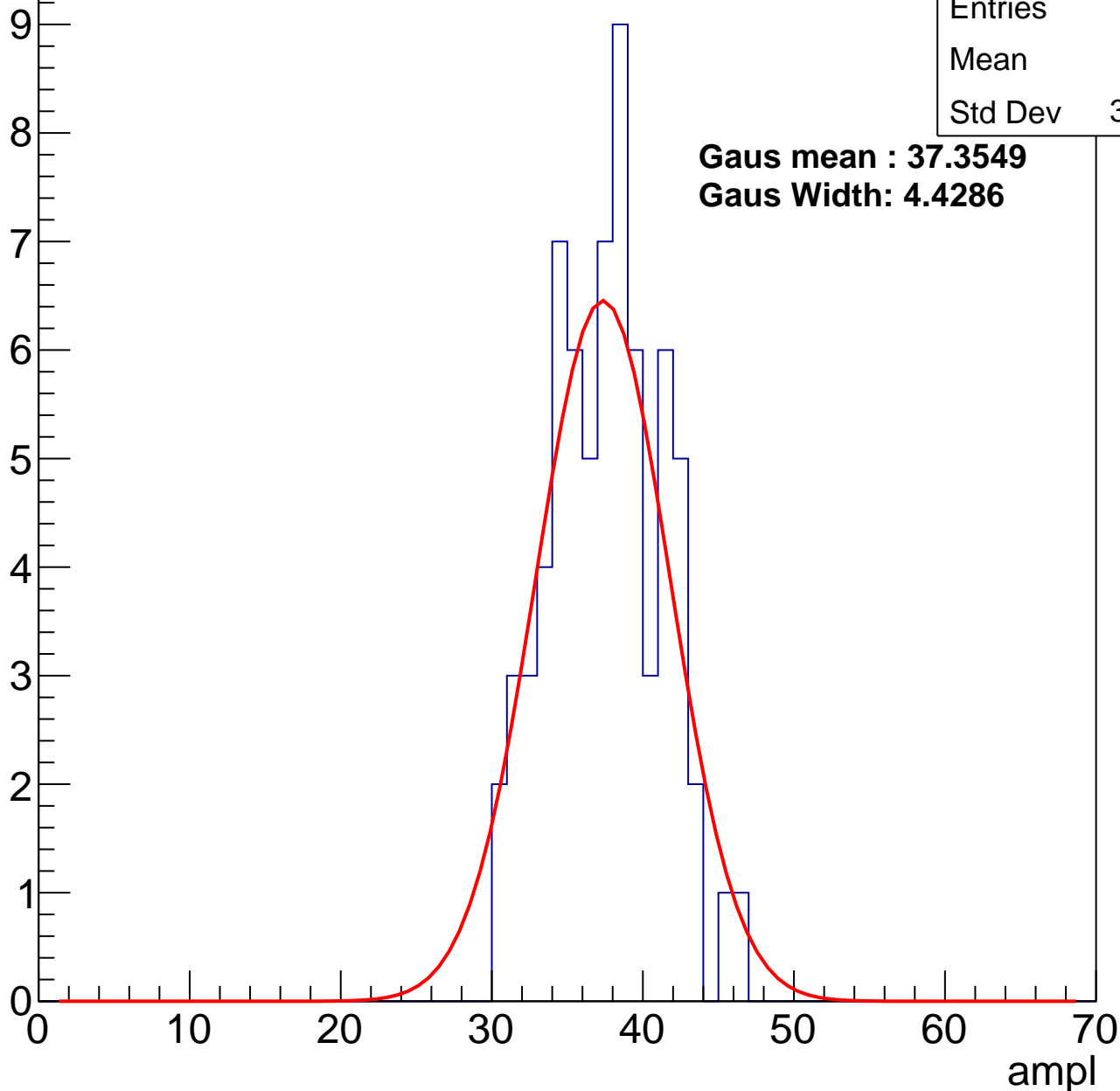
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.1
Std Dev	3.653

Gaus mean : 37.3549

Gaus Width: 4.4286



B1L003S, U16-ch6, adc2

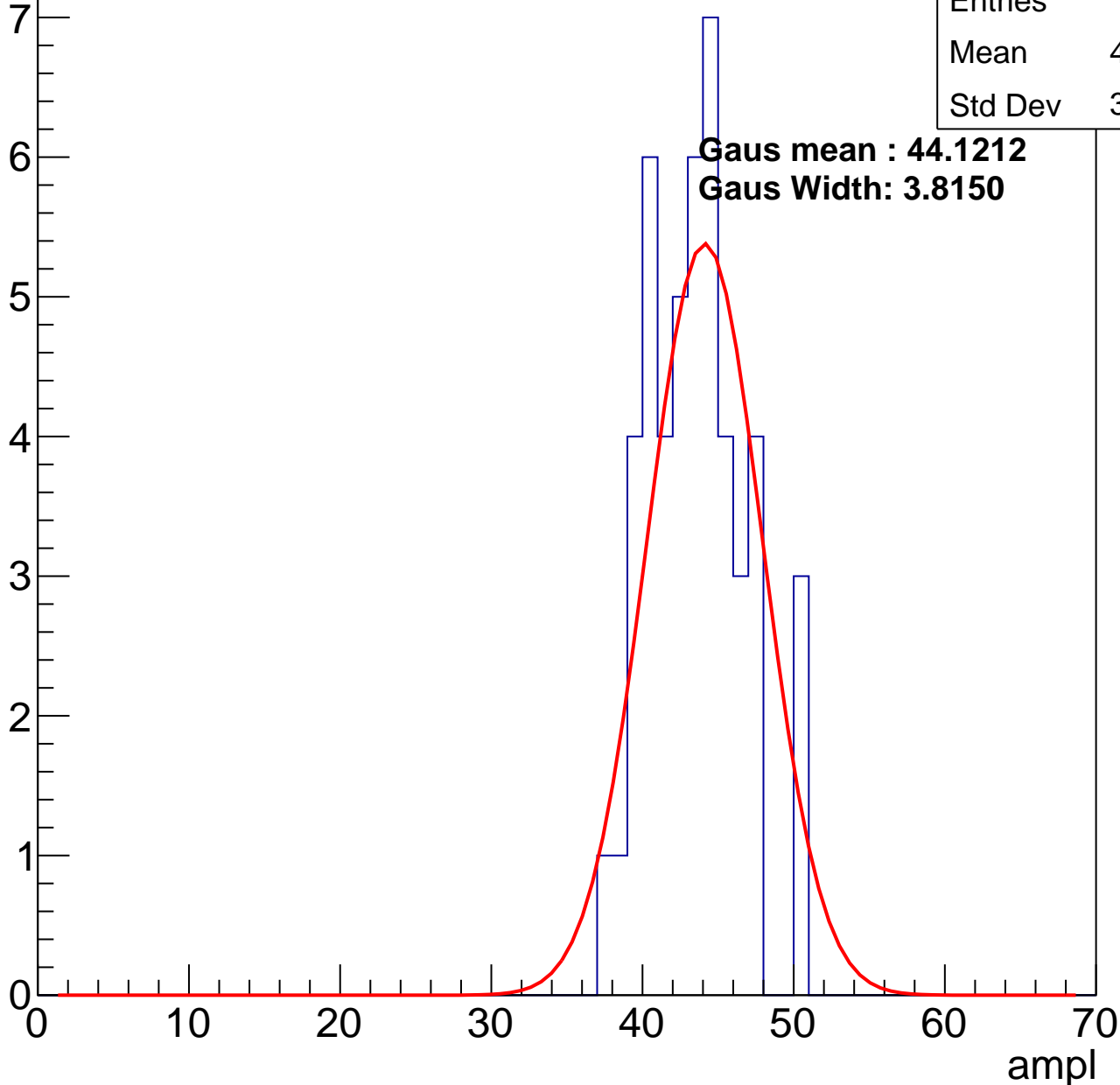
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	43.06
Std Dev	3.098

Gaus mean : 44.1212

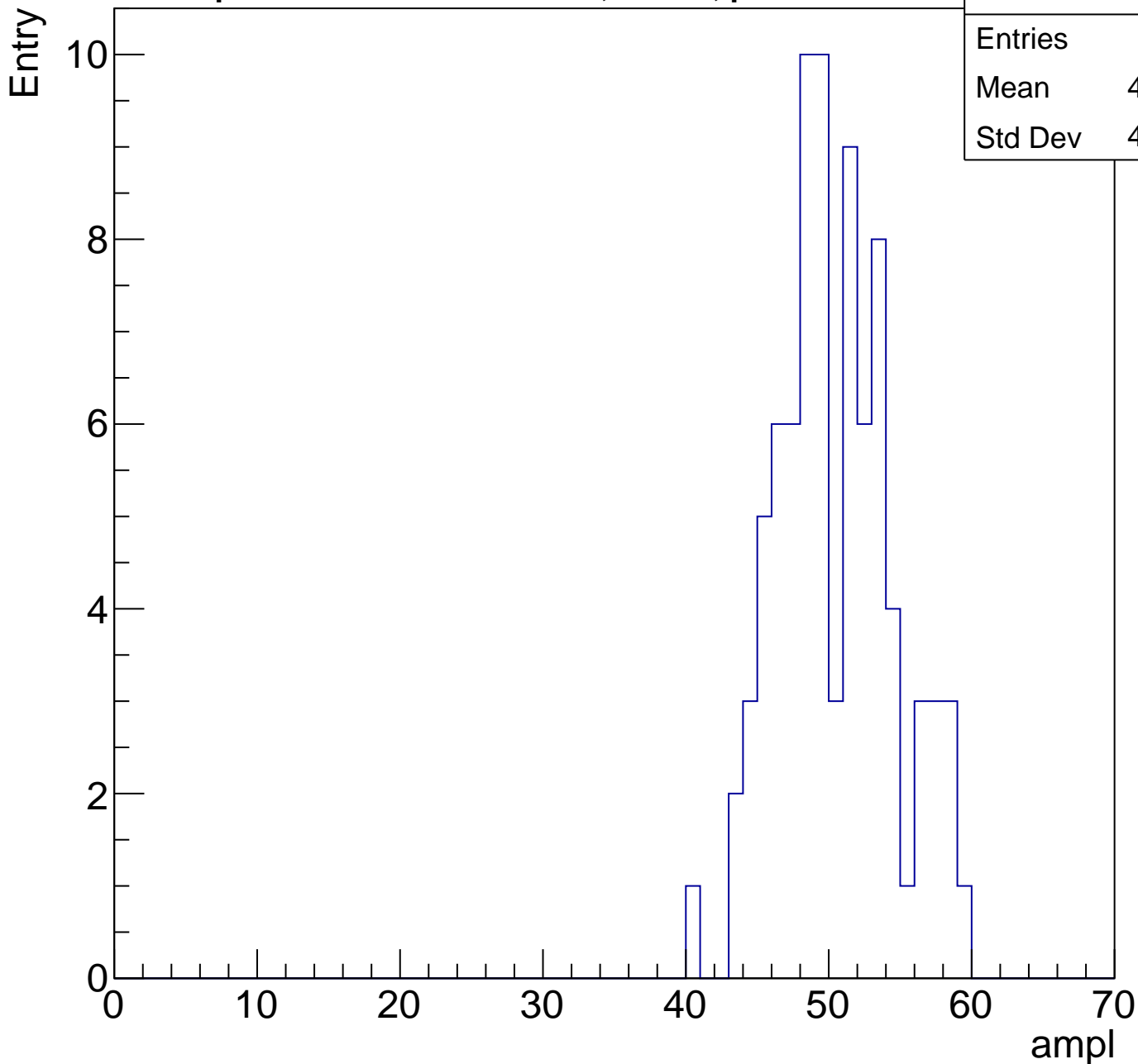
Gaus Width: 3.8150



B1L003S, U16-ch6, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	84
Mean	49.99
Std Dev	4.013

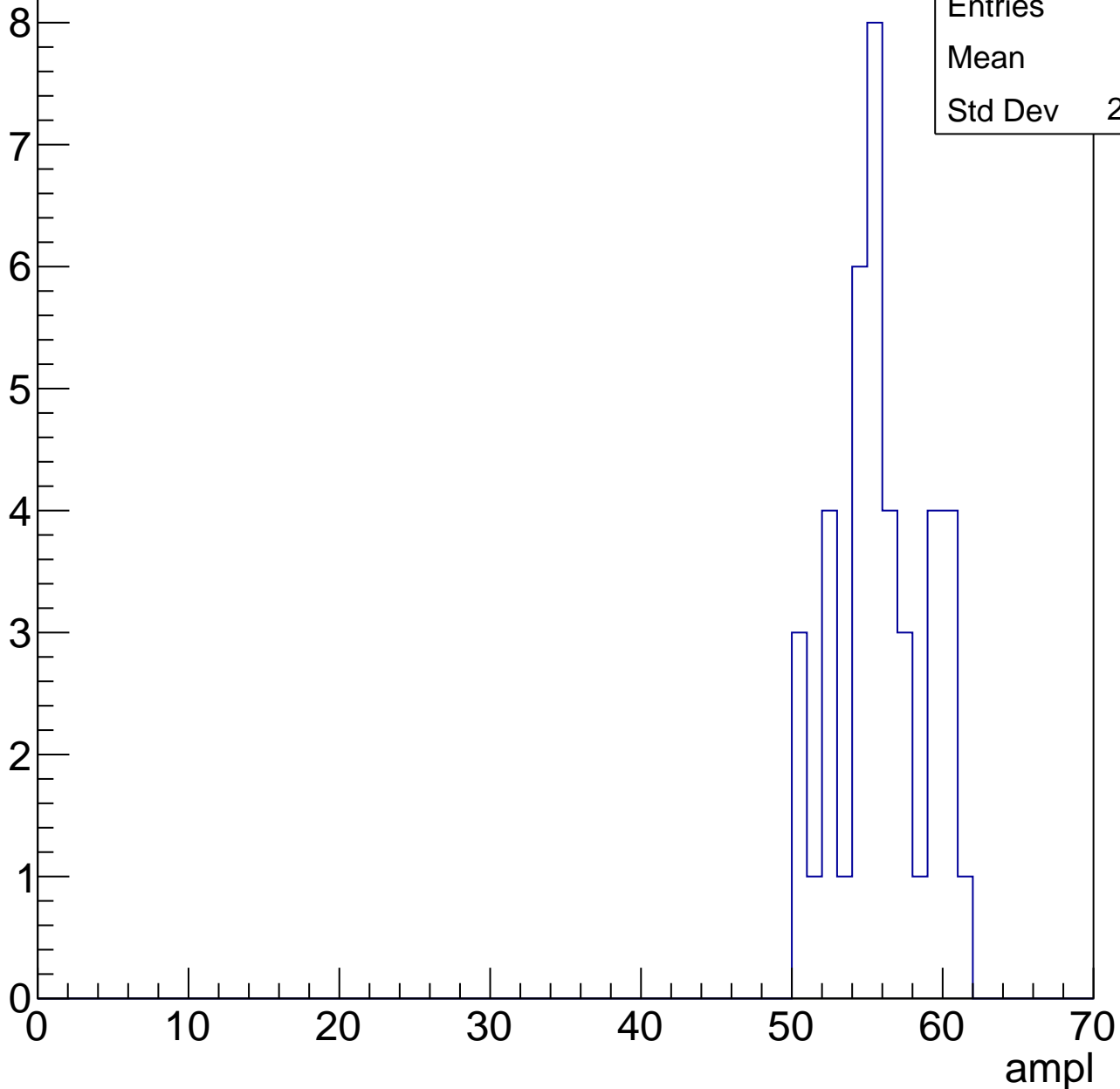


B1L003S, U16-ch6, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	55.4
Std Dev	2.982

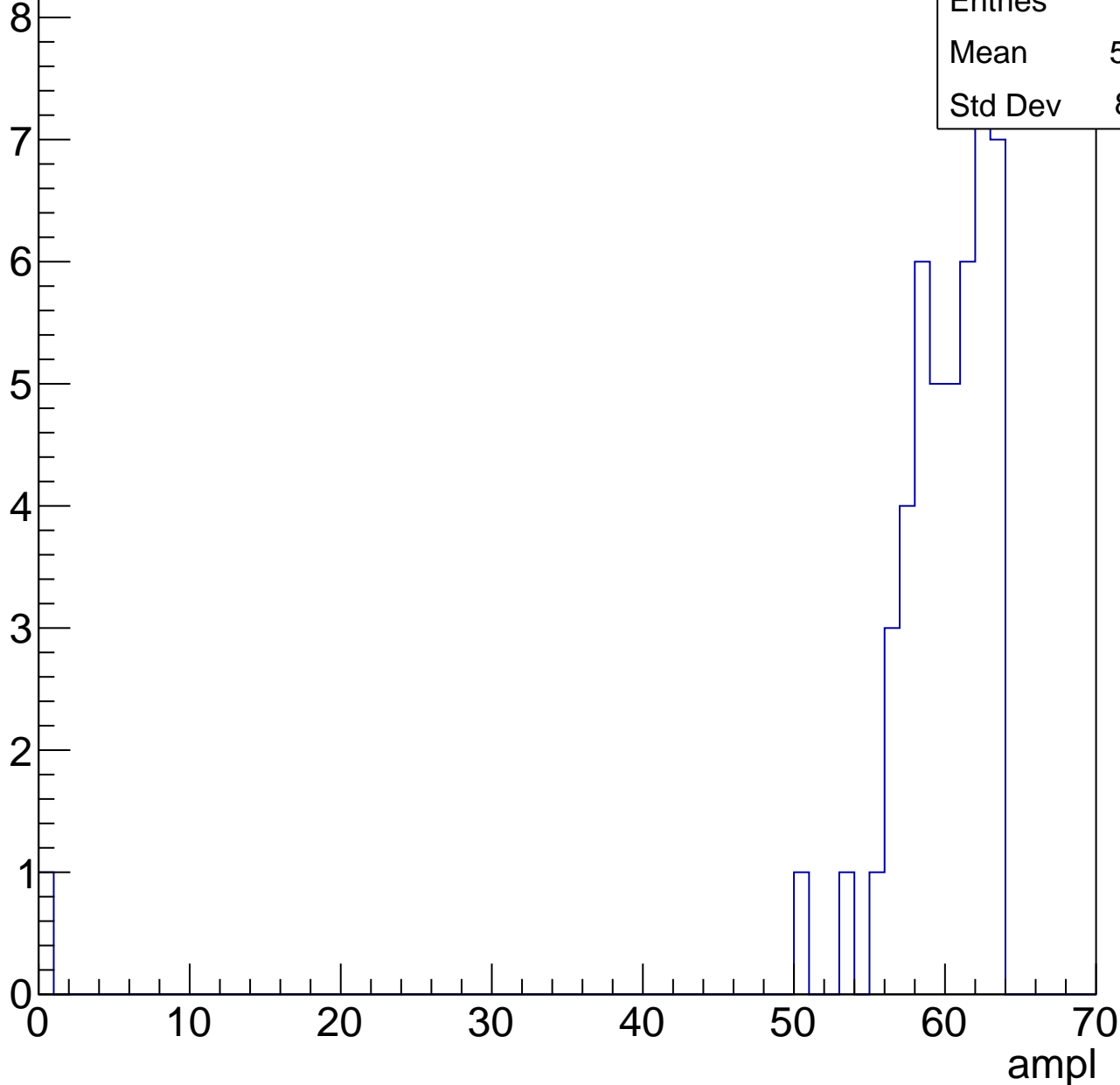


B1L003S, U16-ch6, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	58.33
Std Dev	8.961



B1L003S, U16-ch6, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.78

Std Dev

1.03

B1L003S, U16-ch6, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch7, adc0

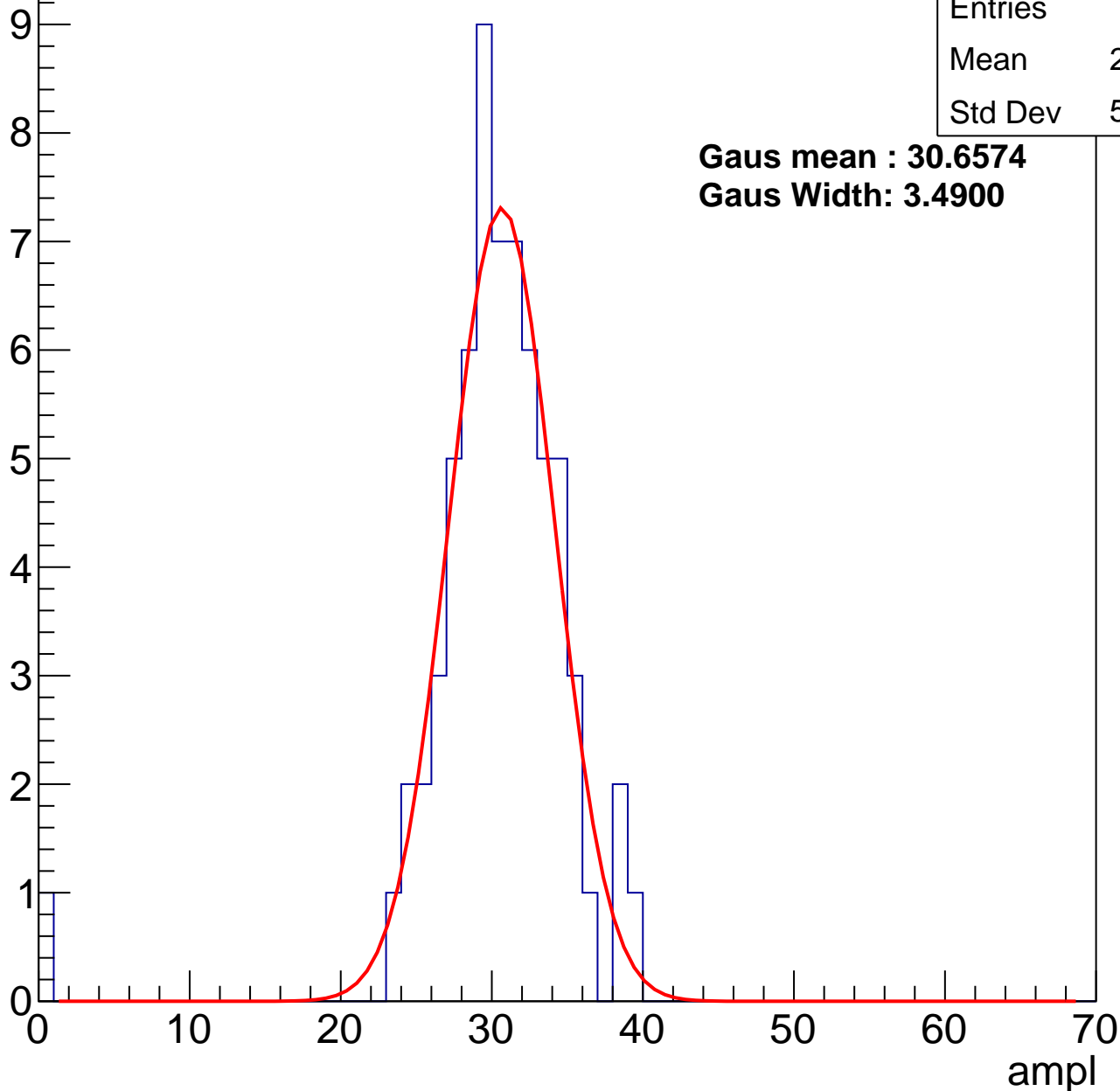
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.89
Std Dev	5.034

Gaus mean : 30.6574

Gaus Width: 3.4900



B1L003S, U16-ch7, adc1

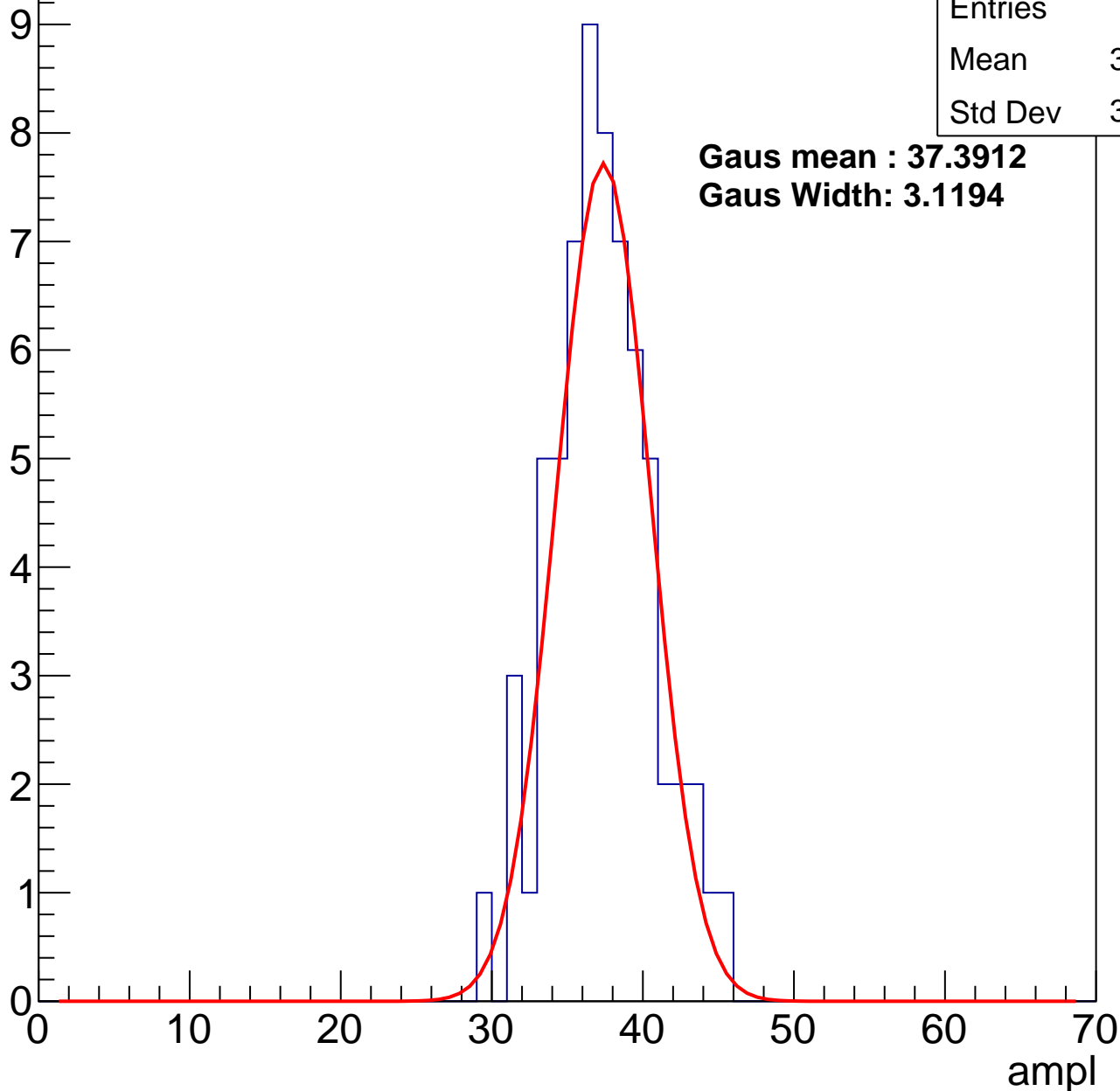
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.85
Std Dev	3.283

Gaus mean : 37.3912

Gaus Width: 3.1194



B1L003S, U16-ch7, adc2

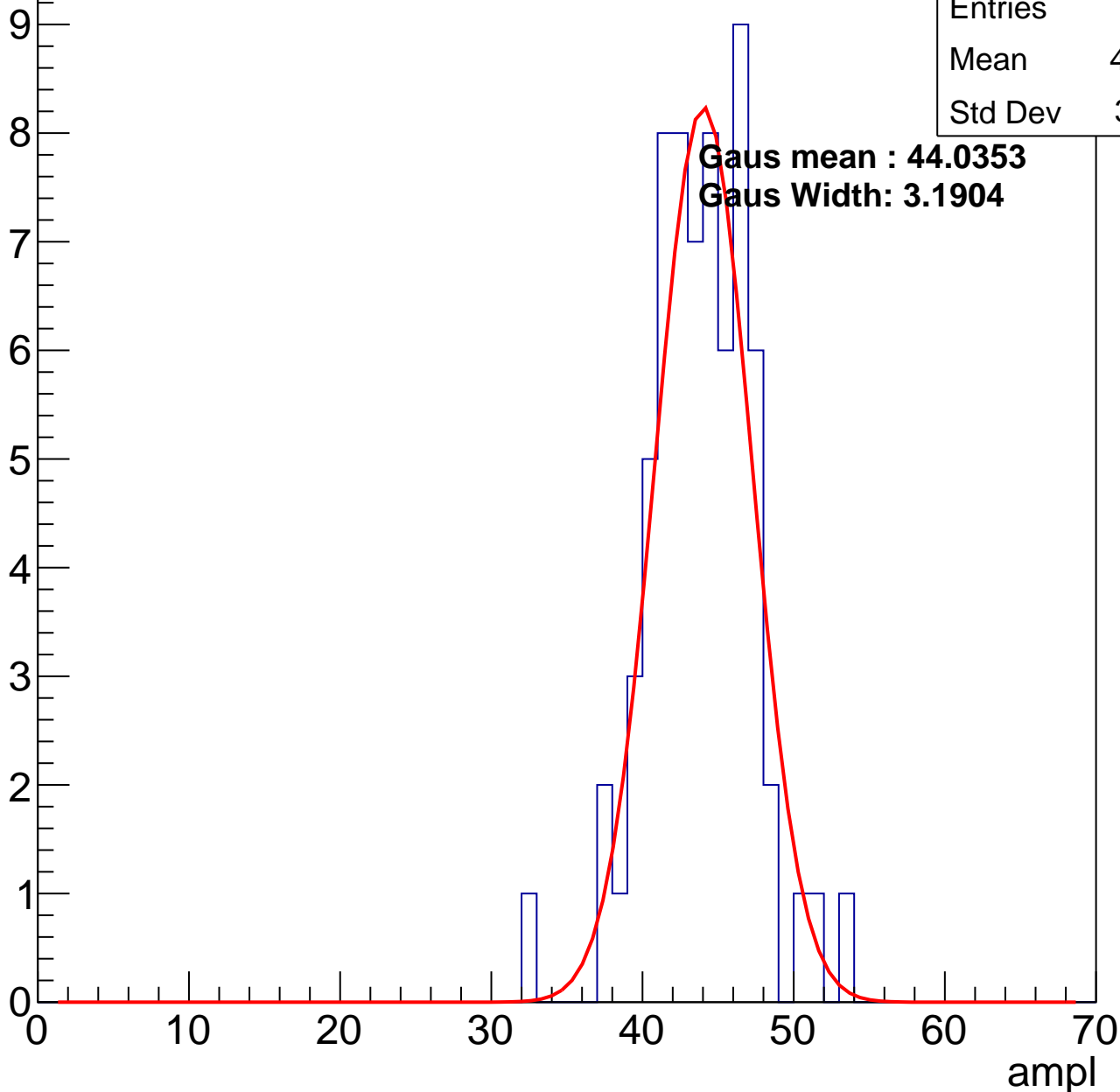
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.39
Std Dev	3.431

Gaus mean : 44.0353

Gaus Width: 3.1904

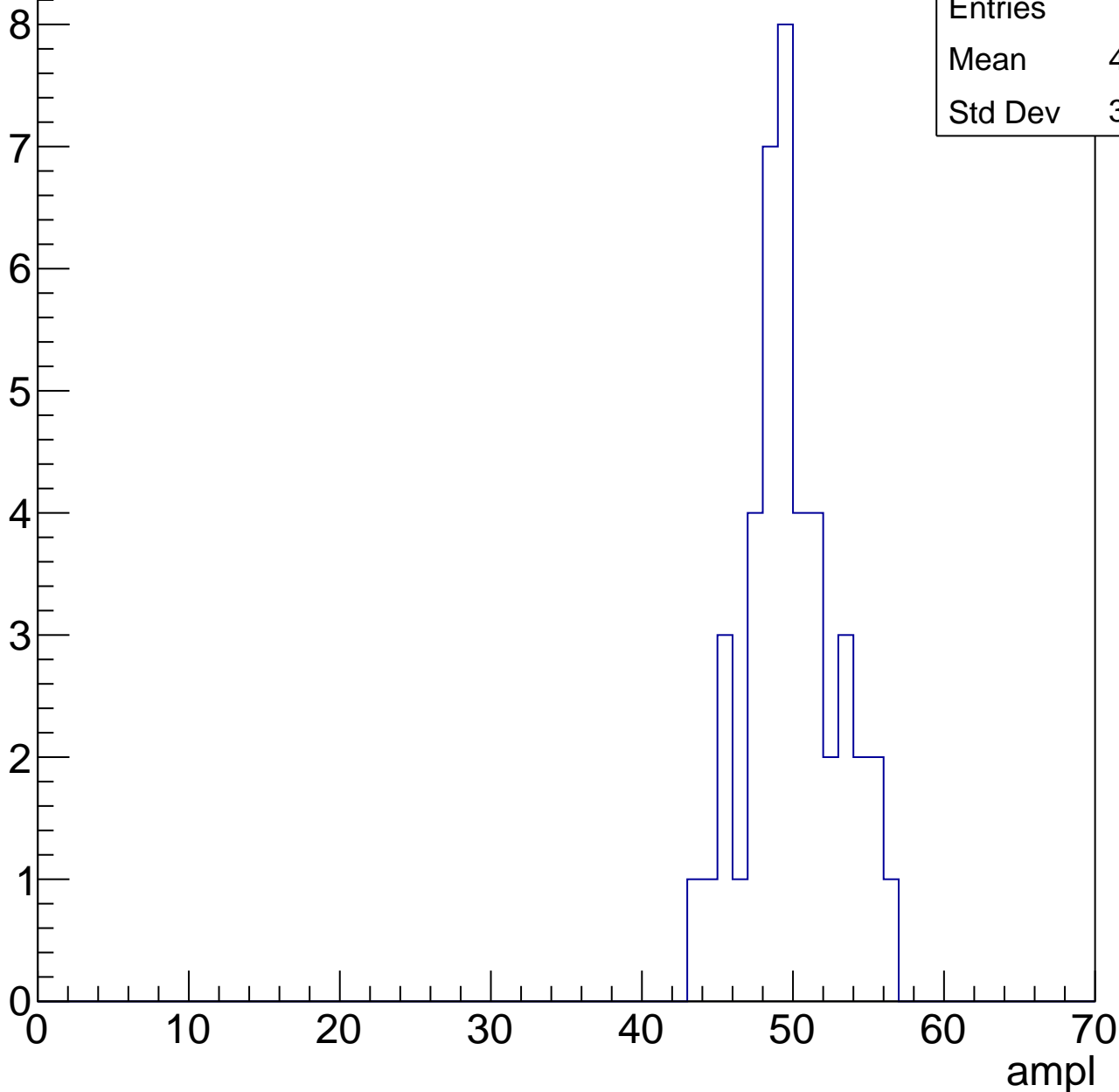


B1L003S, U16-ch7, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	49.42
Std Dev	3.013

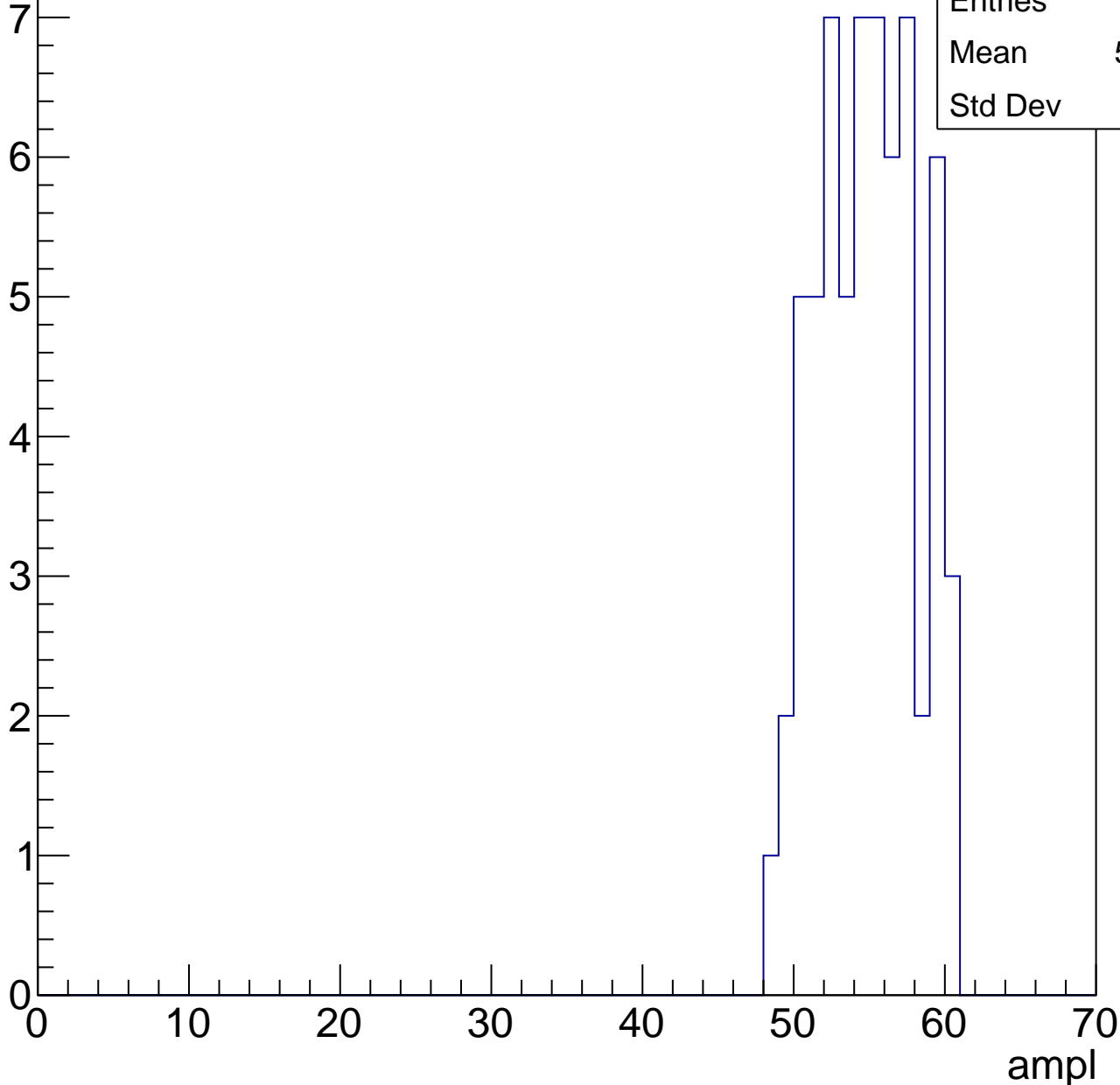


B1L003S, U16-ch7, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	54.41
Std Dev	3.13



B1L003S, U16-ch7, adc5

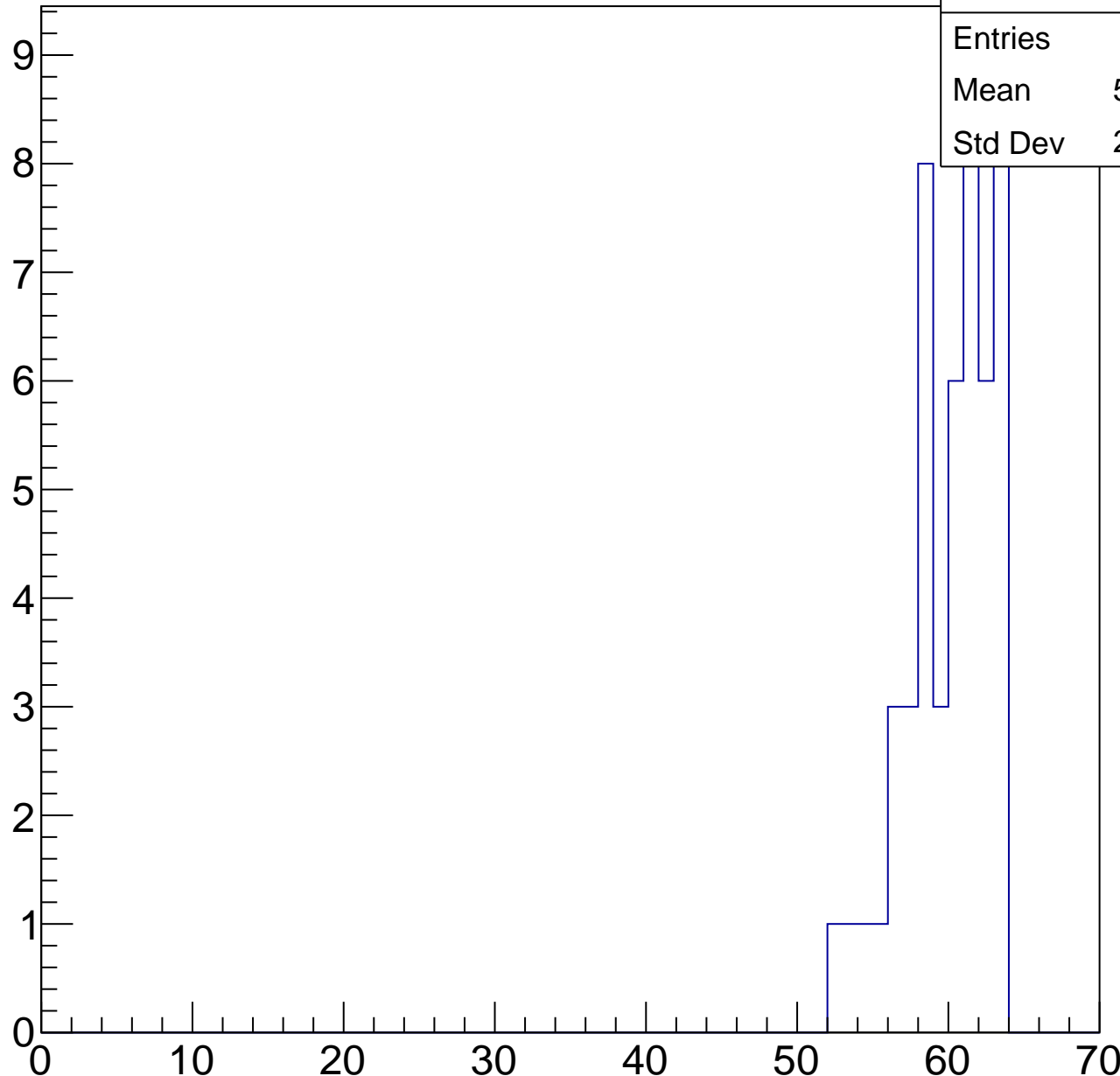
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.62
Std Dev	2.792

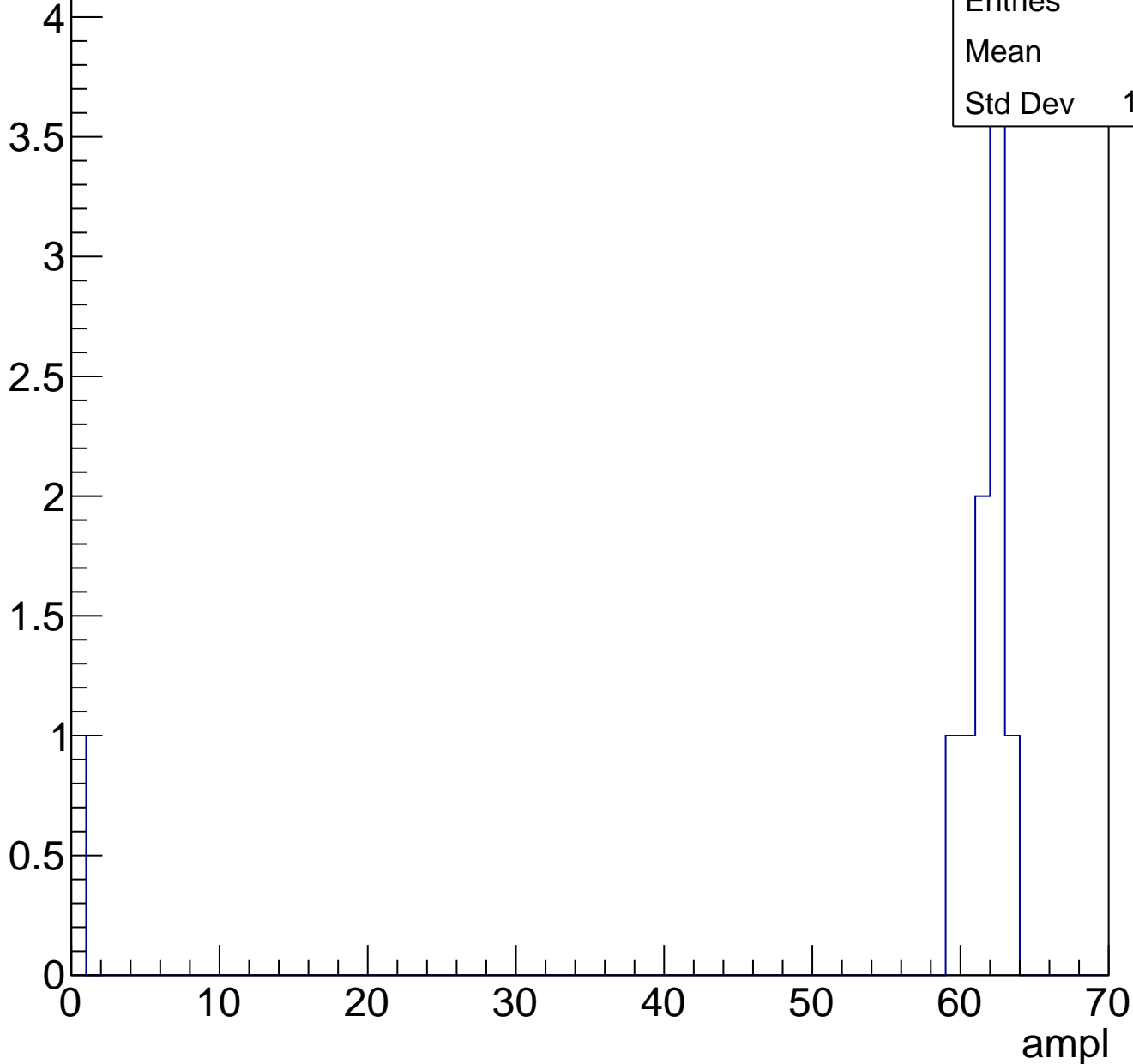
ampl



B1L003S, U16-ch7, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch7, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch8, adc0

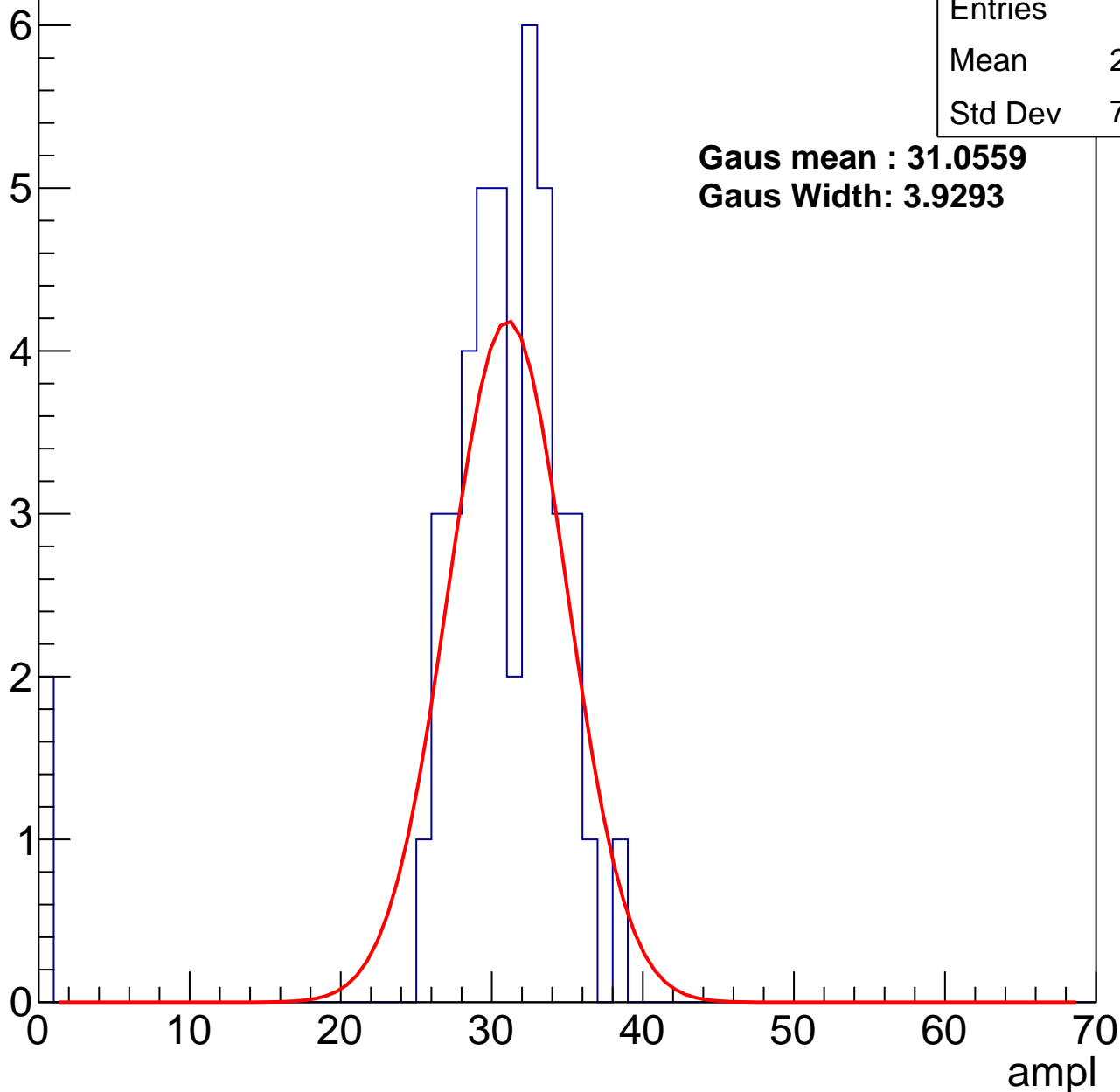
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	29.34
Std Dev	7.064

Gaus mean : 31.0559

Gaus Width: 3.9293



B1L003S, U16-ch8, adc1

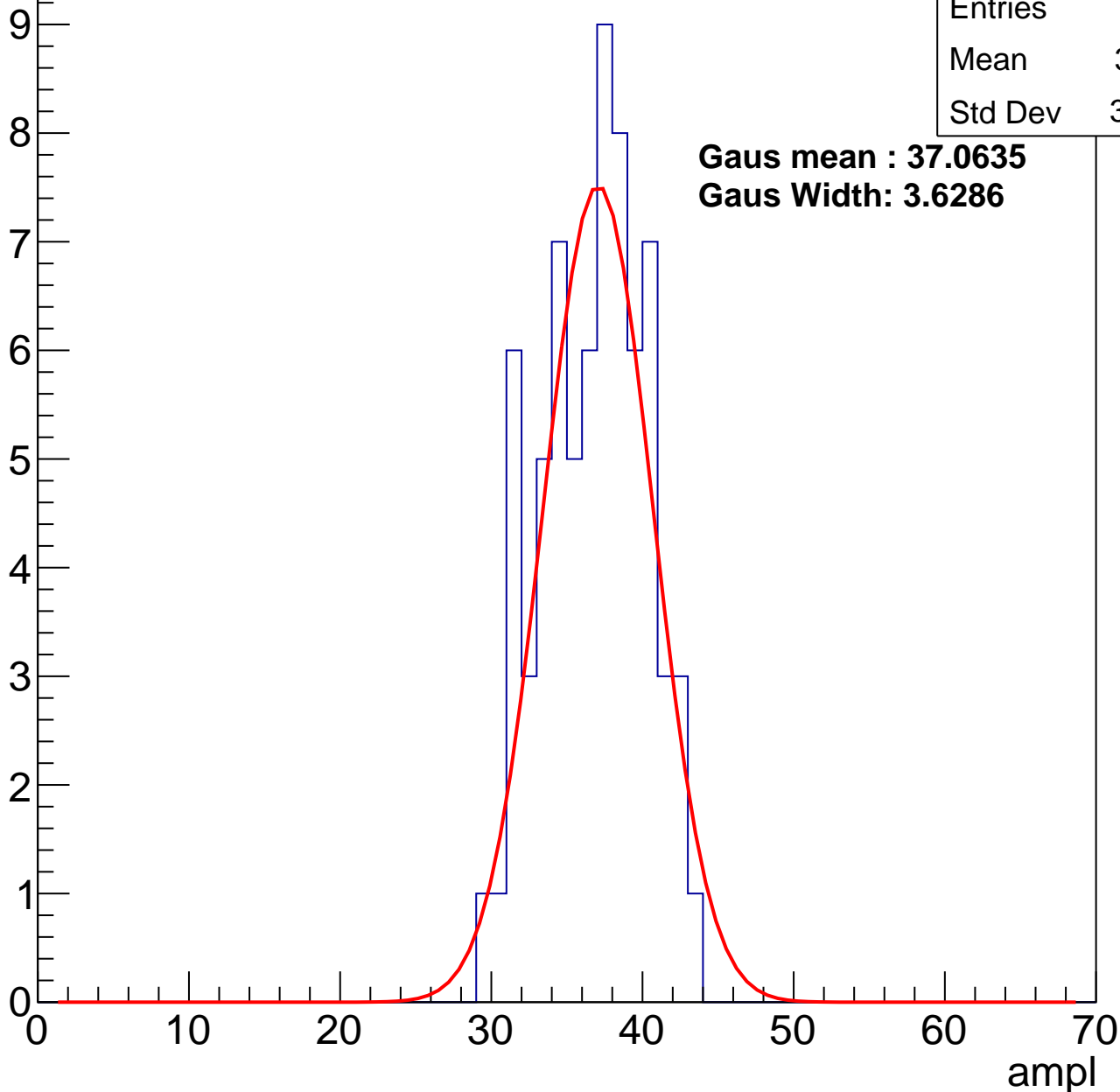
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.31
Std Dev	3.342

Gaus mean : 37.0635

Gaus Width: 3.6286



B1L003S, U16-ch8, adc2

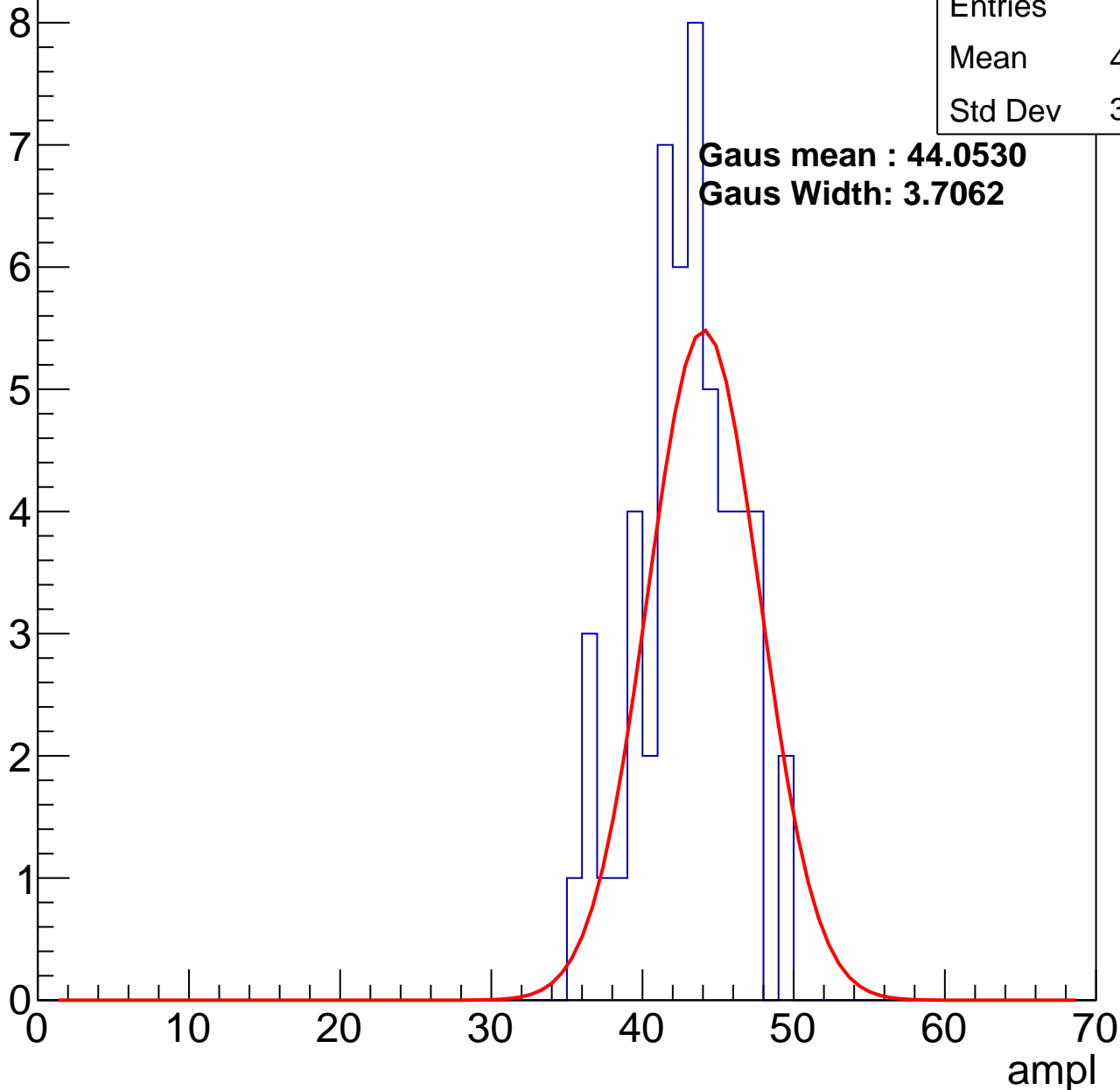
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.44
Std Dev	3.296

Gaus mean : 44.0530

Gaus Width: 3.7062

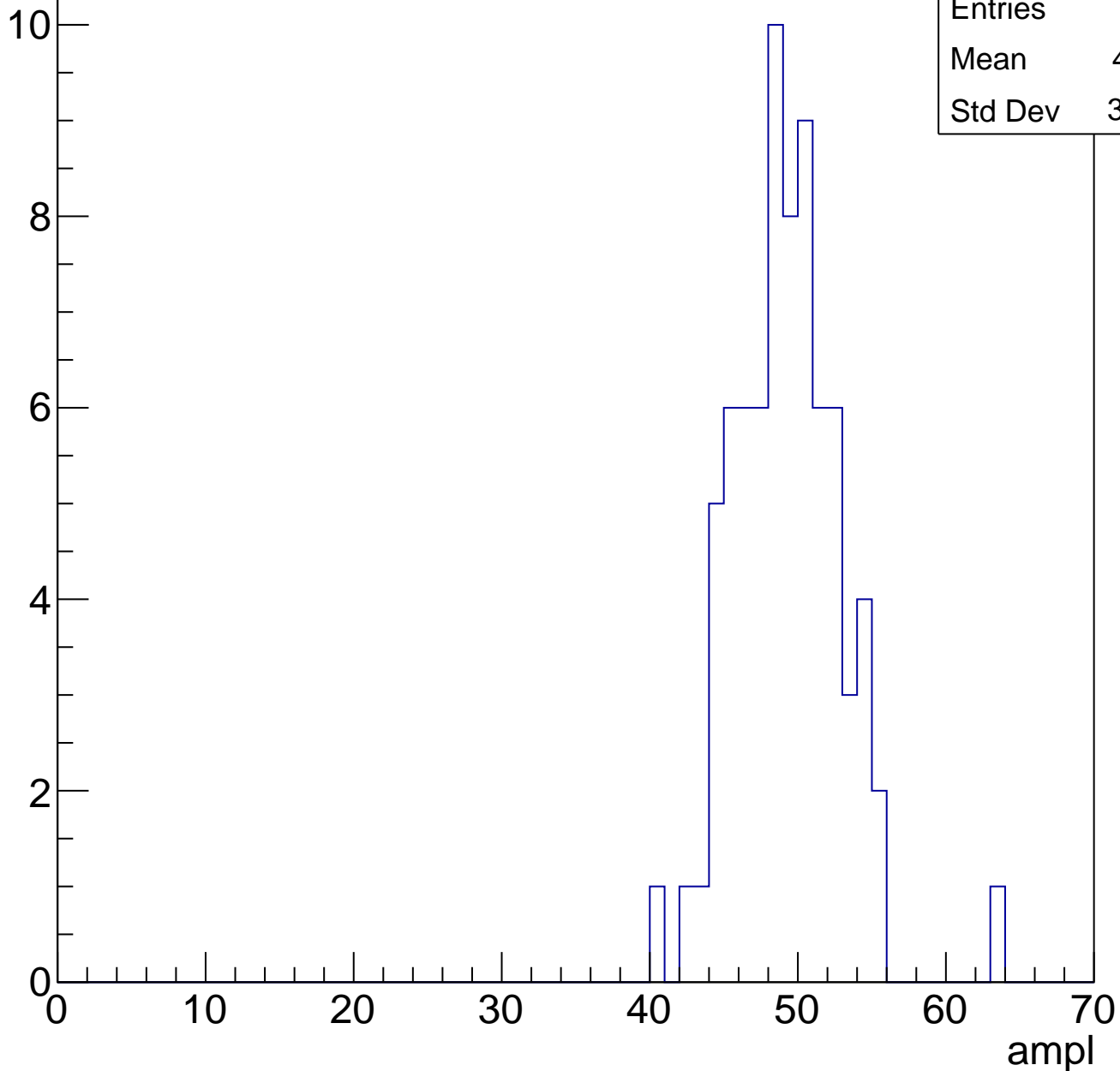


B1L003S, U16-ch8, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	48.81
Std Dev	3.606

Entry

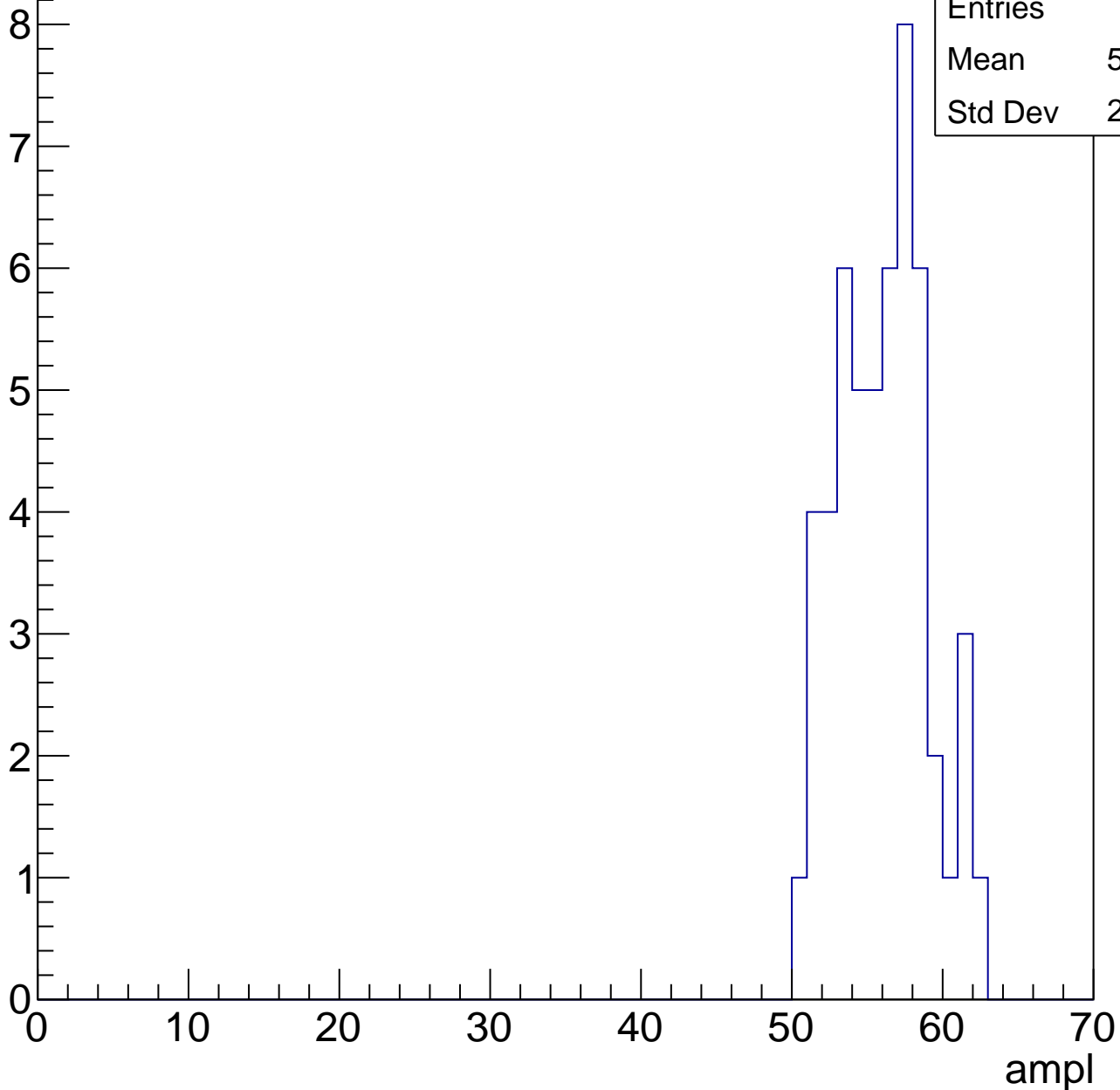


B1L003S, U16-ch8, adc4

calib_packv5_042523_0143.root, FC#13, port D2

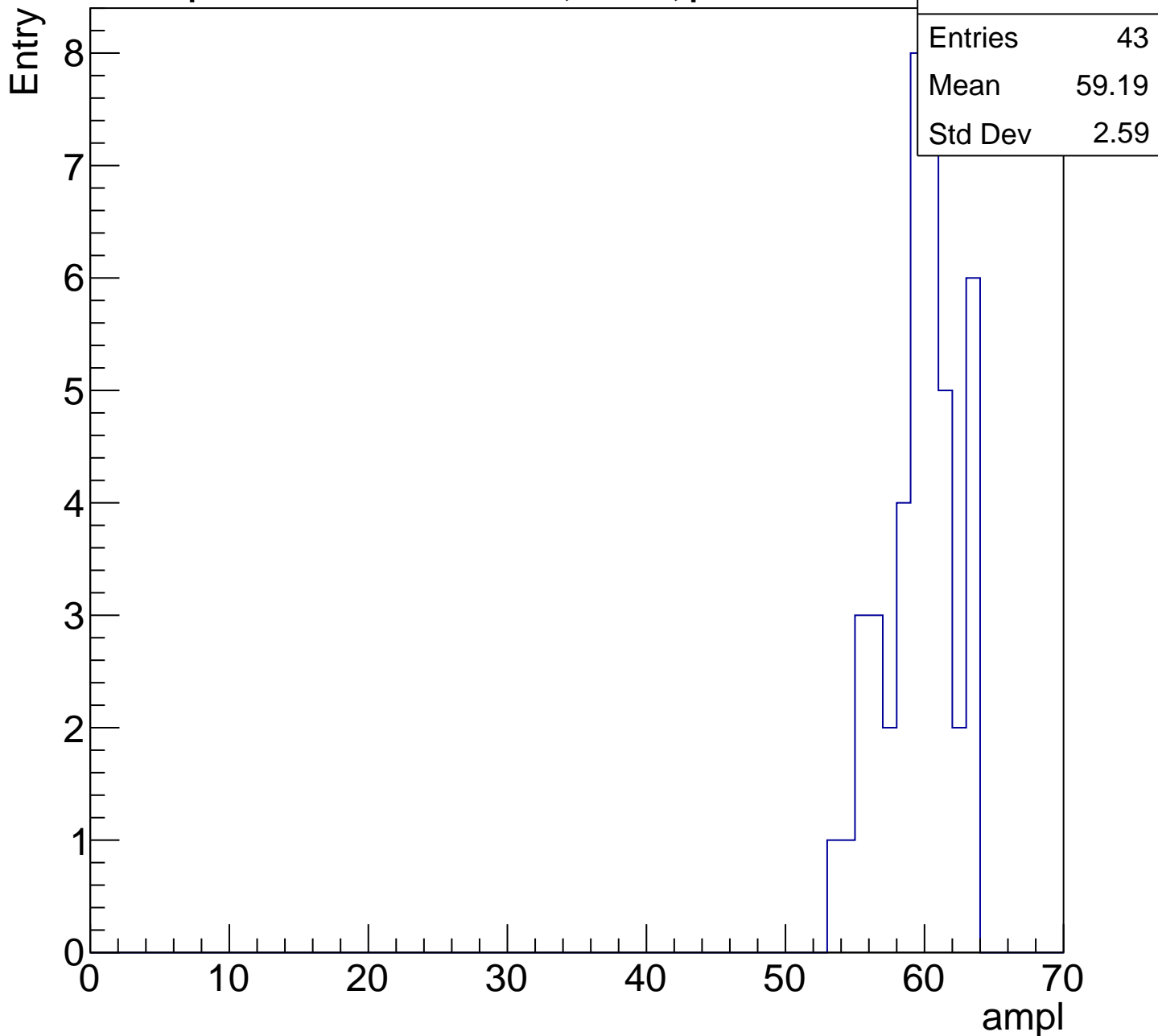
Entry

Entries	52
Mean	55.54
Std Dev	2.925



B1L003S, U16-ch8, adc5

calib_packv5_042523_0143.root, FC#13, port D2

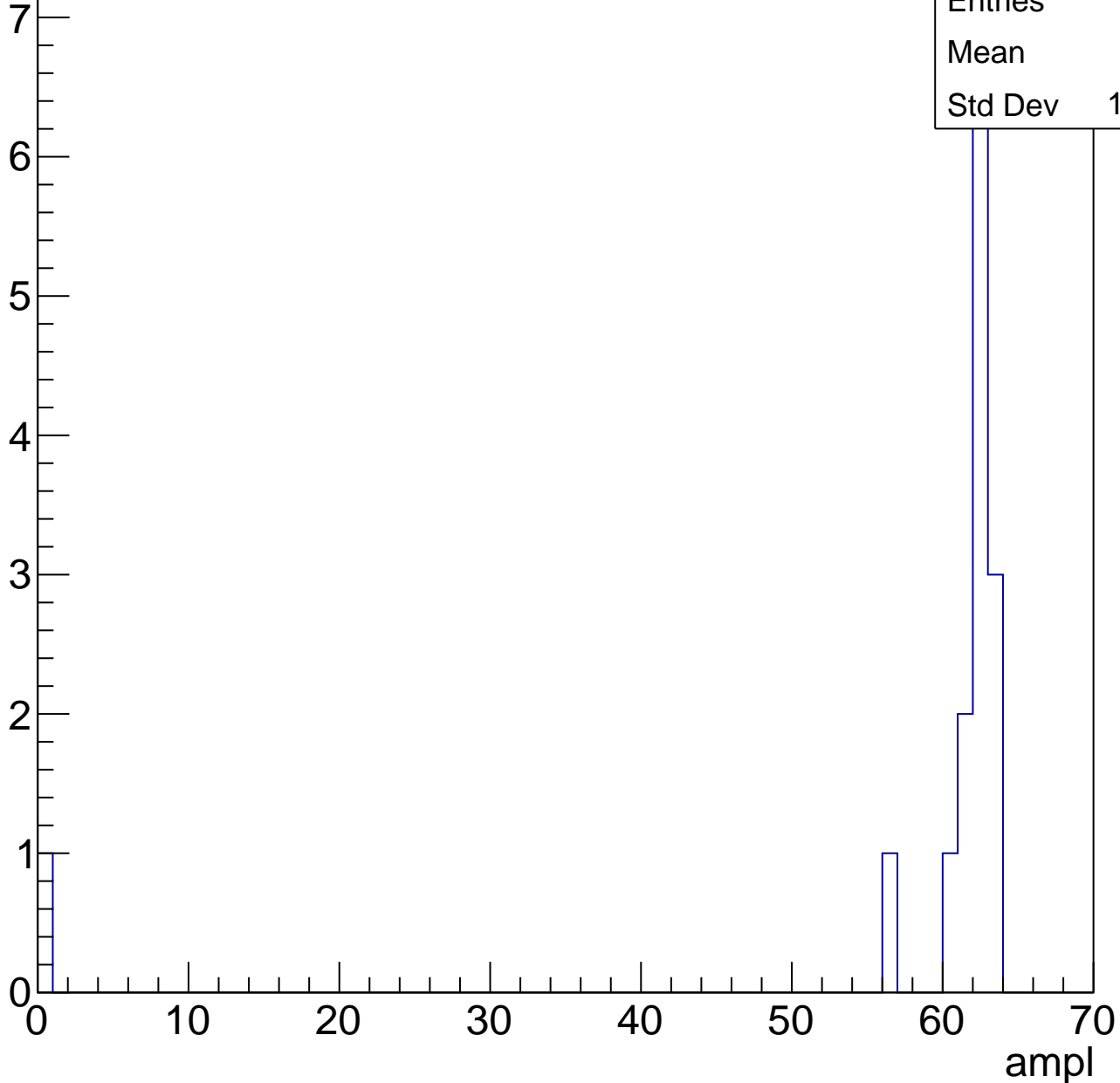


B1L003S, U16-ch8, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

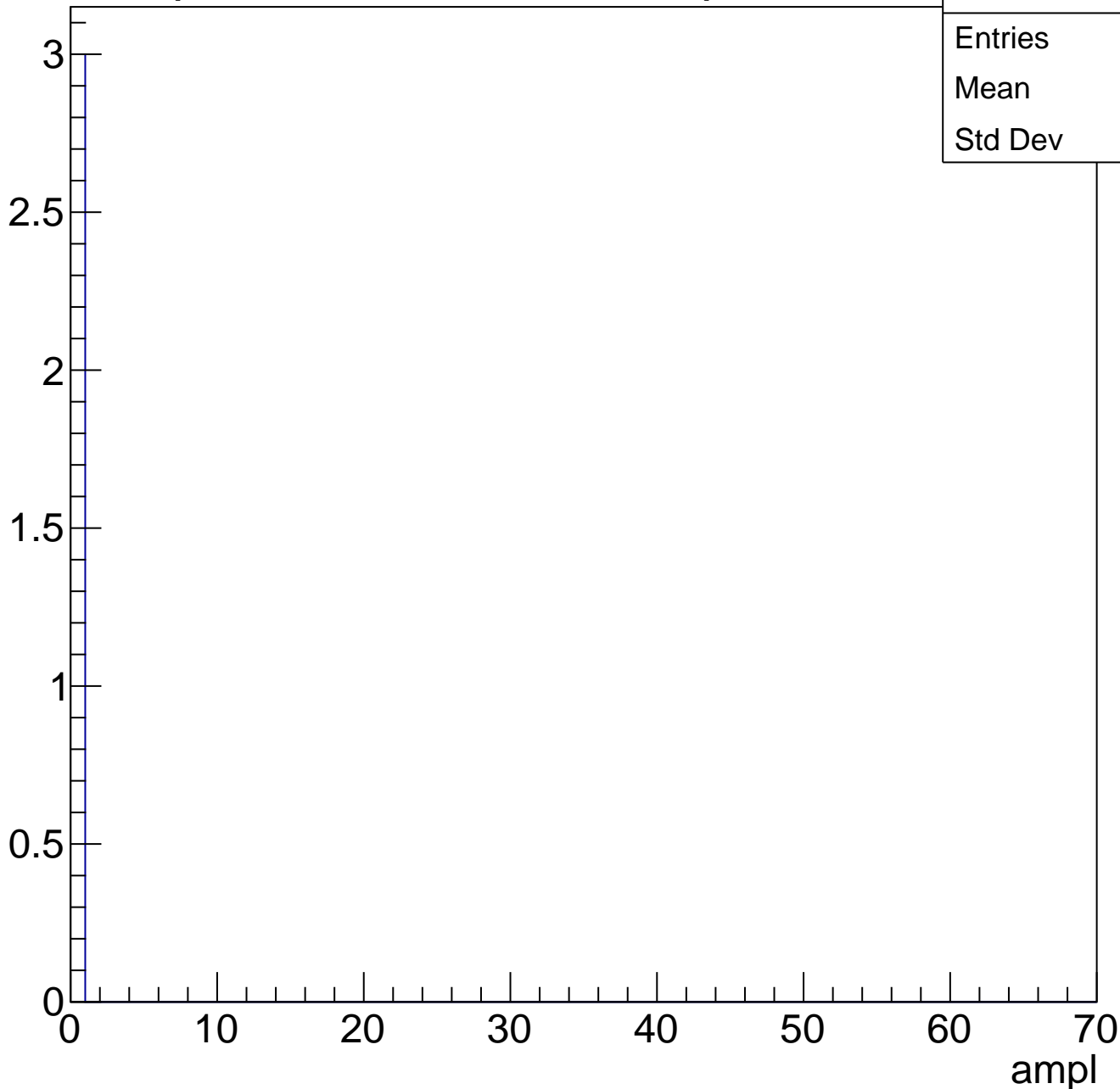
Entries	15
Mean	57.4
Std Dev	15.43



B1L003S, U16-ch8, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U16-ch9, adc0

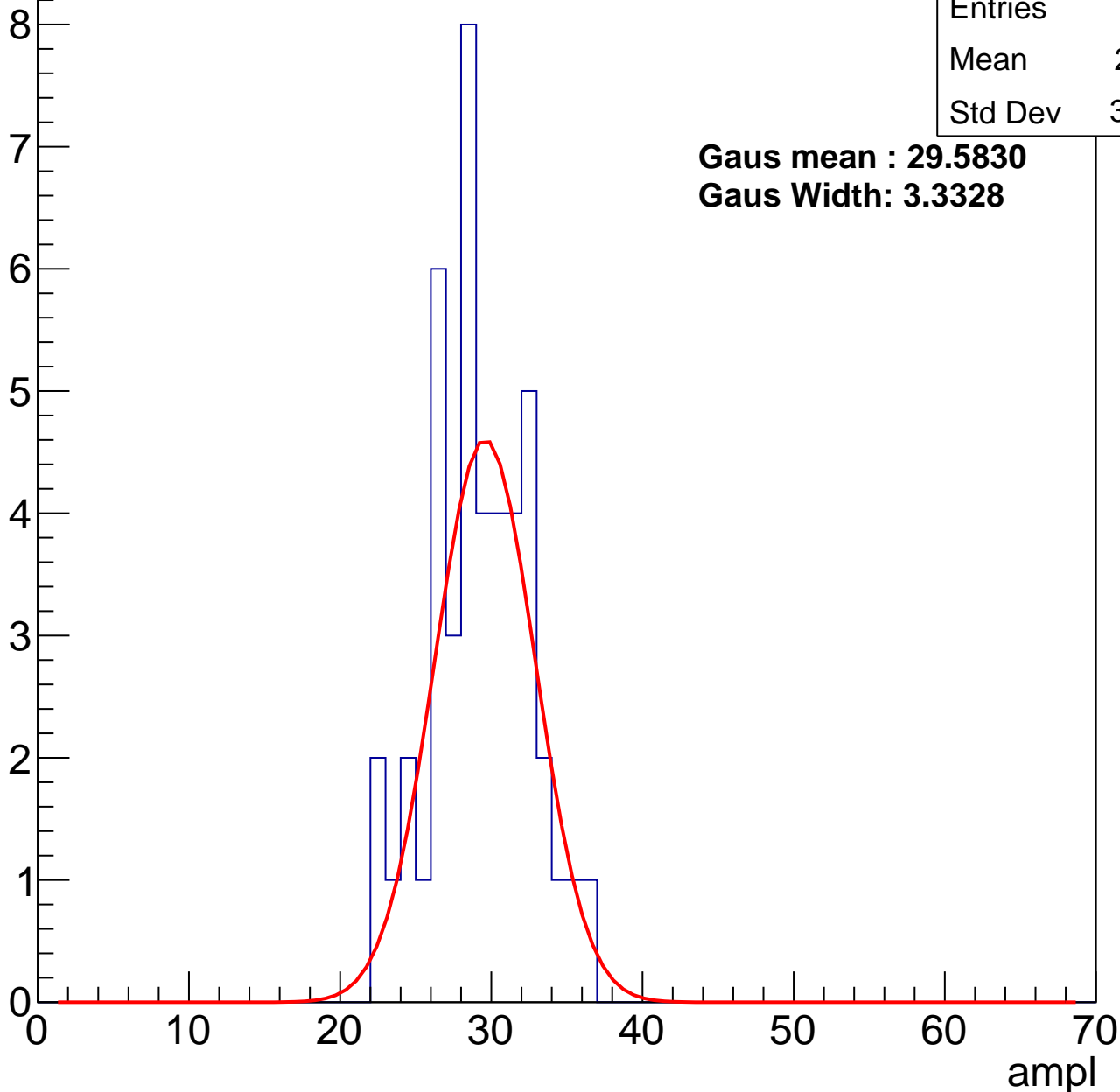
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	28.71
Std Dev	3.257

Gaus mean : 29.5830

Gaus Width: 3.3328



B1L003S, U16-ch9, adc1

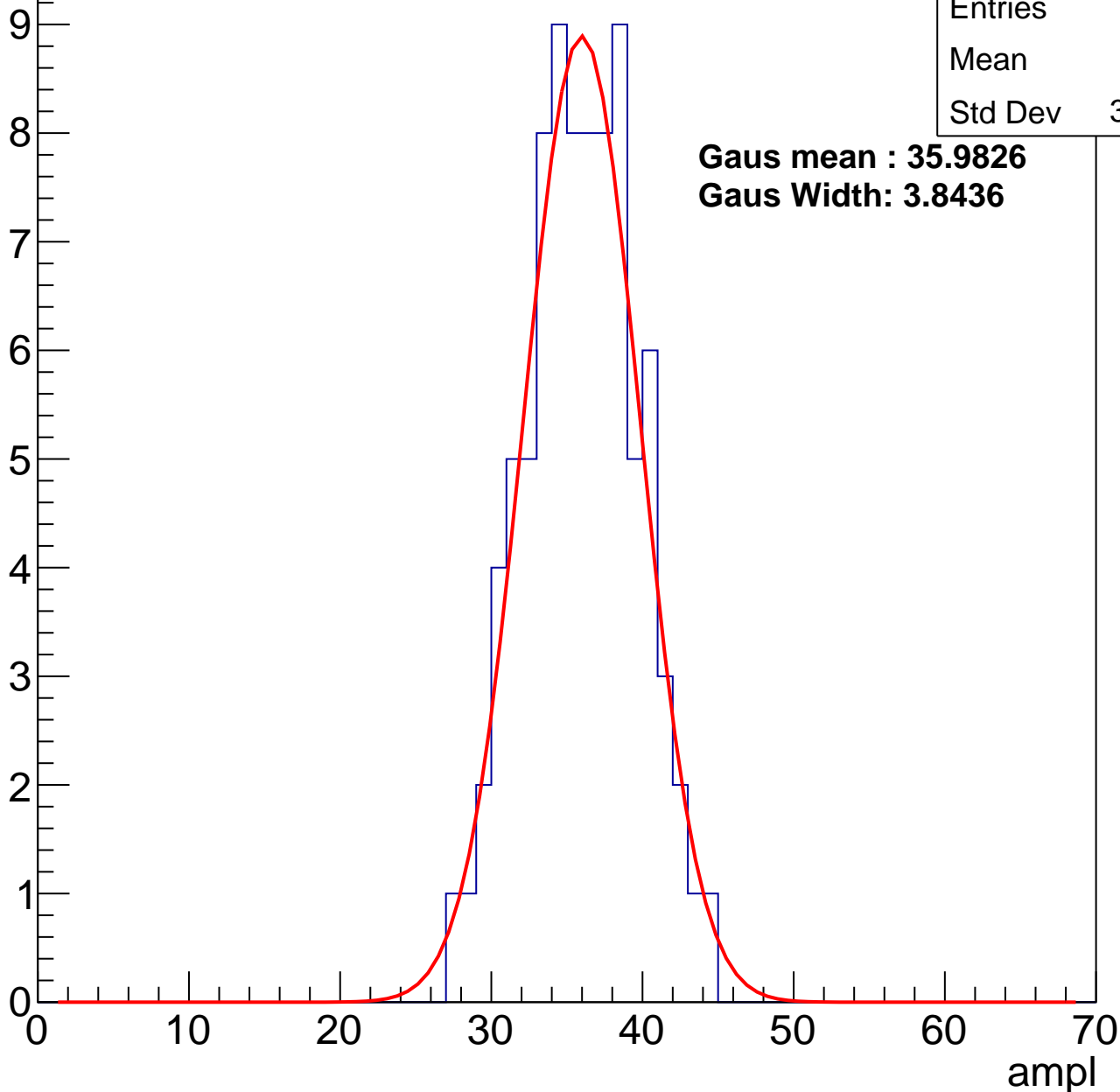
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	35.5
Std Dev	3.627

Gaus mean : 35.9826

Gaus Width: 3.8436



B1L003S, U16-ch9, adc2

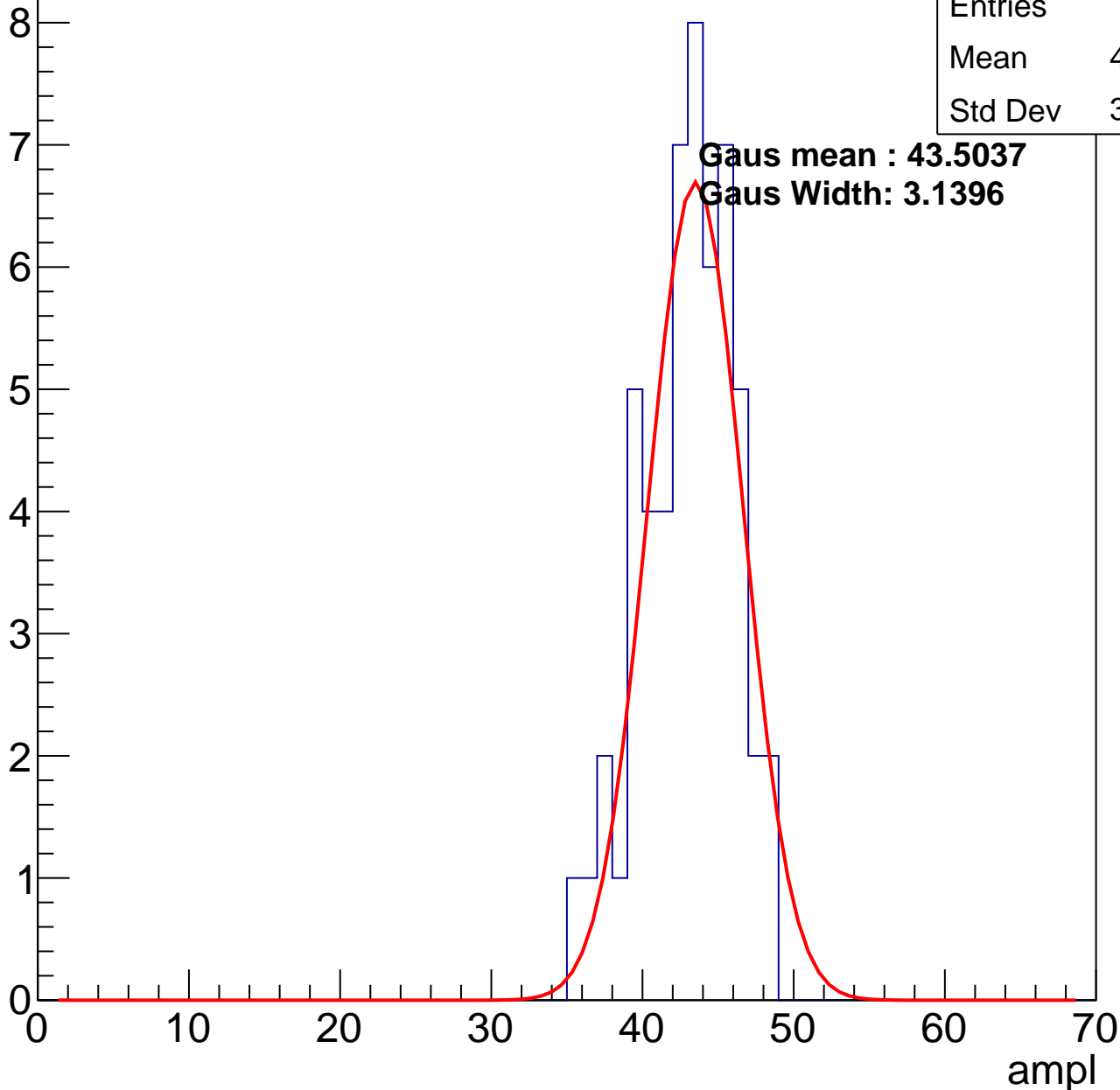
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.53
Std Dev	3.008

Gaus mean : 43.5037

Gaus Width: 3.1396

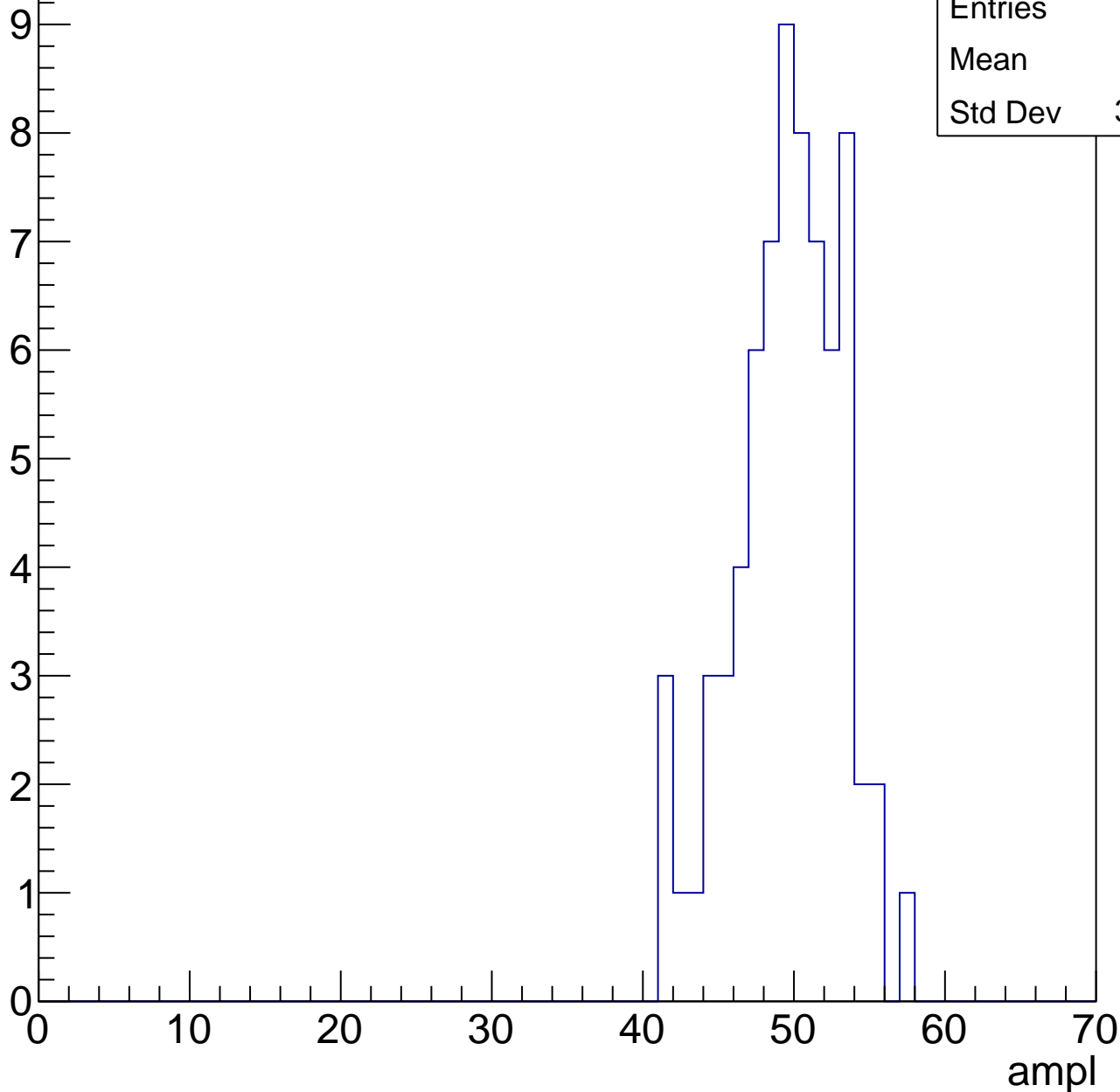


B1L003S, U16-ch9, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.1
Std Dev	3.501

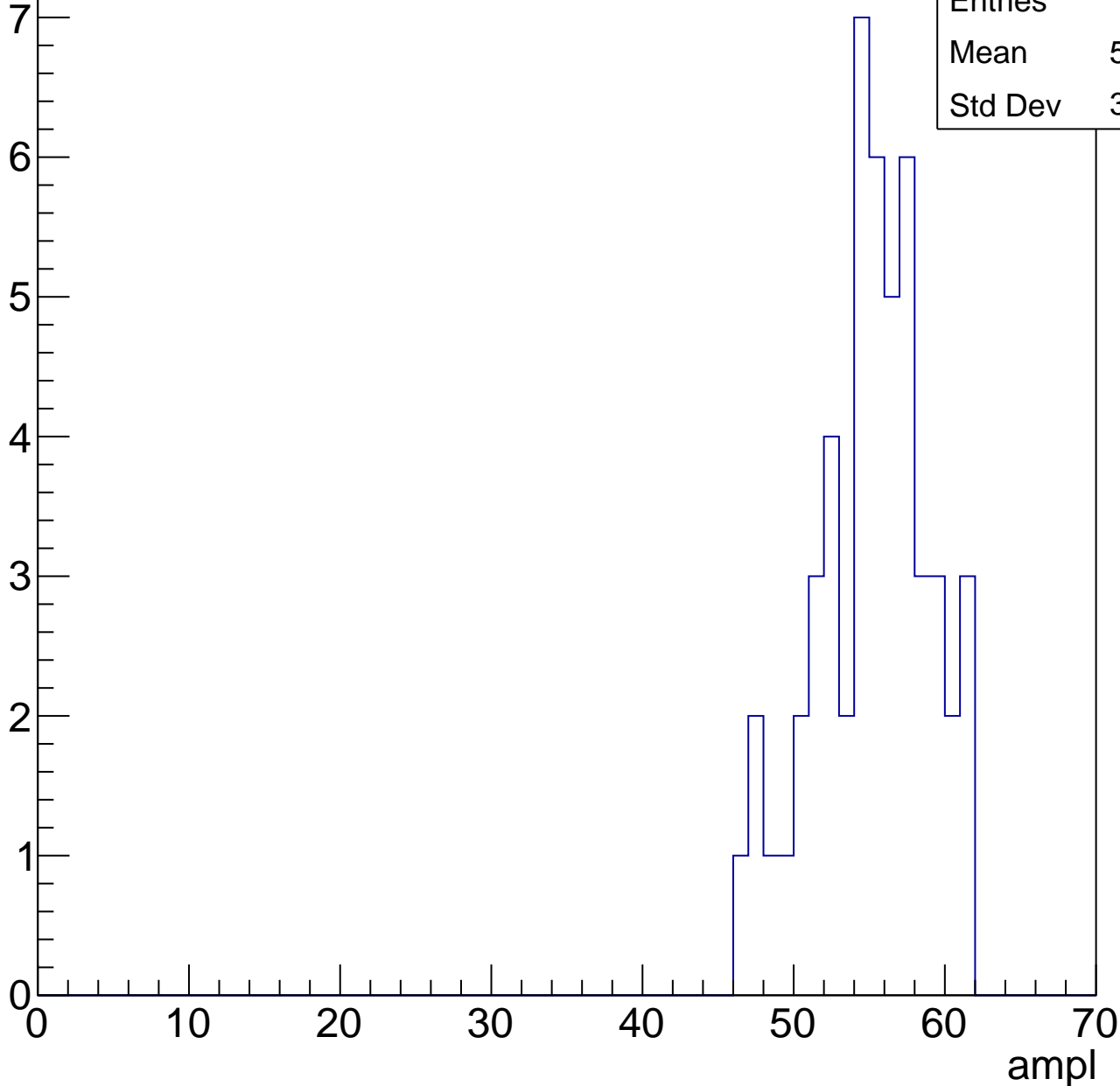


B1L003S, U16-ch9, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.67
Std Dev	3.703

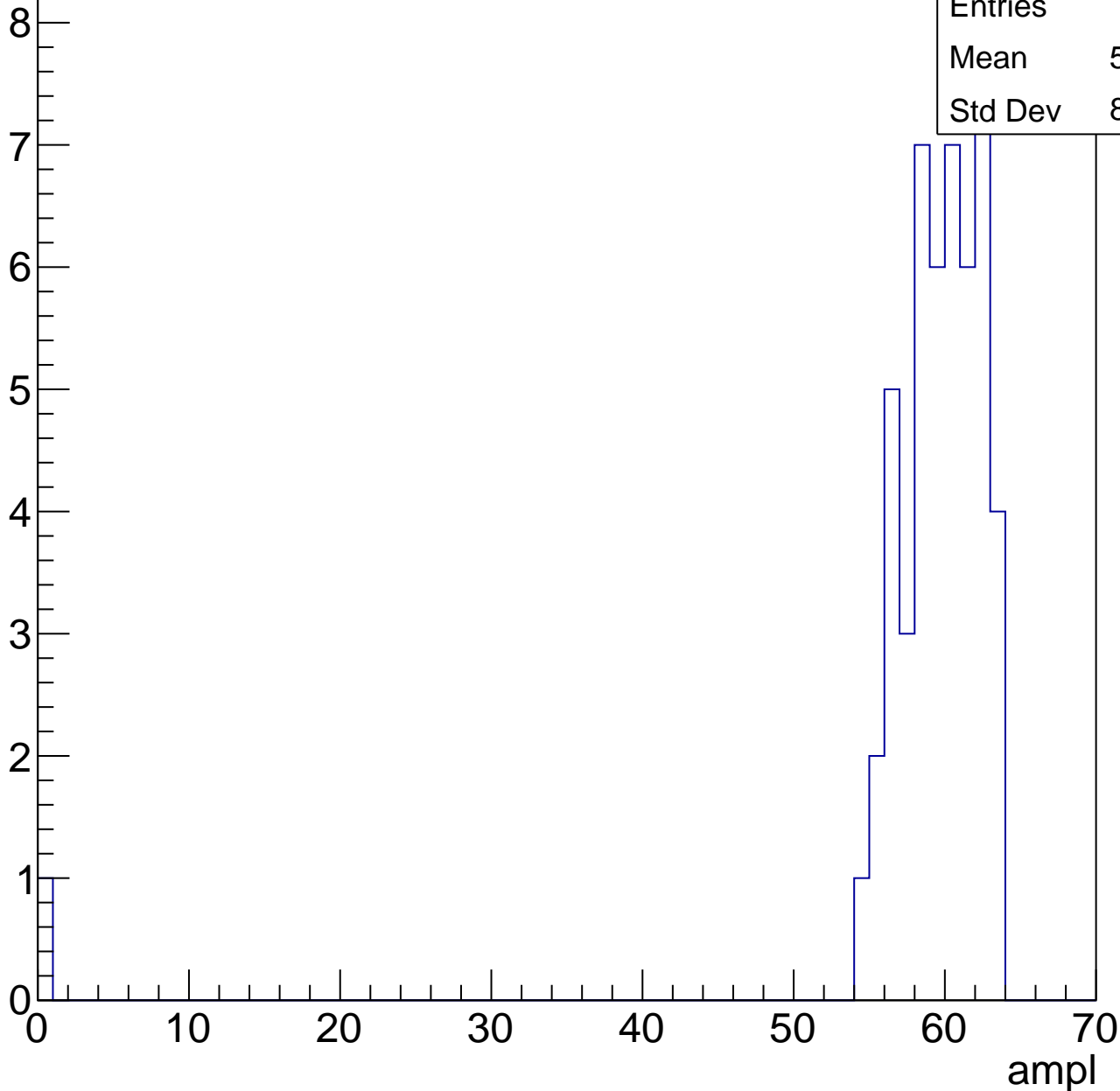


B1L003S, U16-ch9, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	58.18
Std Dev	8.643

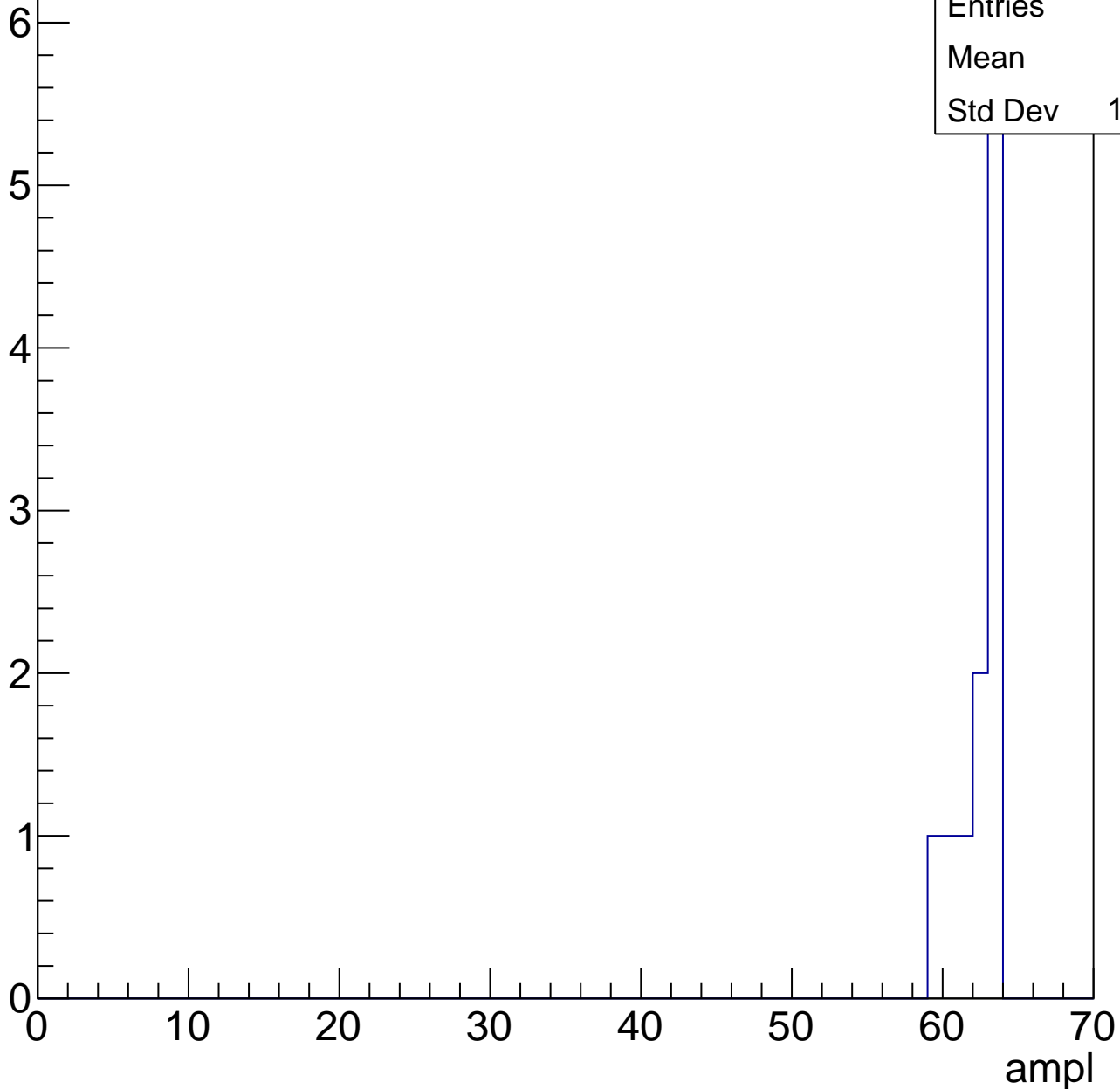


B1L003S, U16-ch9, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62
Std Dev	1.348



B1L003S, U16-ch9, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch10, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	31.56
Std Dev	5.152

Gaus mean : 32.1969

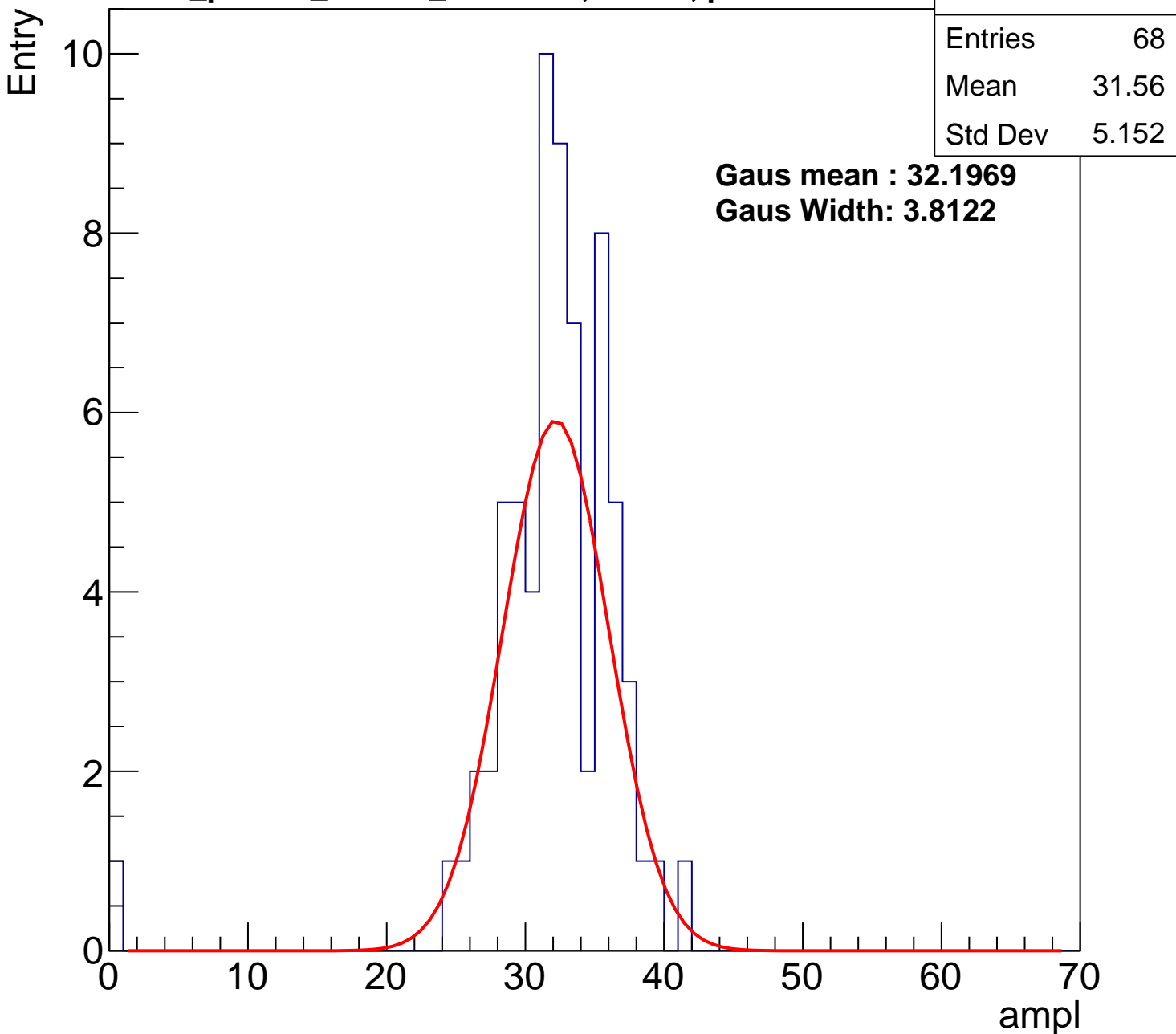
Gaus Width: 3.8122

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch10, adc1

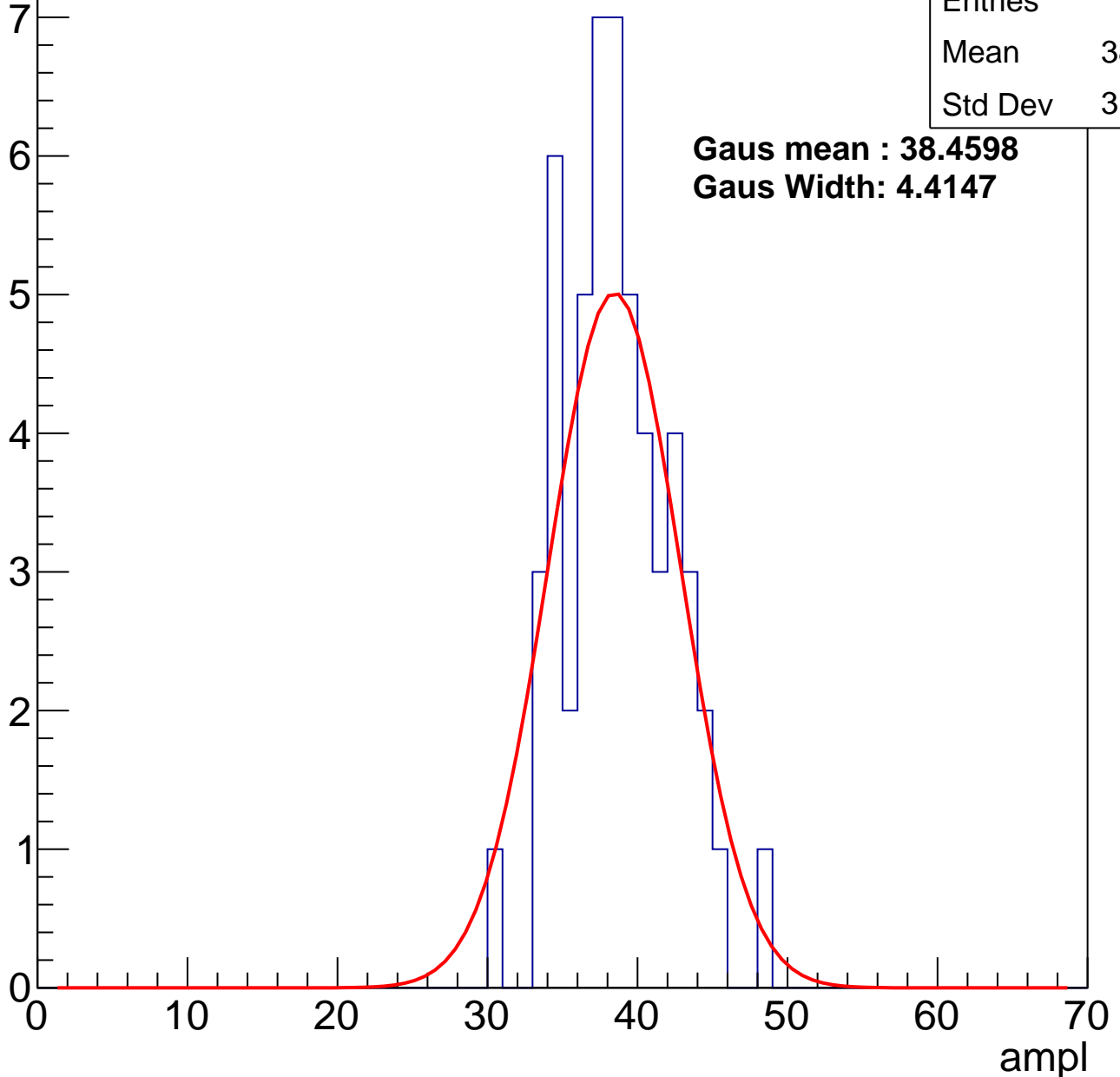
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	38.22
Std Dev	3.562

Gaus mean : 38.4598

Gaus Width: 4.4147



B1L003S, U16-ch10, adc2

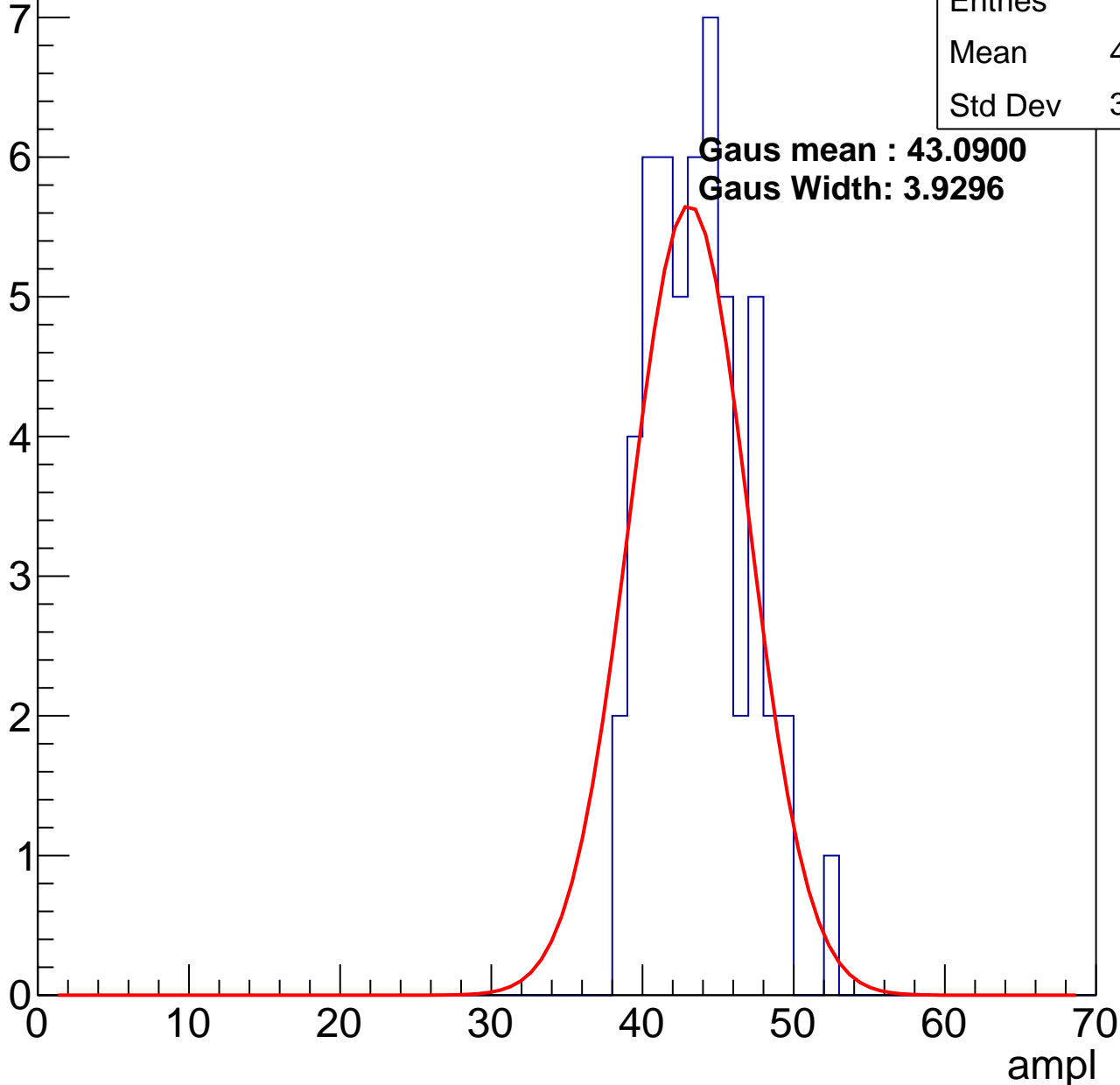
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.25
Std Dev	3.138

Gaus mean : 43.0900

Gaus Width: 3.9296



B1L003S, U16-ch10, adc3

calib_packv5_042523_0143.root, FC#13, port D2

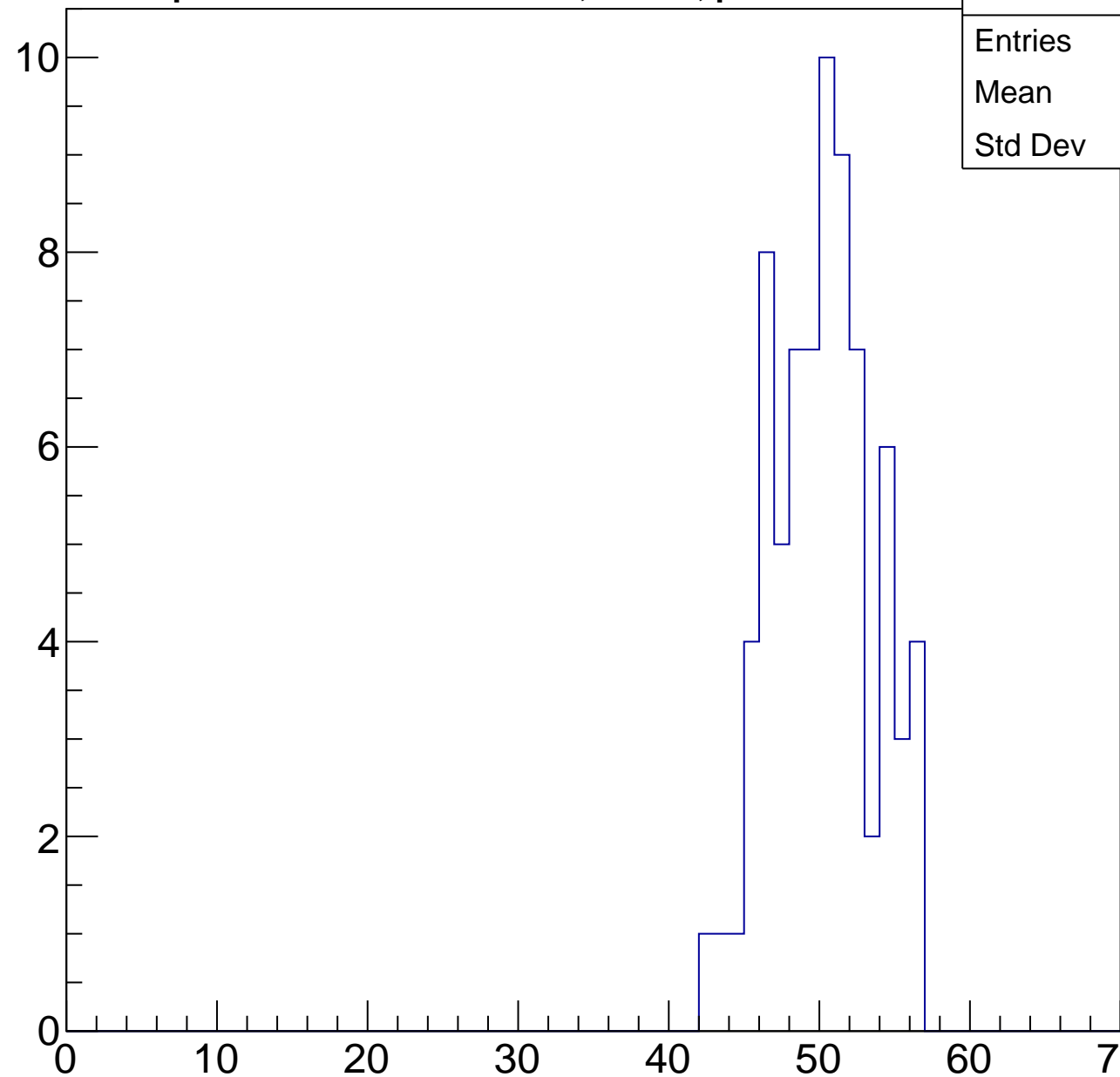
Entries	75
Mean	49.77
Std Dev	3.313

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

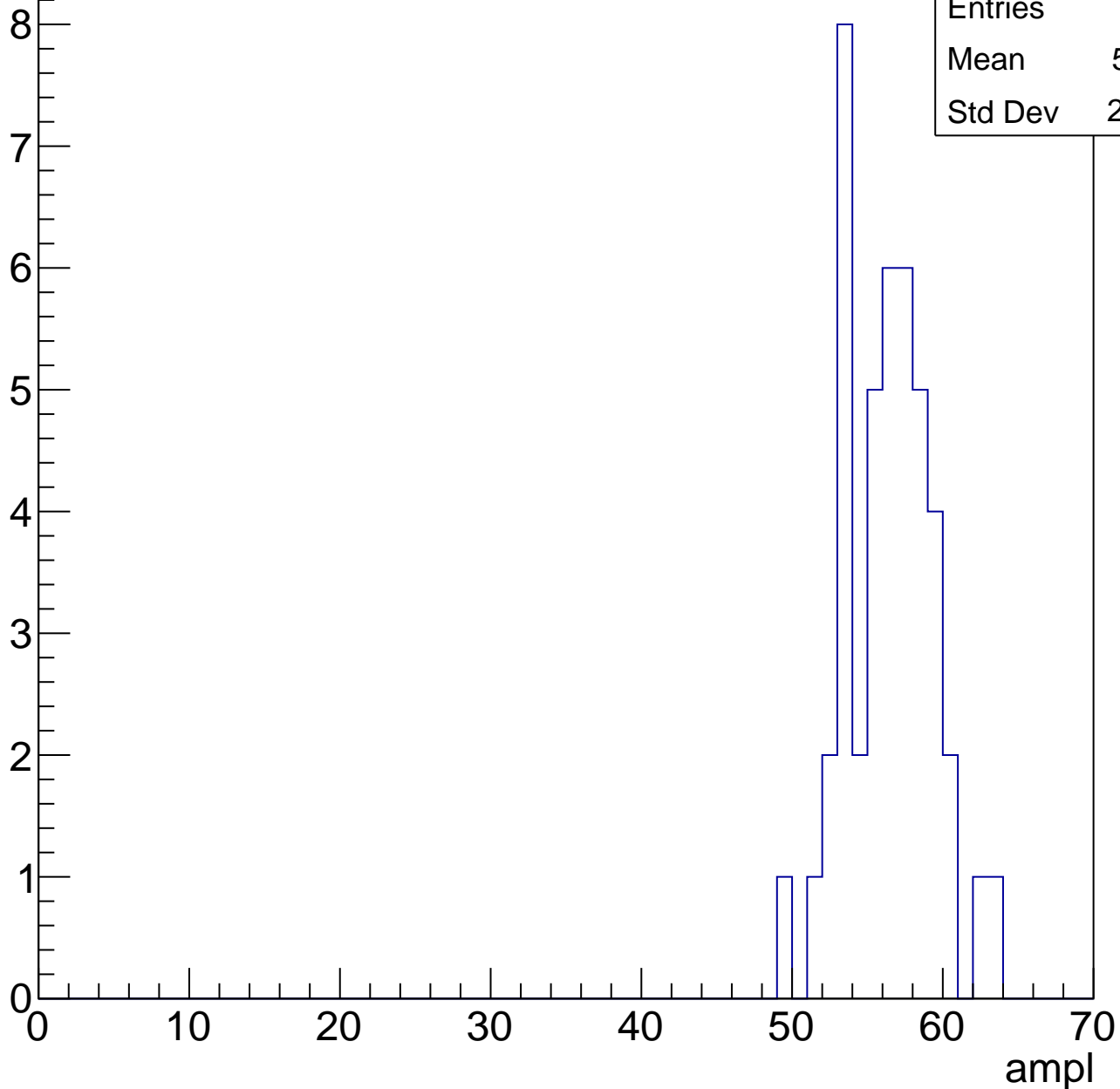


B1L003S, U16-ch10, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	55.91
Std Dev	2.898



B1L003S, U16-ch10, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.38
Std Dev	8.915

ampl

0

10

20

30

40

50

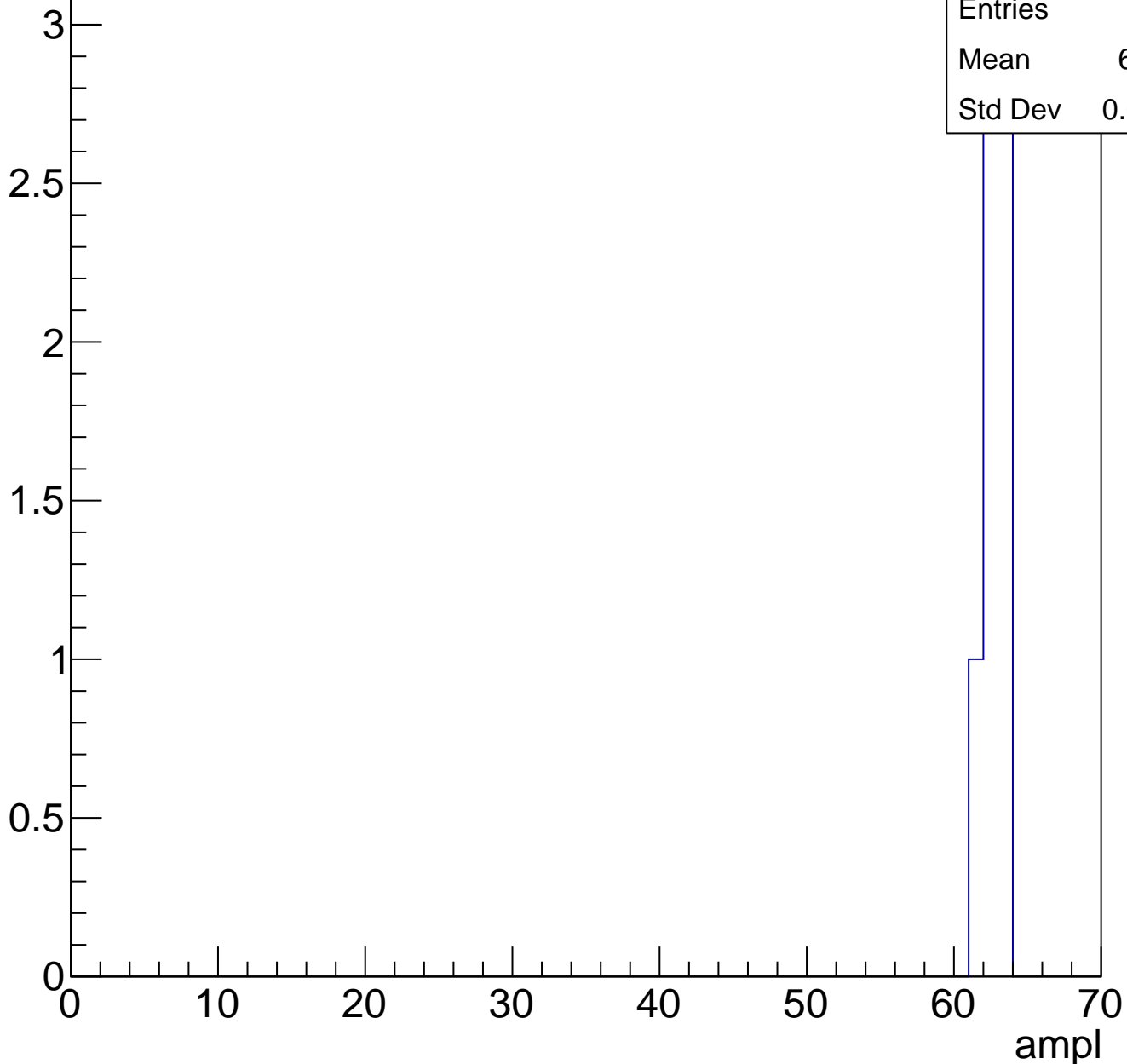
60

70

B1L003S, U16-ch10, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch10, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch11, adc0

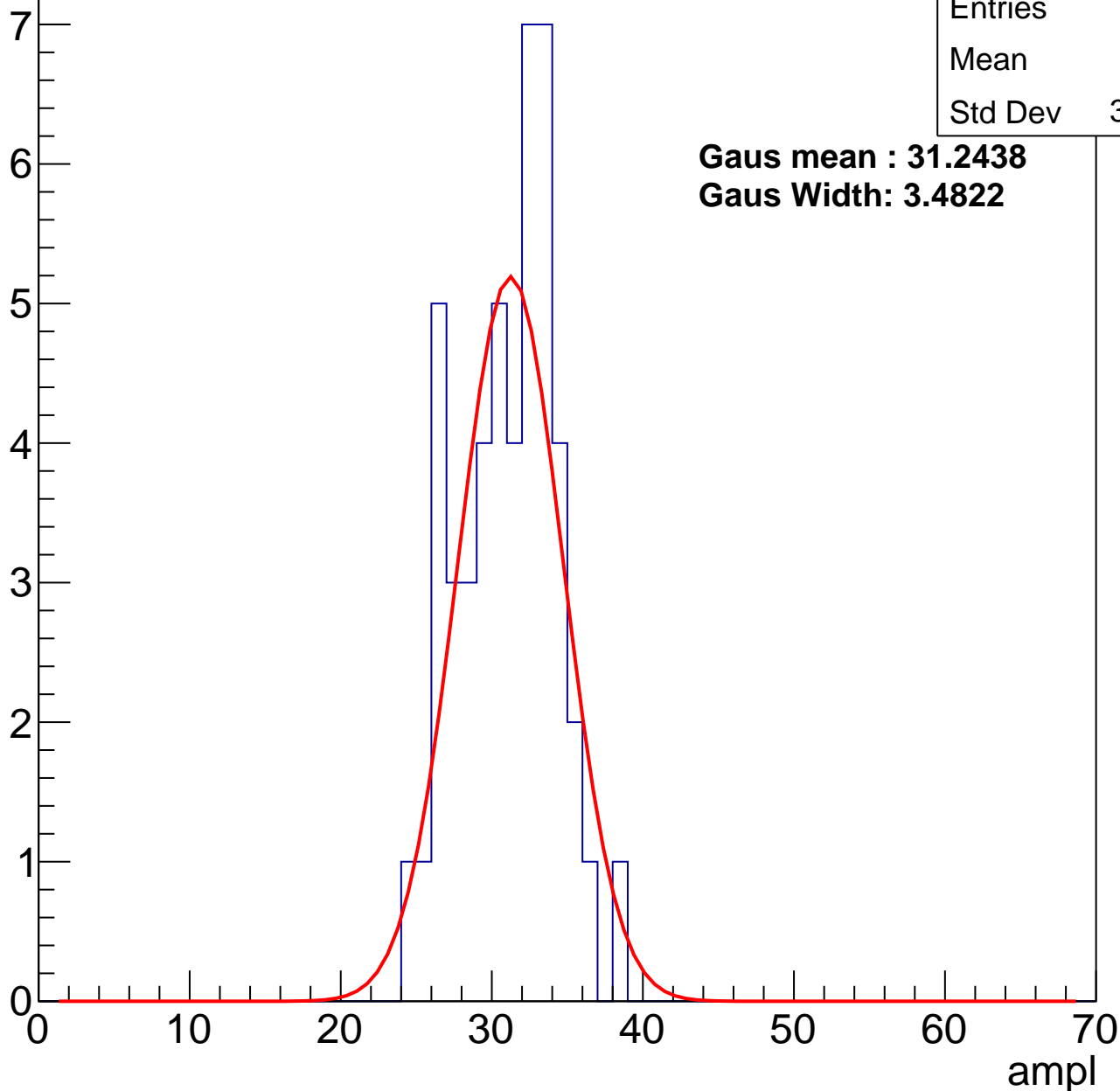
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	30.6
Std Dev	3.147

Gaus mean : 31.2438

Gaus Width: 3.4822



B1L003S, U16-ch11, adc1

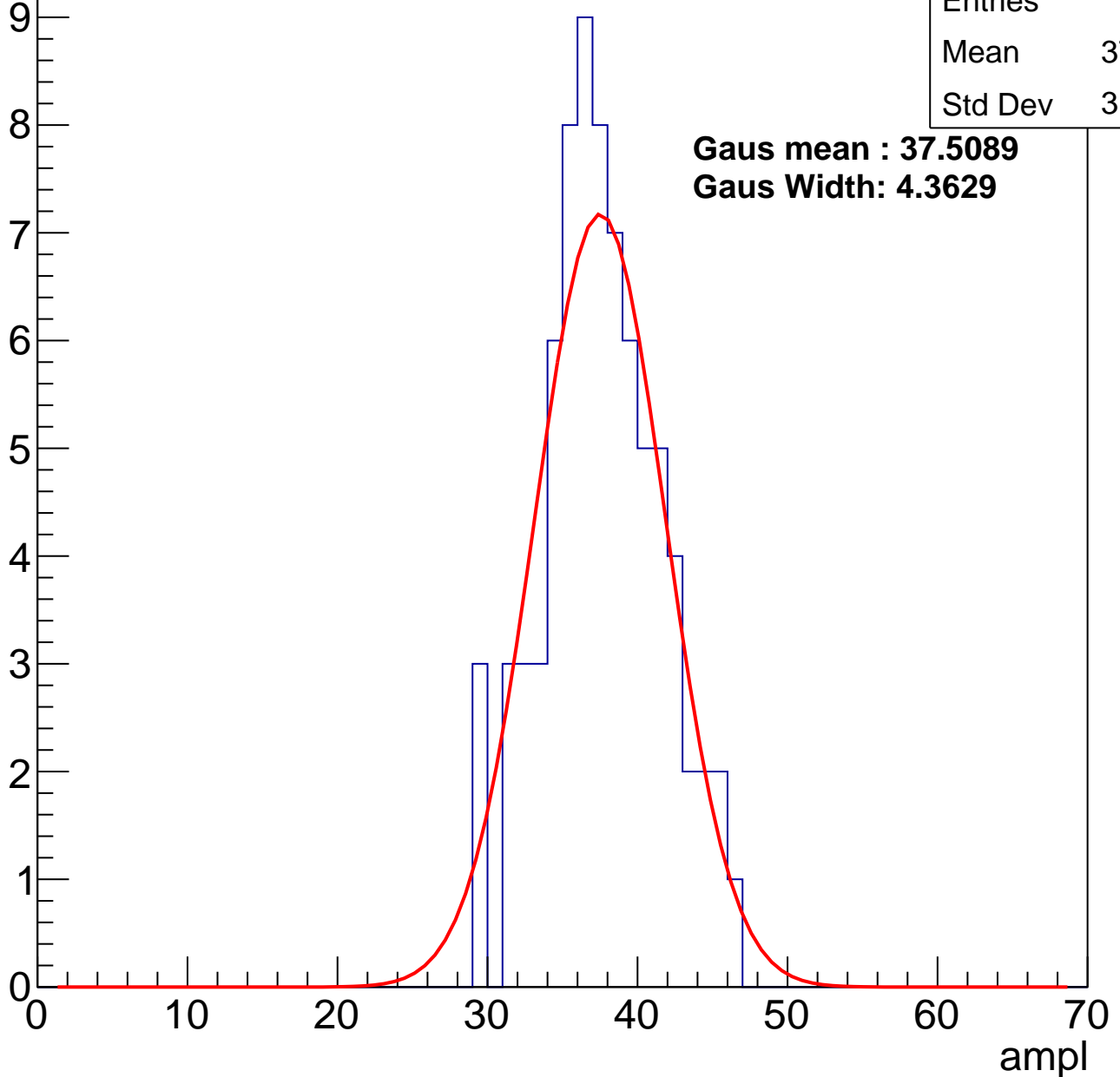
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	37.17
Std Dev	3.866

Gaus mean : 37.5089

Gaus Width: 4.3629



B1L003S, U16-ch11, adc2

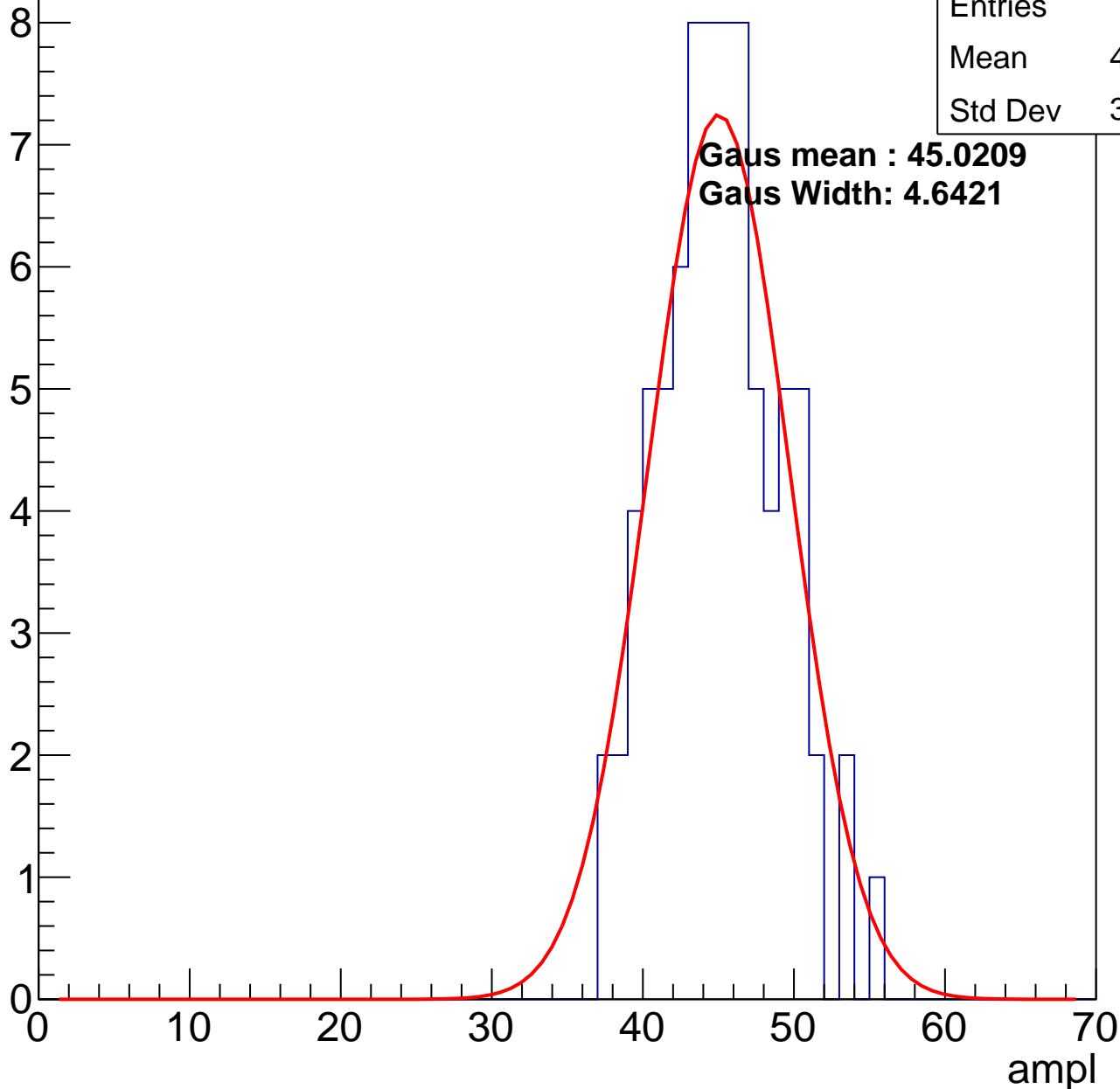
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	44.65
Std Dev	3.918

Gaus mean : 45.0209

Gaus Width: 4.6421

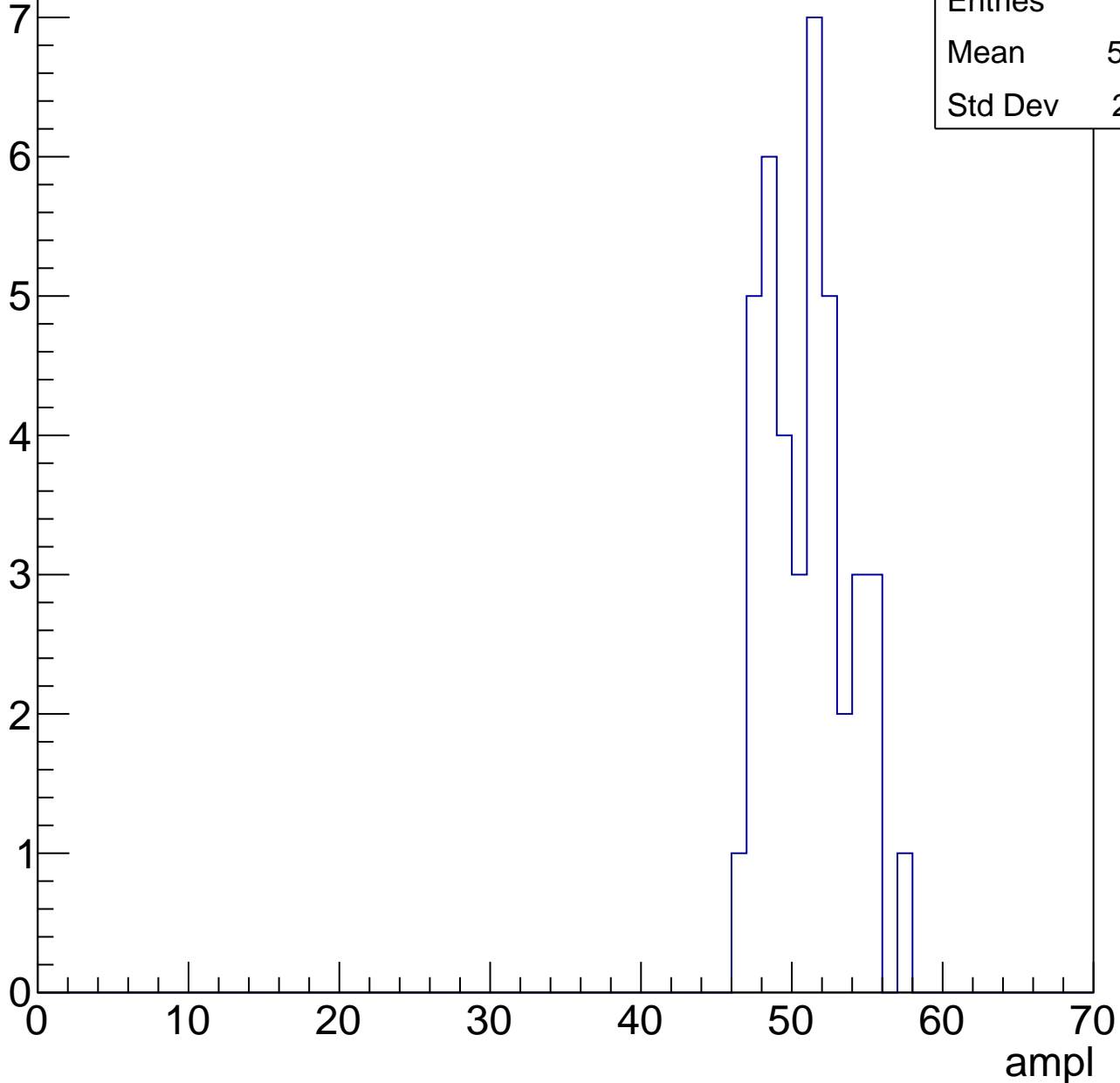


B1L003S, U16-ch11, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	50.55
Std Dev	2.711

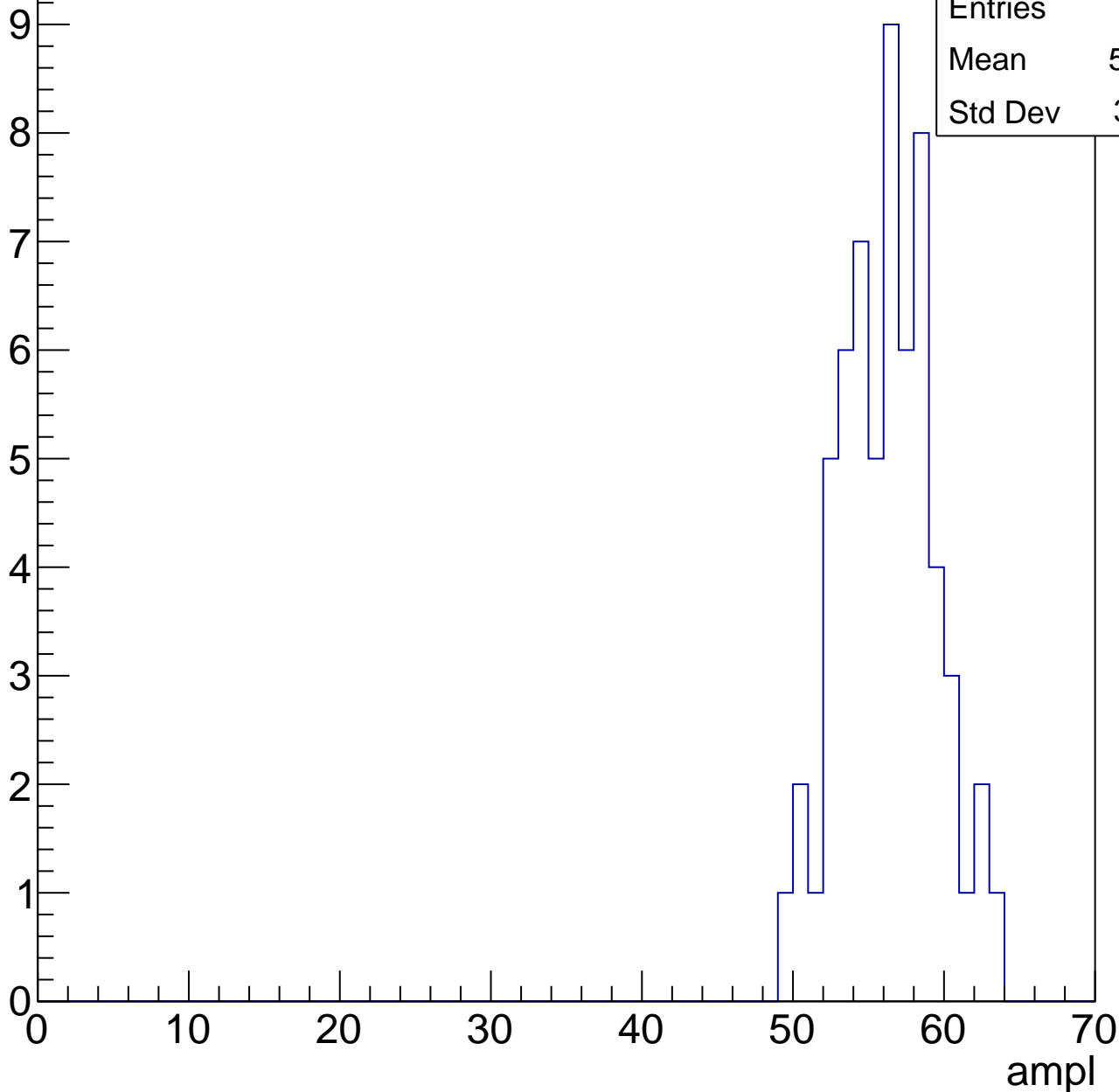


B1L003S, U16-ch11, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.82
Std Dev	3.081

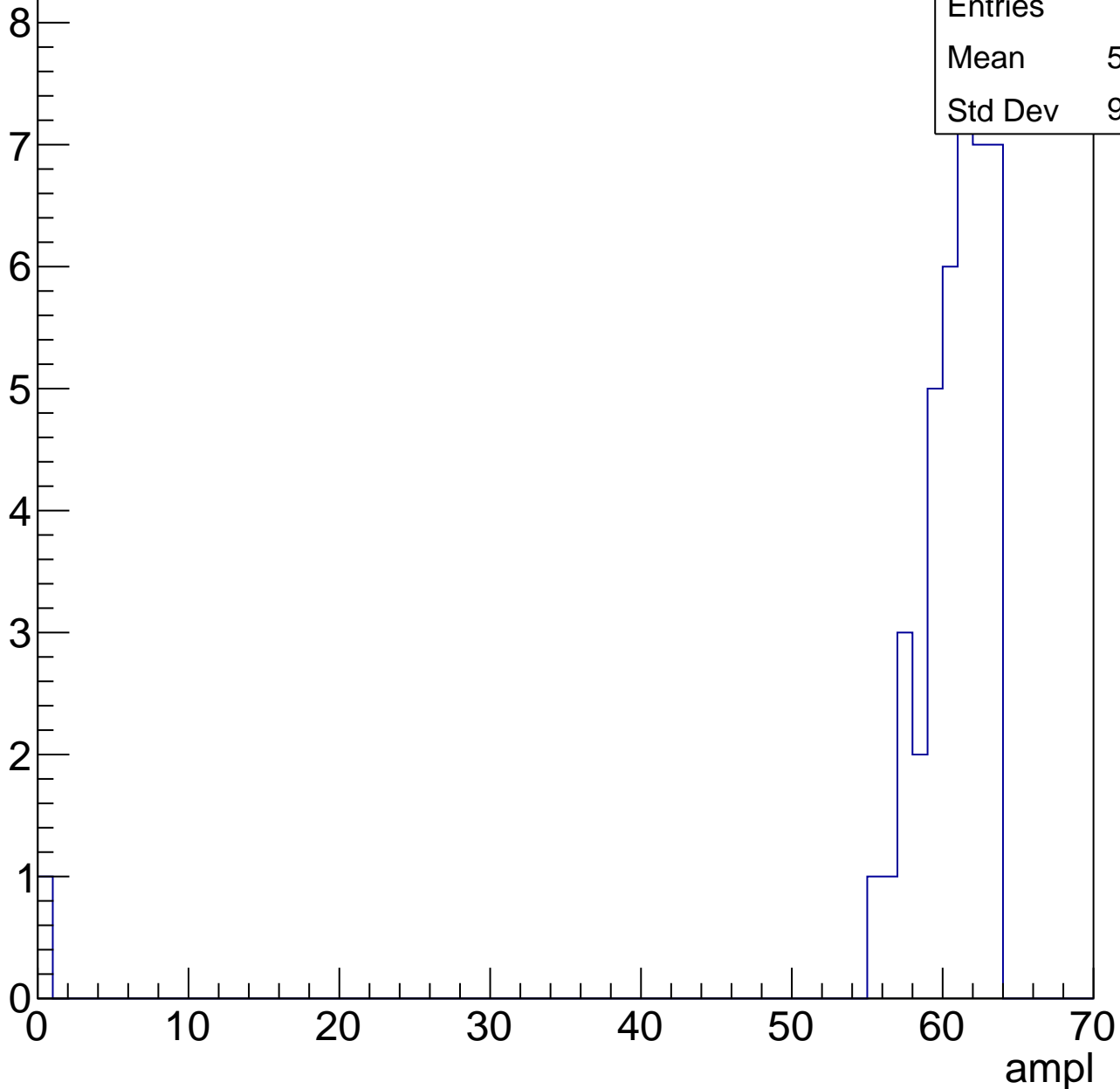


B1L003S, U16-ch11, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.93
Std Dev	9.542



B1L003S, U16-ch11, adc6

calib_packv5_042523_0143.root, FC#13, port D2

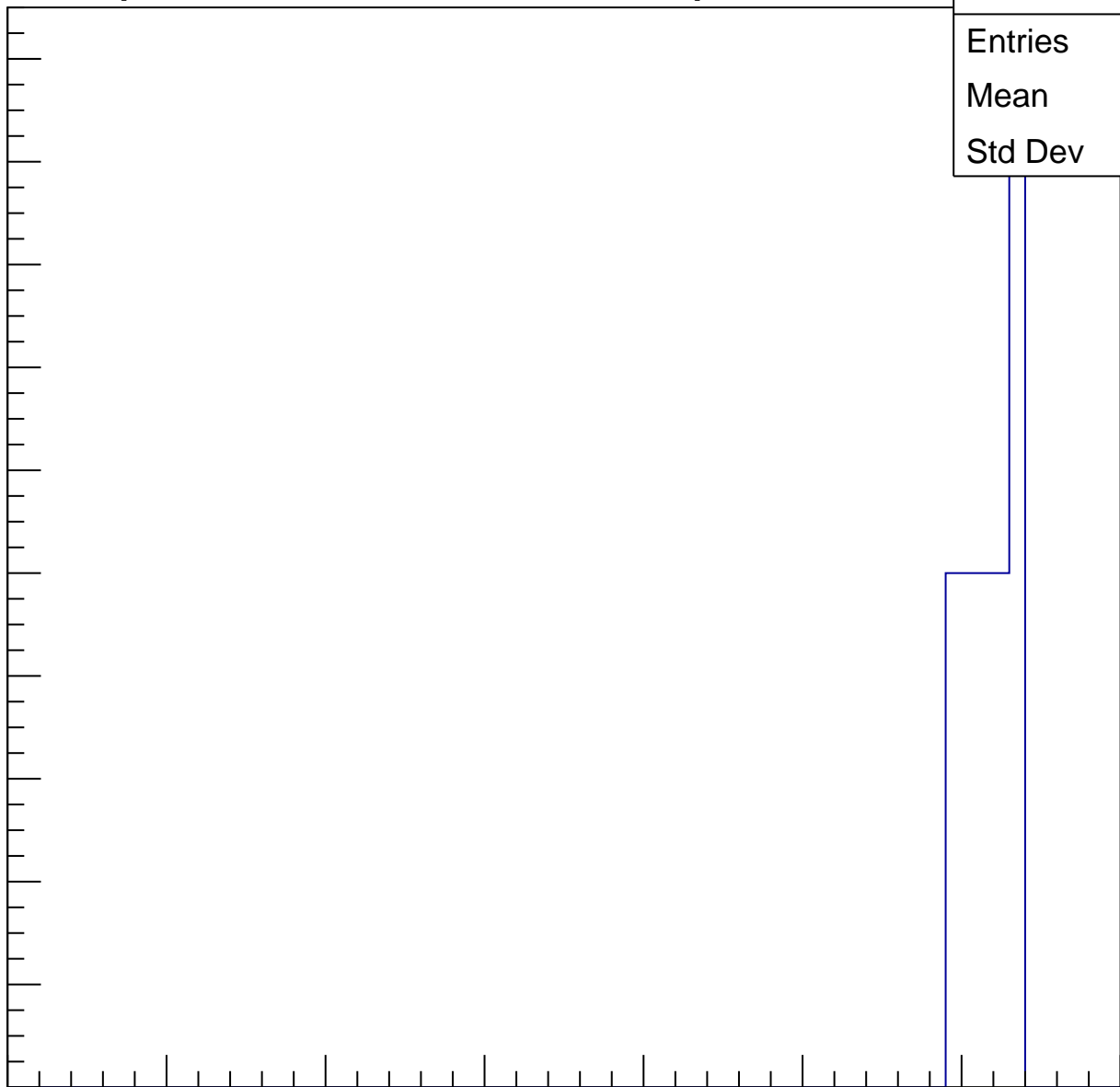
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

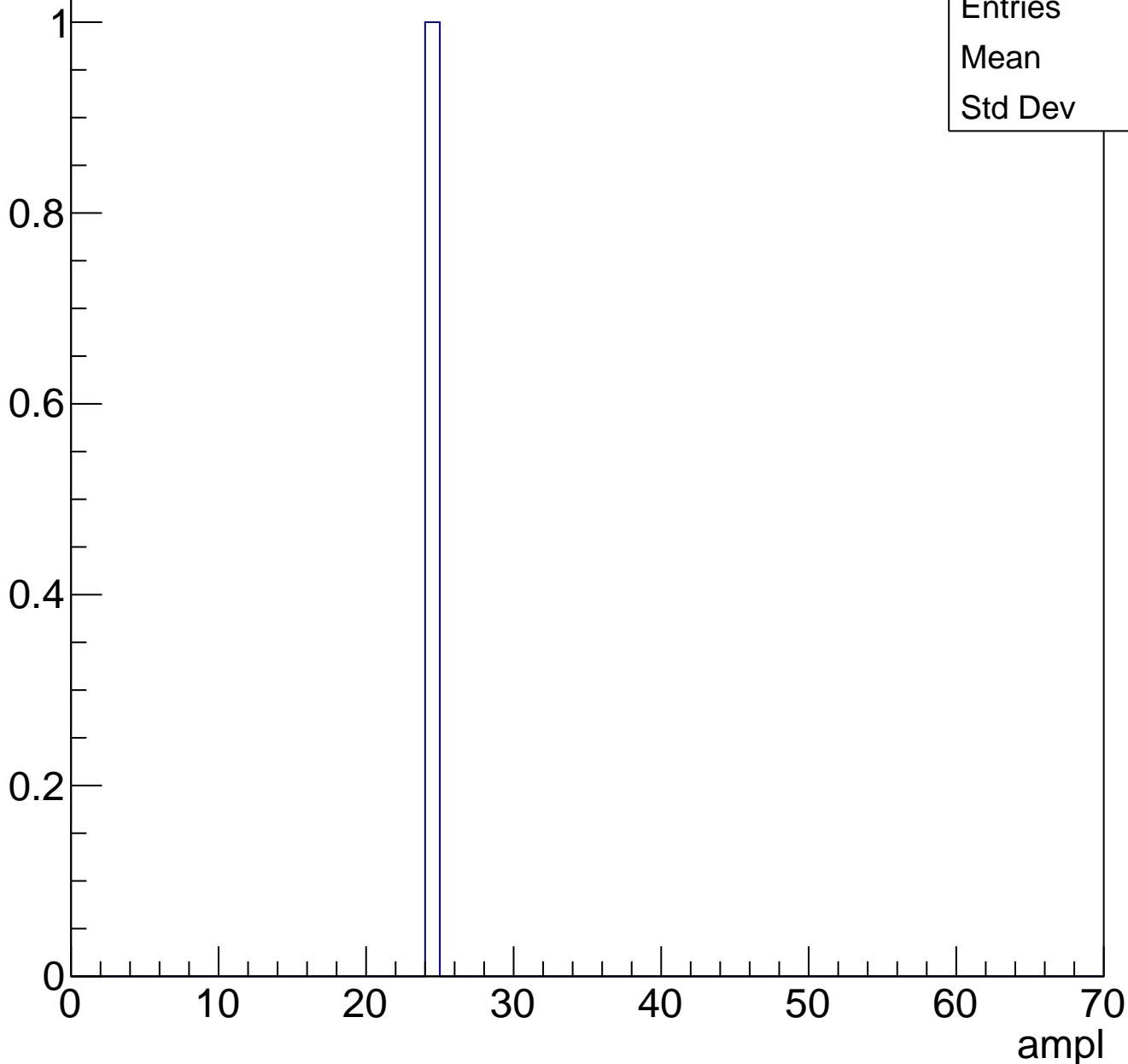
ampl



B1L003S, U16-ch11, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch12, adc0

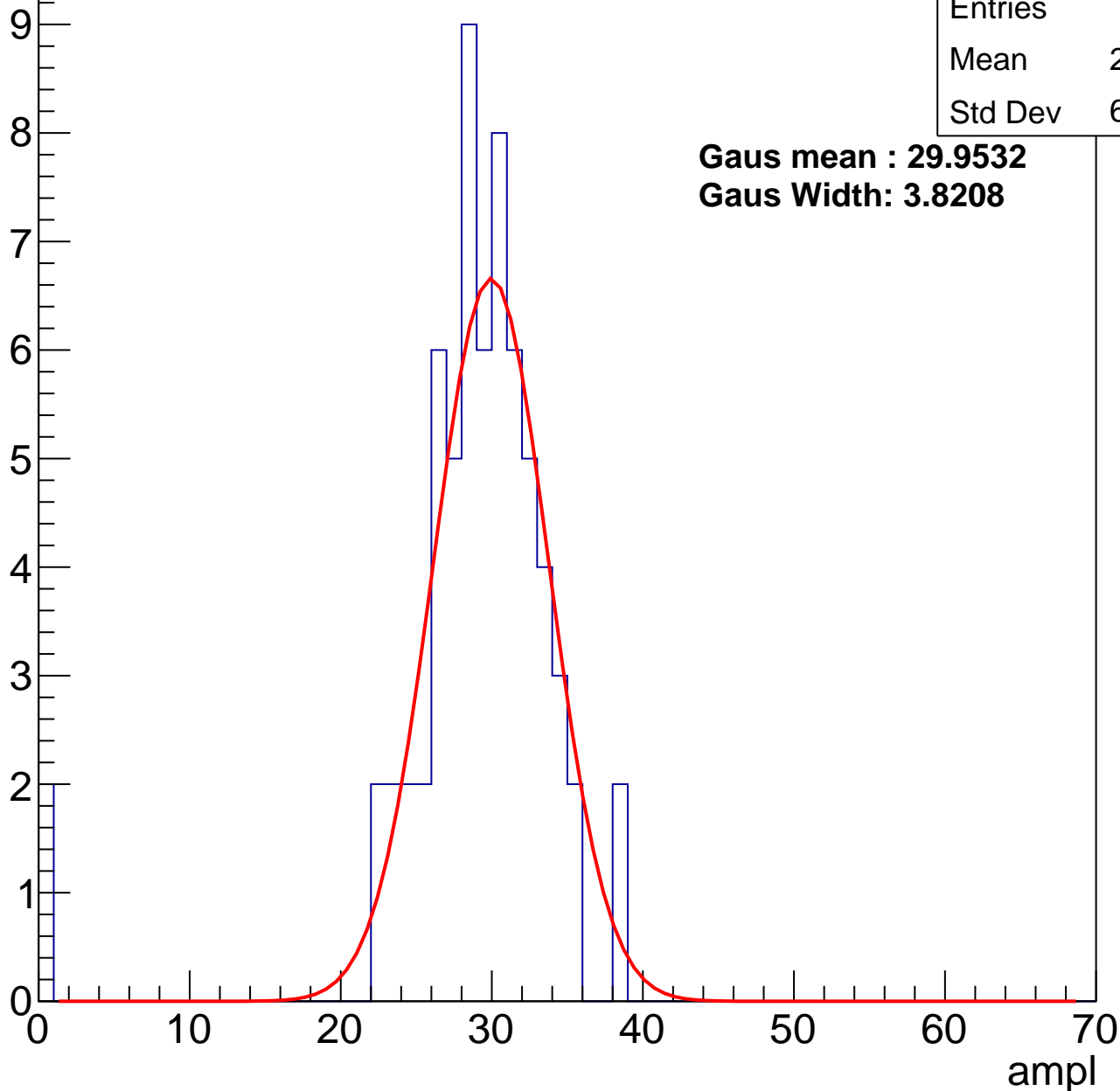
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.35
Std Dev	6.079

Gaus mean : 29.9532

Gaus Width: 3.8208



B1L003S, U16-ch12, adc1

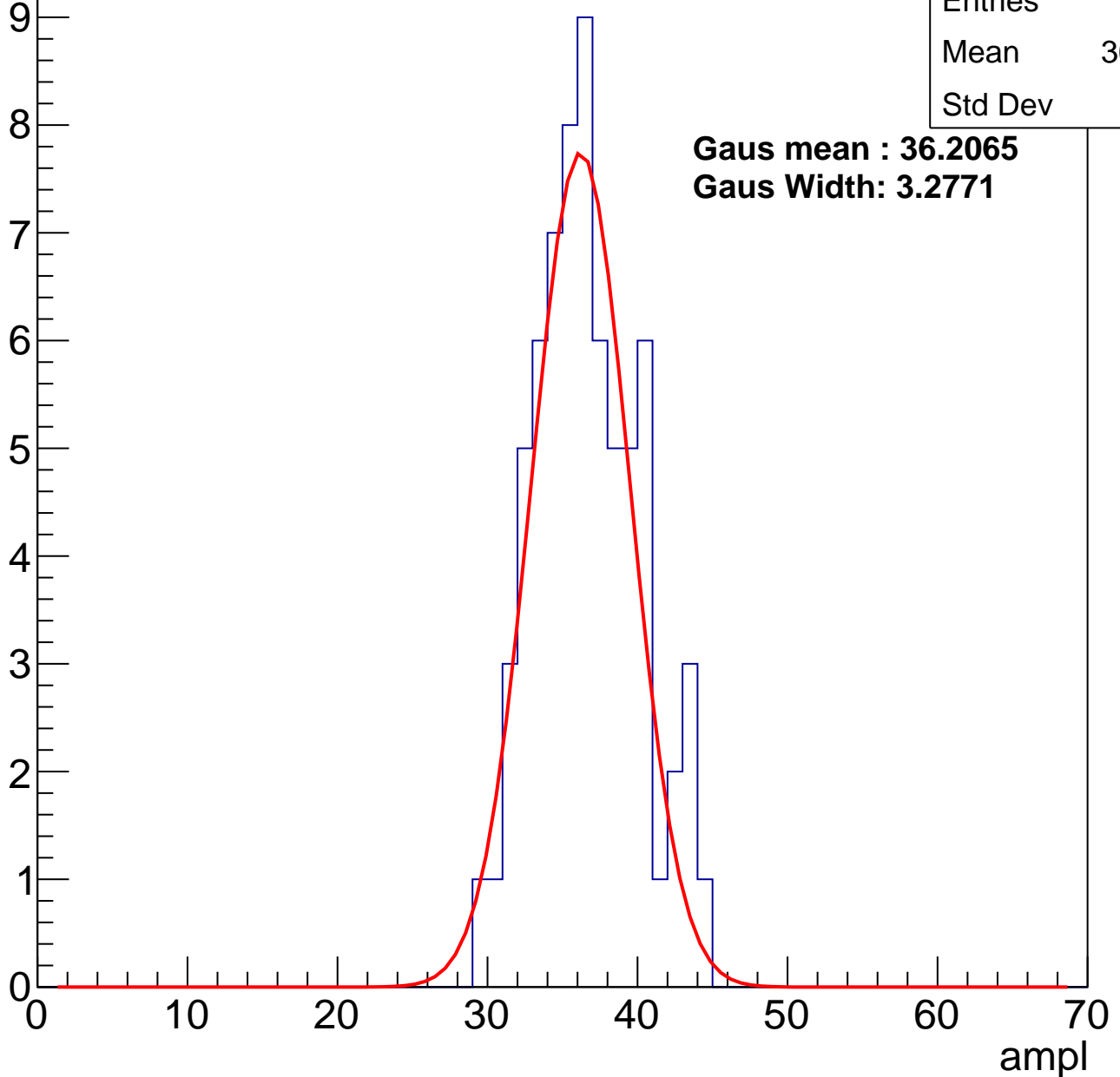
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	36.19
Std Dev	3.41

Gaus mean : 36.2065

Gaus Width: 3.2771



B1L003S, U16-ch12, adc2

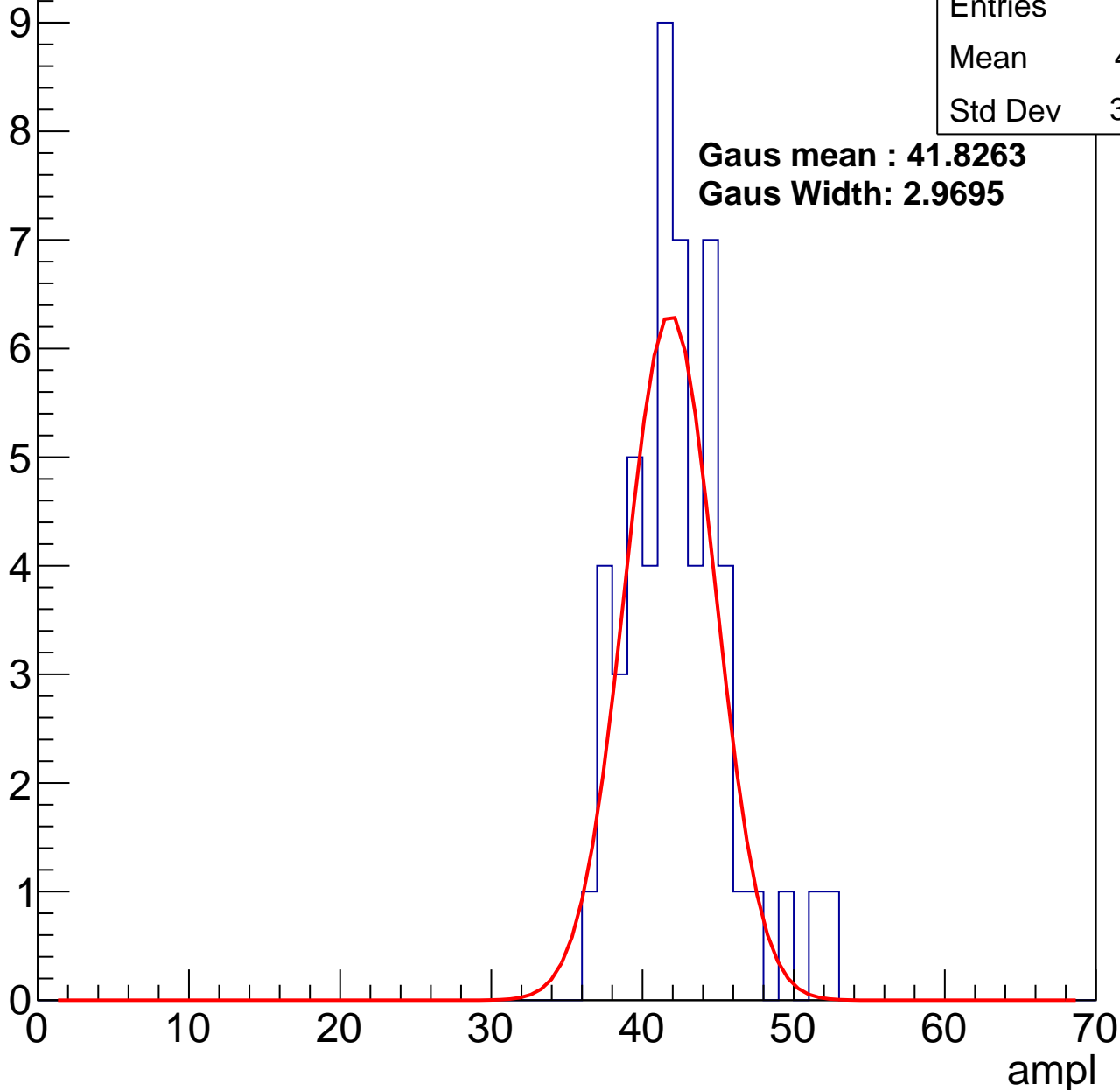
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	41.91
Std Dev	3.344

Gaus mean : 41.8263

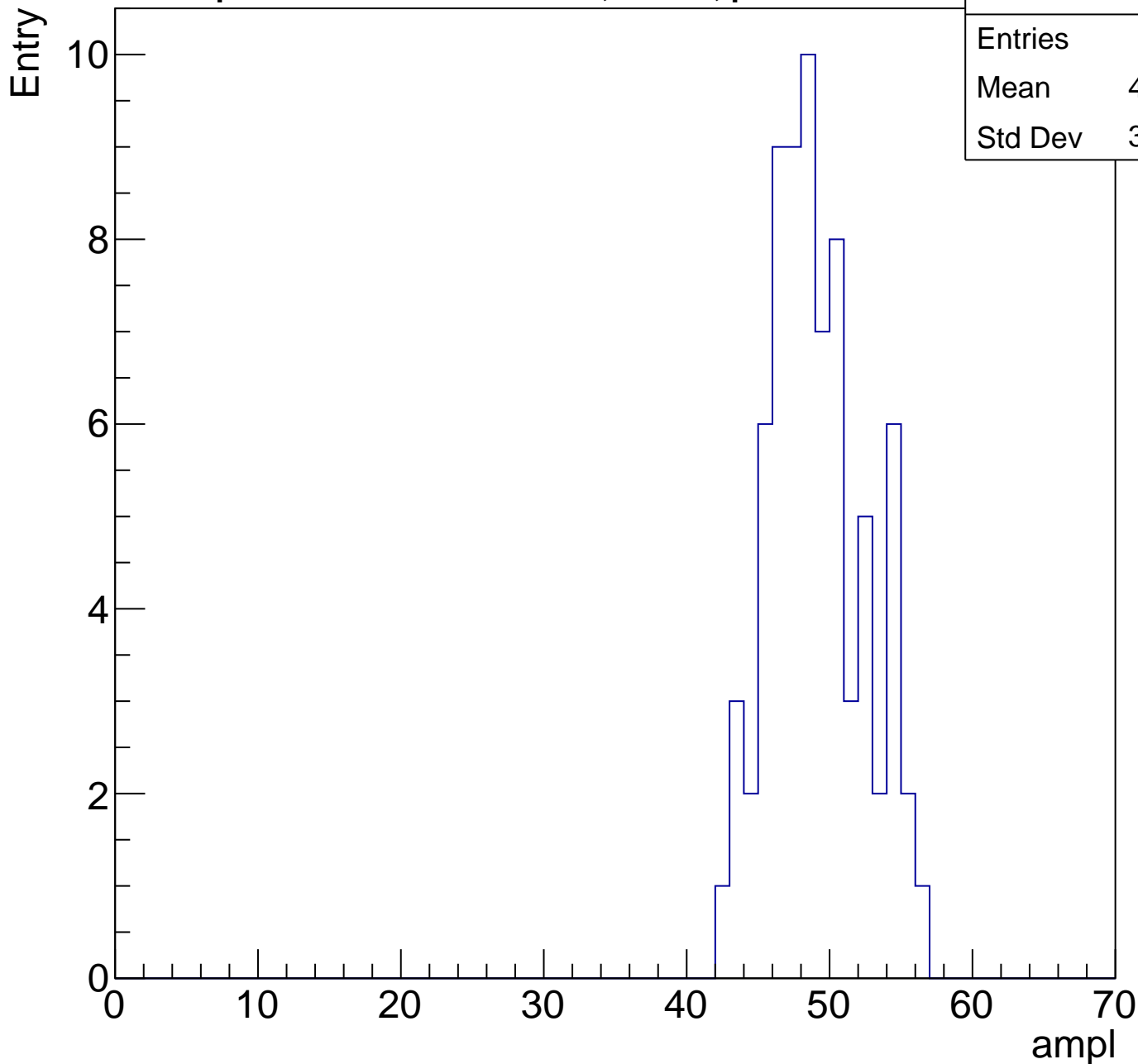
Gaus Width: 2.9695



B1L003S, U16-ch12, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	48.62
Std Dev	3.262

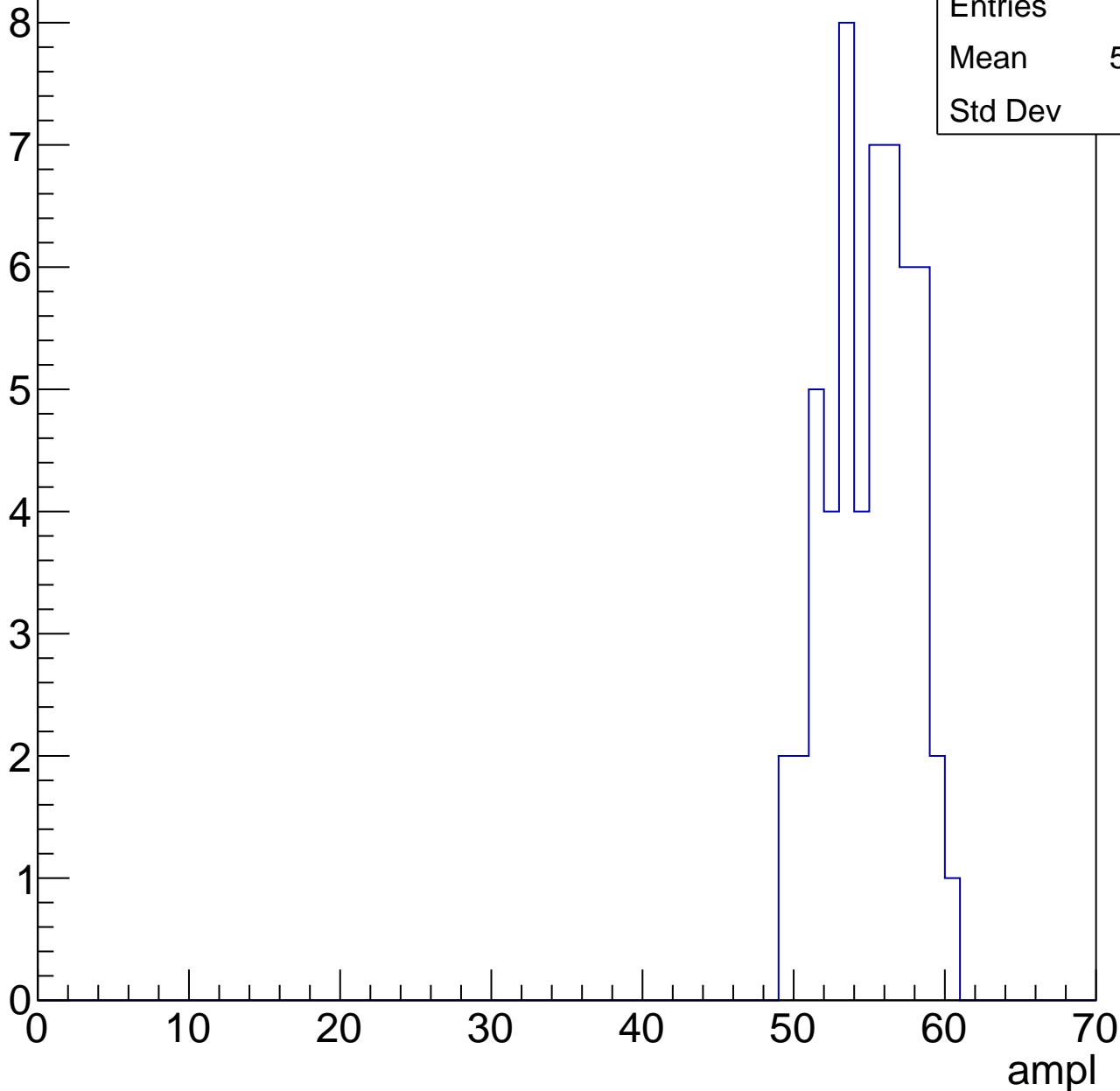


B1L003S, U16-ch12, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	54.56
Std Dev	2.74

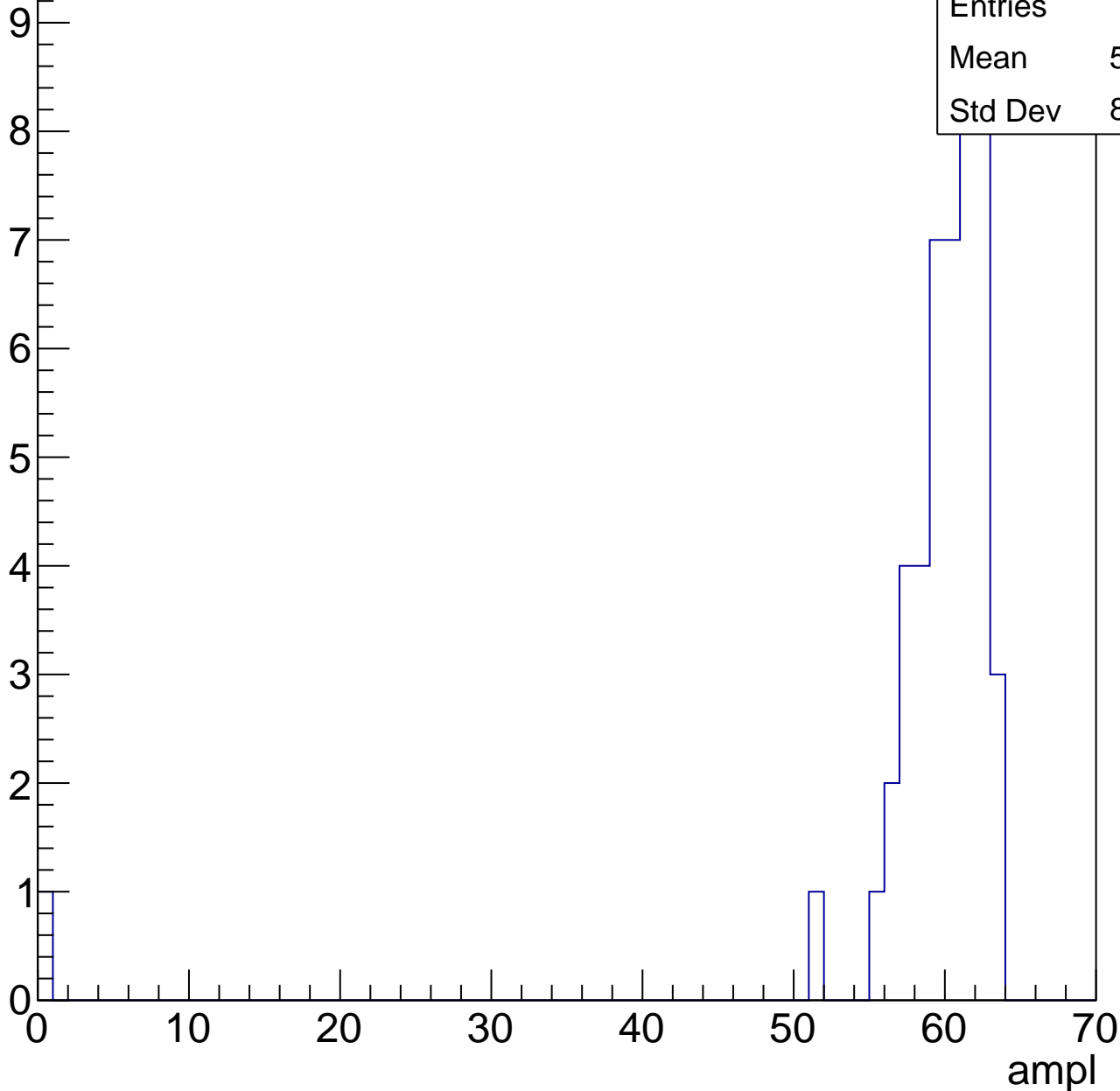


B1L003S, U16-ch12, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

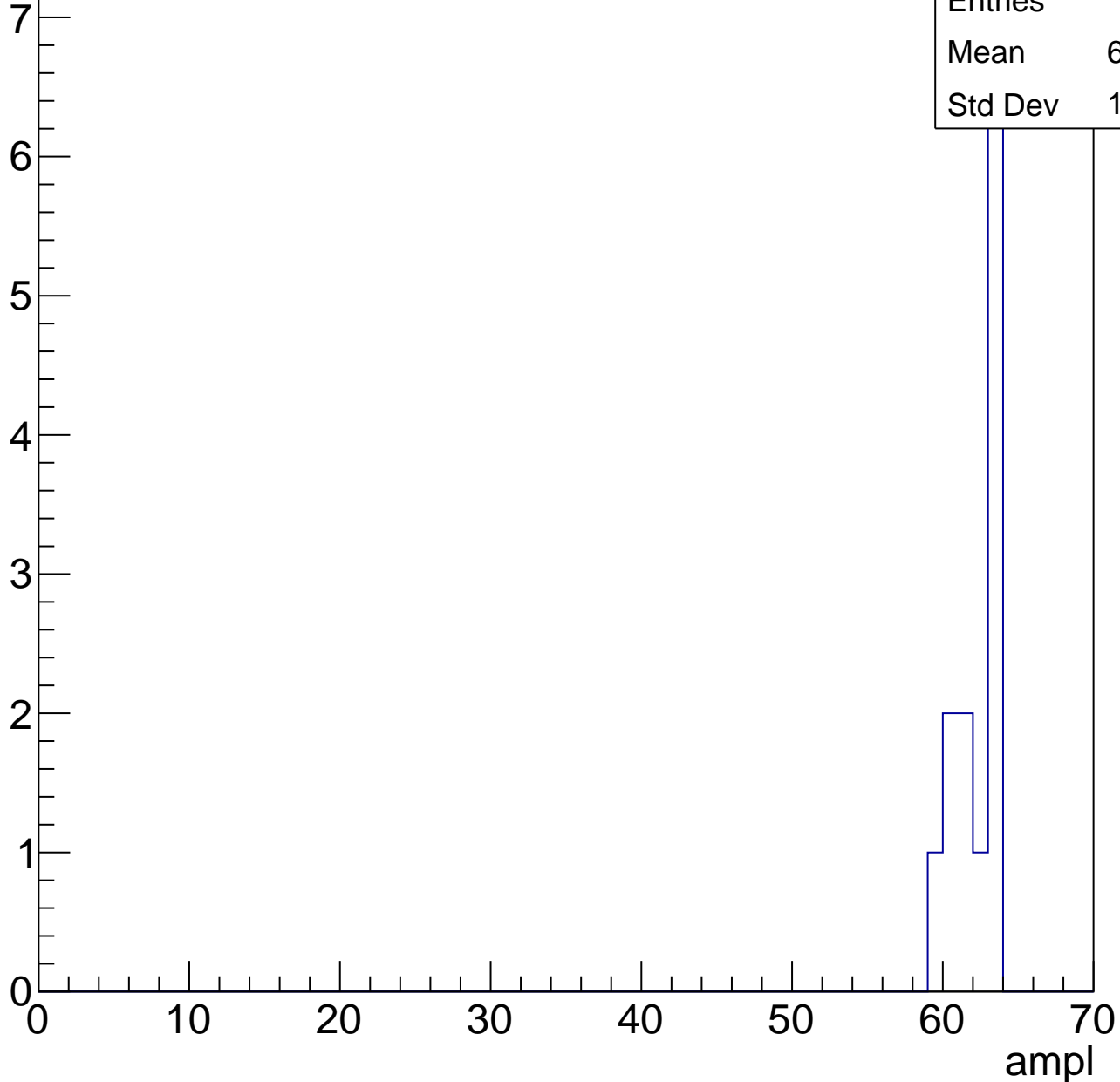
Entries	47
Mean	58.43
Std Dev	8.934



B1L003S, U16-ch12, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch12, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch13, adc0

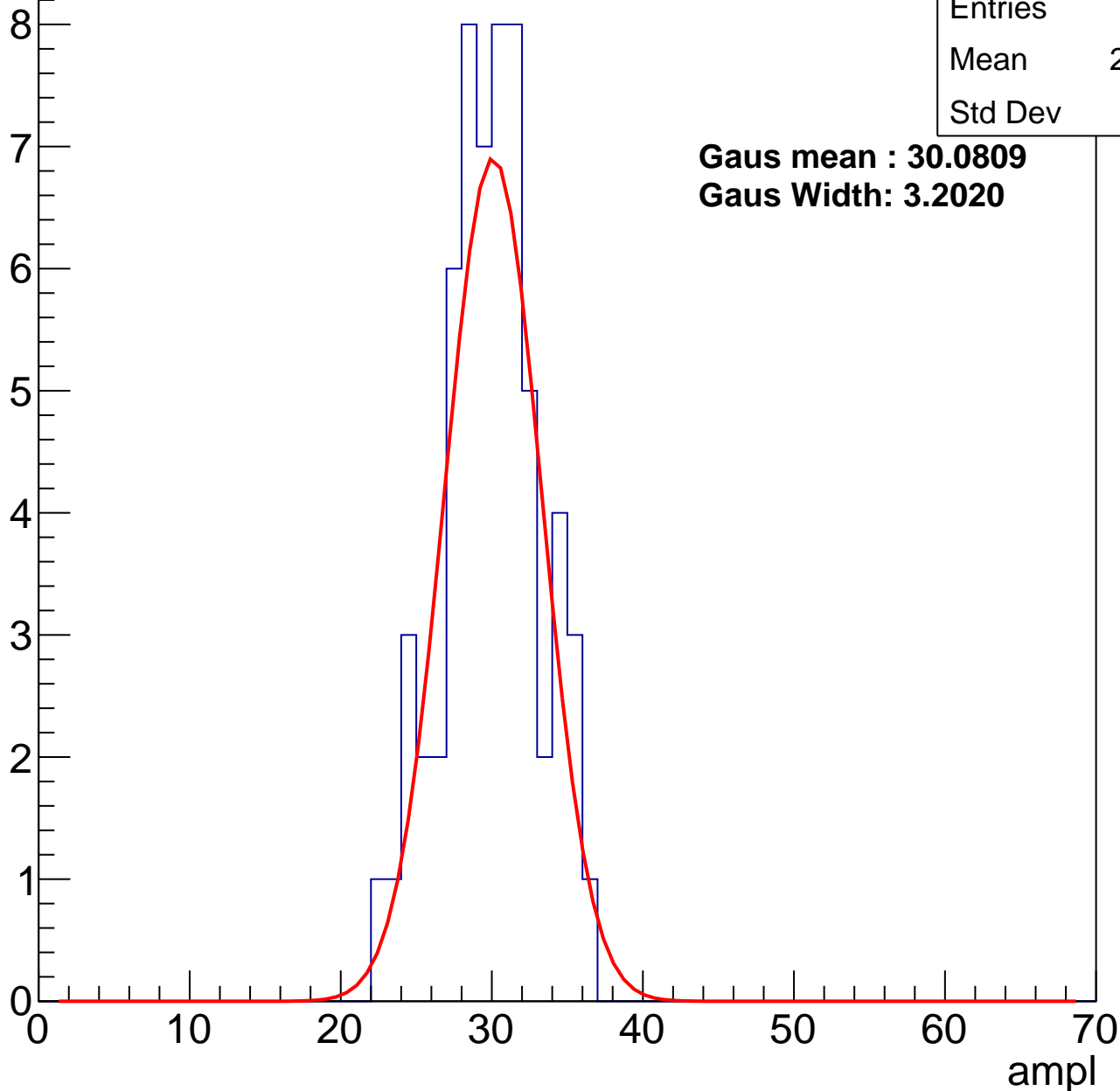
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.49
Std Dev	3.14

Gaus mean : 30.0809

Gaus Width: 3.2020



B1L003S, U16-ch13, adc1

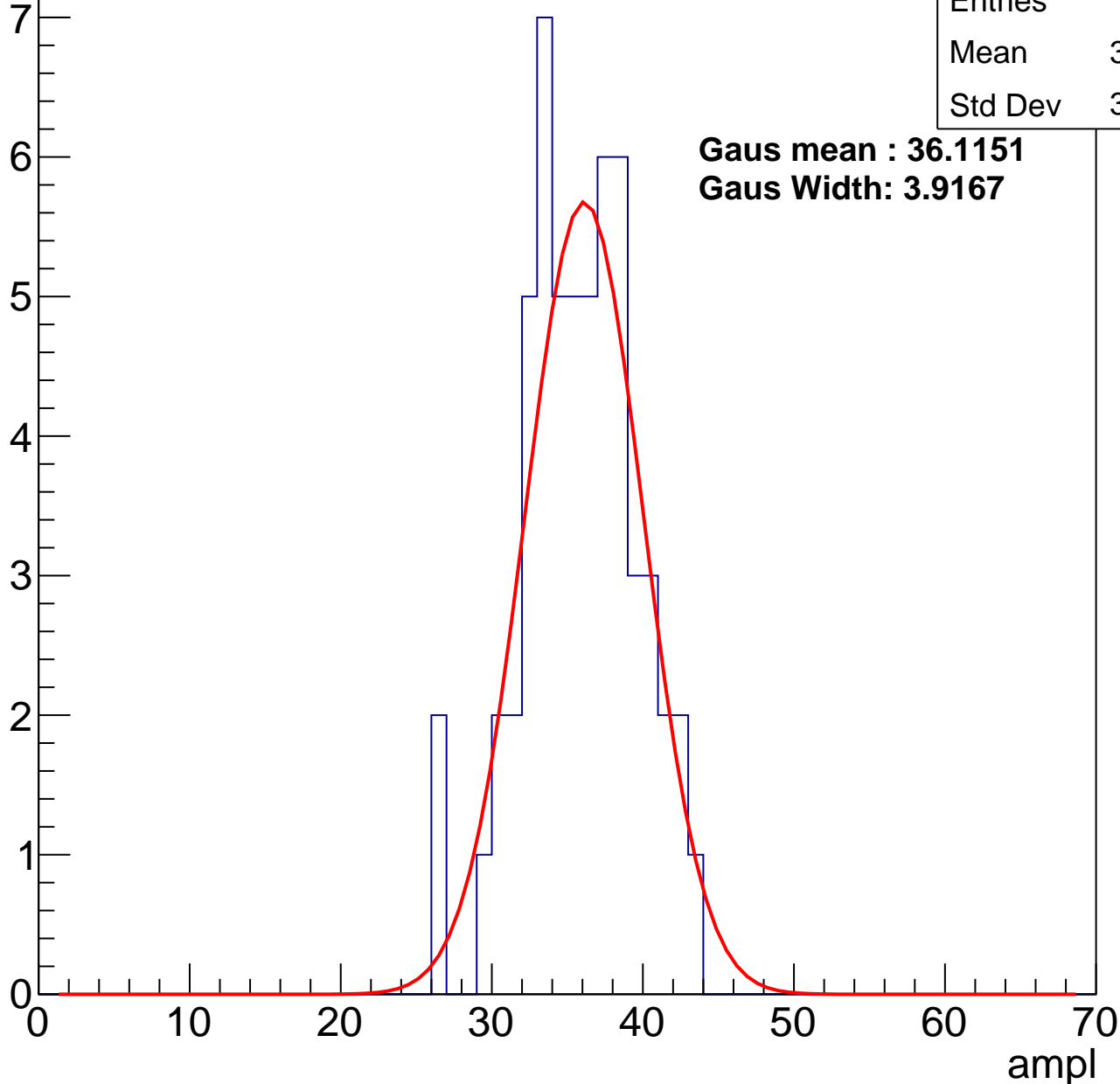
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.35
Std Dev	3.725

Gaus mean : 36.1151

Gaus Width: 3.9167



B1L003S, U16-ch13, adc2

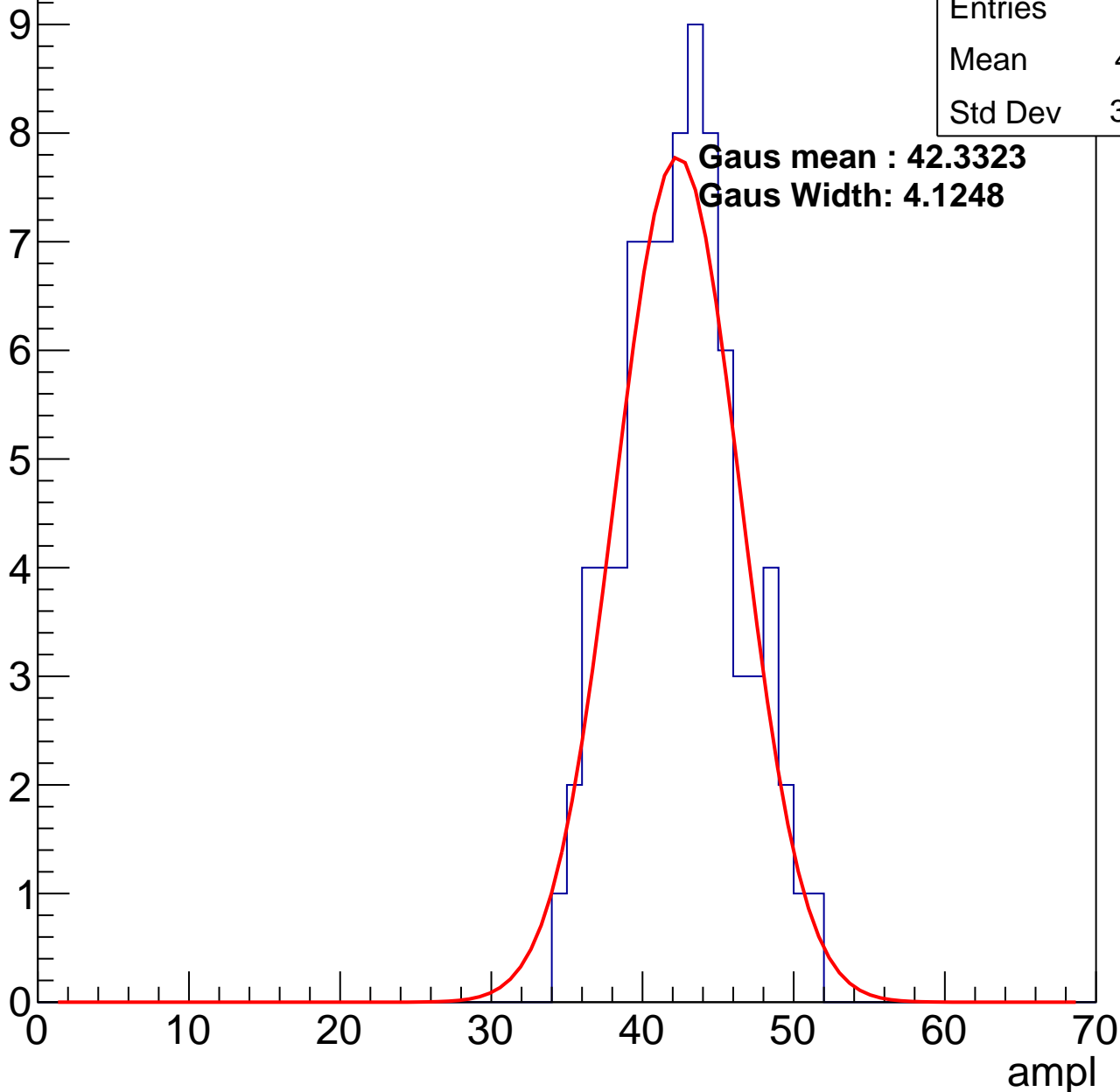
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	42.01
Std Dev	3.805

Gaus mean : 42.3323

Gaus Width: 4.1248

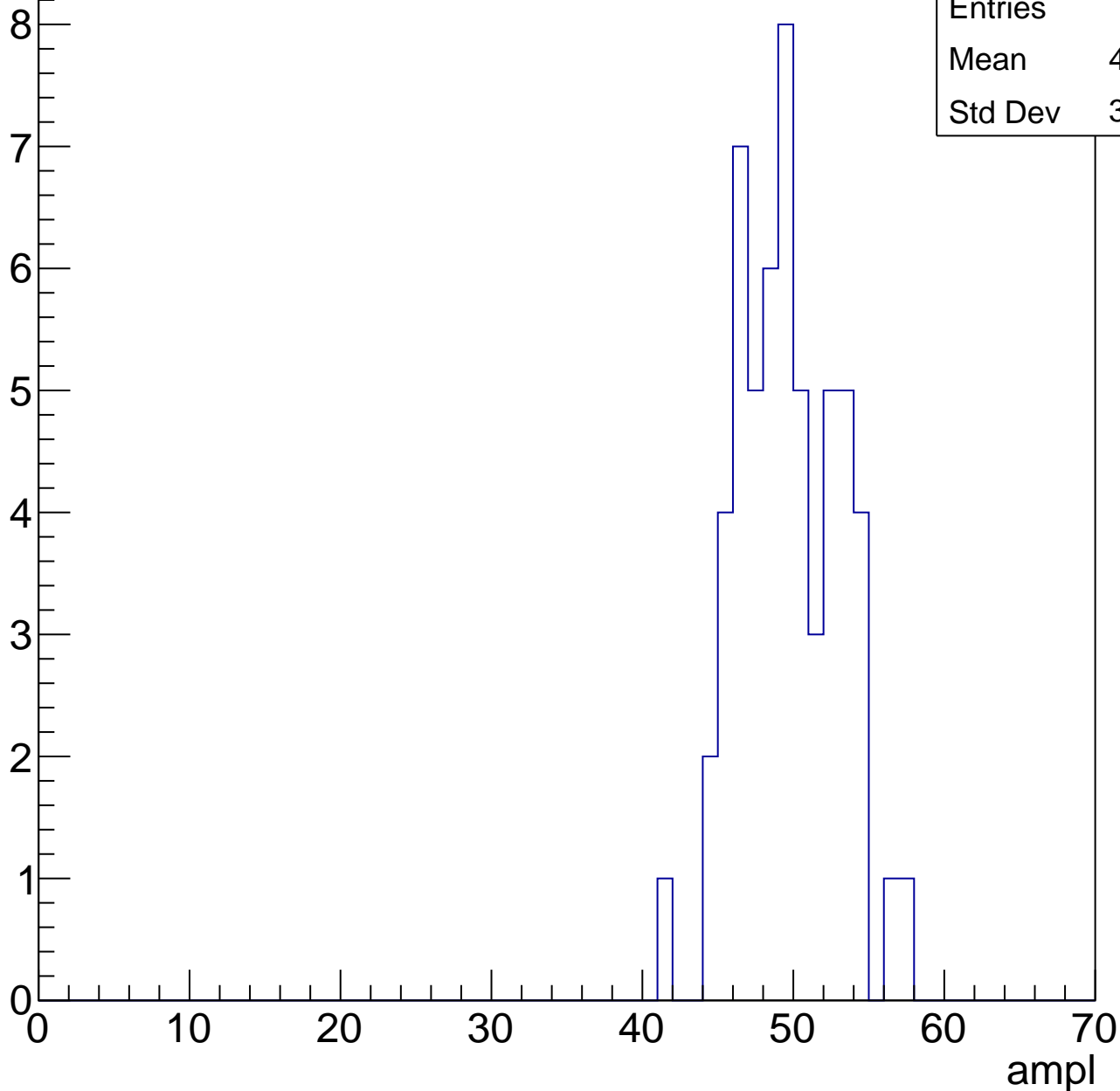


B1L003S, U16-ch13, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	49.18
Std Dev	3.299

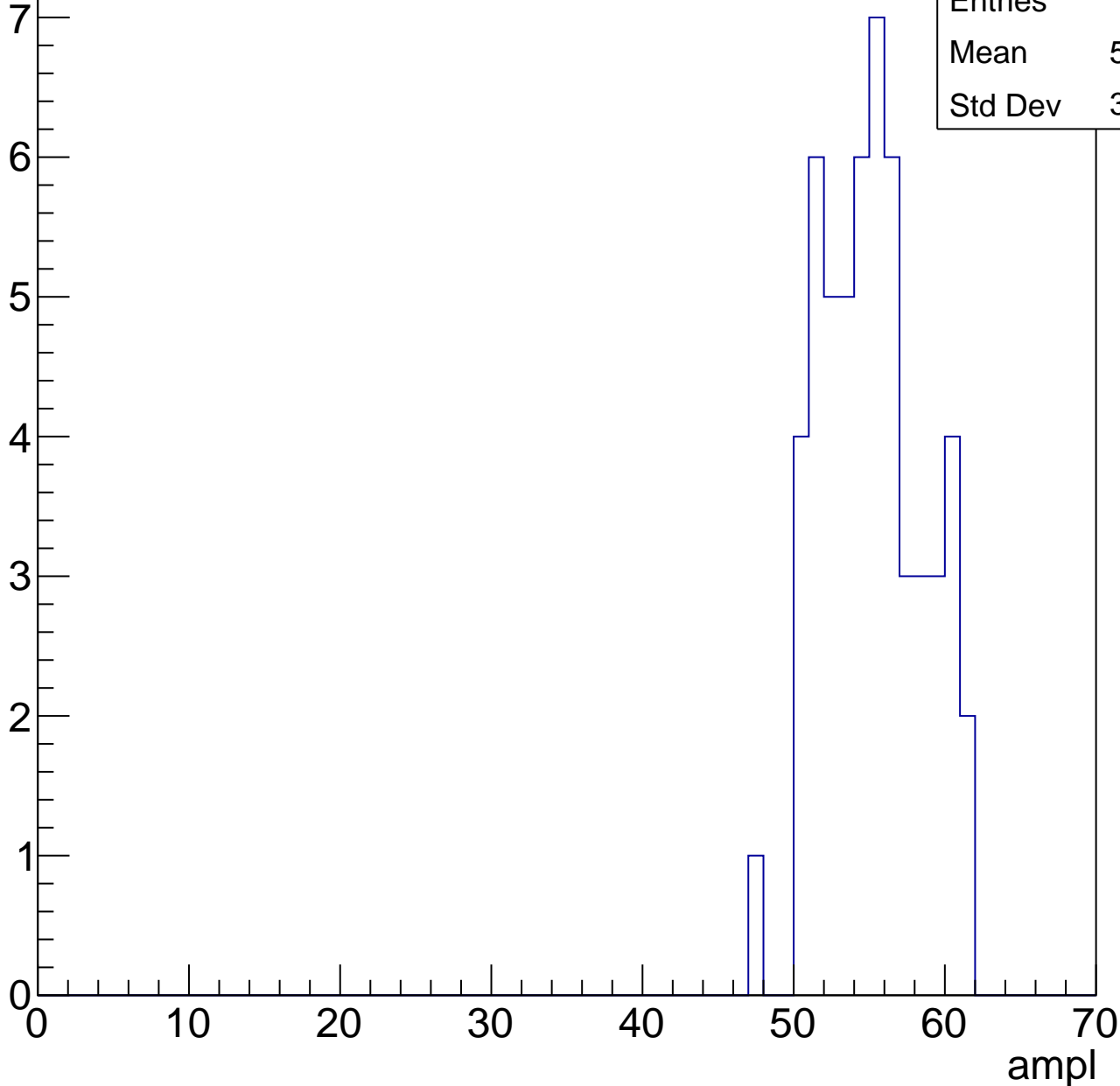


B1L003S, U16-ch13, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.67
Std Dev	3.276

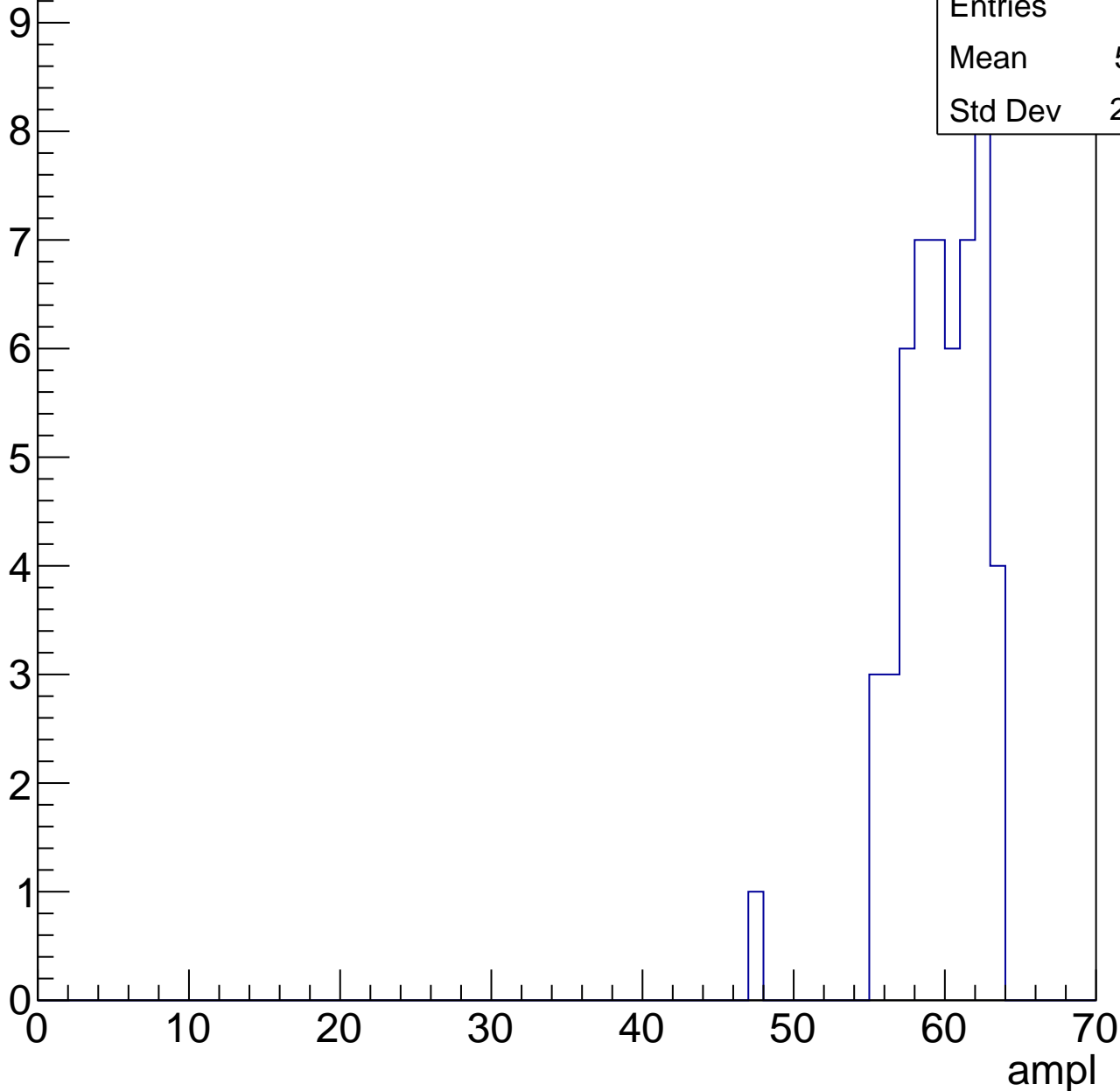


B1L003S, U16-ch13, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.21
Std Dev	2.837

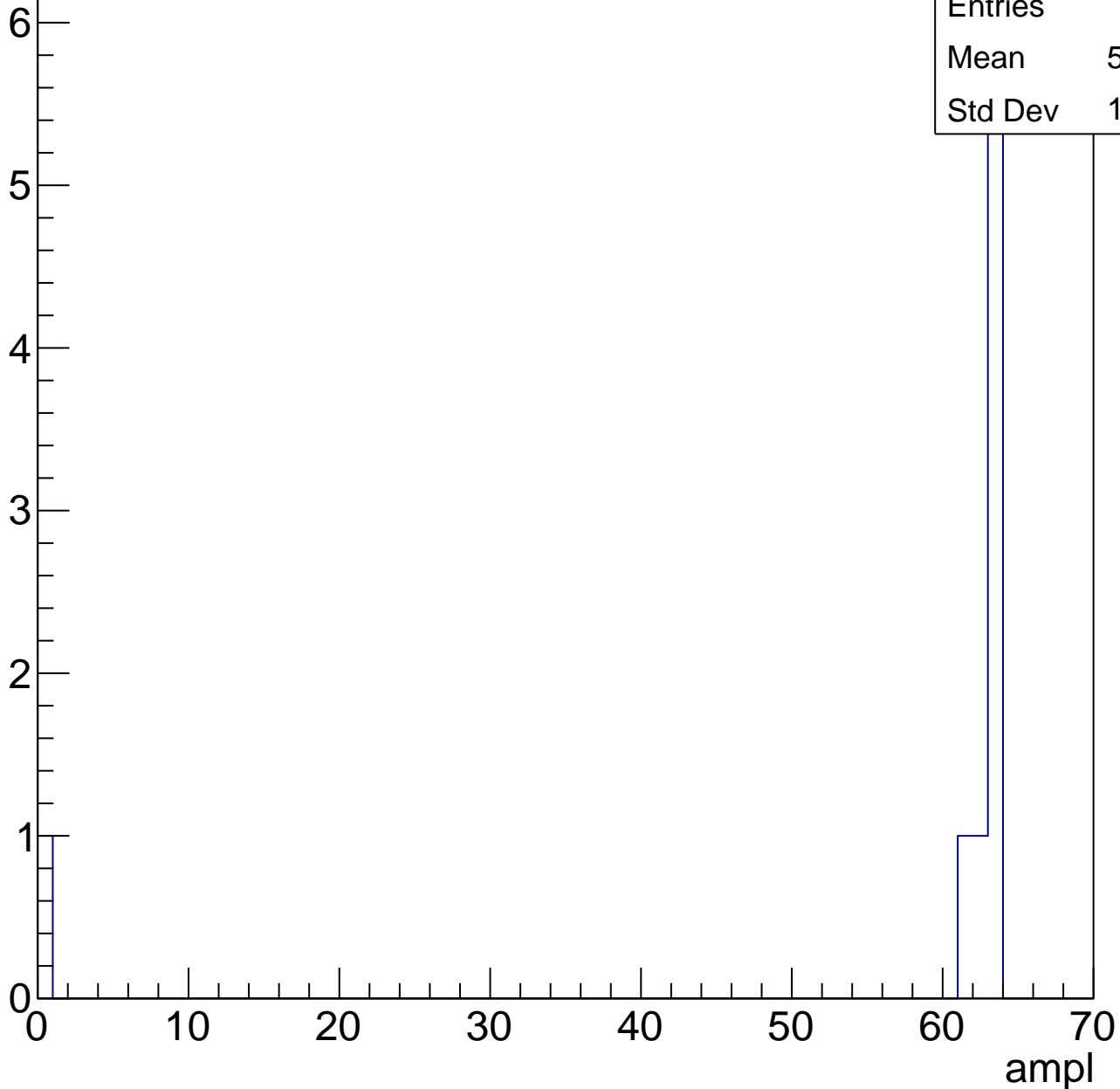


B1L003S, U16-ch13, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	55.67
Std Dev	19.69



B1L003S, U16-ch13, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch14, adc0

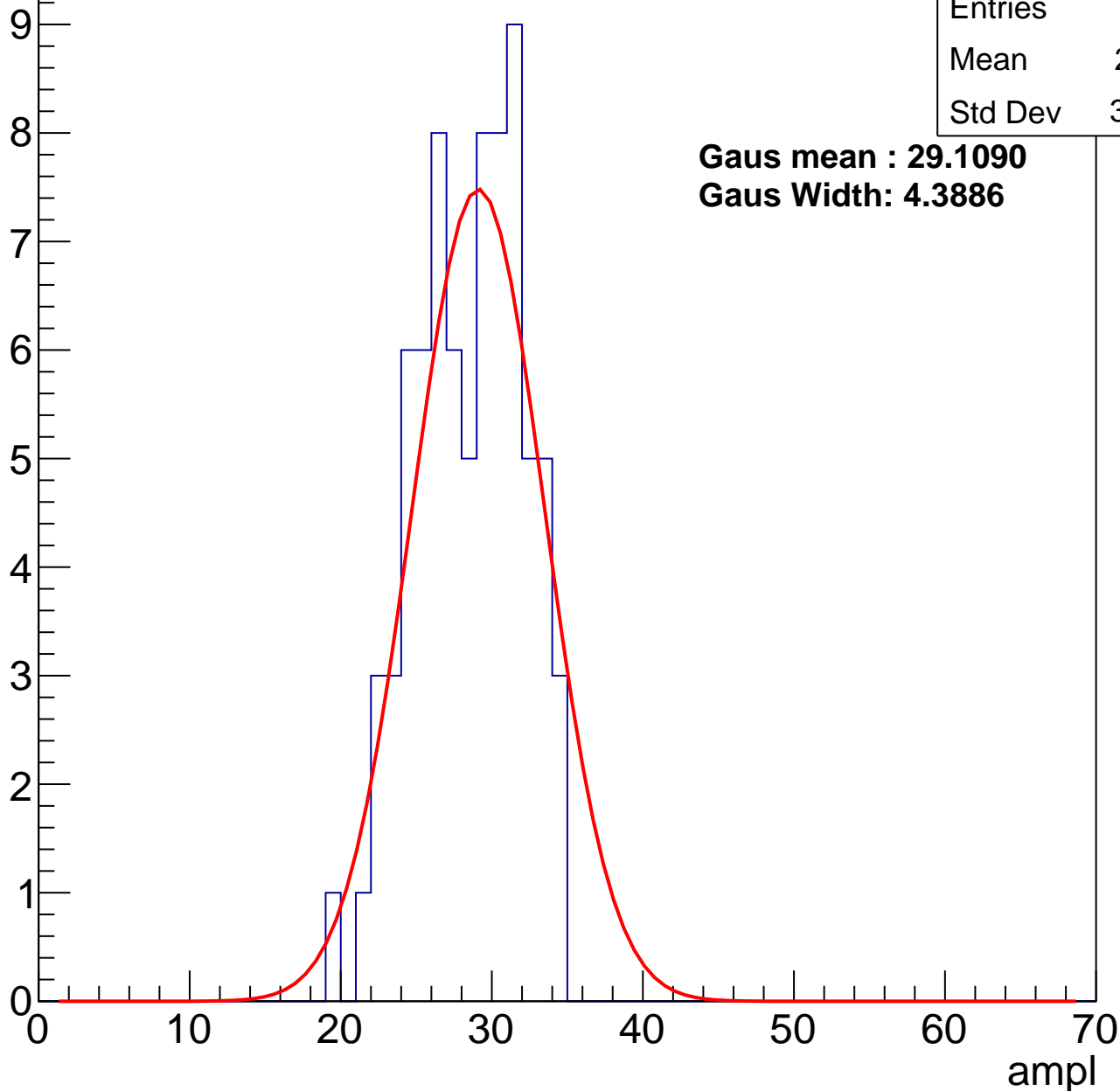
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	28.01
Std Dev	3.485

Gaus mean : 29.1090

Gaus Width: 4.3886



B1L003S, U16-ch14, adc1

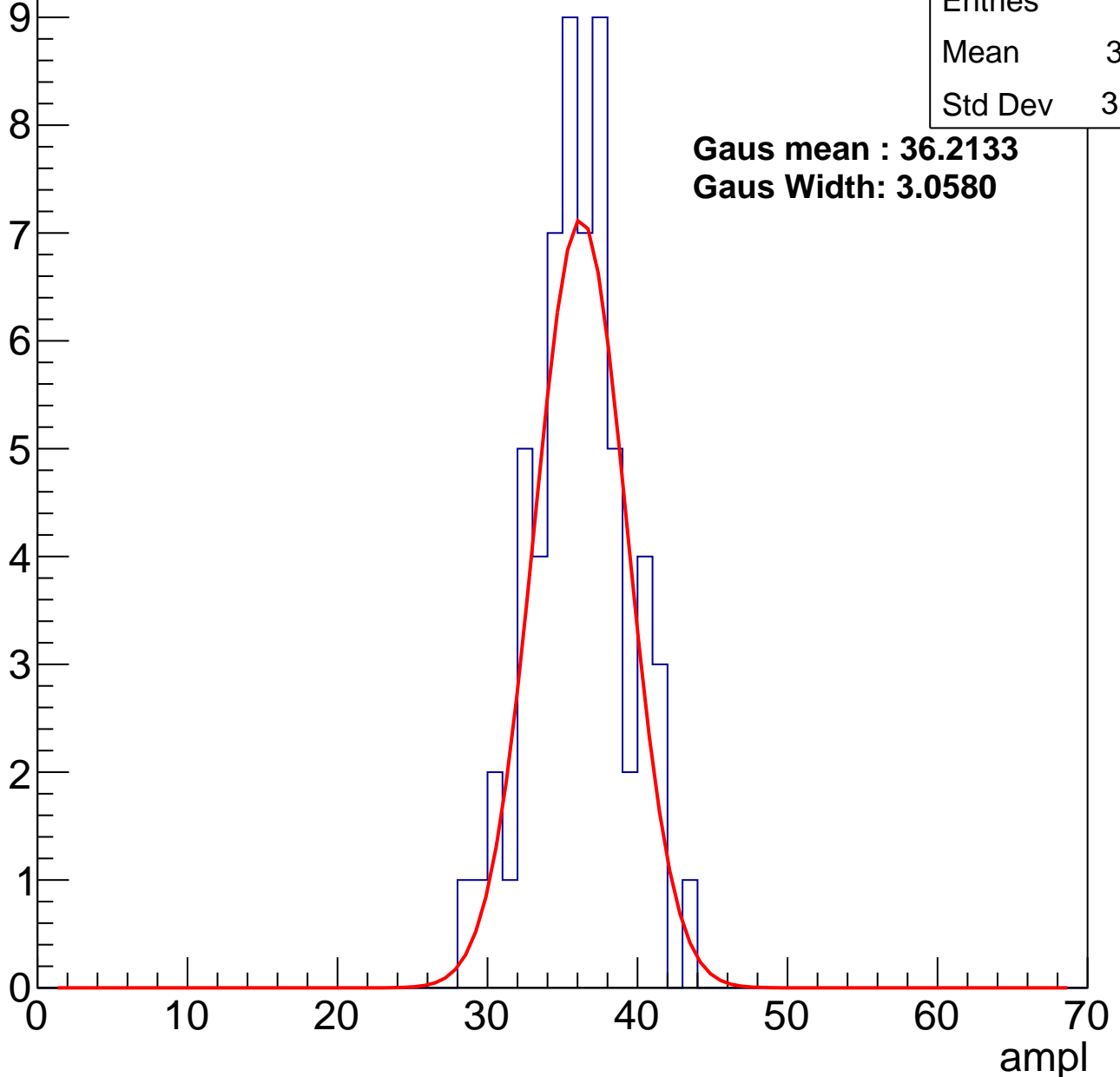
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.61
Std Dev	3.106

Gaus mean : 36.2133

Gaus Width: 3.0580



B1L003S, U16-ch14, adc2

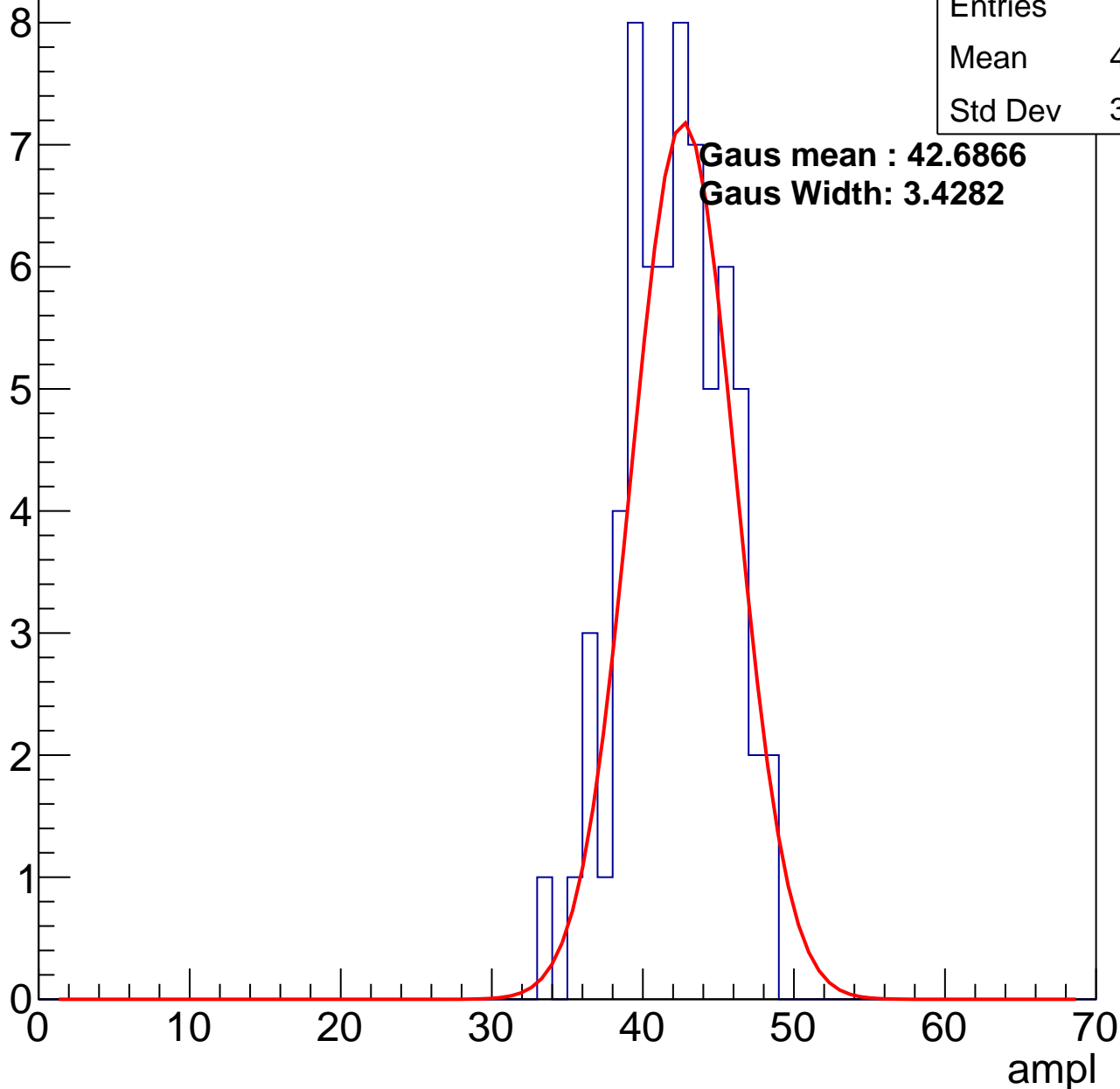
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.69
Std Dev	3.295

Gaus mean : 42.6866

Gaus Width: 3.4282



B1L003S, U16-ch14, adc3

calib_packv5_042523_0143.root, FC#13, port D2

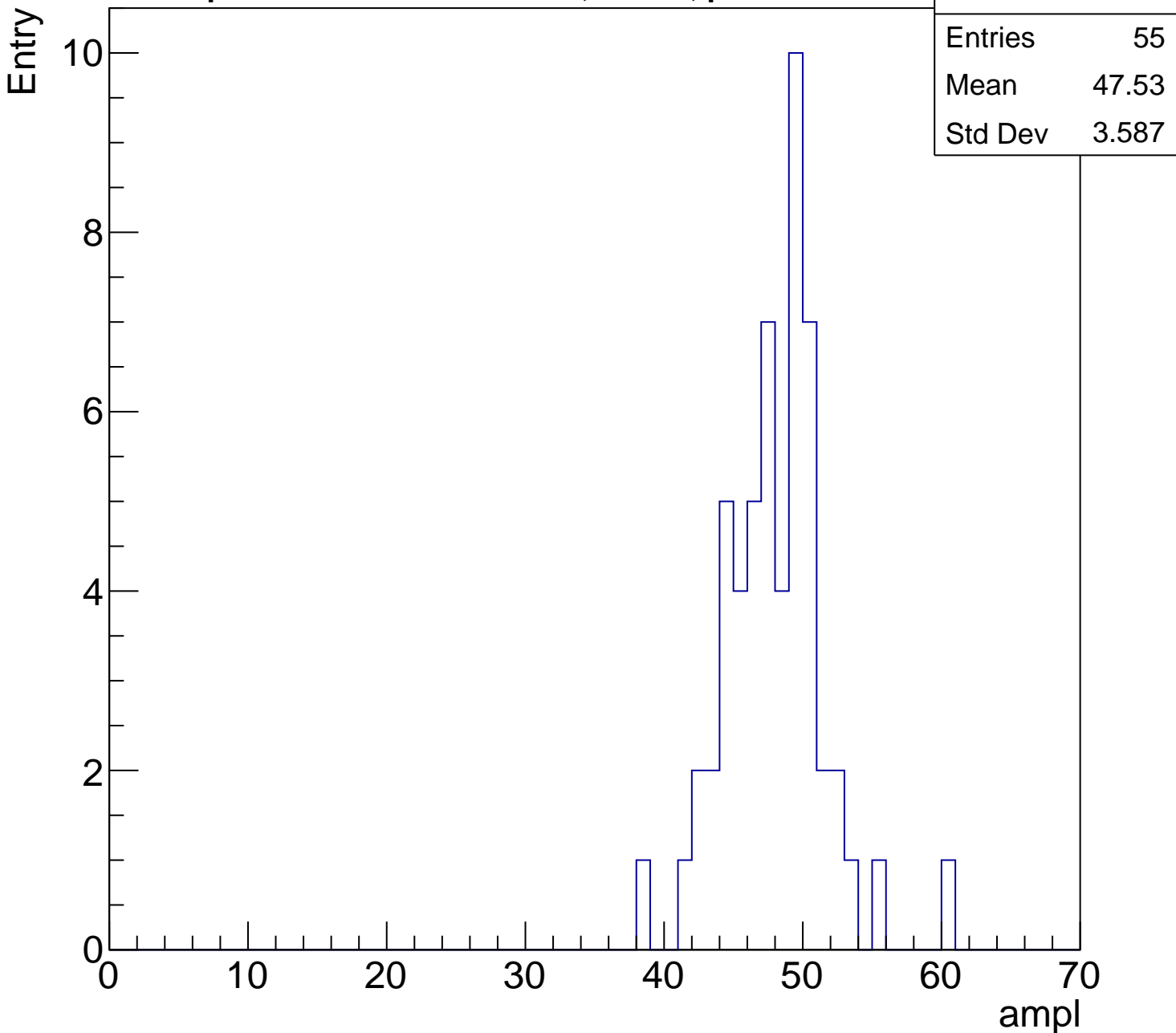
Entries	55
Mean	47.53
Std Dev	3.587

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

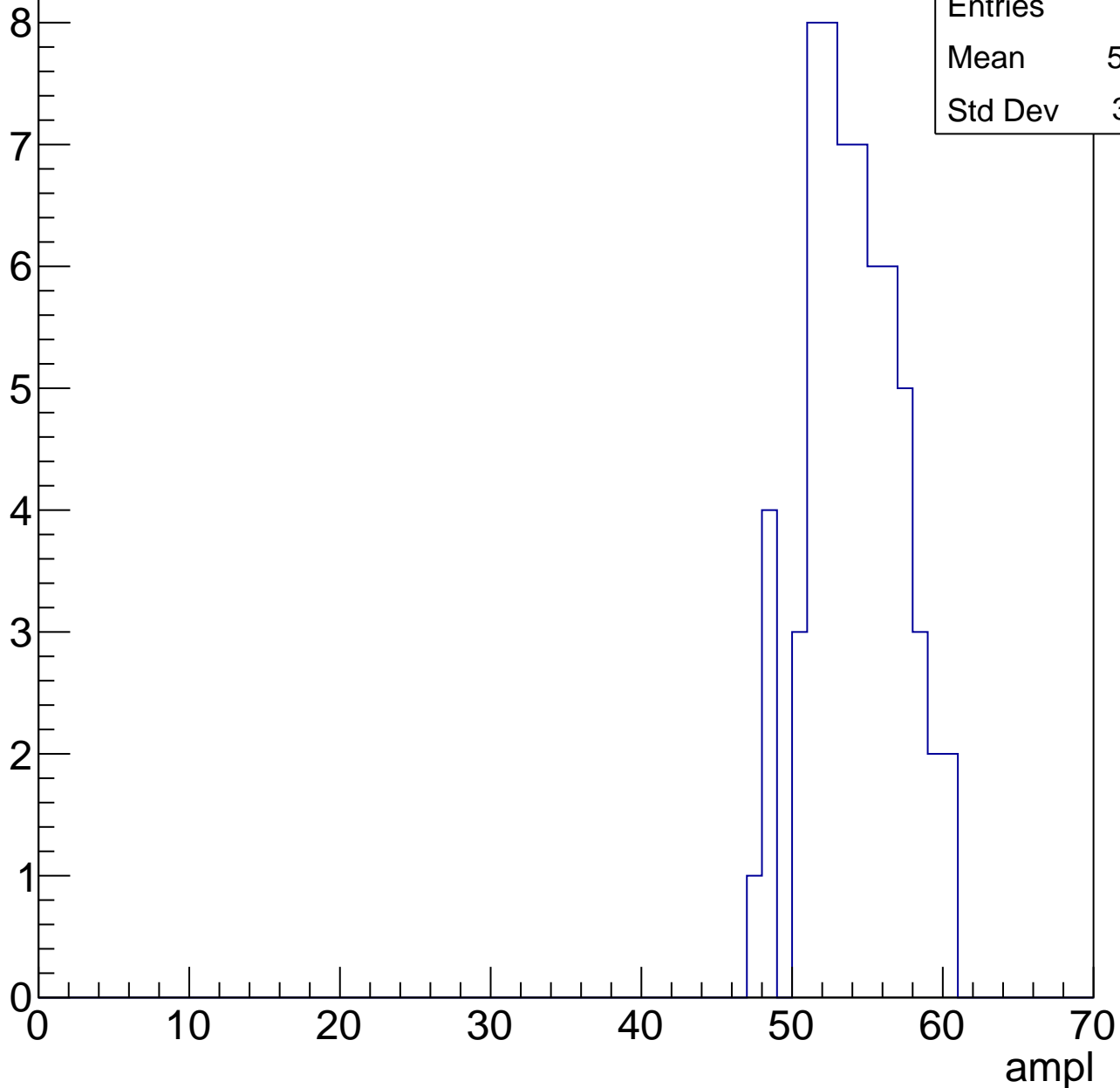


B1L003S, U16-ch14, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

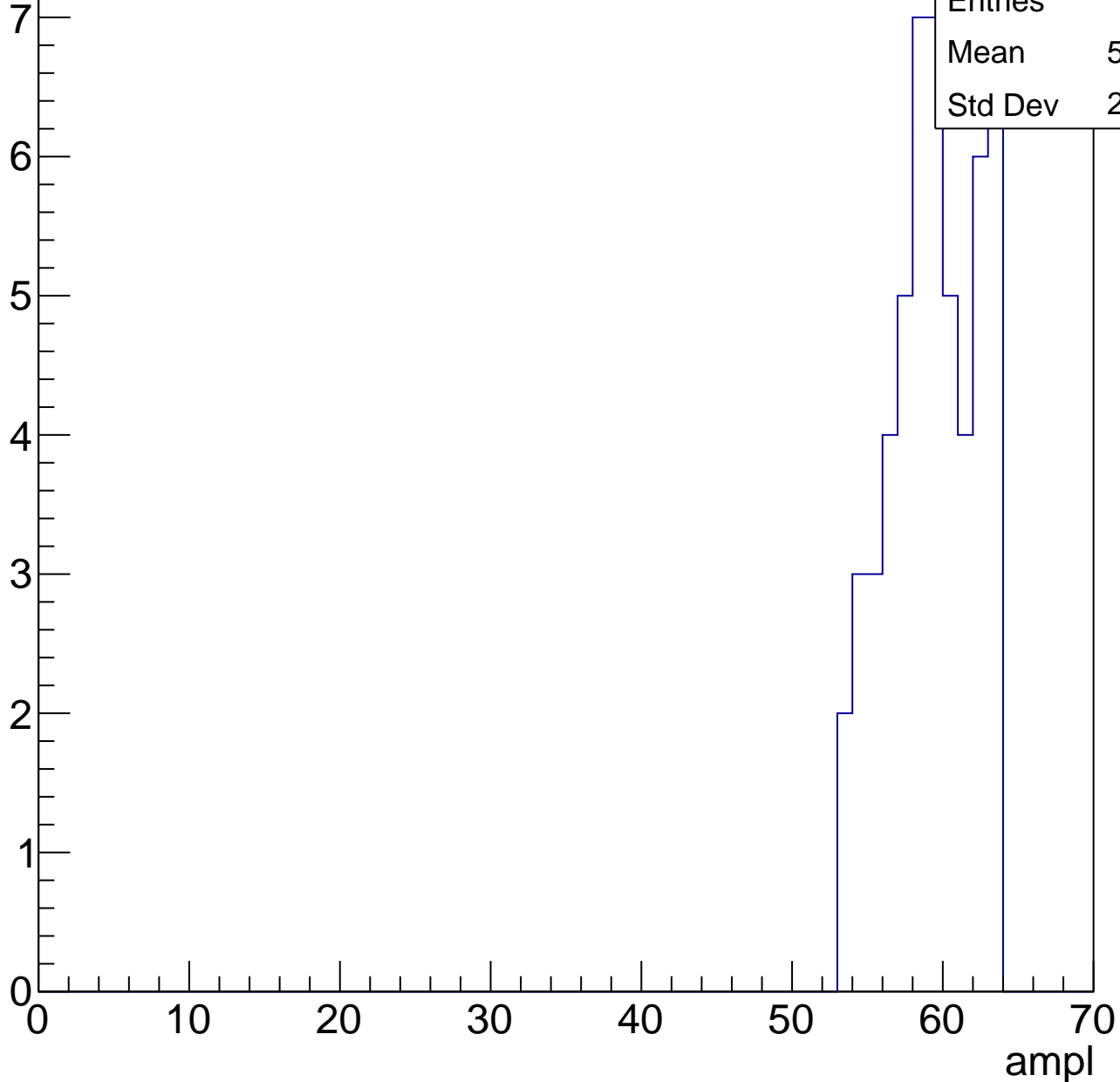
Entries	62
Mean	53.63
Std Dev	3.081



B1L003S, U16-ch14, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



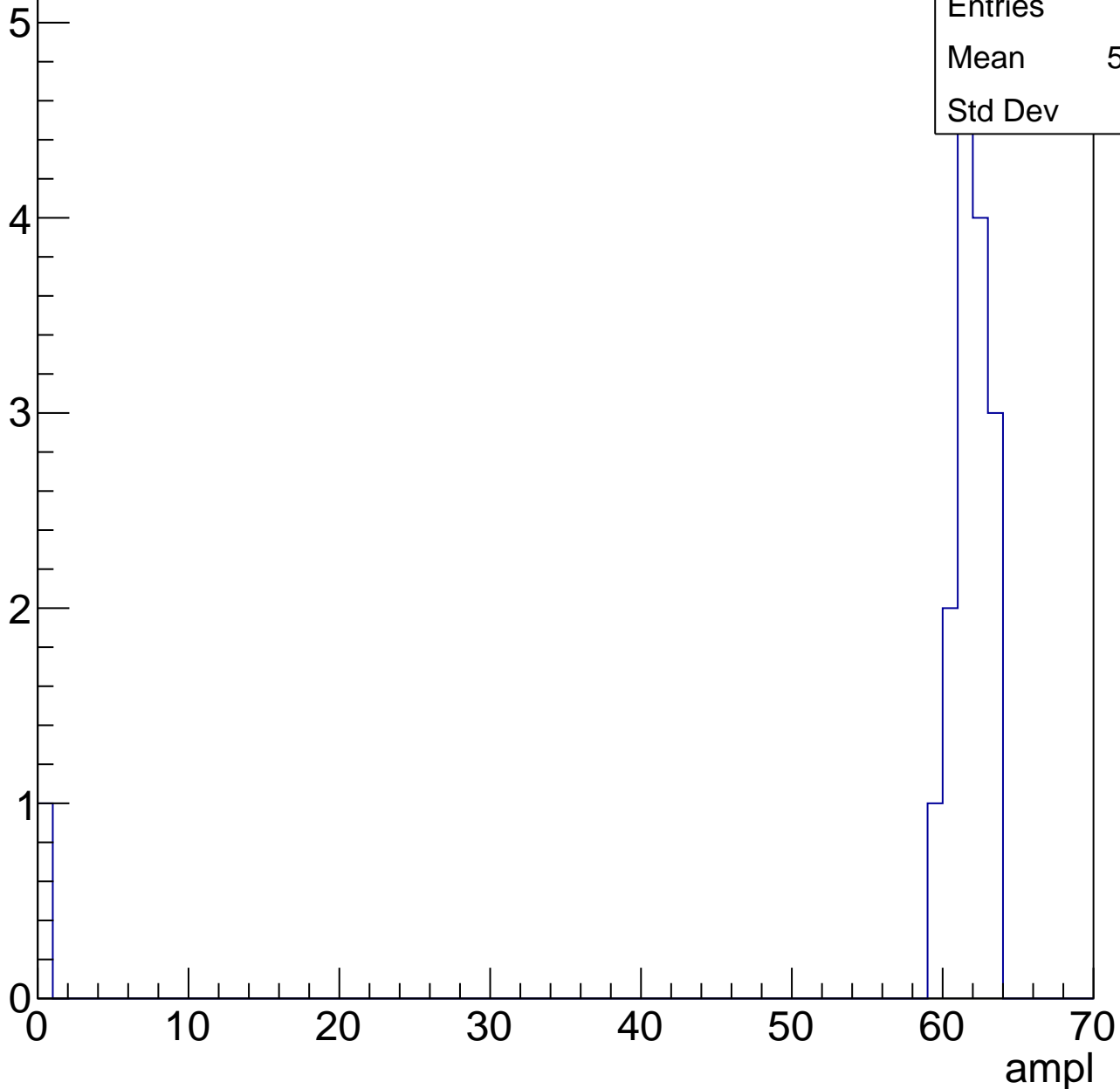
Entries	53
Mean	58.83
Std Dev	2.893

B1L003S, U16-ch14, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

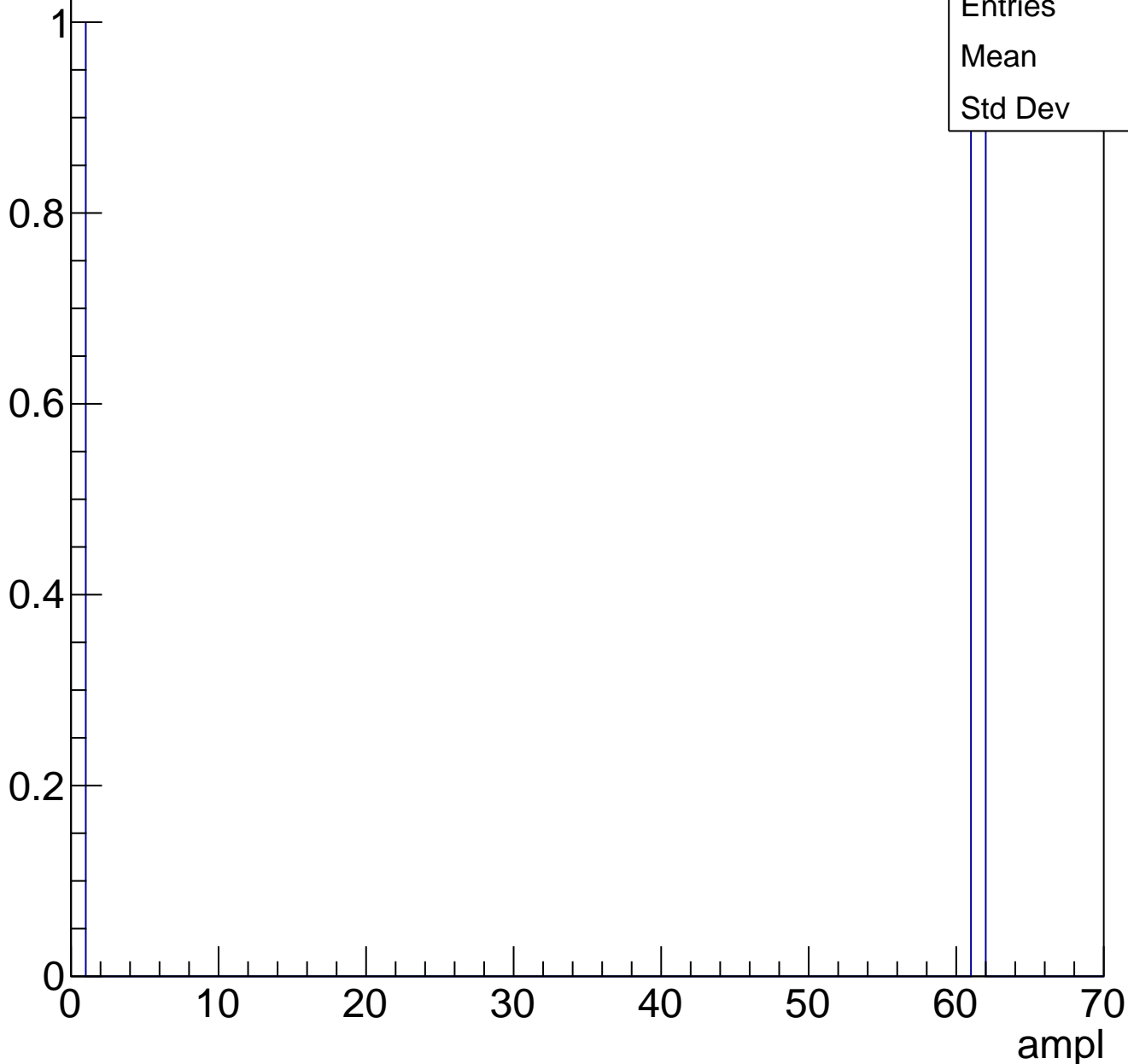
Entries	16
Mean	57.56
Std Dev	14.9



B1L003S, U16-ch14, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	30.5
Std Dev	30.5

B1L003S, U16-ch15, adc0

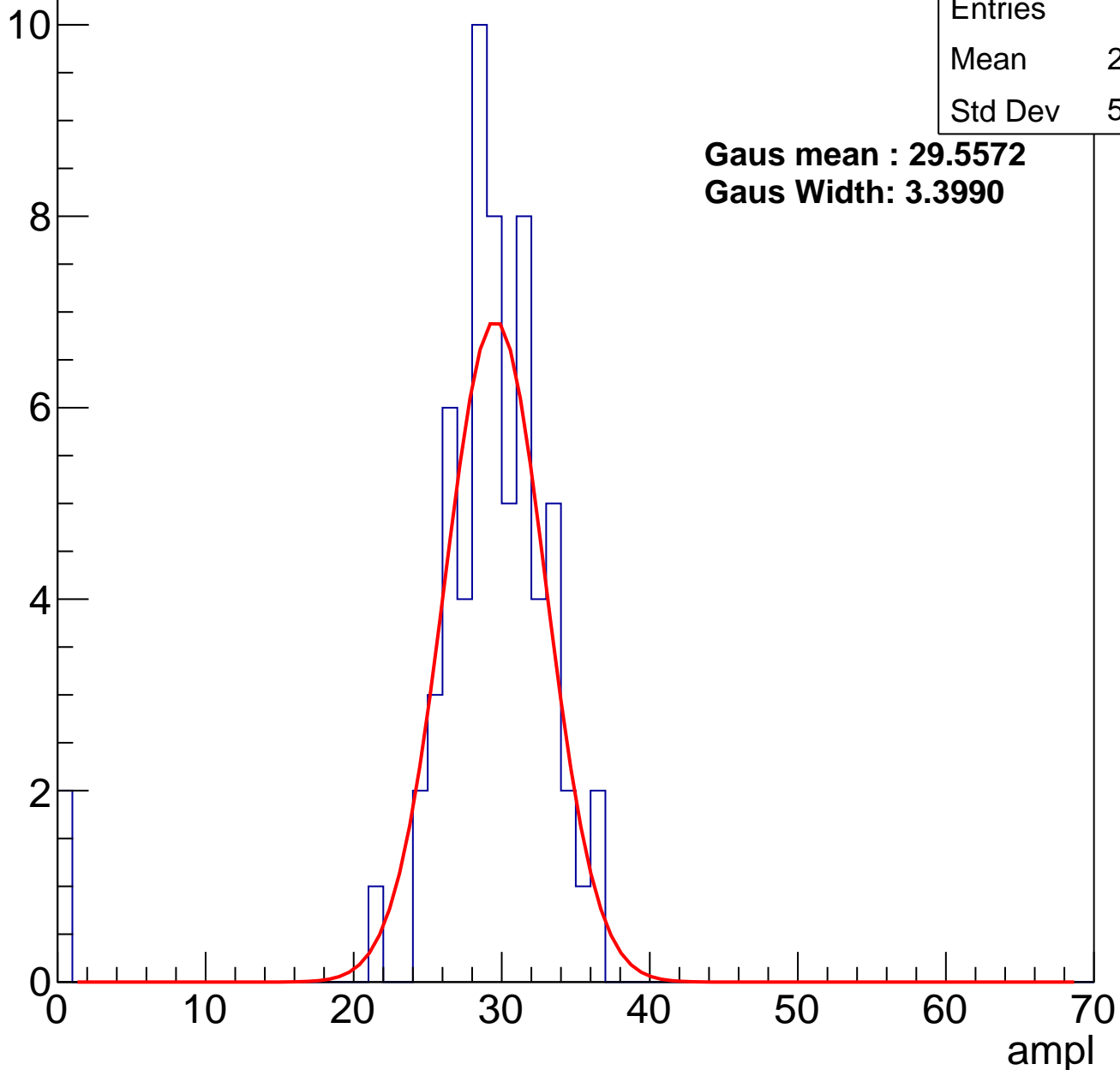
calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	28.35
Std Dev	5.955

Gaus mean : 29.5572

Gaus Width: 3.3990

Entry



B1L003S, U16-ch15, adc1

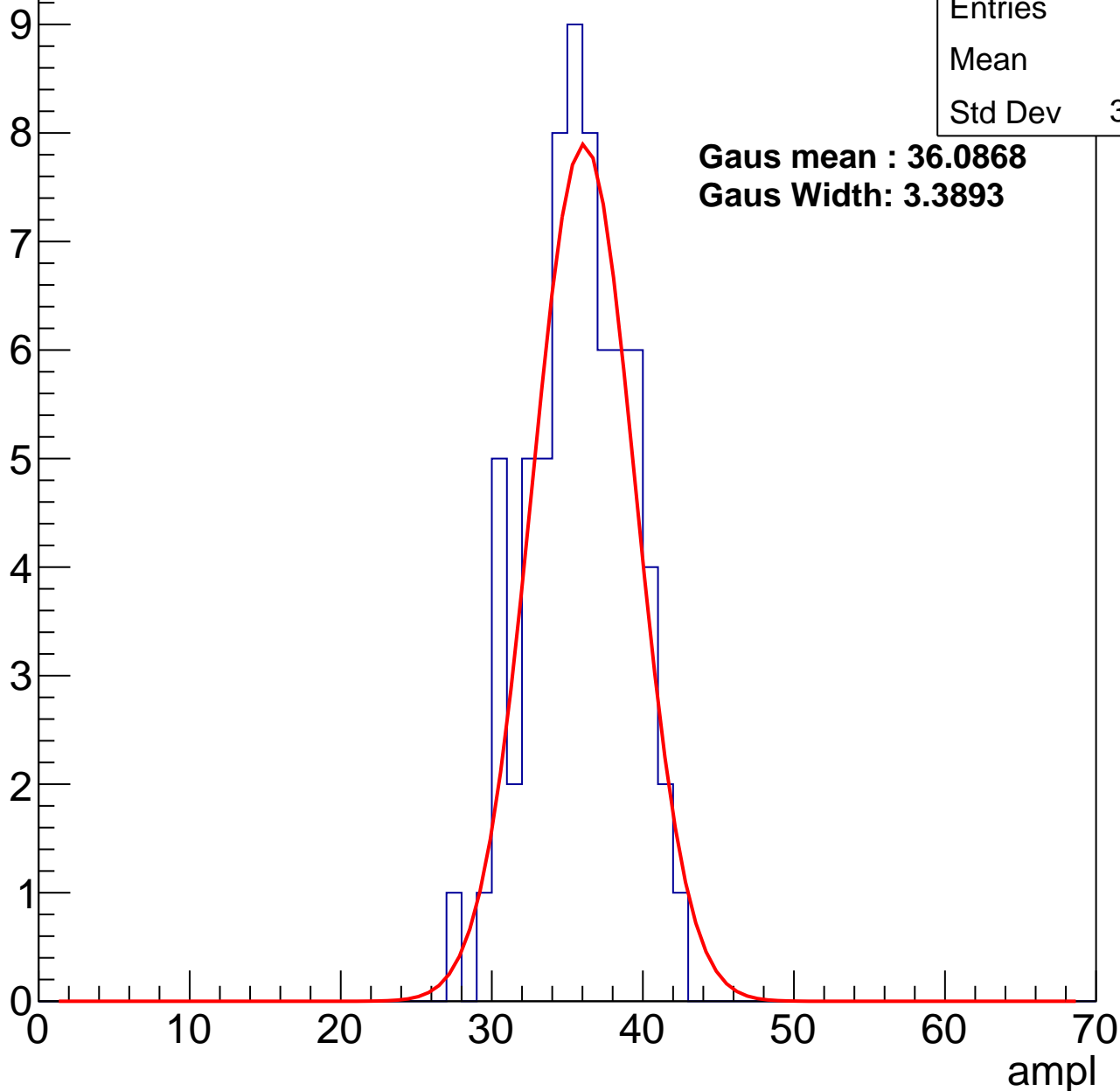
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.3
Std Dev	3.245

Gaus mean : 36.0868

Gaus Width: 3.3893



B1L003S, U16-ch15, adc2

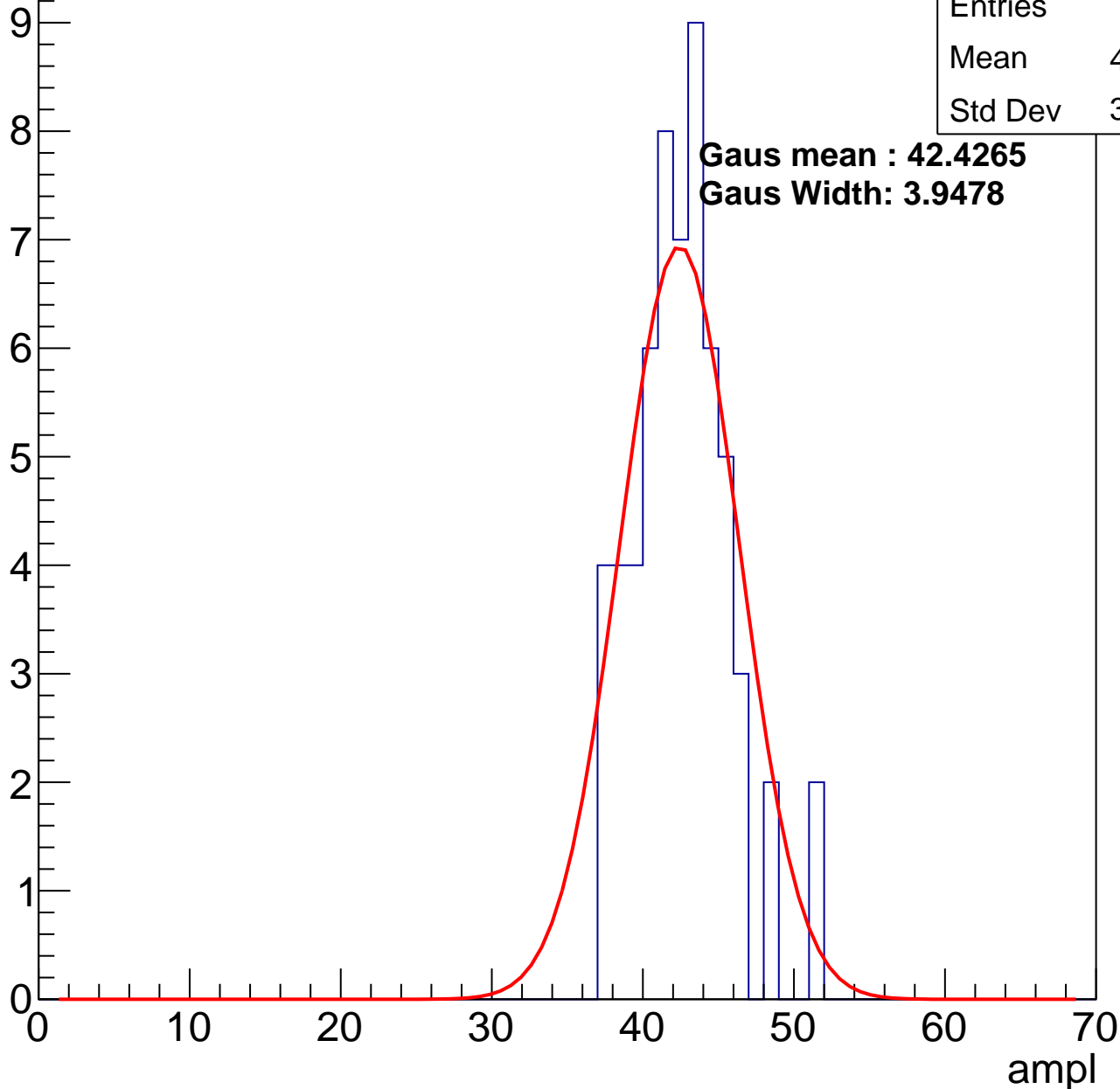
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.17
Std Dev	3.137

Gaus mean : 42.4265

Gaus Width: 3.9478



B1L003S, U16-ch15, adc3

calib_packv5_042523_0143.root, FC#13, port D2

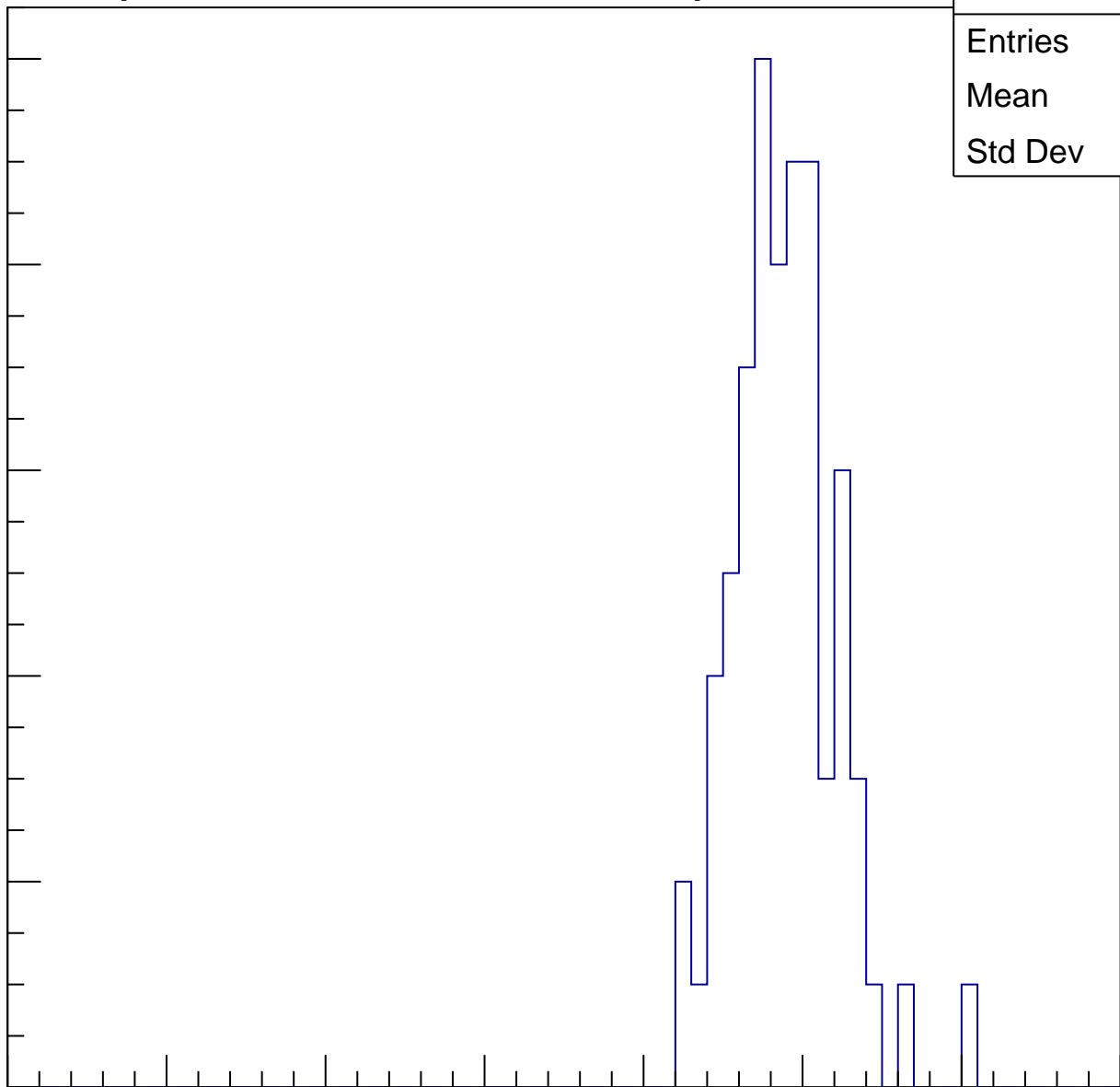
Entries	70
Mean	48.41
Std Dev	3.205

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

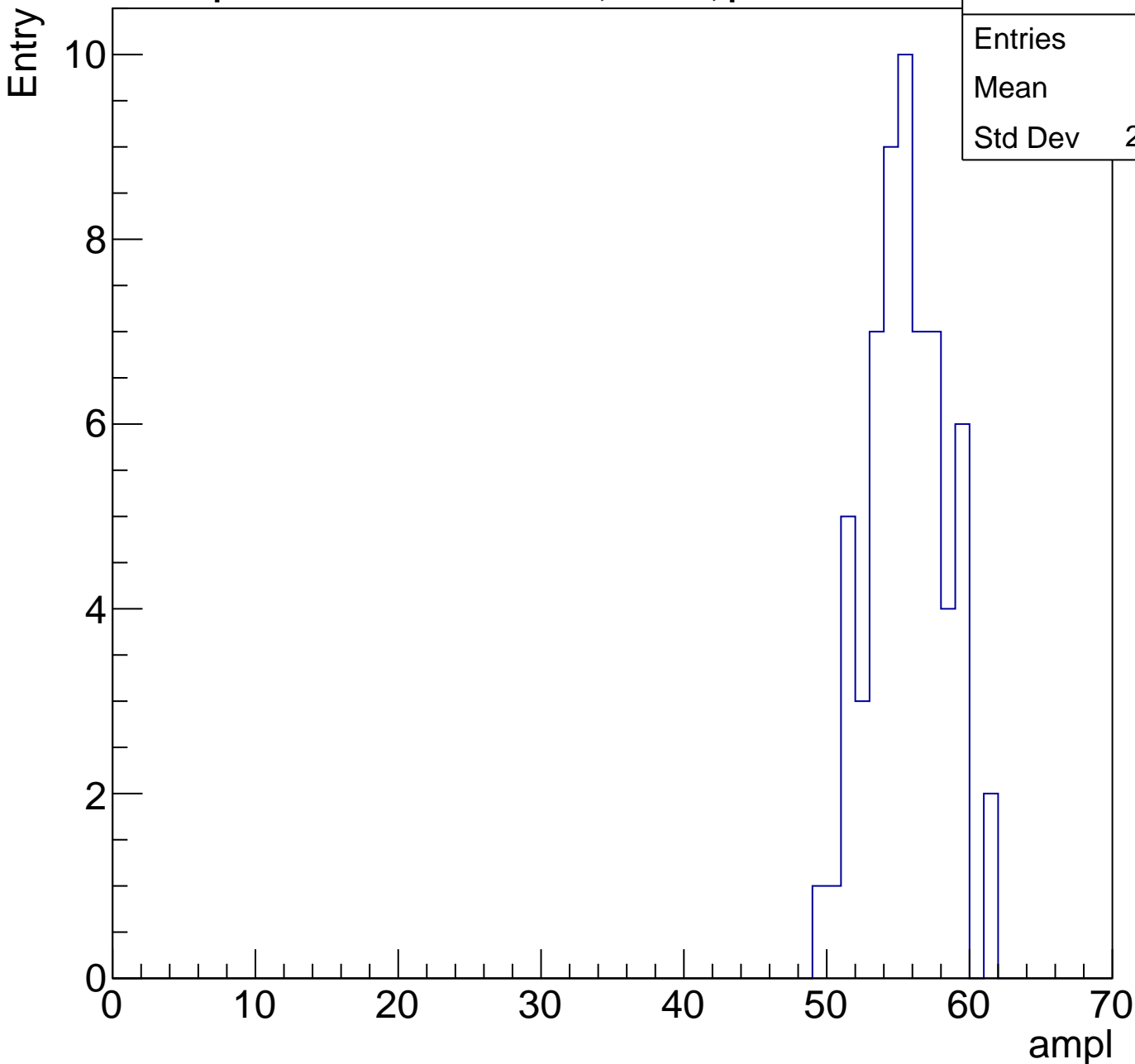
ampl



B1L003S, U16-ch15, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	55.1
Std Dev	2.674

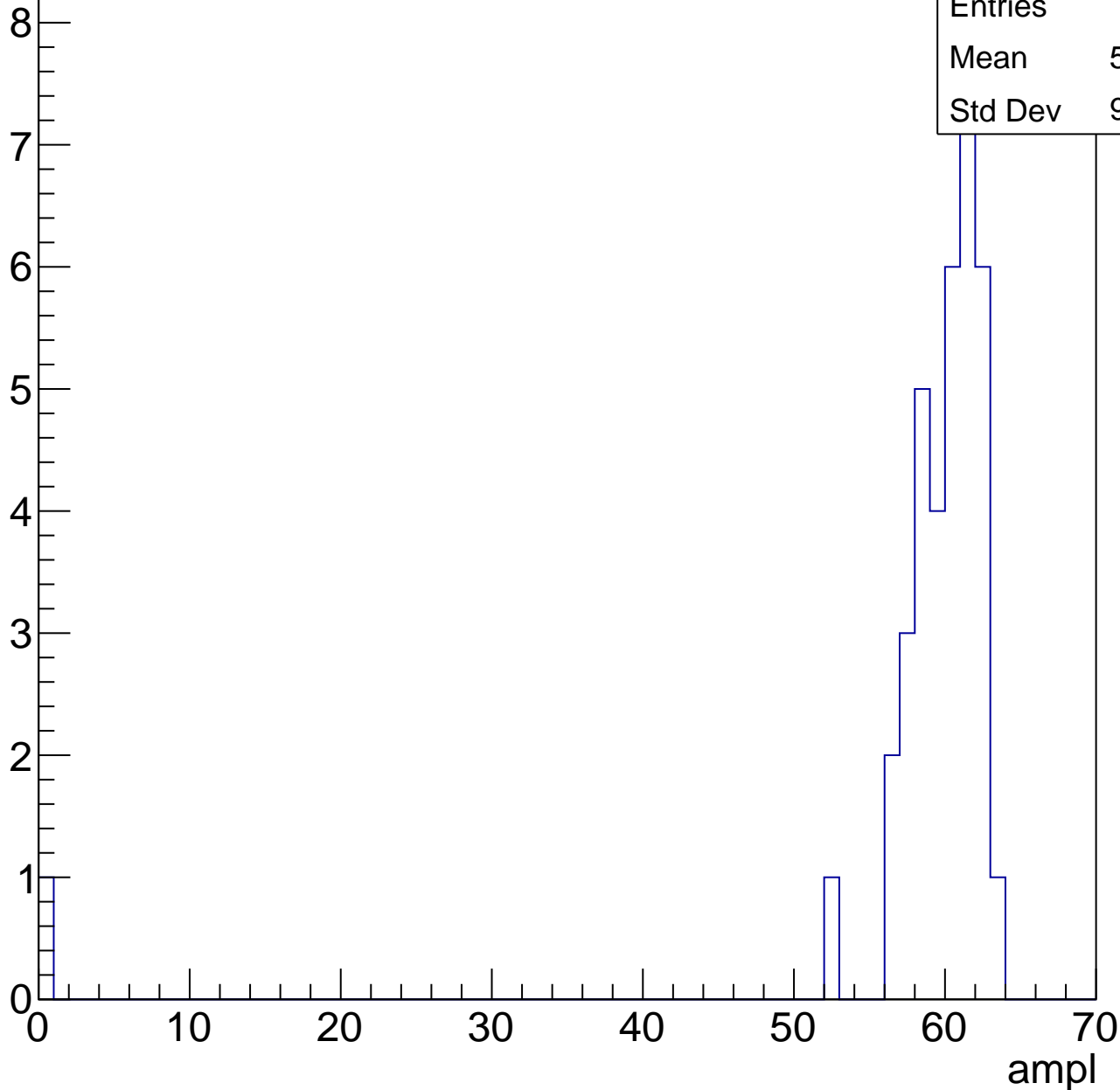


B1L003S, U16-ch15, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	57.95
Std Dev	9.908

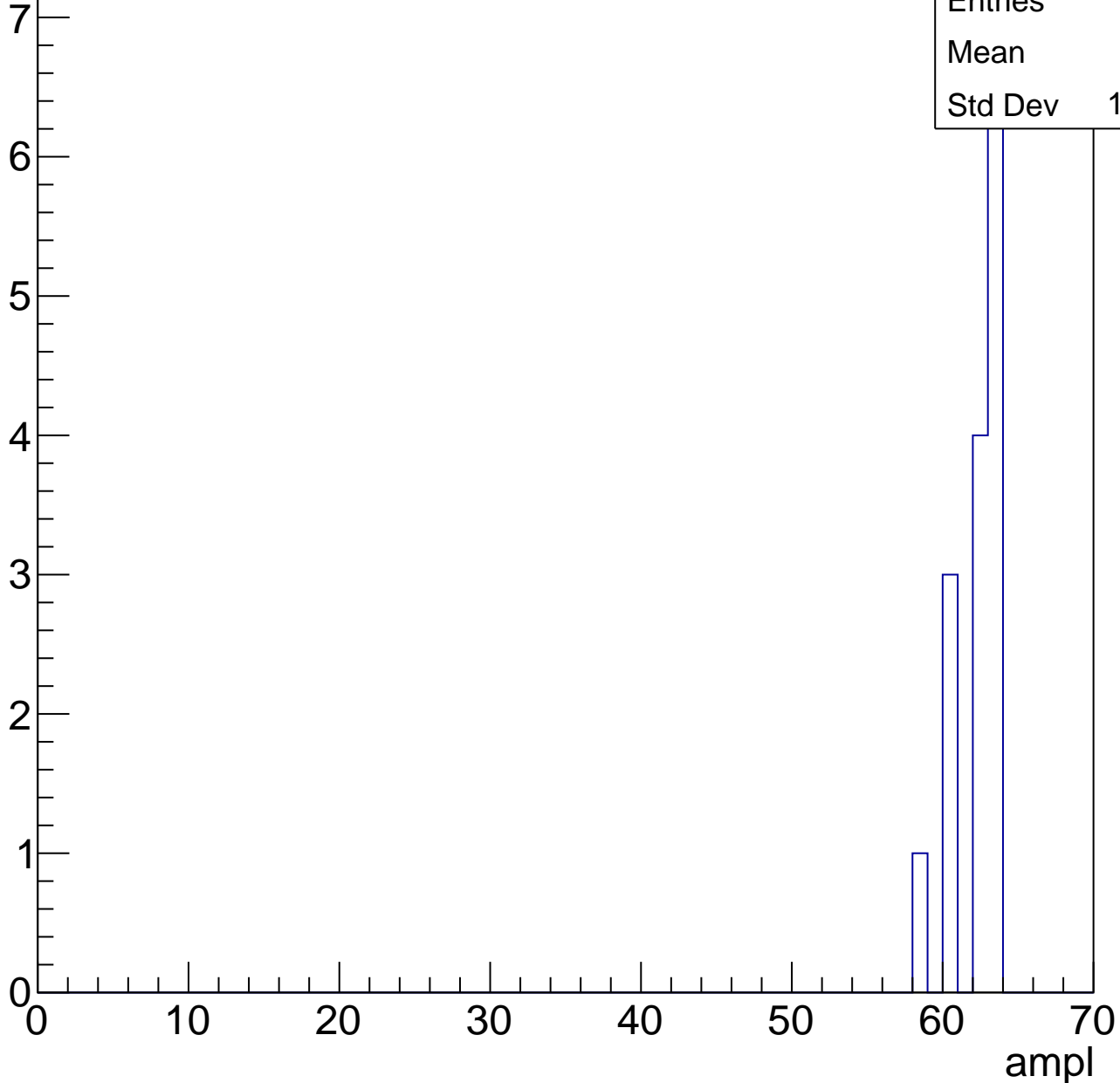


B1L003S, U16-ch15, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.8
Std Dev	1.514



B1L003S, U16-ch15, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch16, adc0

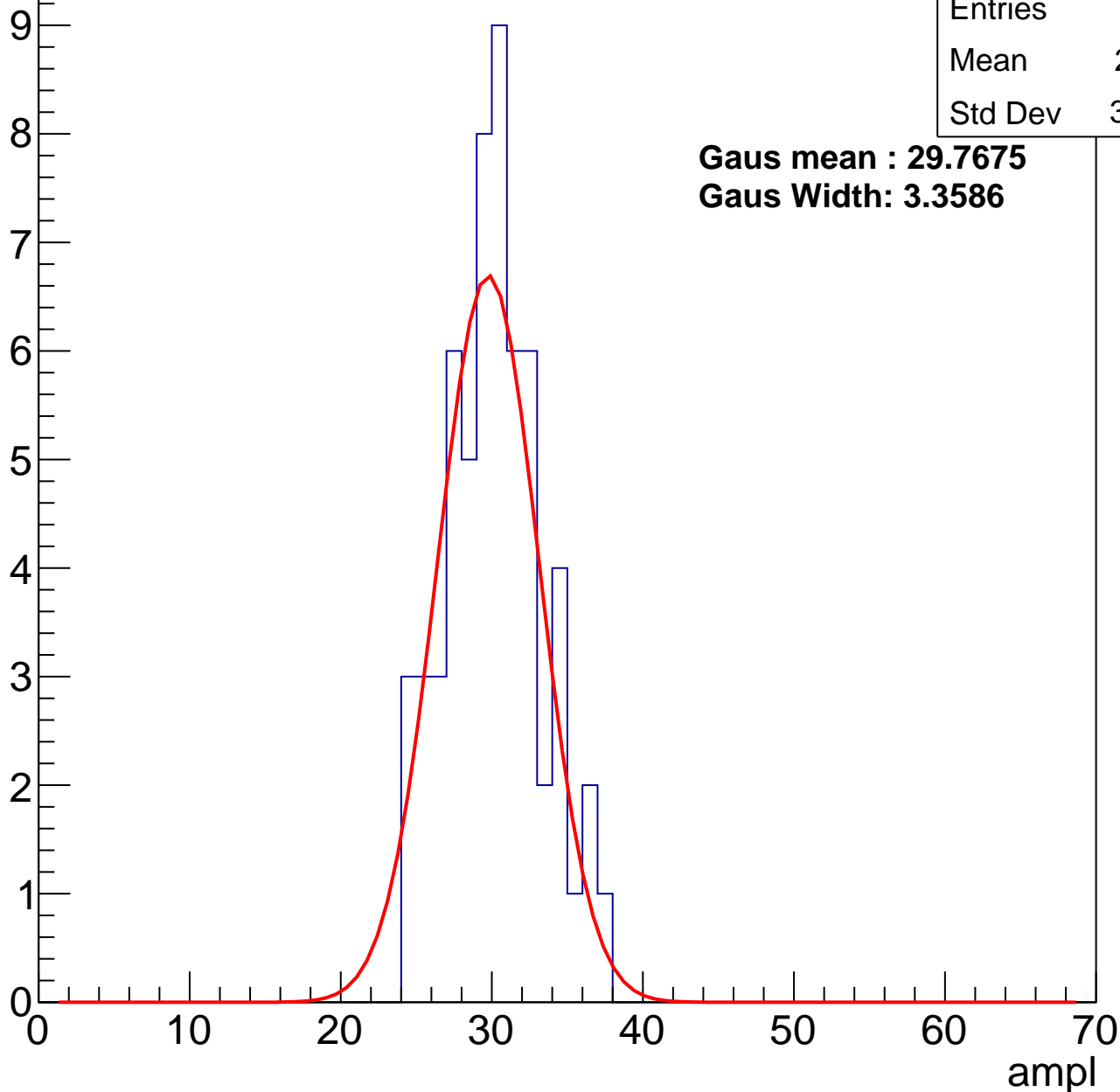
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.71
Std Dev	3.098

Gaus mean : 29.7675

Gaus Width: 3.3586



B1L003S, U16-ch16, adc1

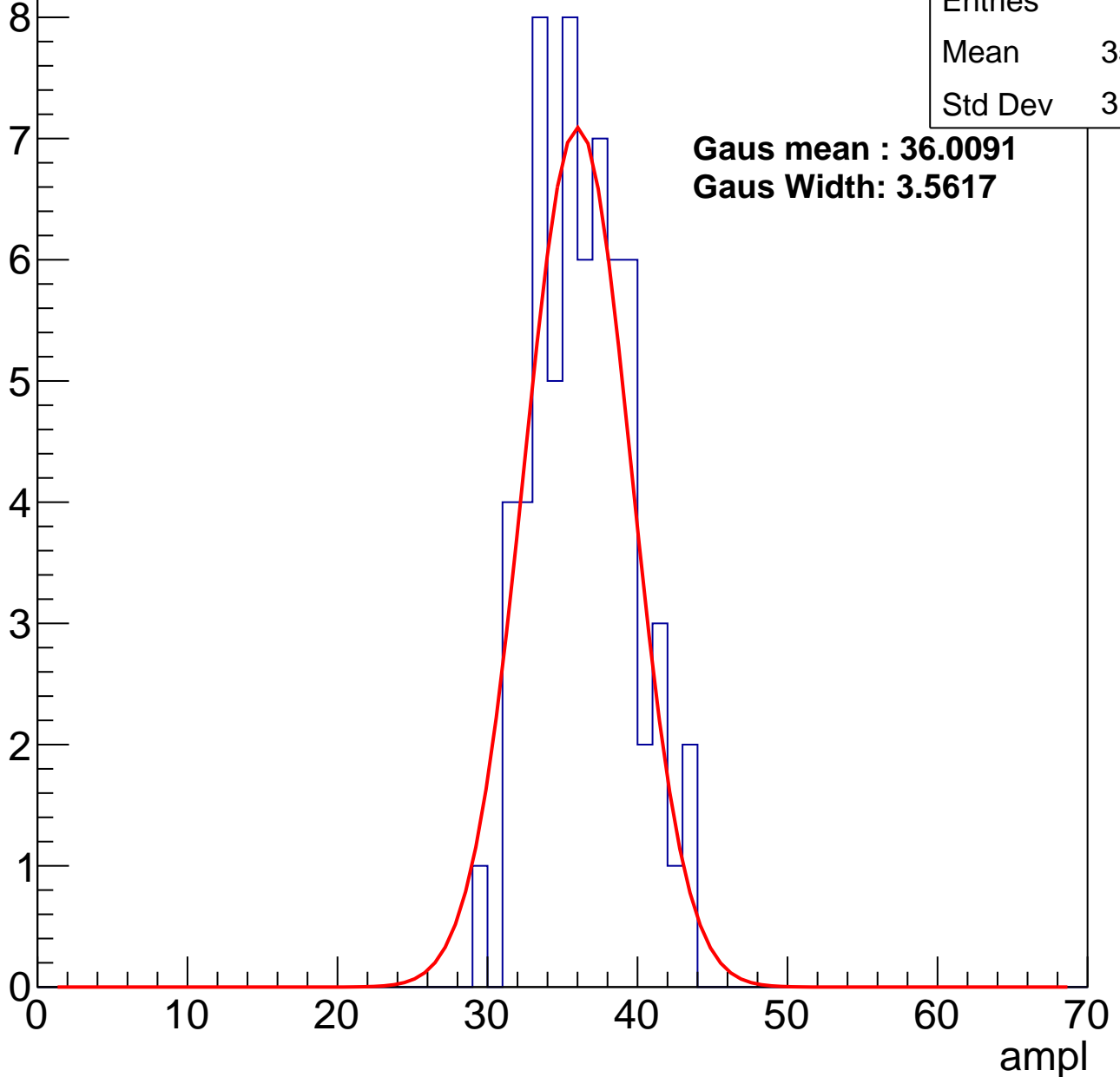
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.92
Std Dev	3.184

Gaus mean : 36.0091

Gaus Width: 3.5617



B1L003S, U16-ch16, adc2

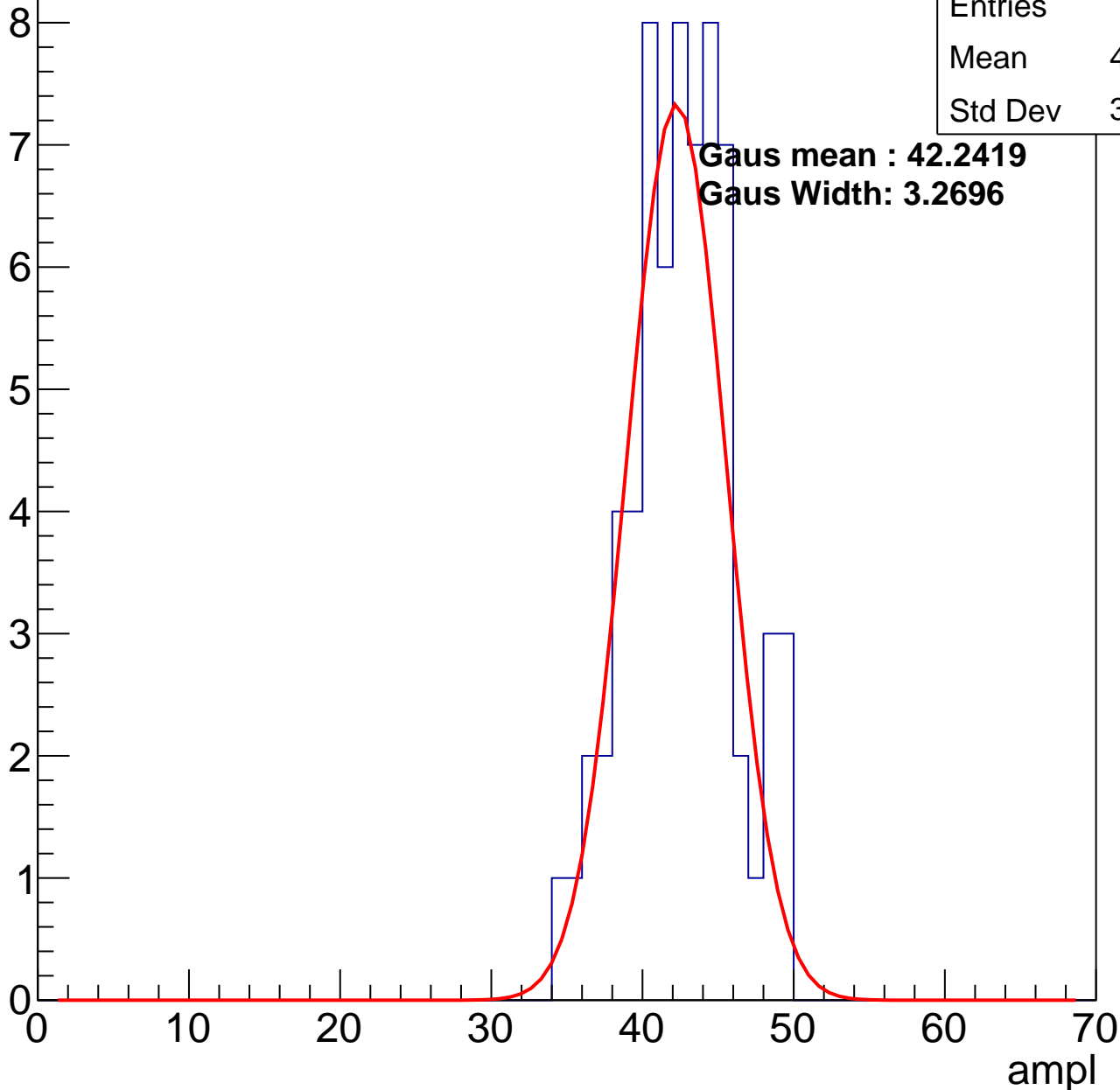
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.13
Std Dev	3.425

Gaus mean : 42.2419

Gaus Width: 3.2696

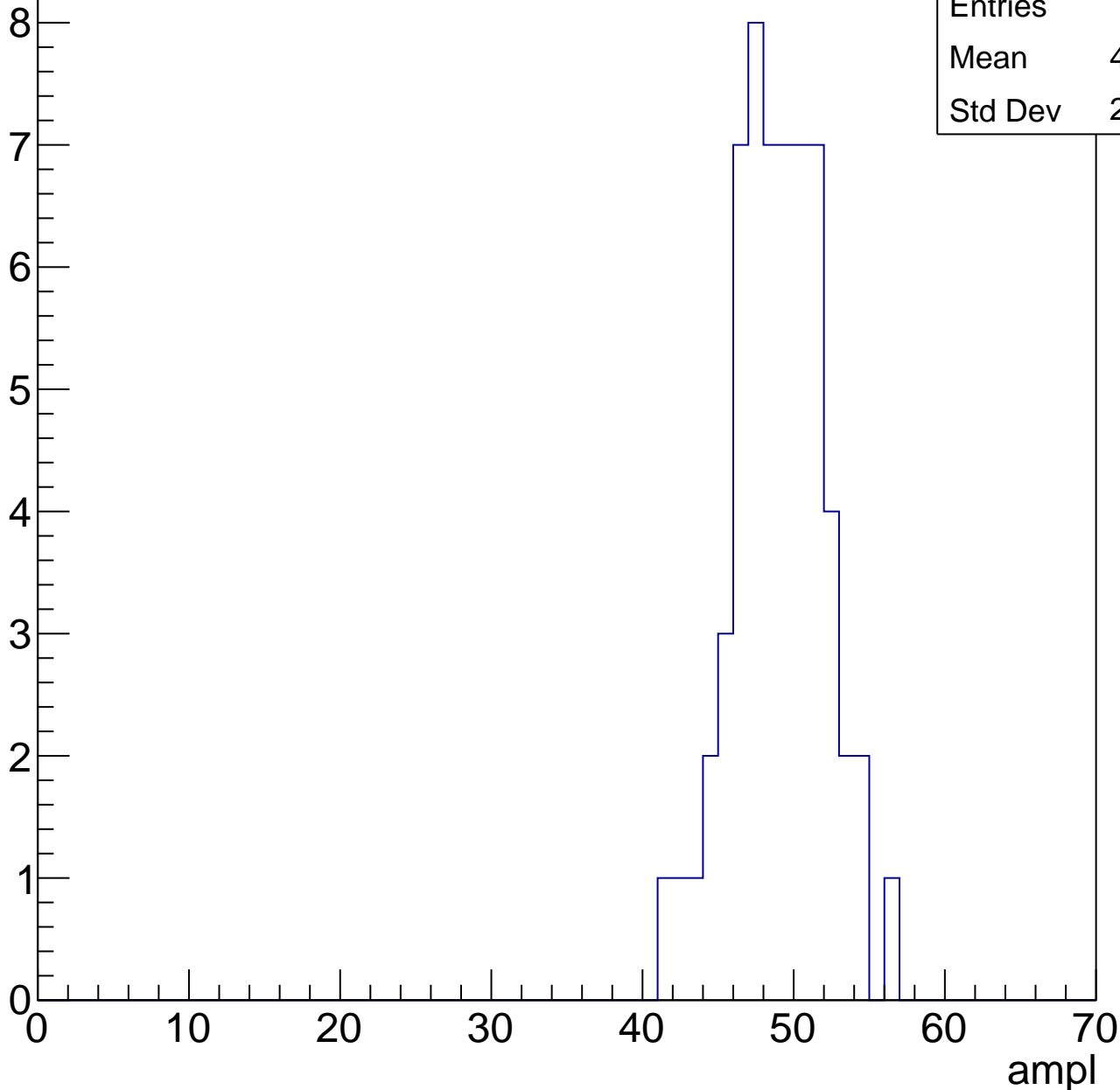


B1L003S, U16-ch16, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.52
Std Dev	2.992

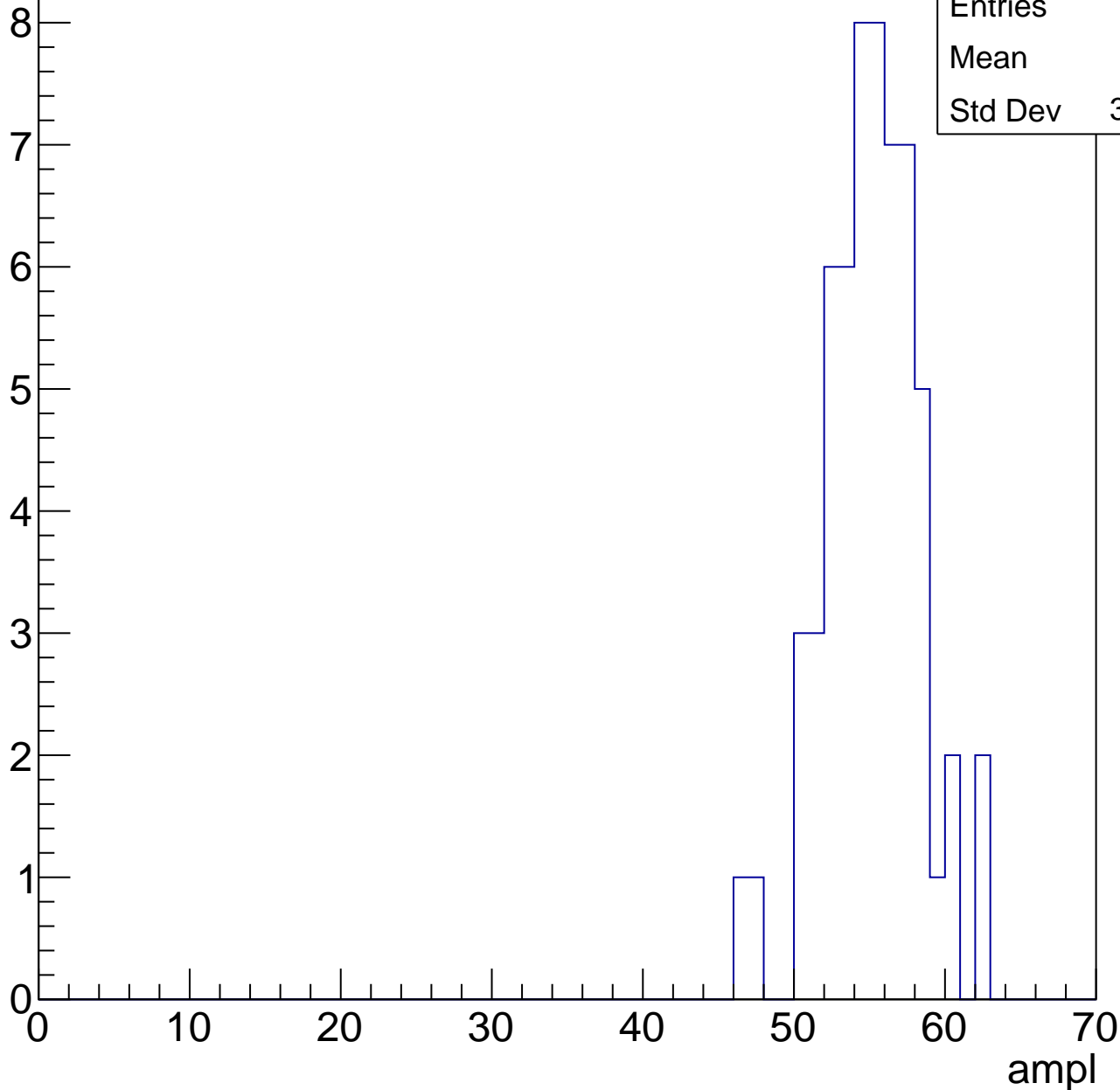


B1L003S, U16-ch16, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.7
Std Dev	3.143



B1L003S, U16-ch16, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries 42

Mean 59.5

Std Dev 2.593

0

10

20

30

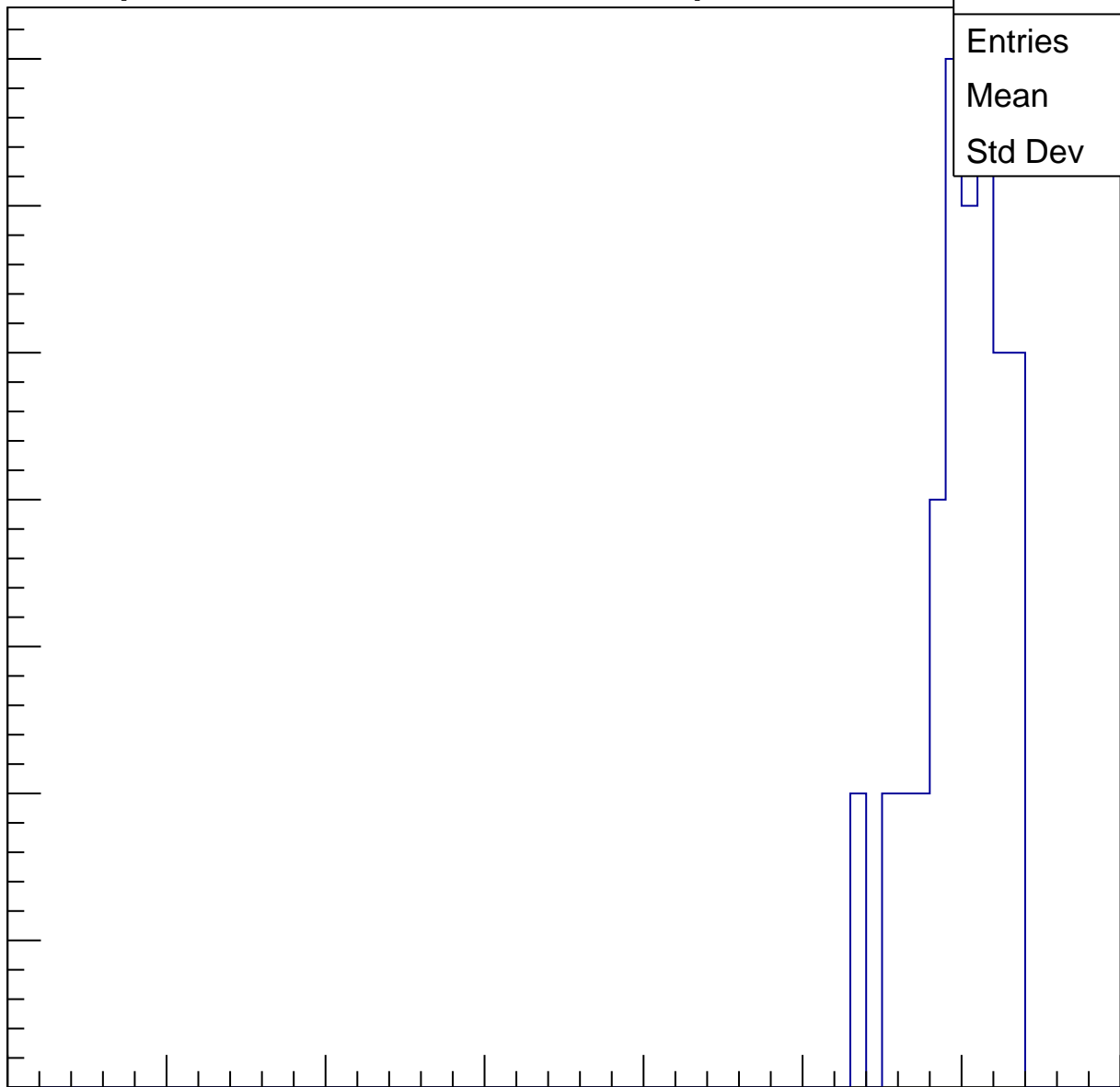
40

50

60

70

ampl

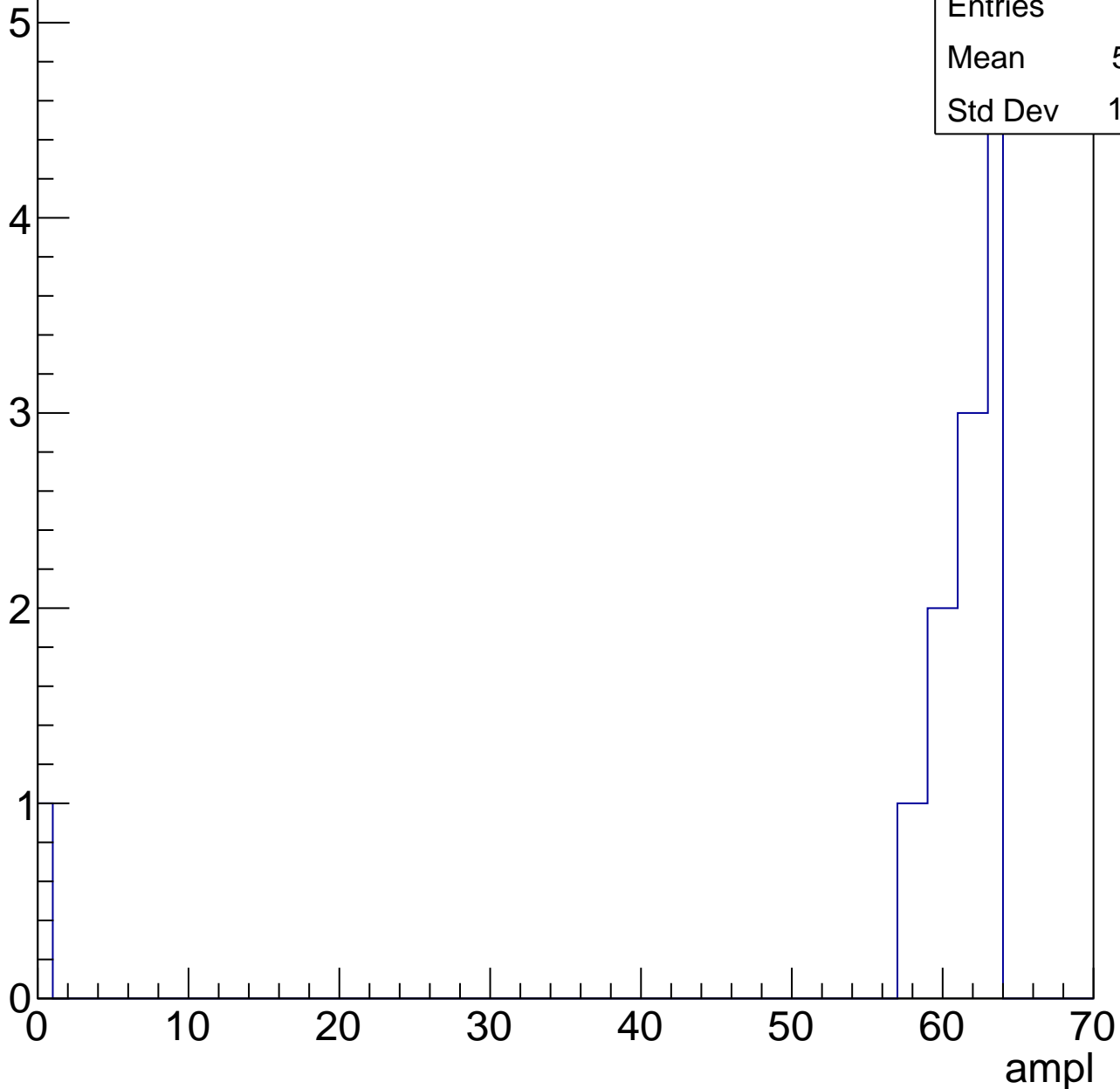


B1L003S, U16-ch16, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	57.61
Std Dev	14.09



B1L003S, U16-ch16, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch17, adc0

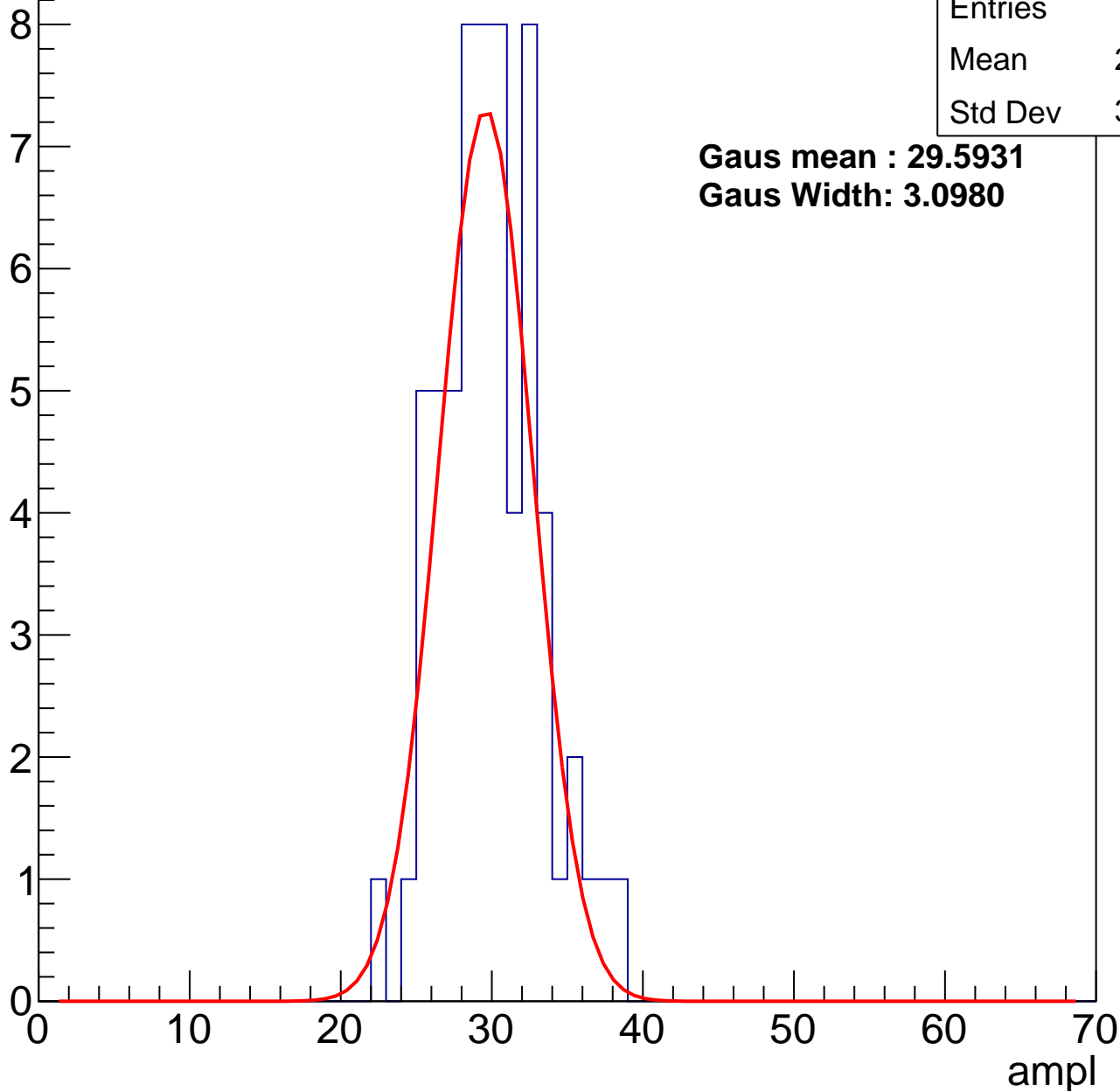
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.51
Std Dev	3.241

Gaus mean : 29.5931

Gaus Width: 3.0980



B1L003S, U16-ch17, adc1

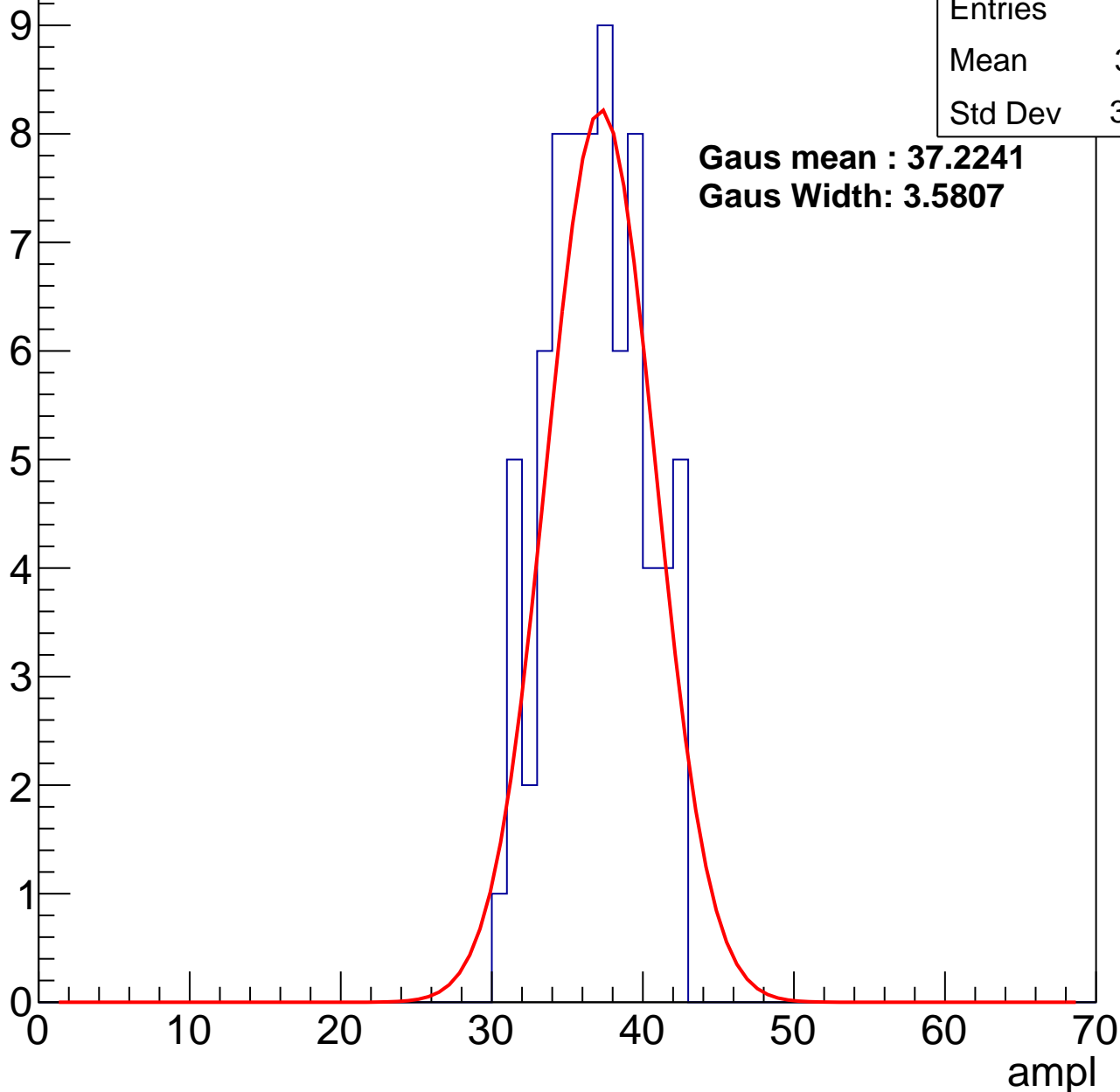
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	36.41
Std Dev	3.128

Gaus mean : 37.2241

Gaus Width: 3.5807



B1L003S, U16-ch17, adc2

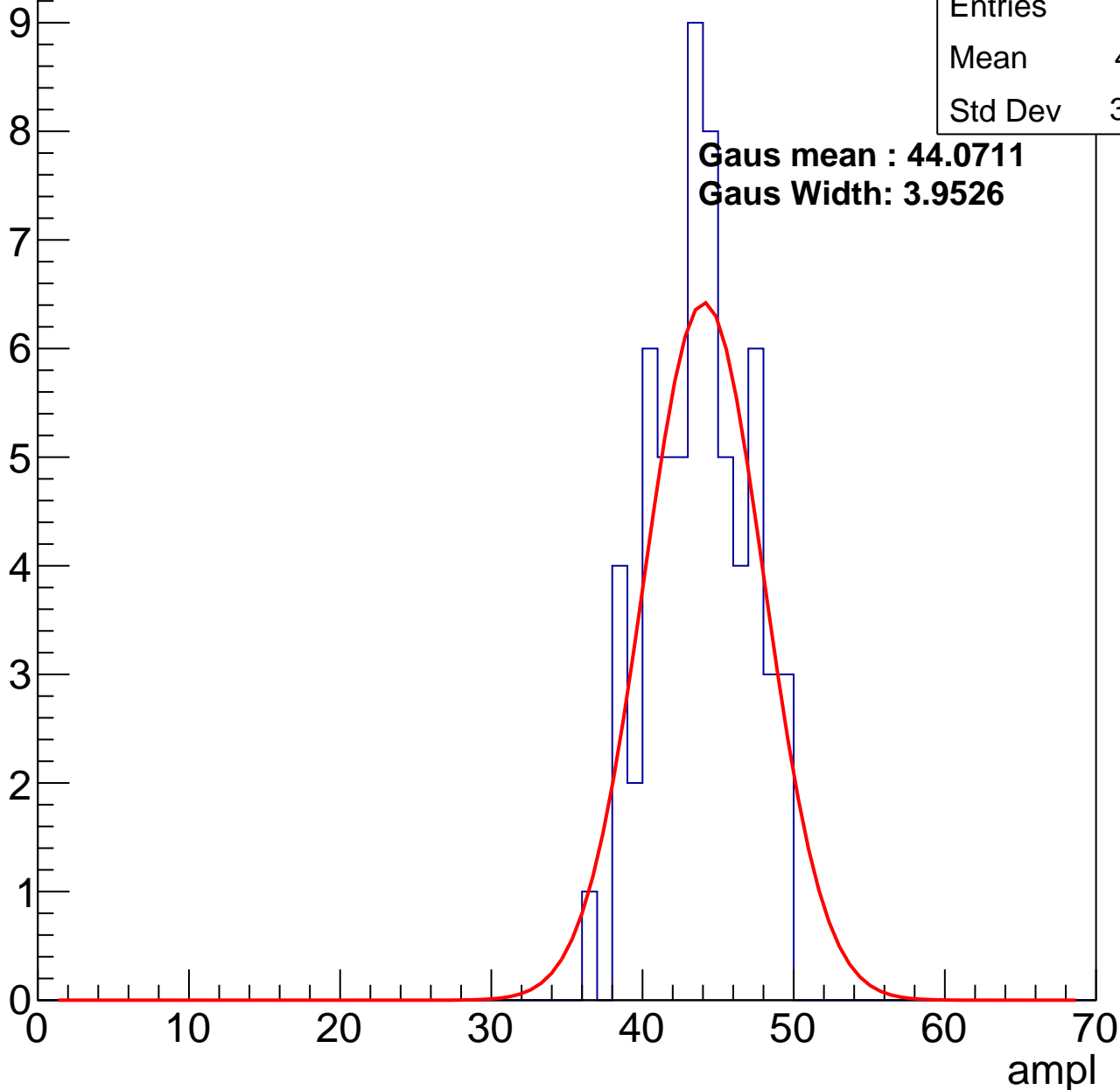
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.31
Std Dev	3.129

Gaus mean : 44.0711

Gaus Width: 3.9526



B1L003S, U16-ch17, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	49.32
Std Dev	3.471

Entry

10

8

6

4

2

0

0

10

20

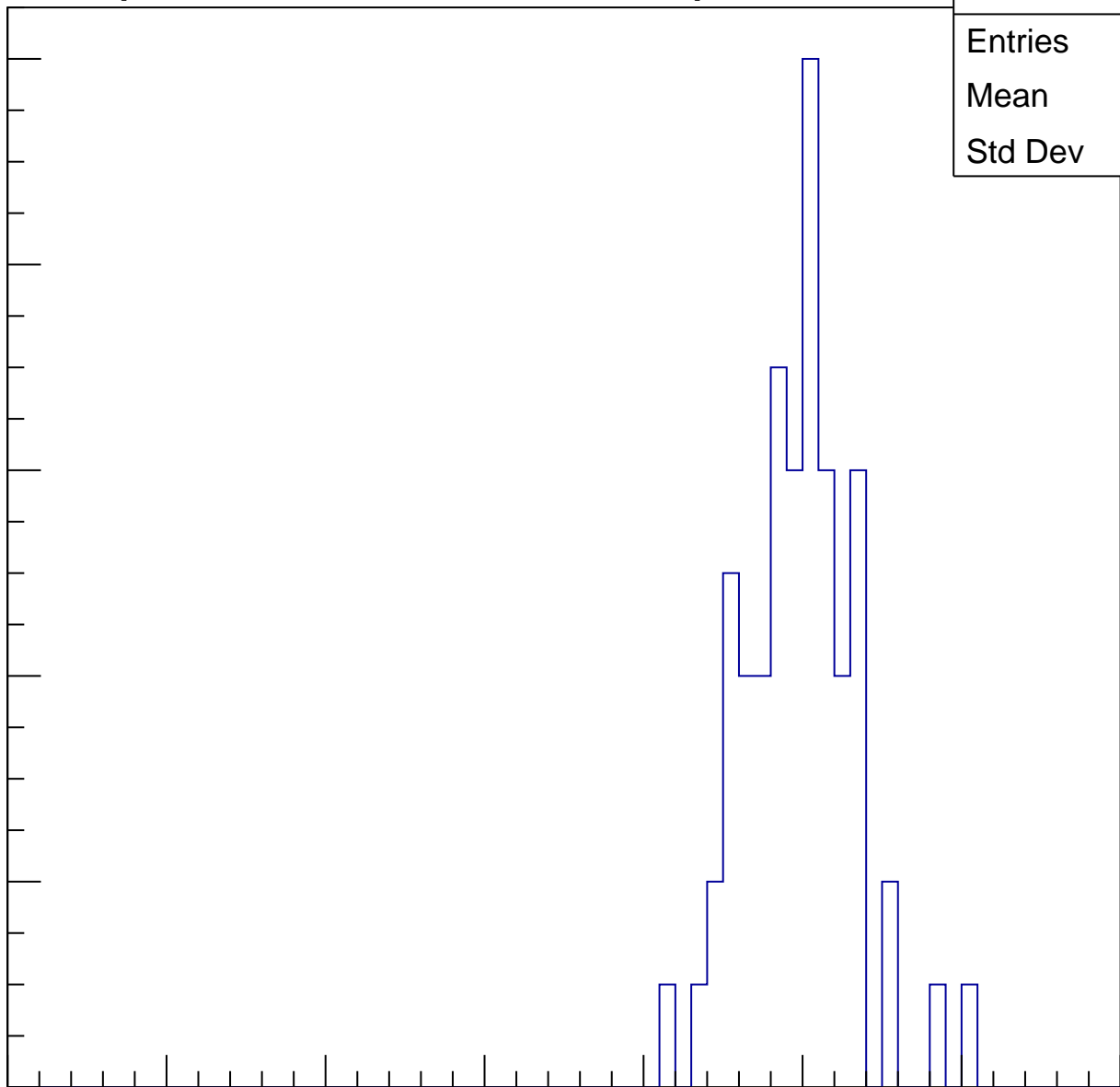
30

40

50

60

ampl

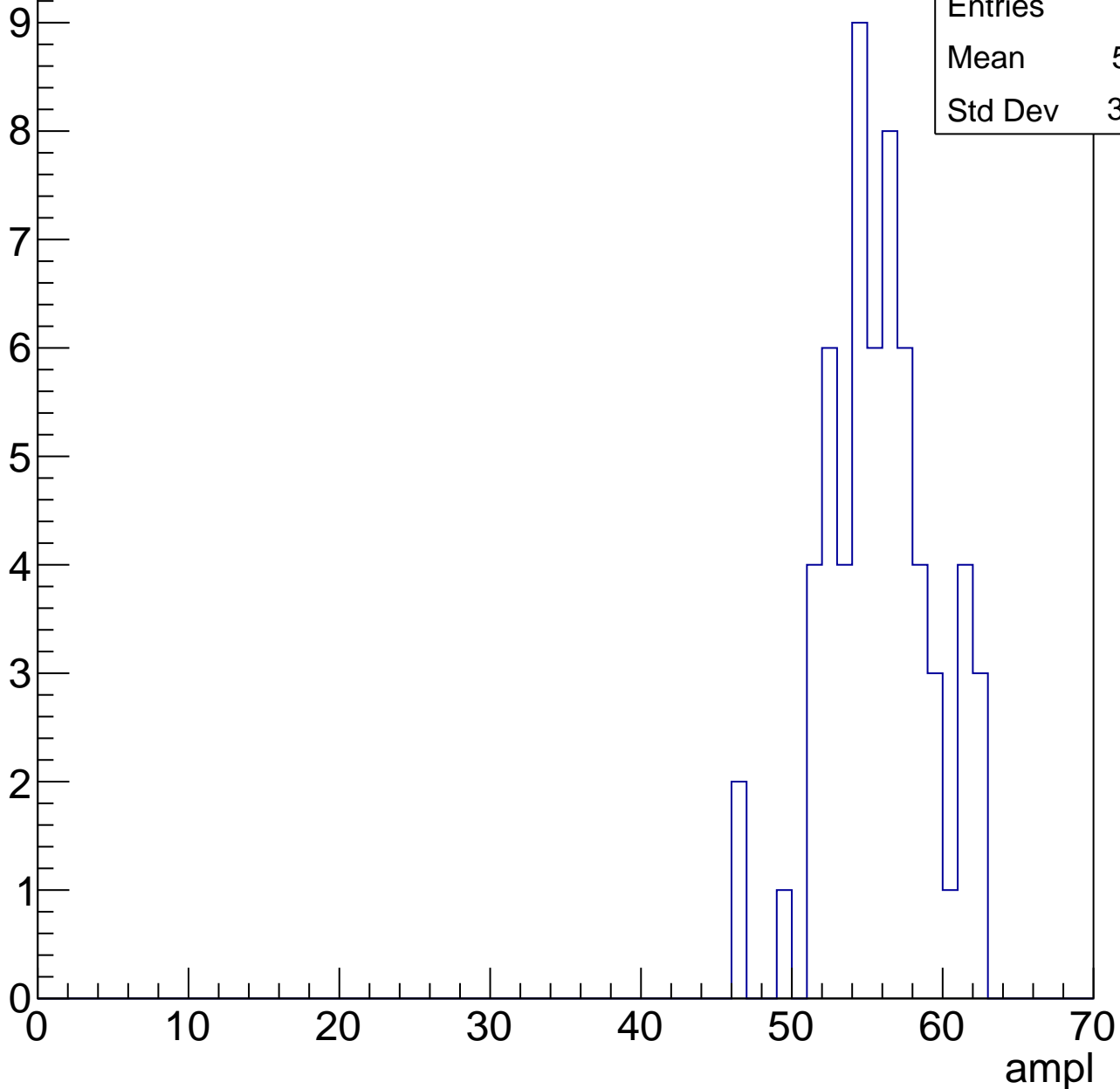


B1L003S, U16-ch17, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

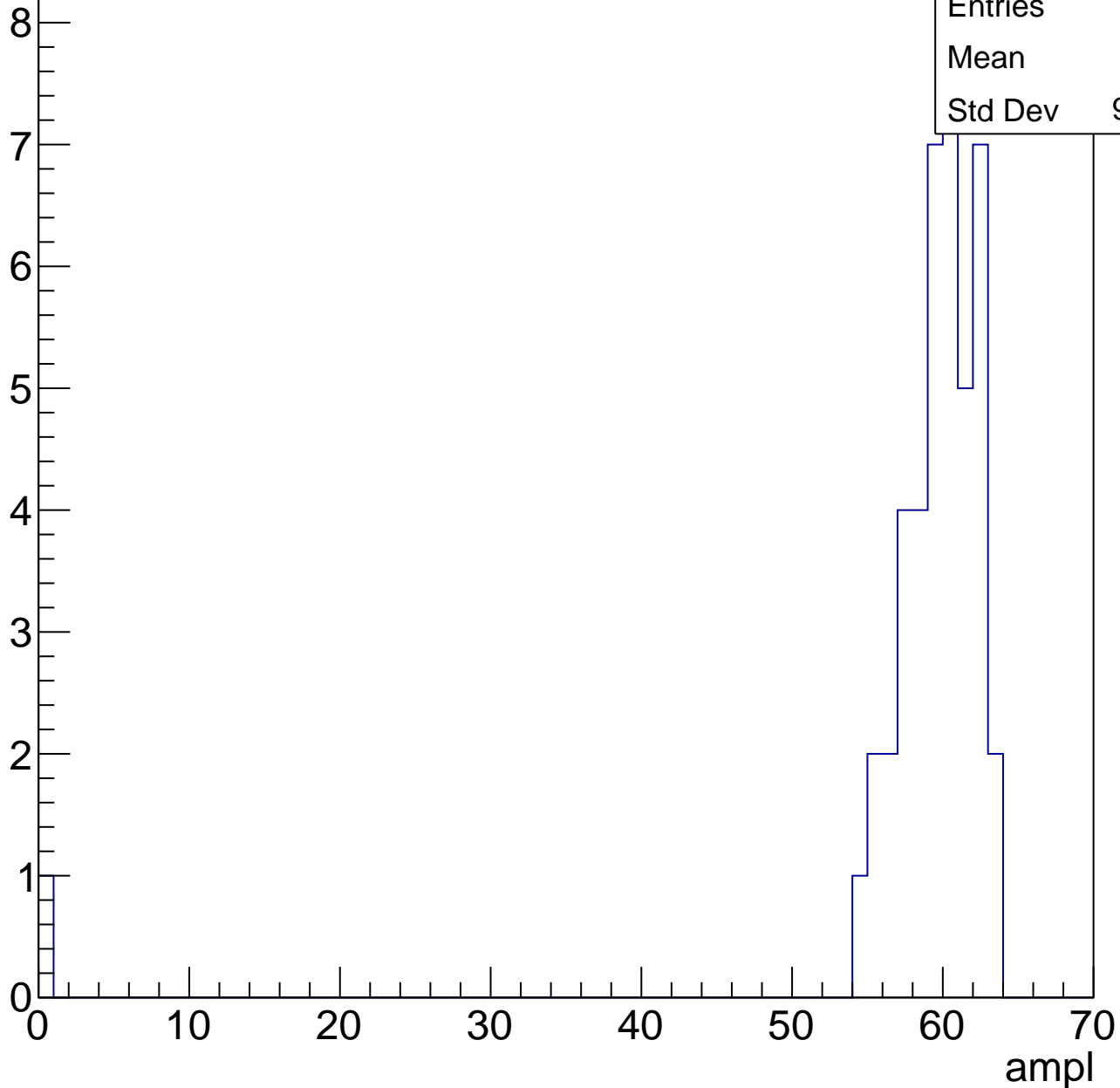
Entries	61
Mean	55.31
Std Dev	3.546



B1L003S, U16-ch17, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

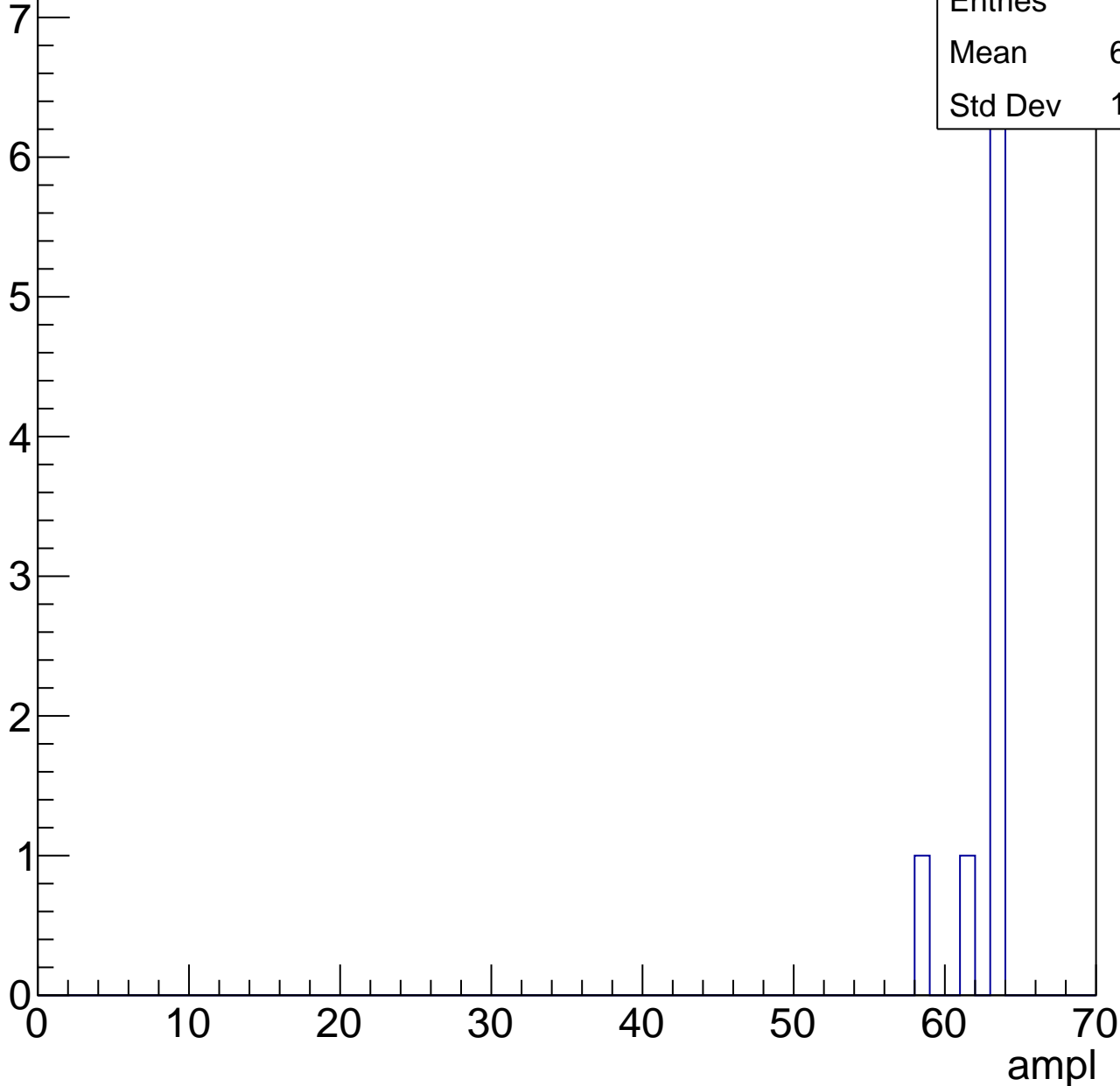


B1L003S, U16-ch17, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

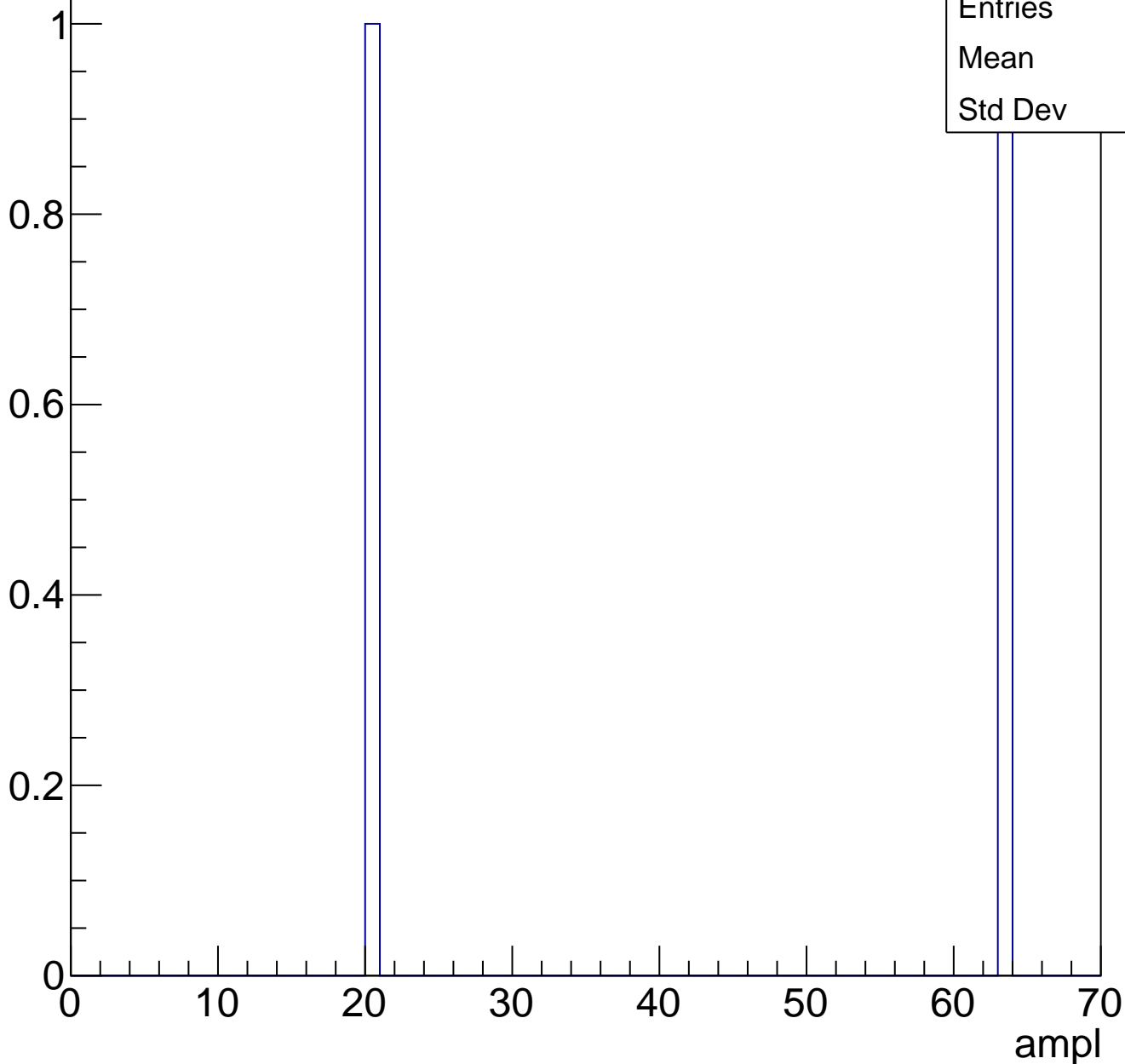
Entries	9
Mean	62.22
Std Dev	1.618



B1L003S, U16-ch17, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch18, adc0

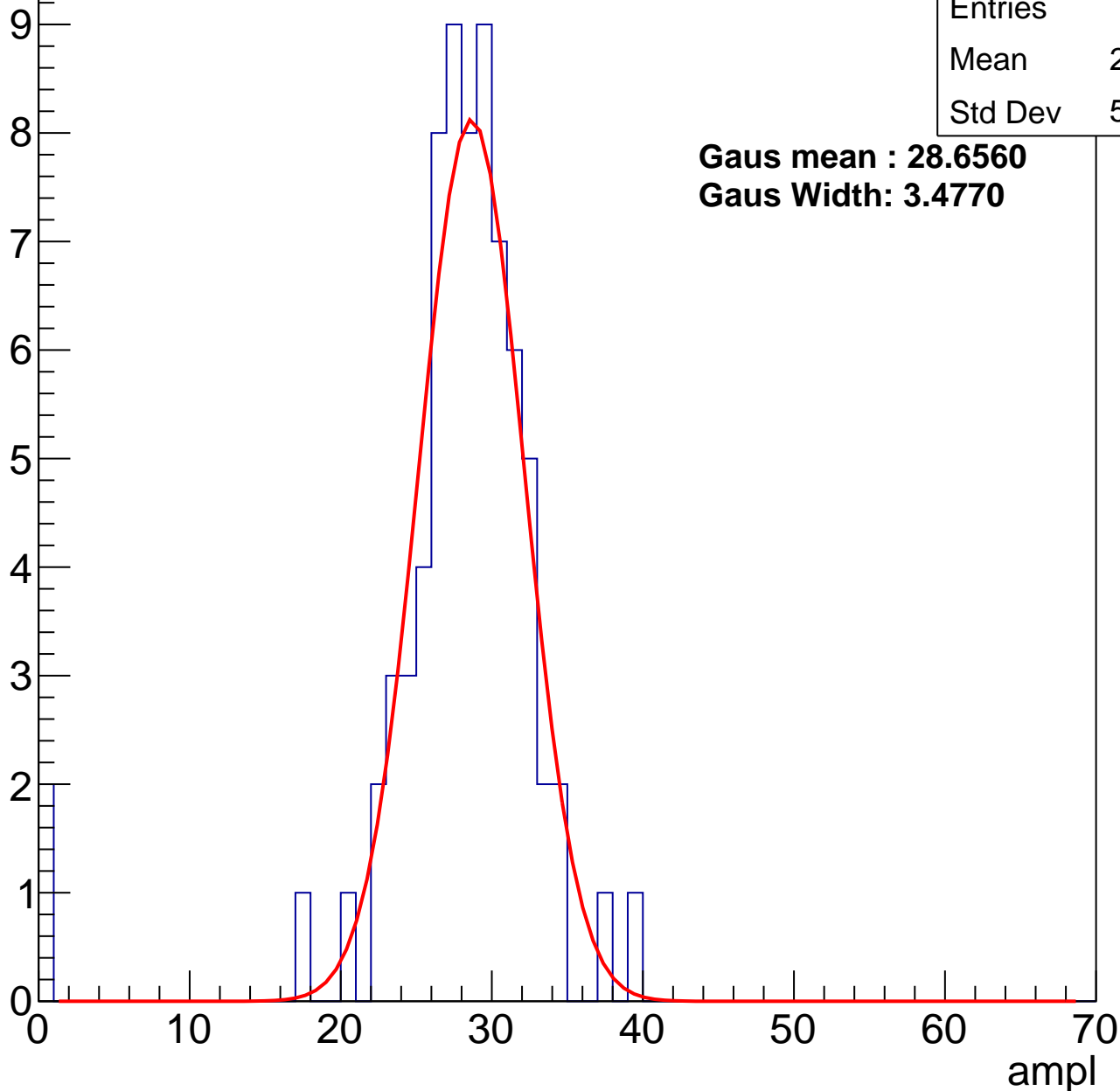
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	27.35
Std Dev	5.799

Gaus mean : 28.6560

Gaus Width: 3.4770



B1L003S, U16-ch18, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	35.26
Std Dev	2.852

Gaus mean : 35.5314

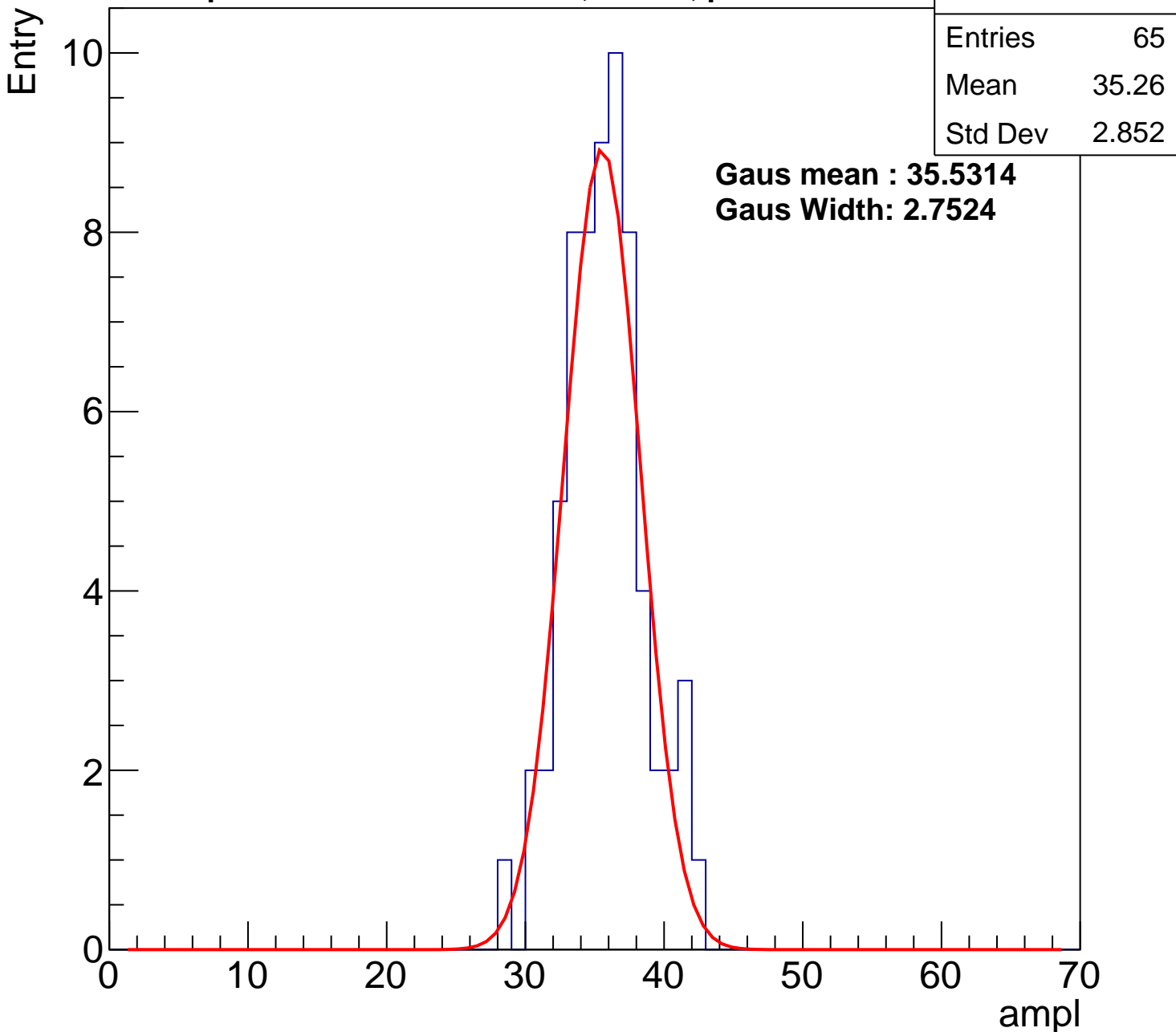
Gaus Width: 2.7524

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch18, adc2

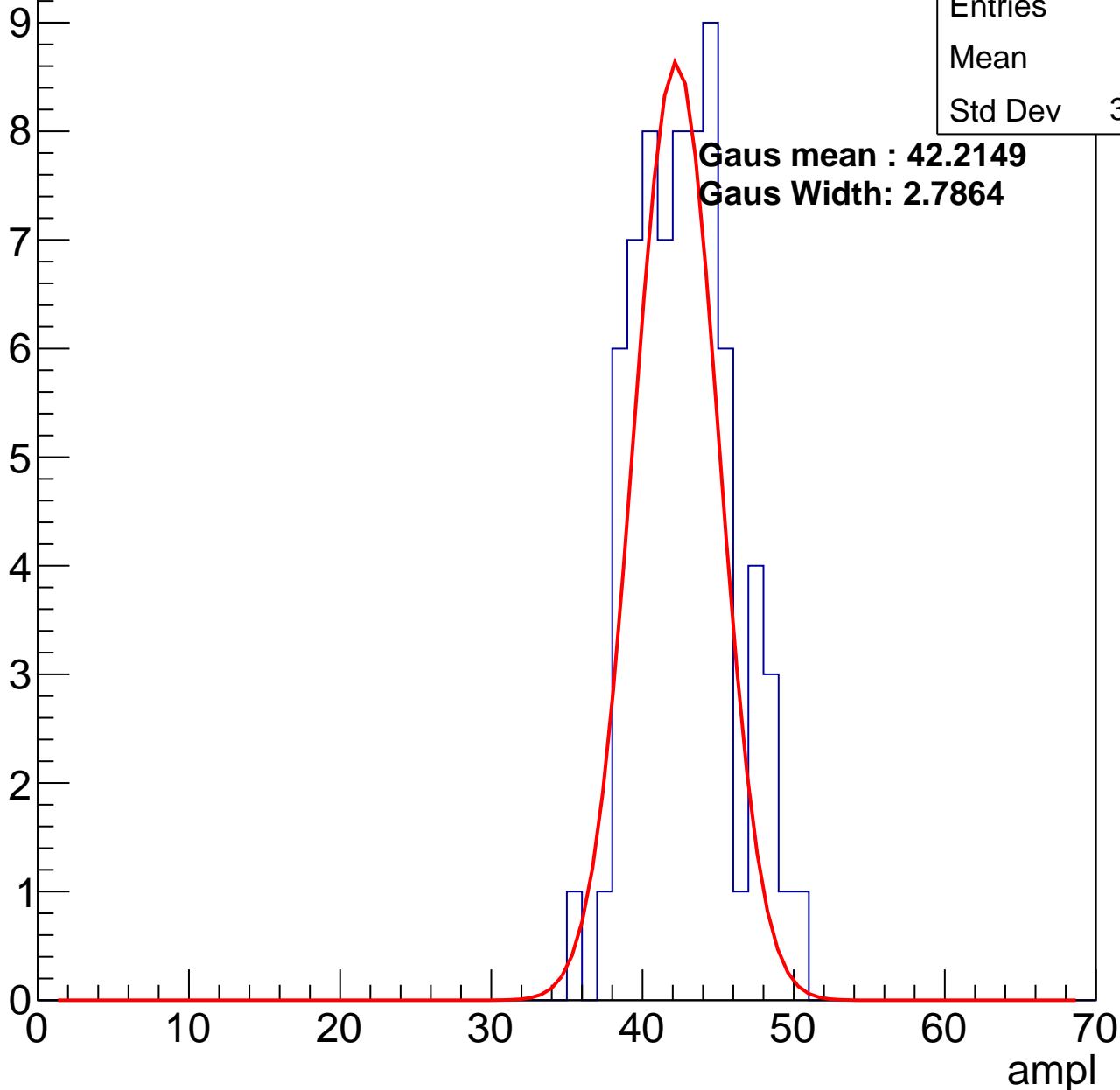
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42.3
Std Dev	3.142

Gaus mean : 42.2149

Gaus Width: 2.7864

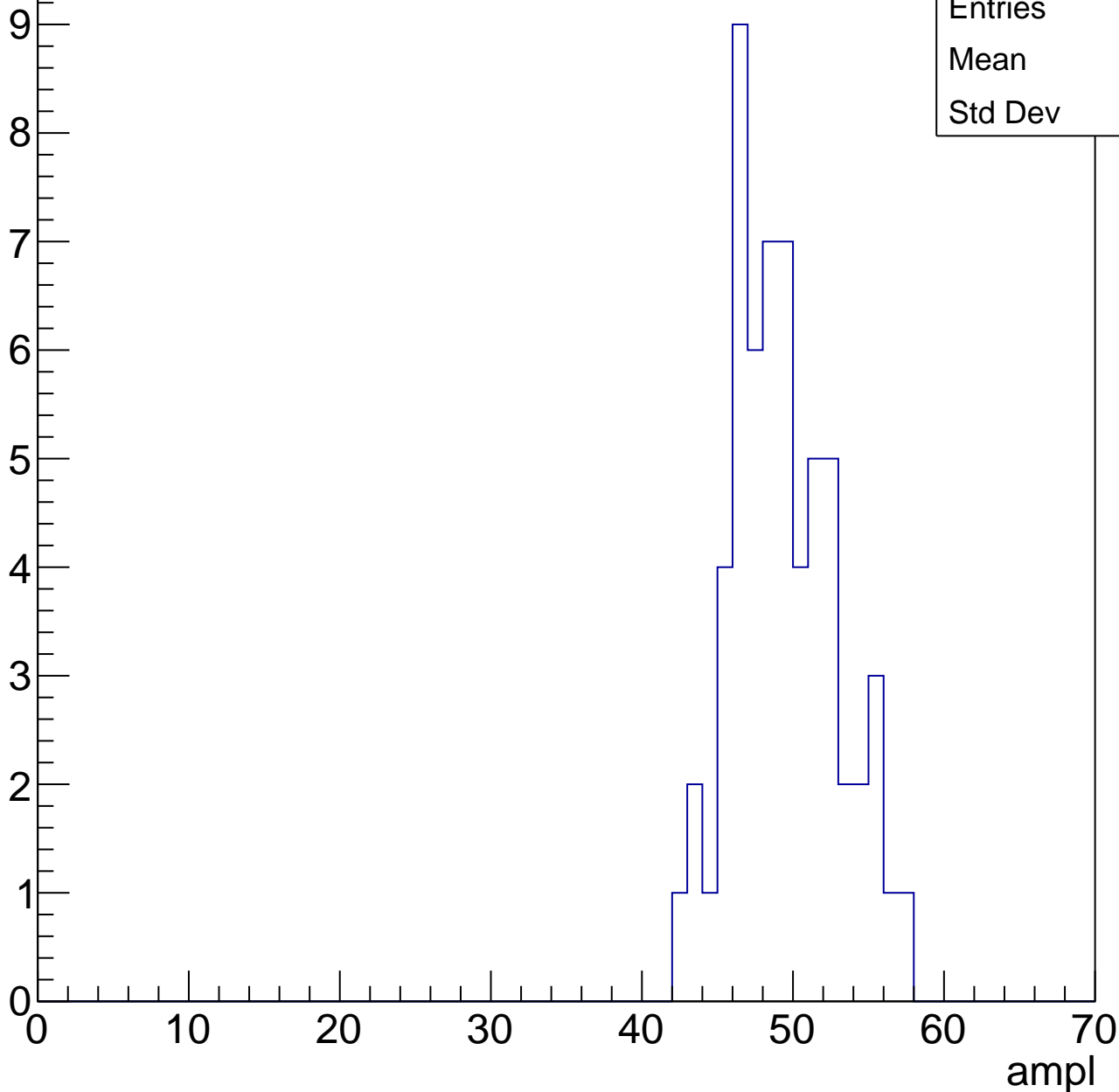


B1L003S, U16-ch18, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.9
Std Dev	3.4

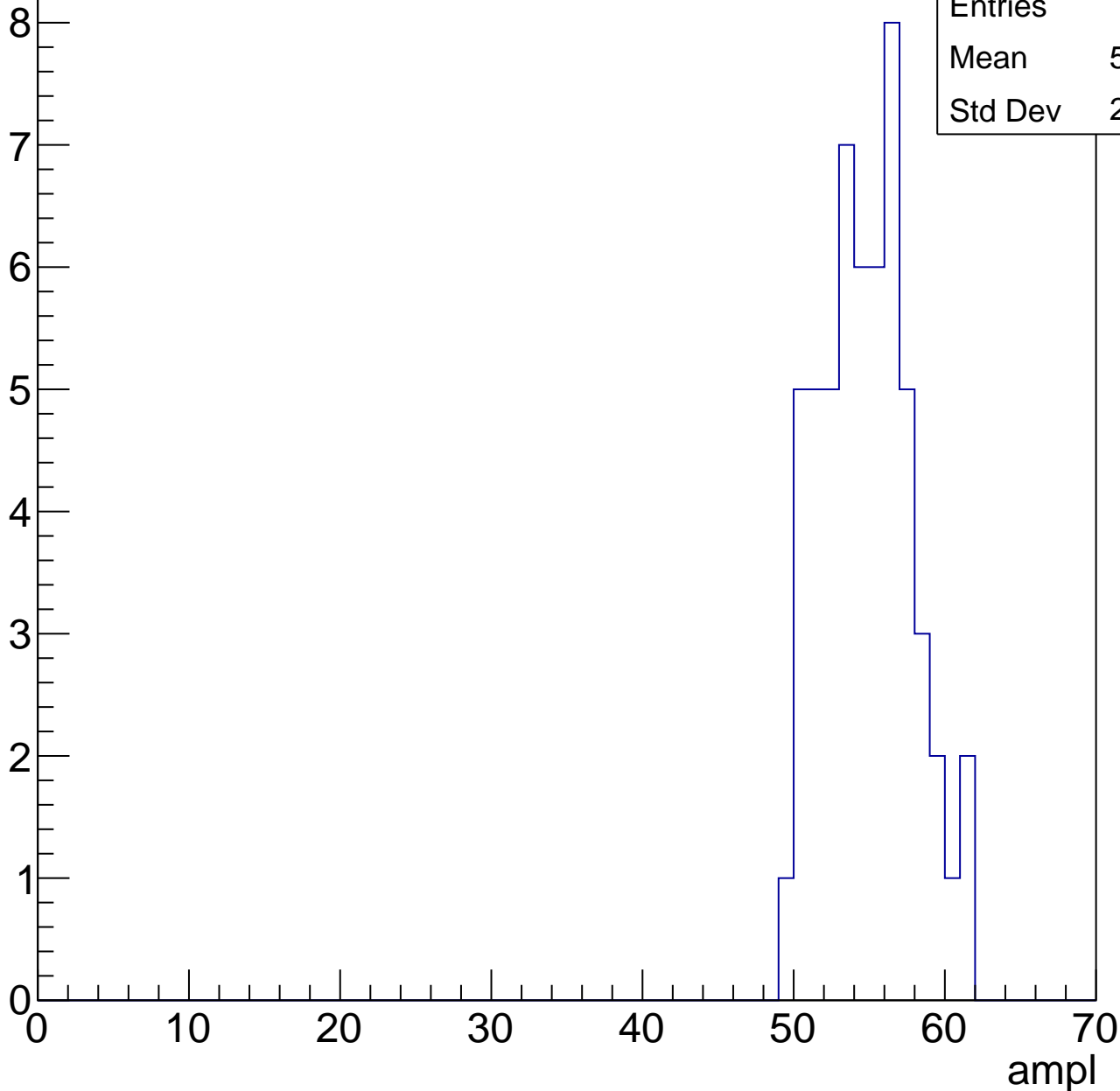


B1L003S, U16-ch18, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.39
Std Dev	2.938

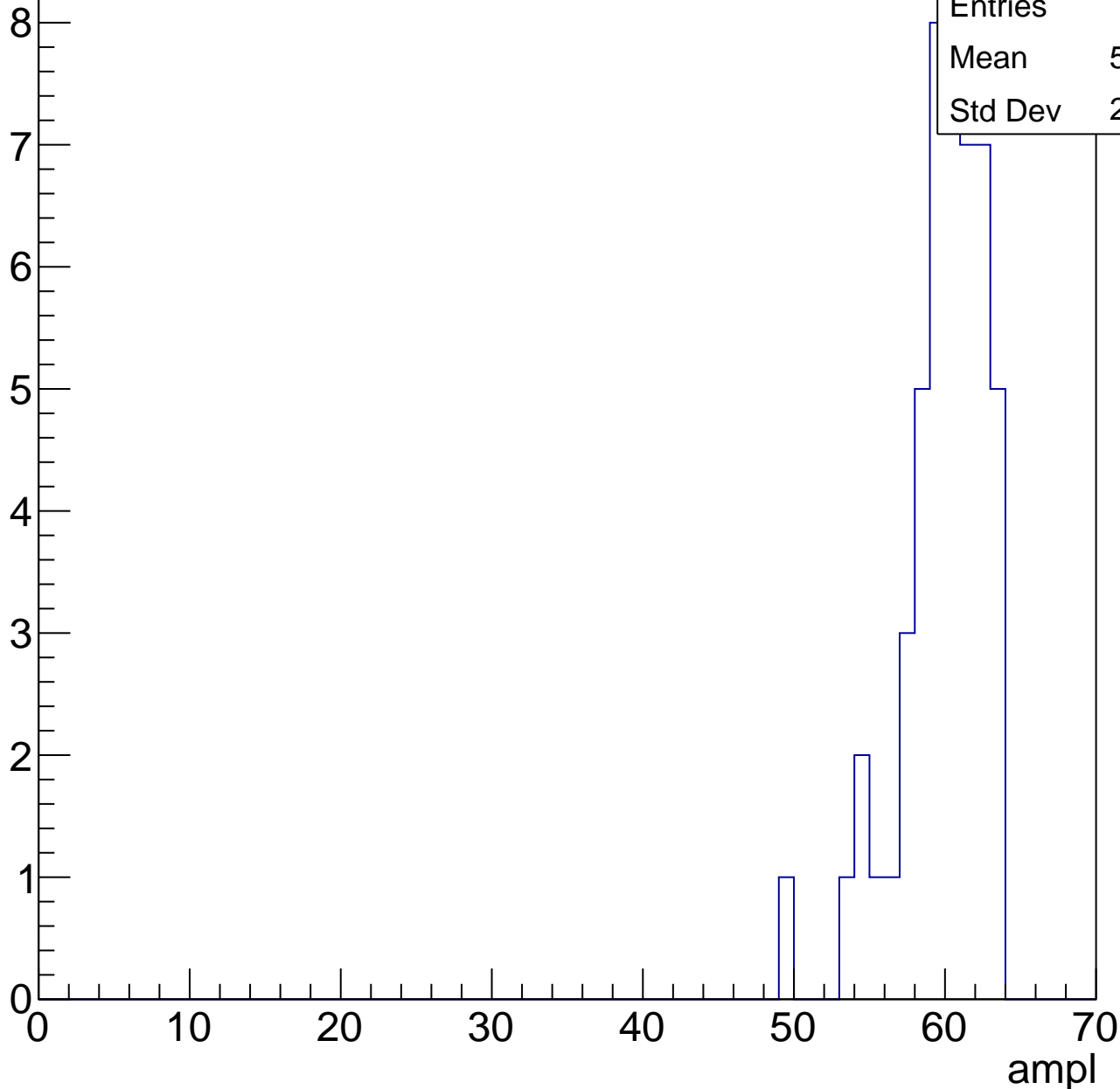


B1L003S, U16-ch18, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

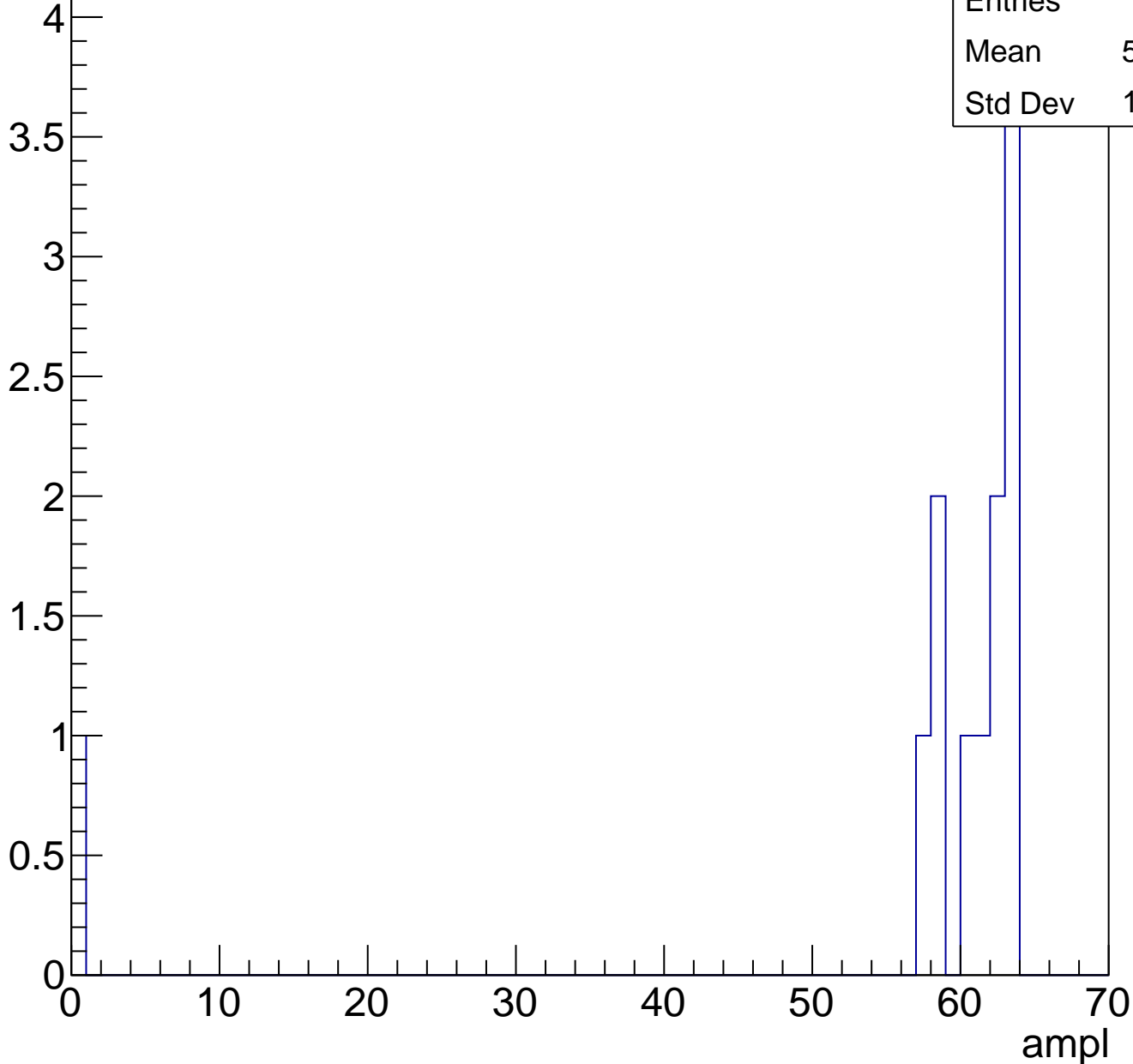
Entries	49
Mean	59.39
Std Dev	2.856



B1L003S, U16-ch18, adc6

calib_packv5_042523_0143.root, FC#13, port D2

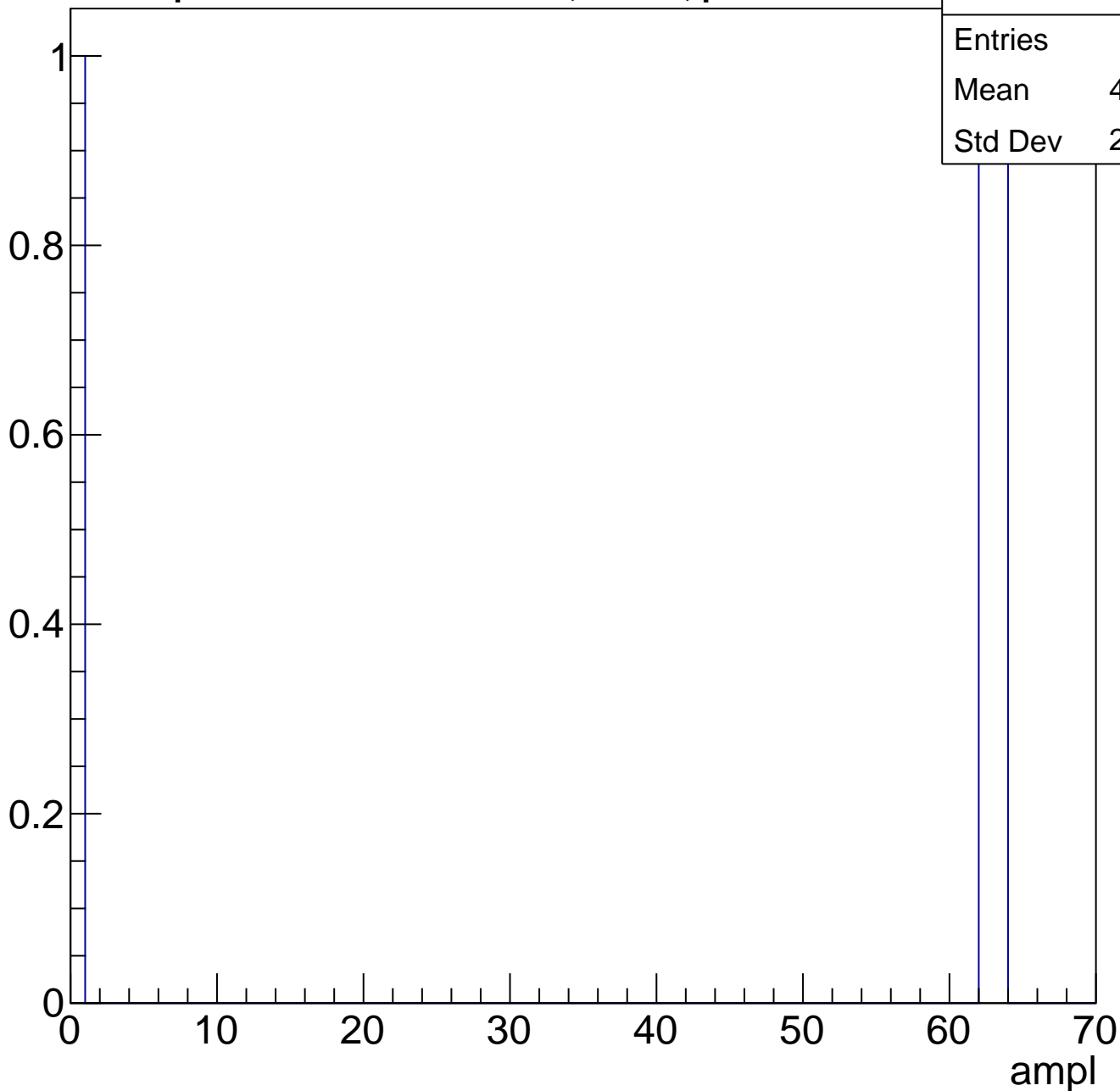
Entry



B1L003S, U16-ch18, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch19, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	105
Mean	30.17
Std Dev	3.927

Gaus mean : 31.0247

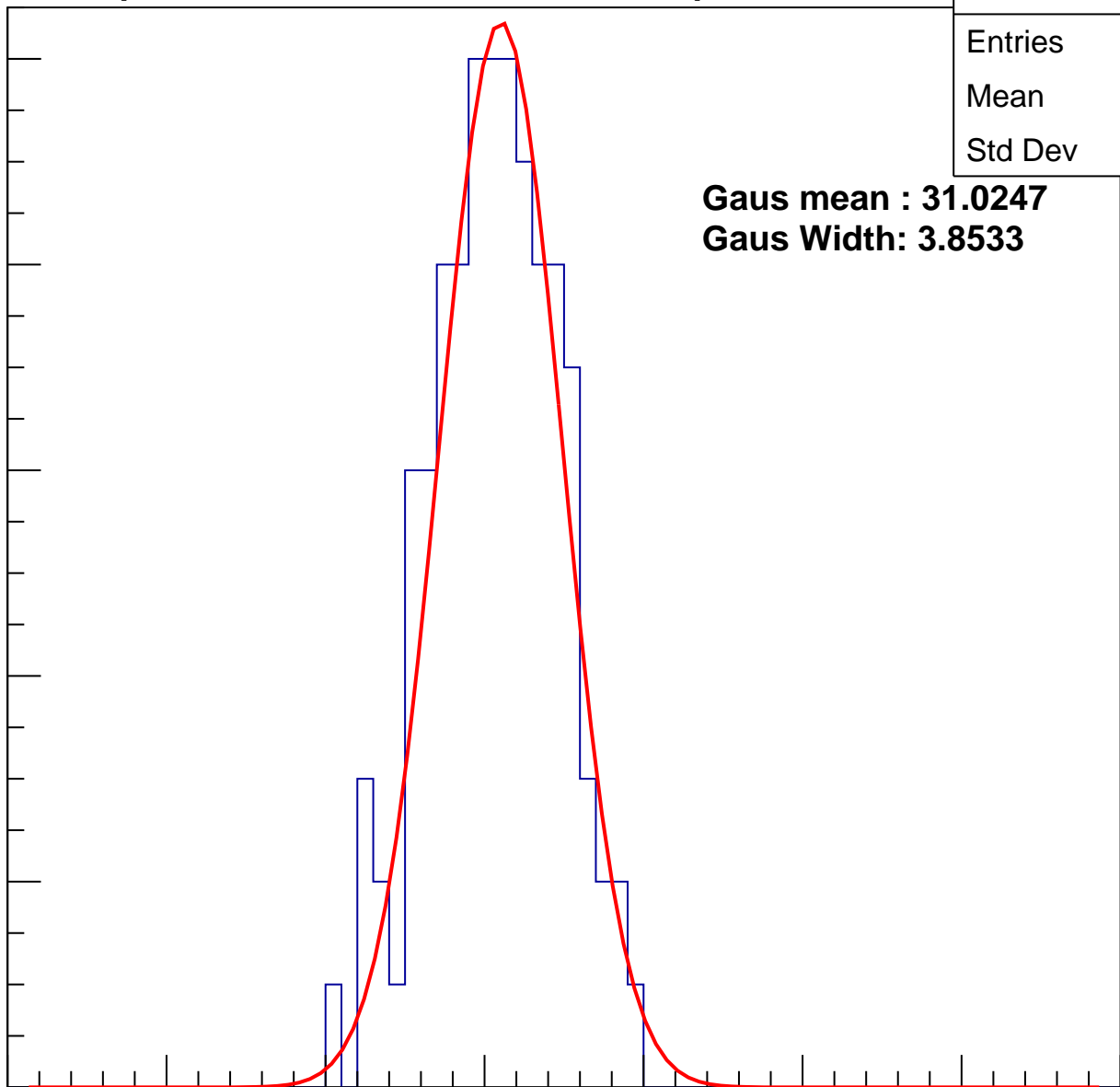
Gaus Width: 3.8533

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



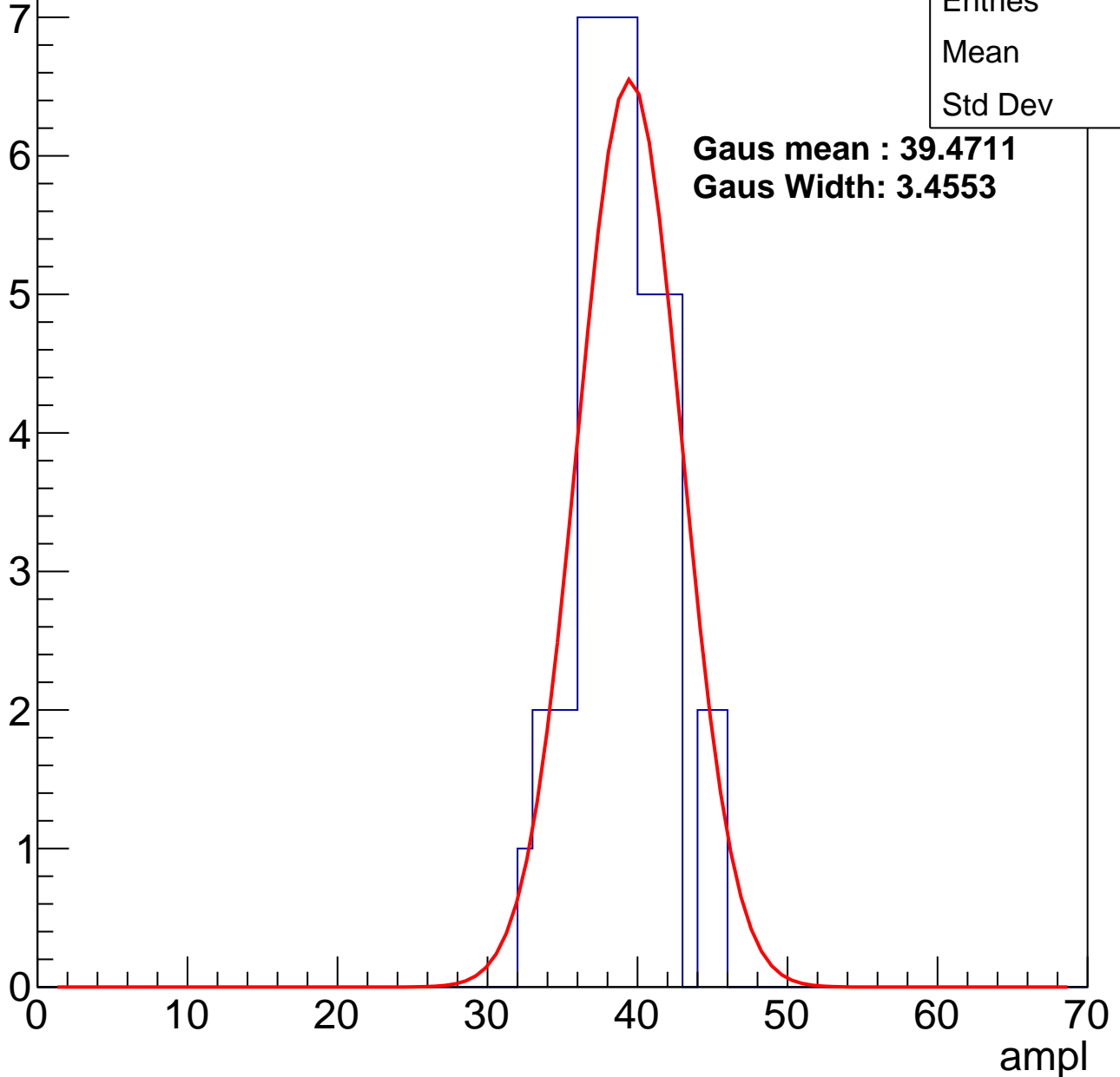
B1L003S, U16-ch19, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	38.5
Std Dev	2.98

Gaus mean : 39.4711
Gaus Width: 3.4553



B1L003S, U16-ch19, adc2

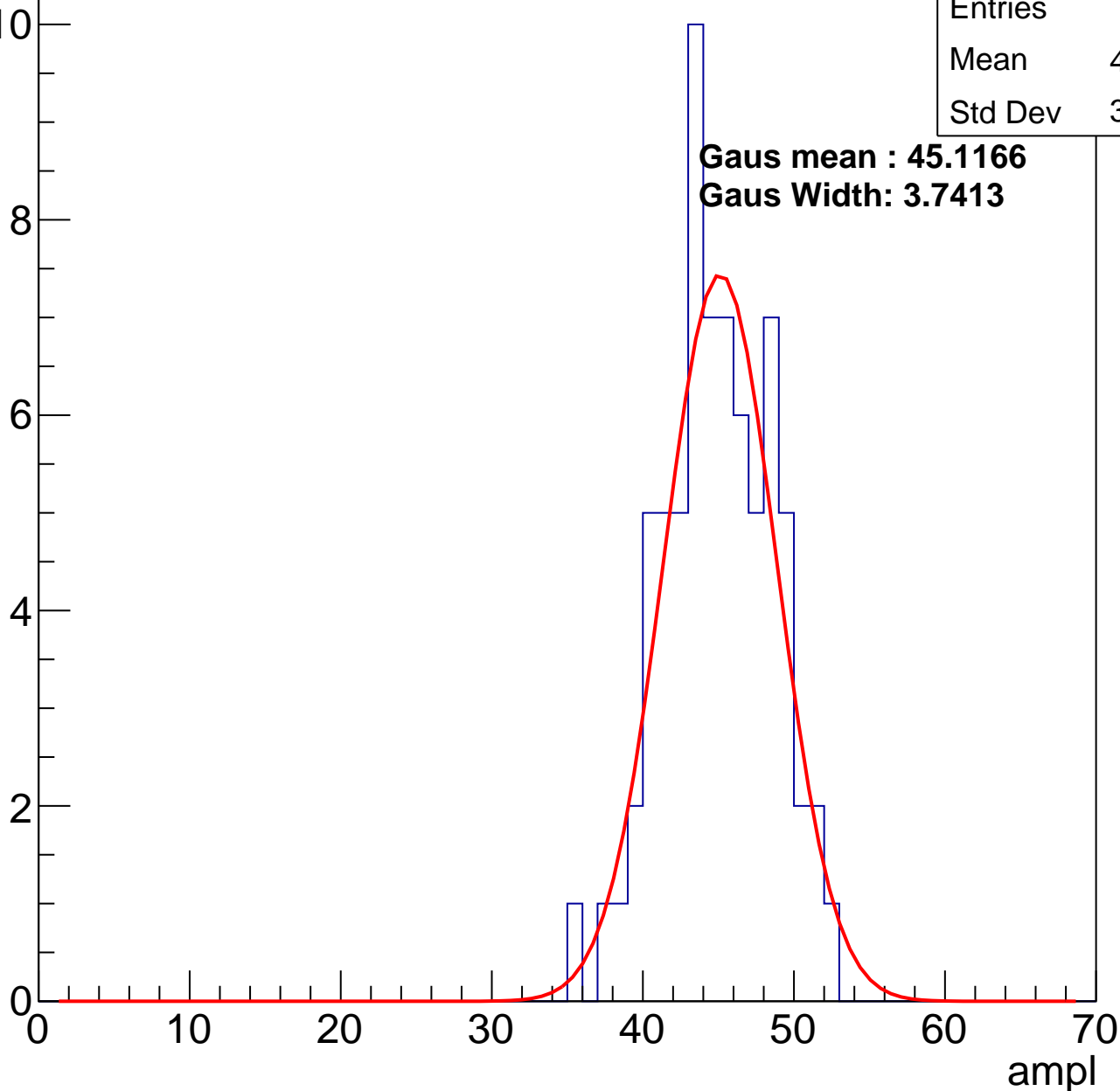
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.47
Std Dev	3.539

Gaus mean : 45.1166

Gaus Width: 3.7413

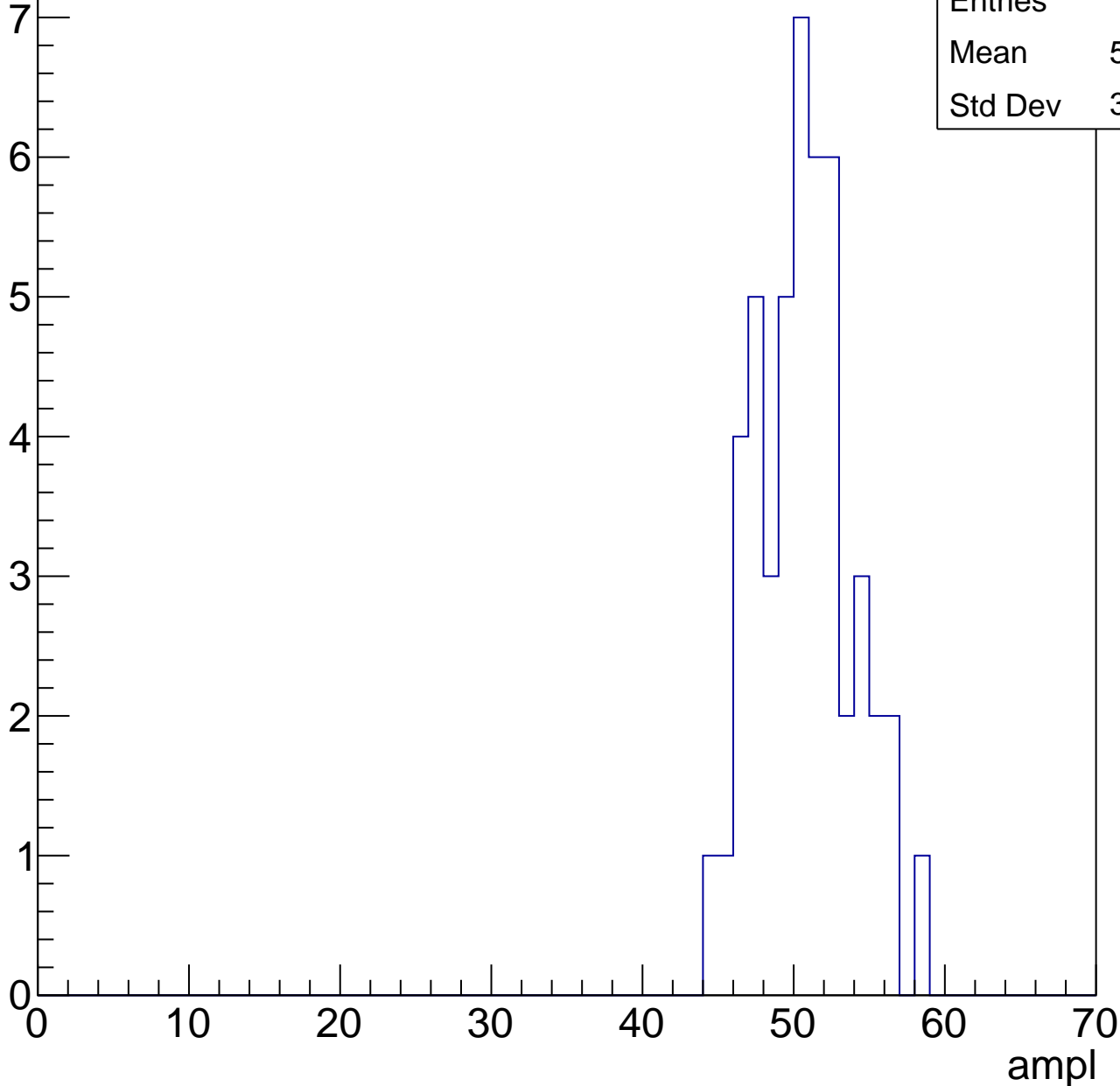


B1L003S, U16-ch19, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	50.27
Std Dev	3.114

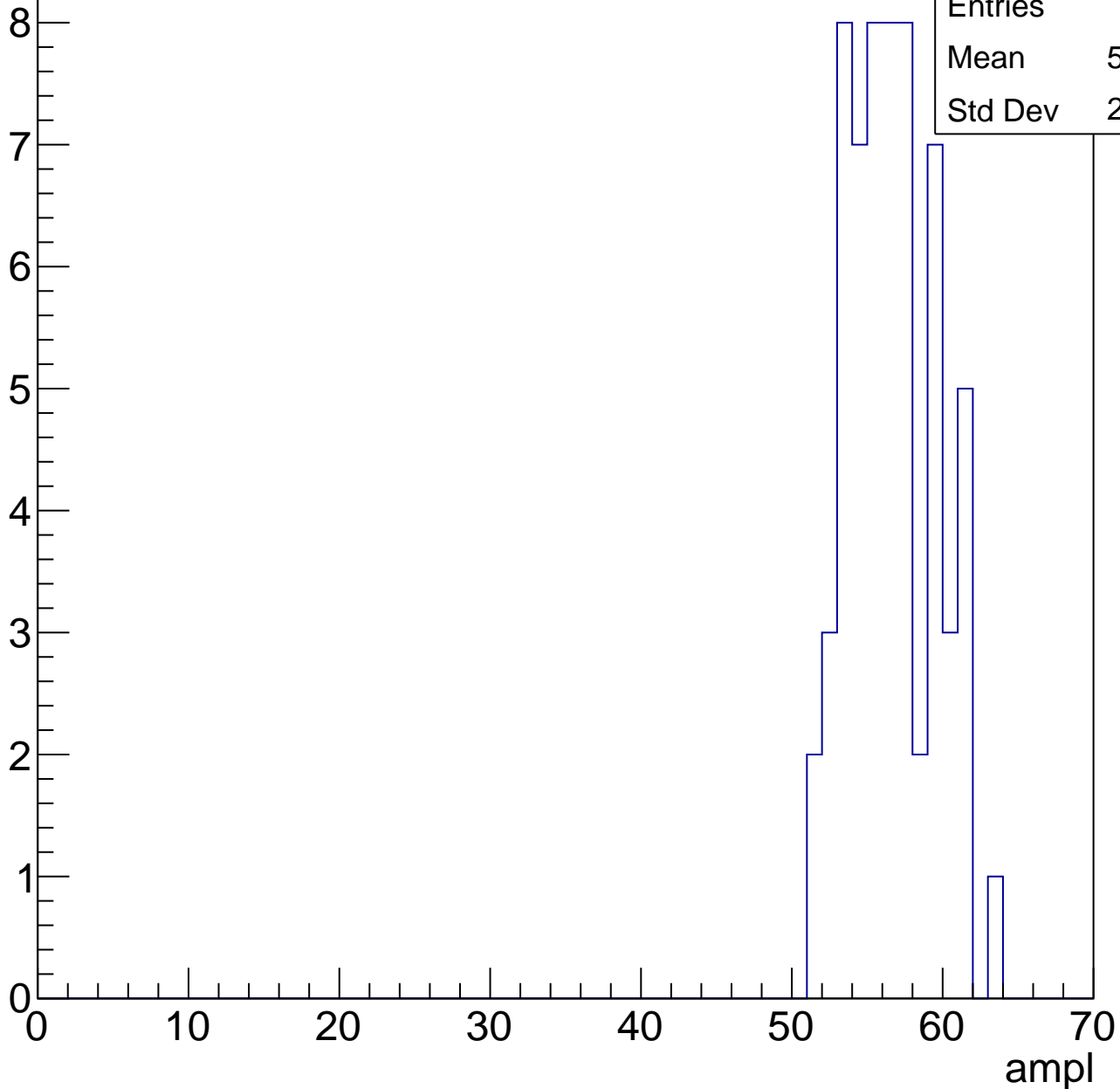


B1L003S, U16-ch19, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	56.15
Std Dev	2.856

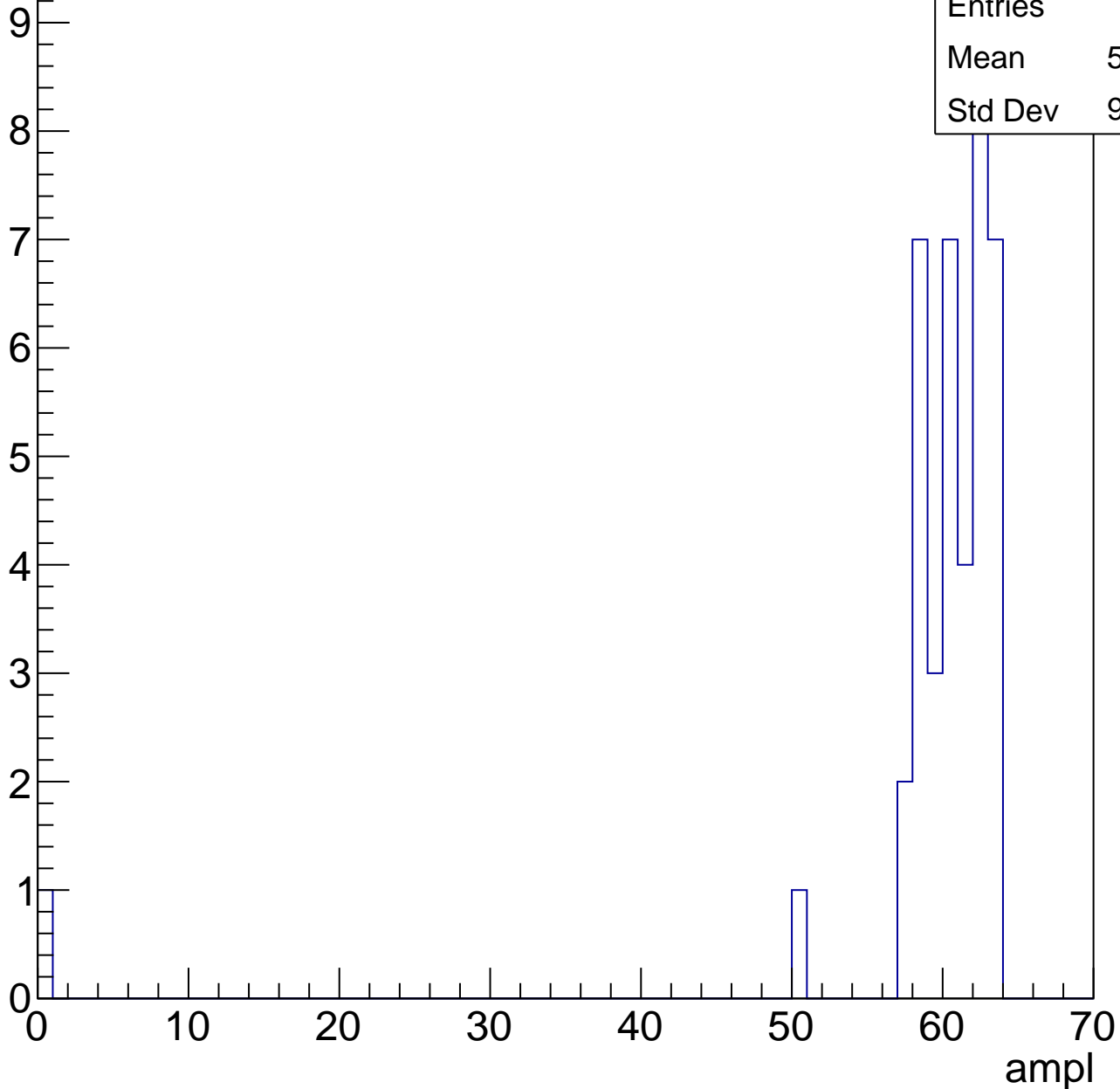


B1L003S, U16-ch19, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.78
Std Dev	9.616



B1L003S, U16-ch19, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch19, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch20, adc0

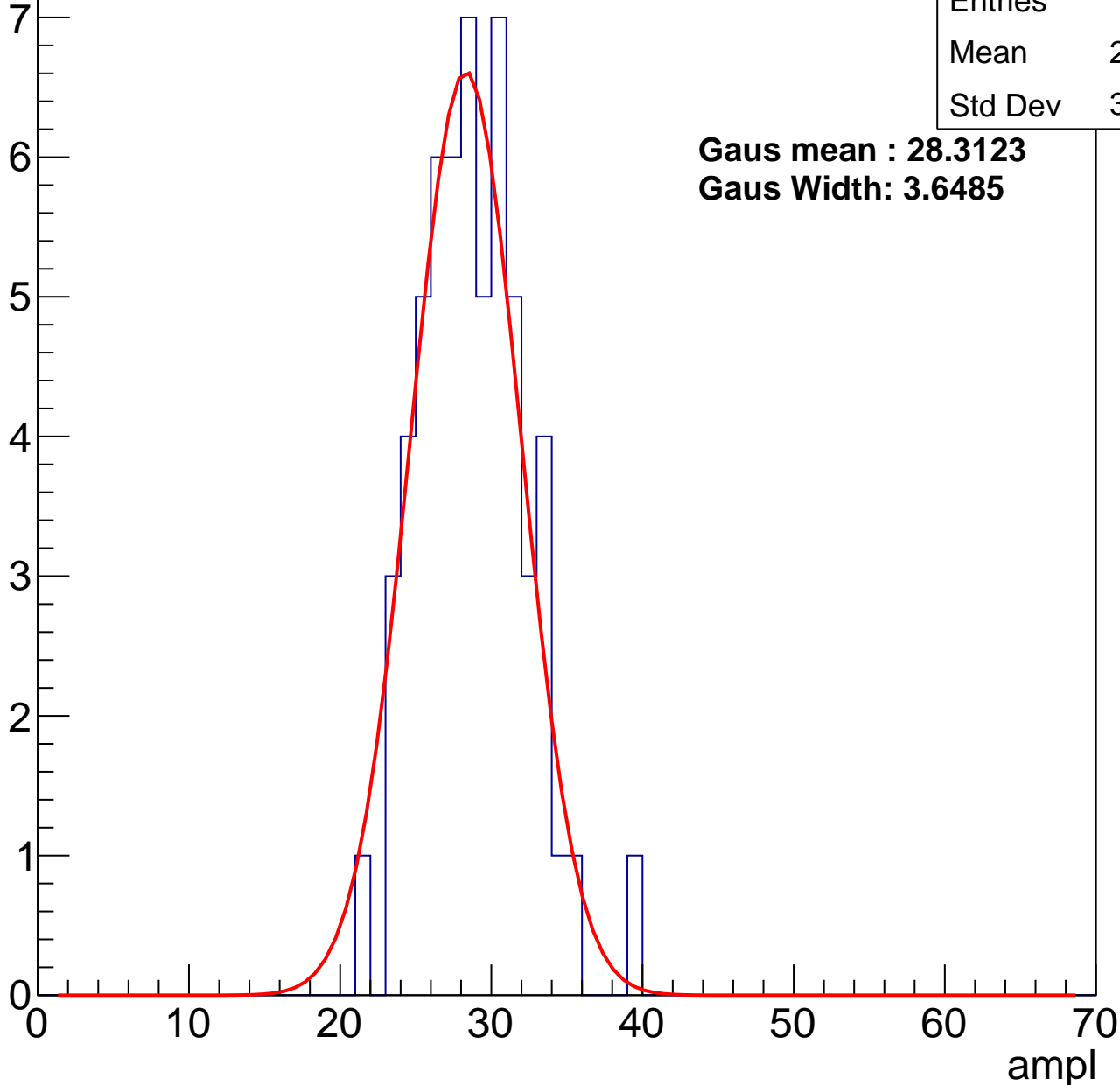
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28.32
Std Dev	3.417

Gaus mean : 28.3123

Gaus Width: 3.6485



B1L003S, U16-ch20, adc1

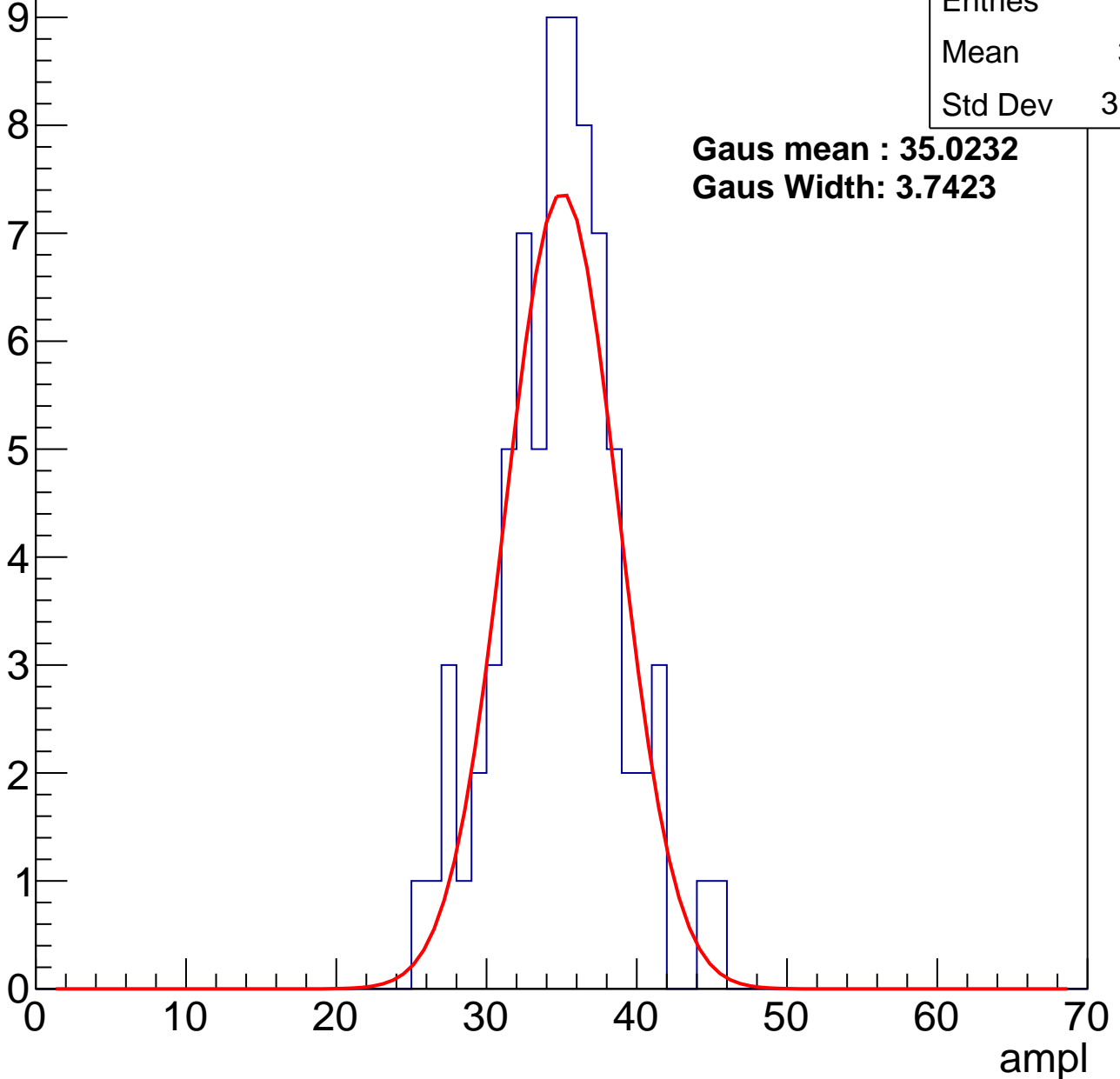
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	34.4
Std Dev	3.943

Gaus mean : 35.0232

Gaus Width: 3.7423



B1L003S, U16-ch20, adc2

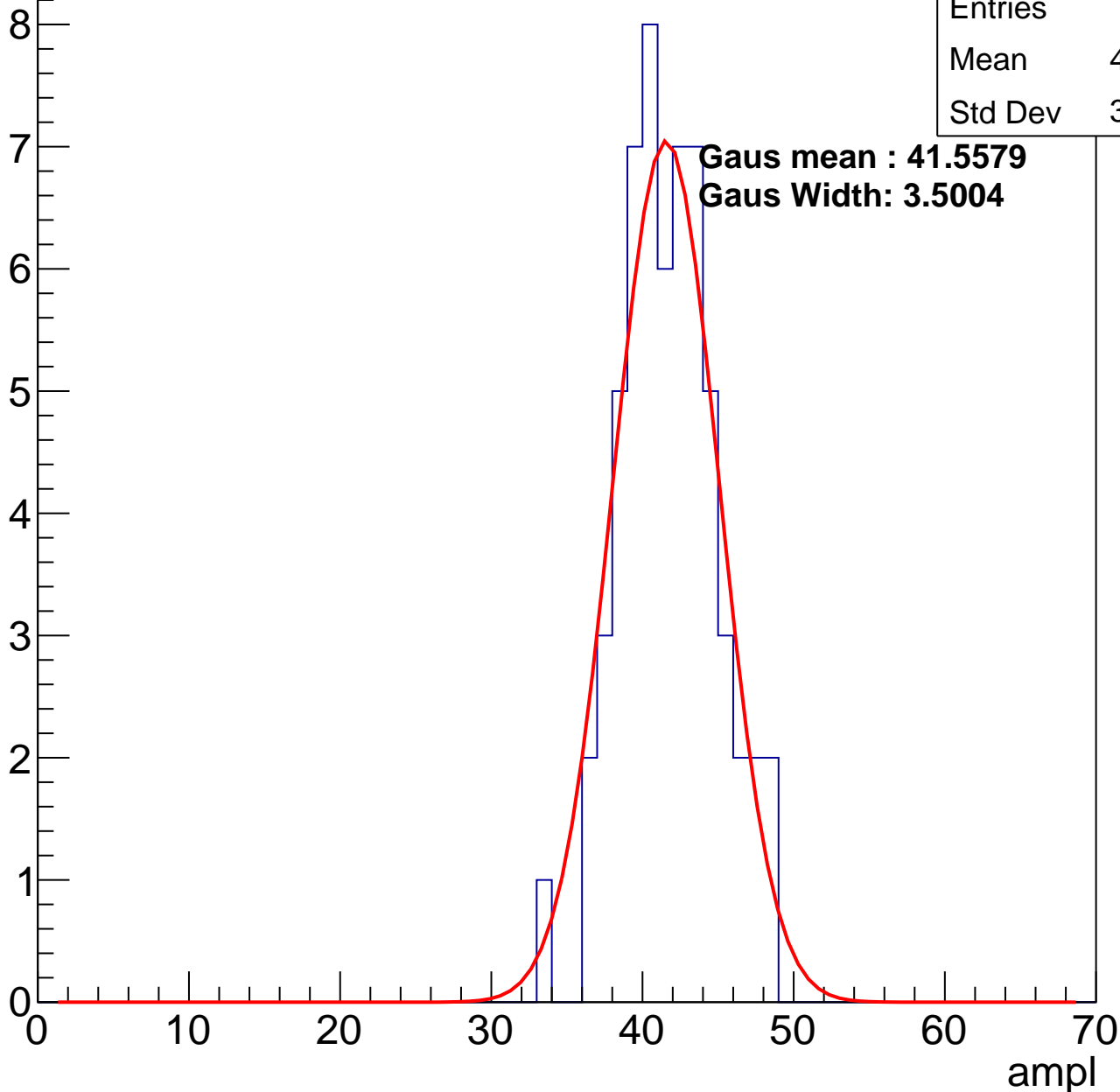
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	41.28
Std Dev	3.126

Gaus mean : 41.5579

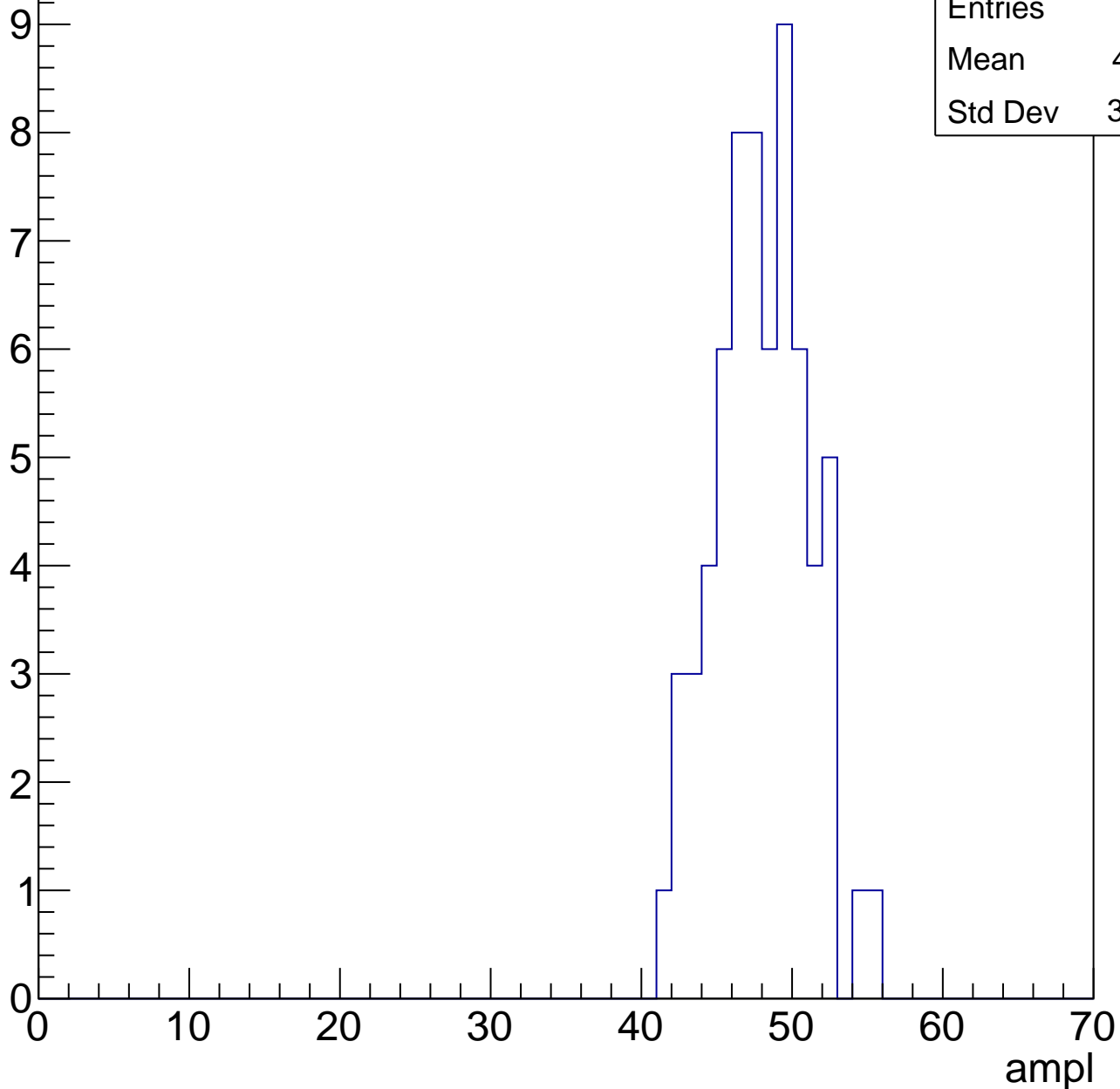
Gaus Width: 3.5004



B1L003S, U16-ch20, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



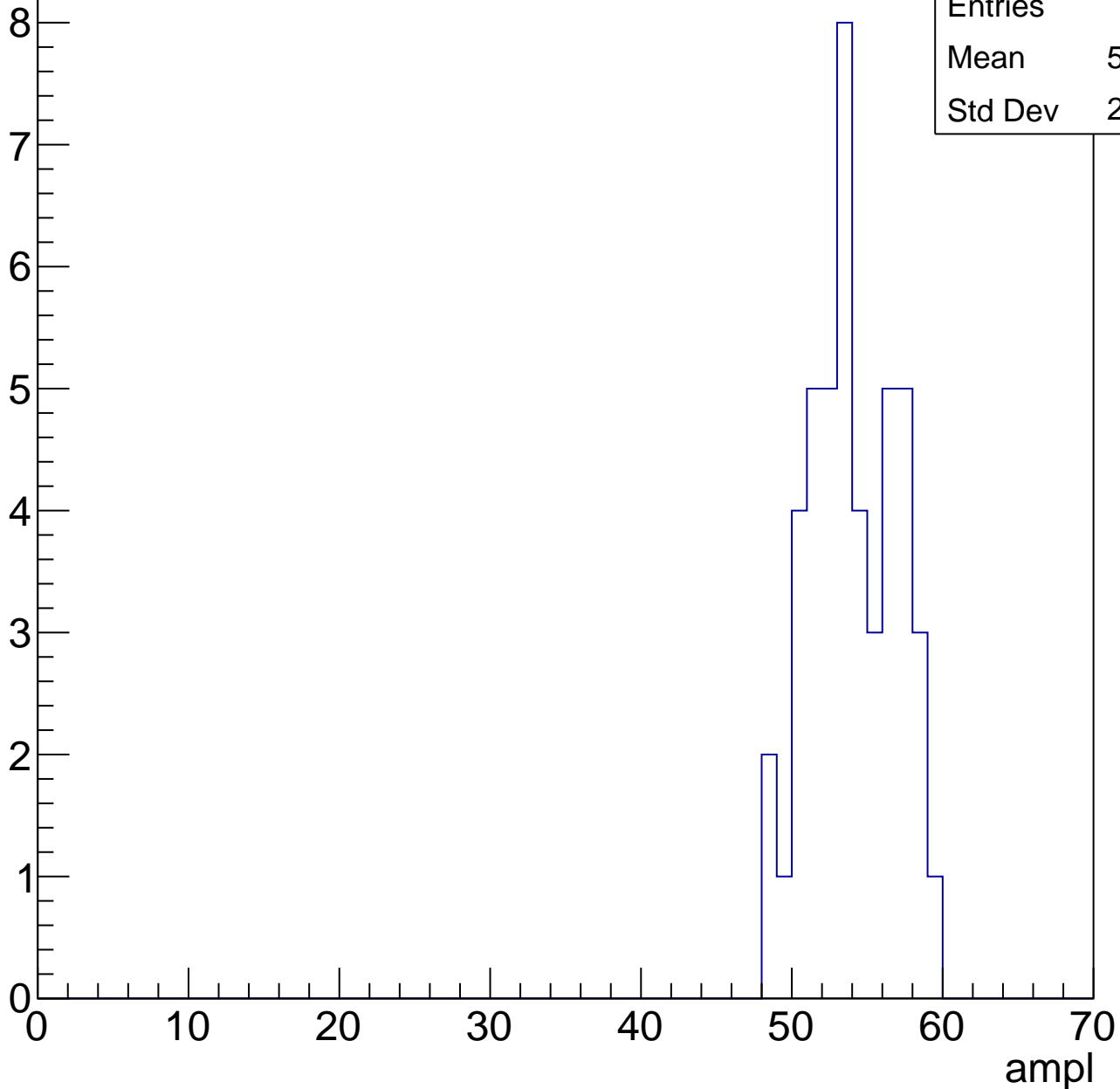
Entries	65
Mean	47.51
Std Dev	3.059

B1L003S, U16-ch20, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	53.54
Std Dev	2.818

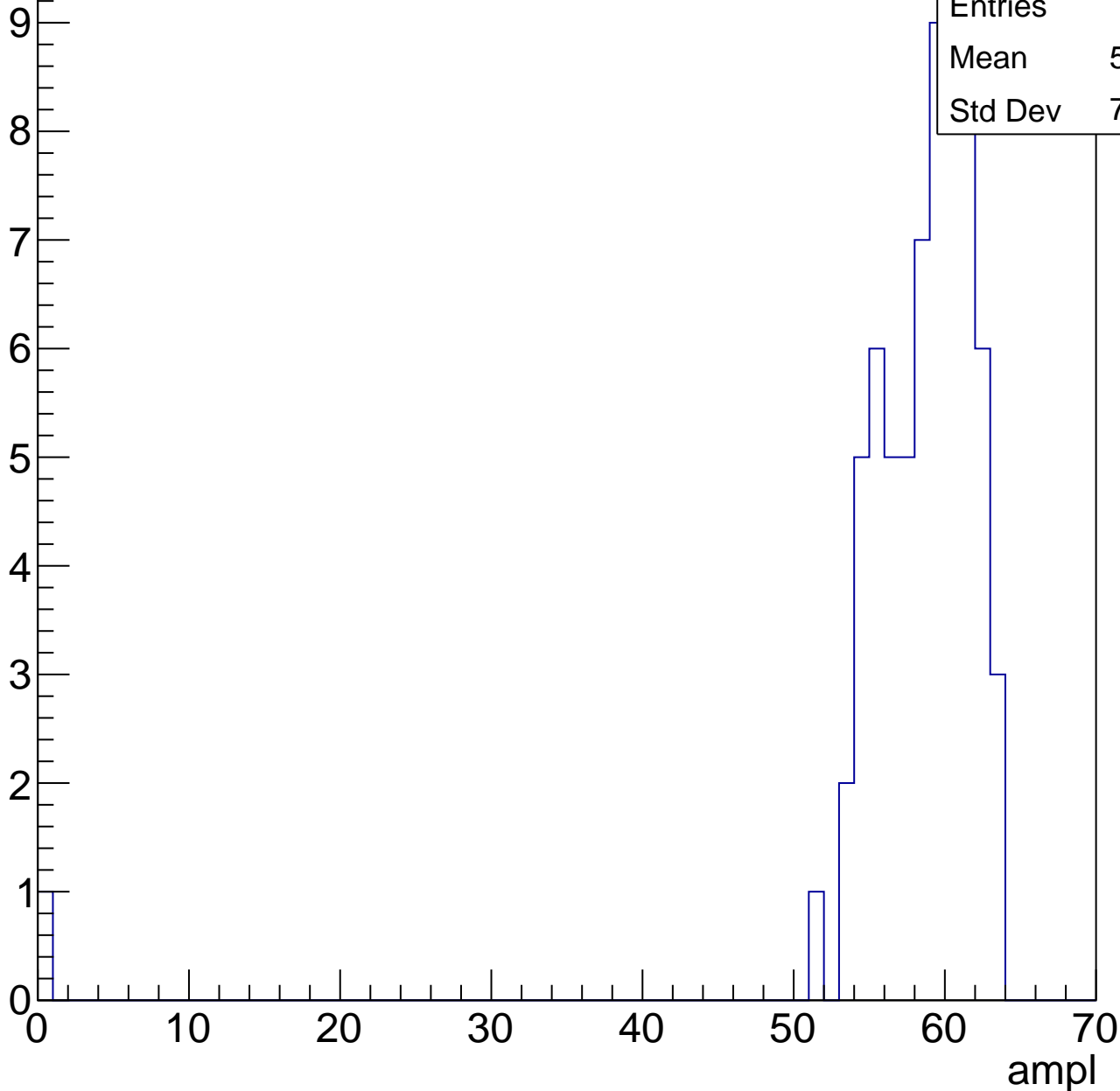


B1L003S, U16-ch20, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	57.39
Std Dev	7.669

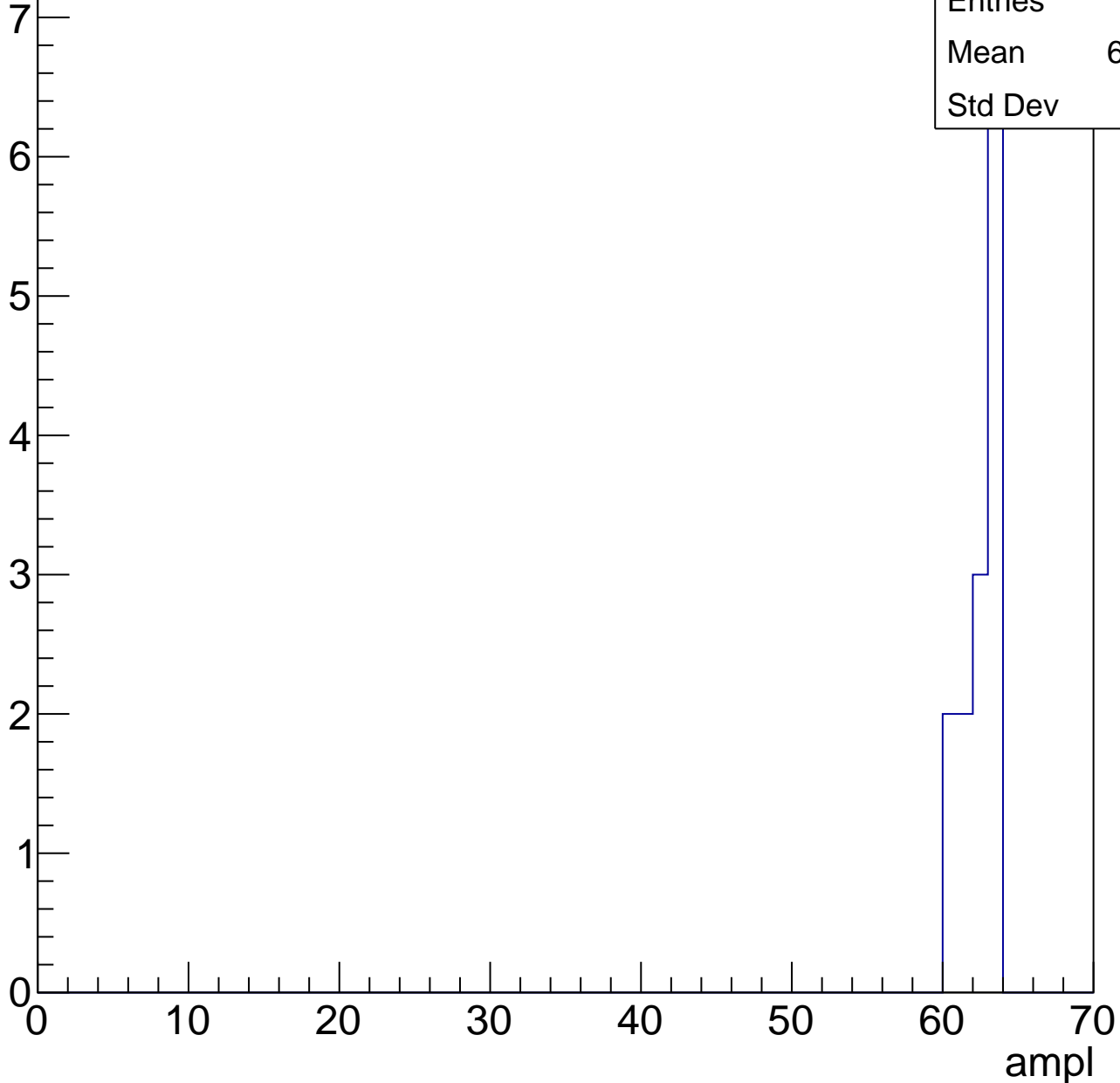


B1L003S, U16-ch20, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	62.07
Std Dev	1.1



B1L003S, U16-ch20, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch21, adc0

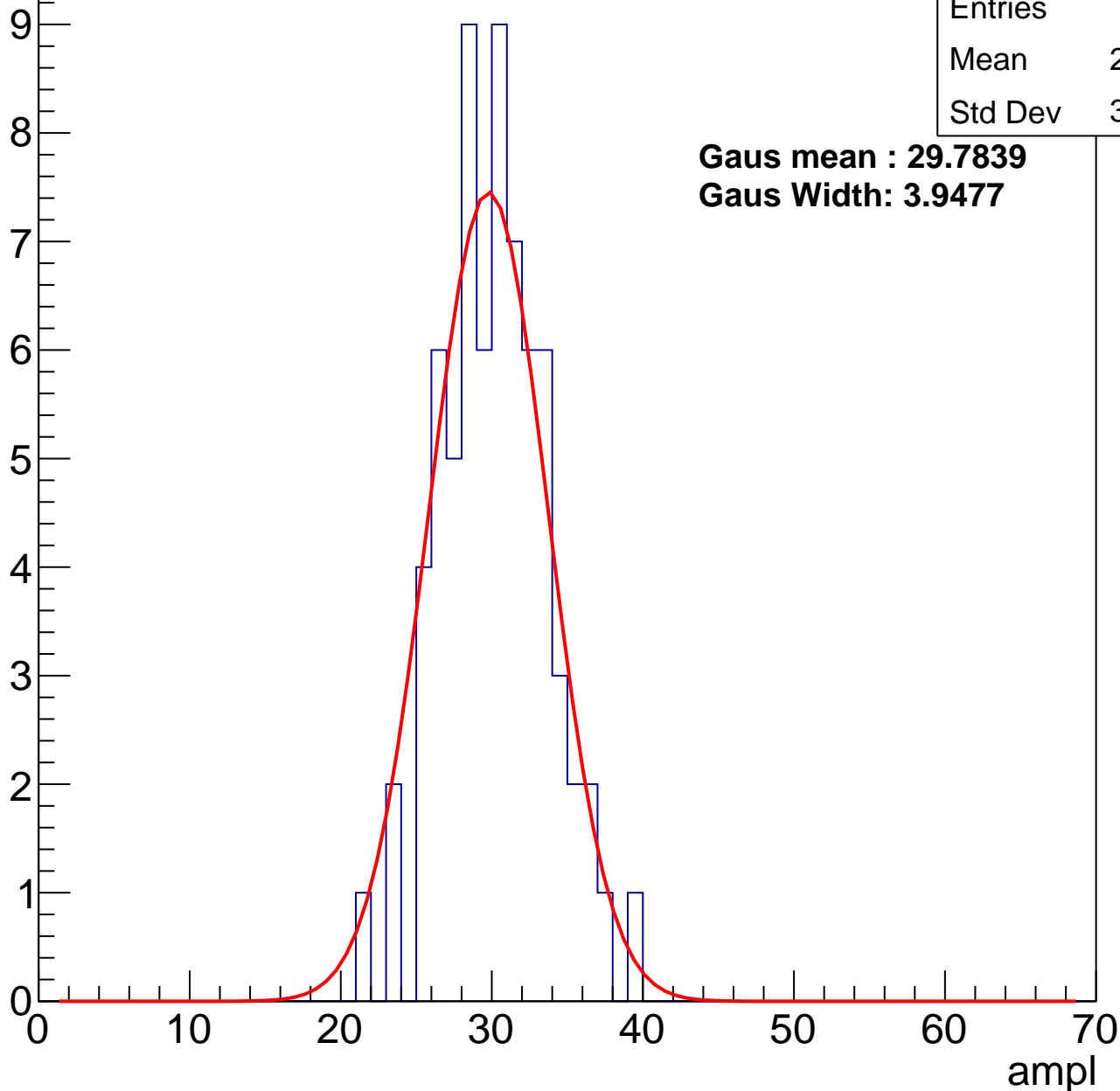
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.73
Std Dev	3.468

Gaus mean : 29.7839

Gaus Width: 3.9477



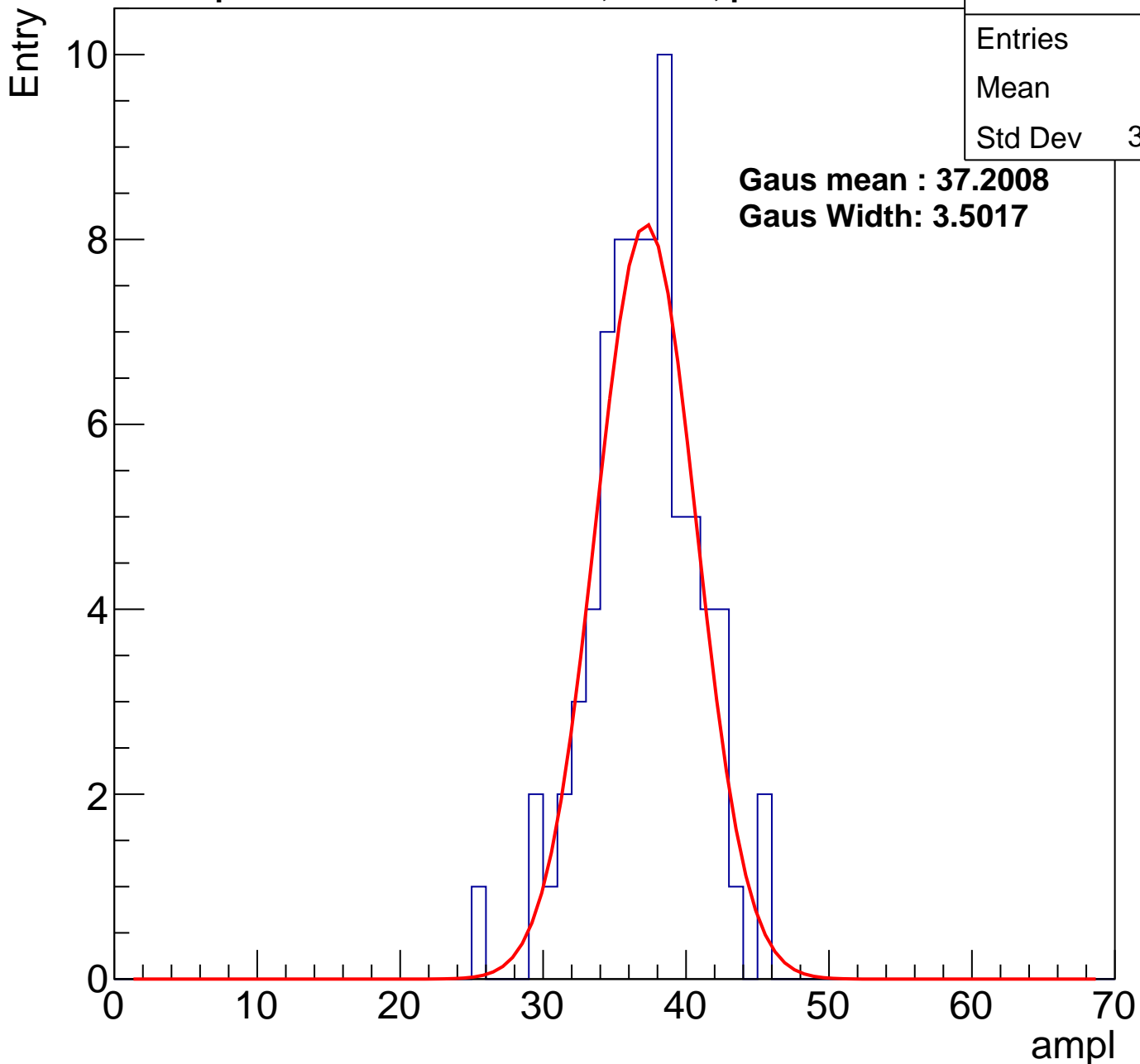
B1L003S, U16-ch21, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	36.6
Std Dev	3.699

Gaus mean : 37.2008

Gaus Width: 3.5017



B1L003S, U16-ch21, adc2

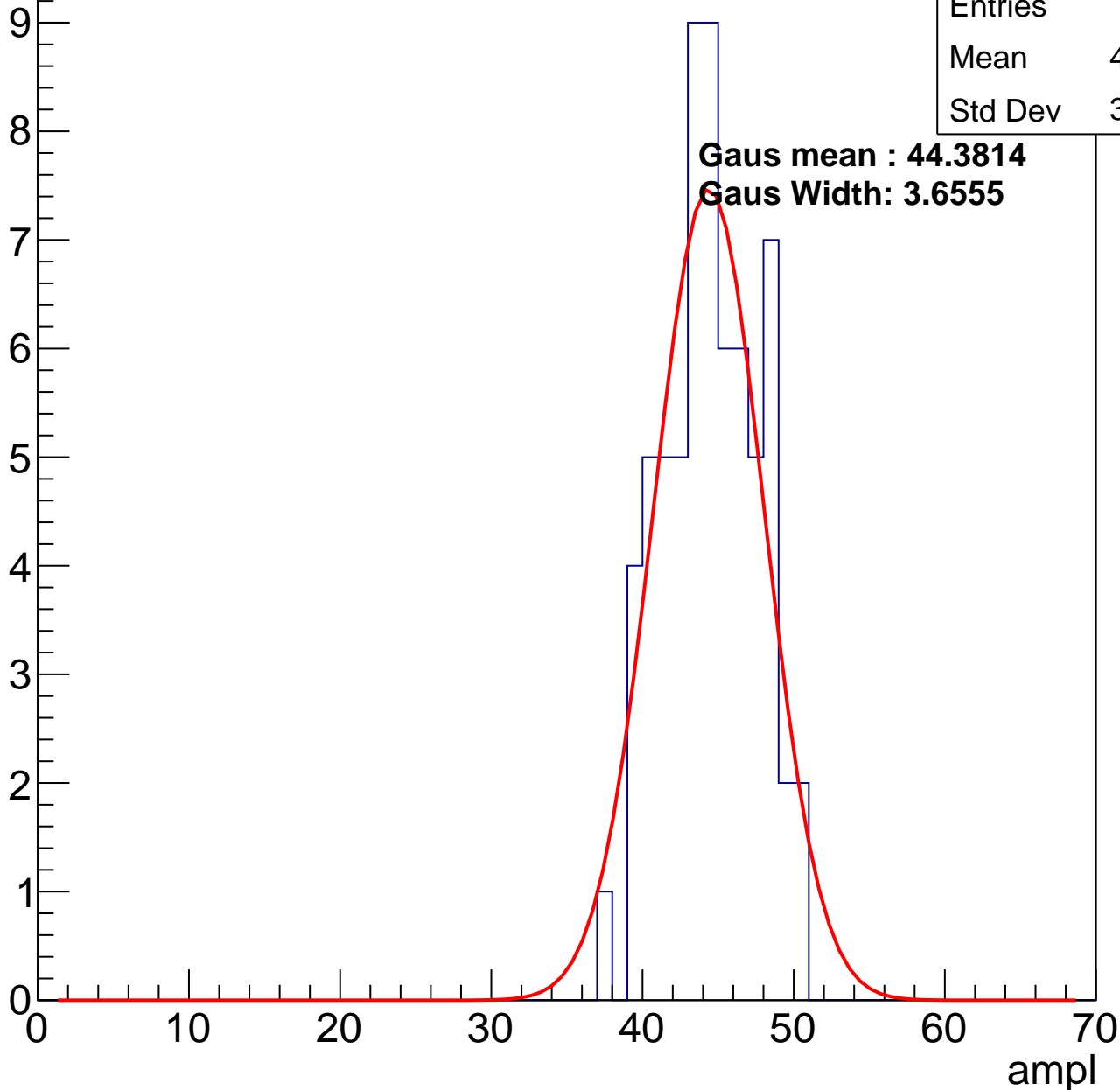
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.03
Std Dev	3.045

Gaus mean : 44.3814

Gaus Width: 3.6555

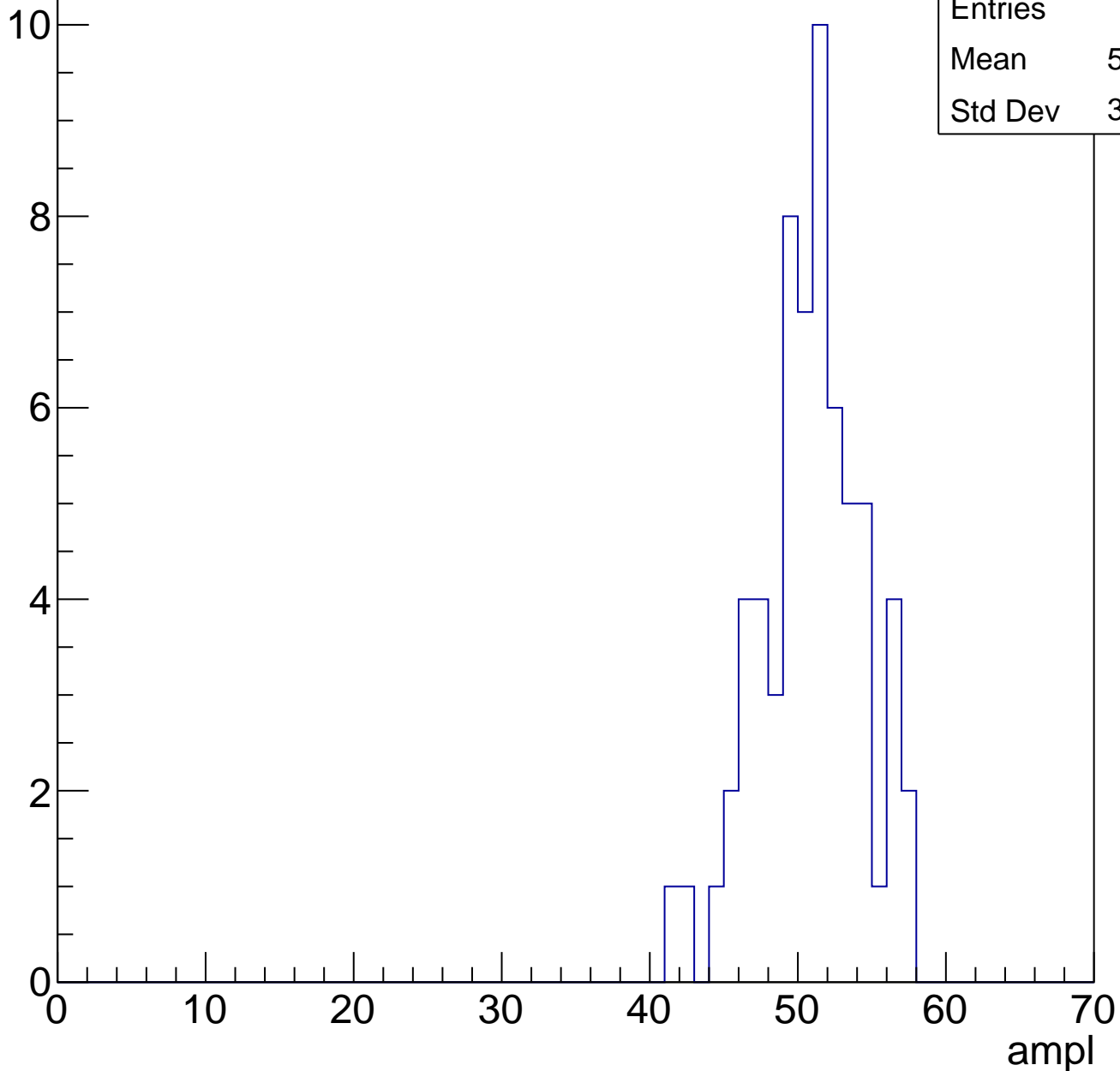


B1L003S, U16-ch21, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	50.39
Std Dev	3.462

Entry

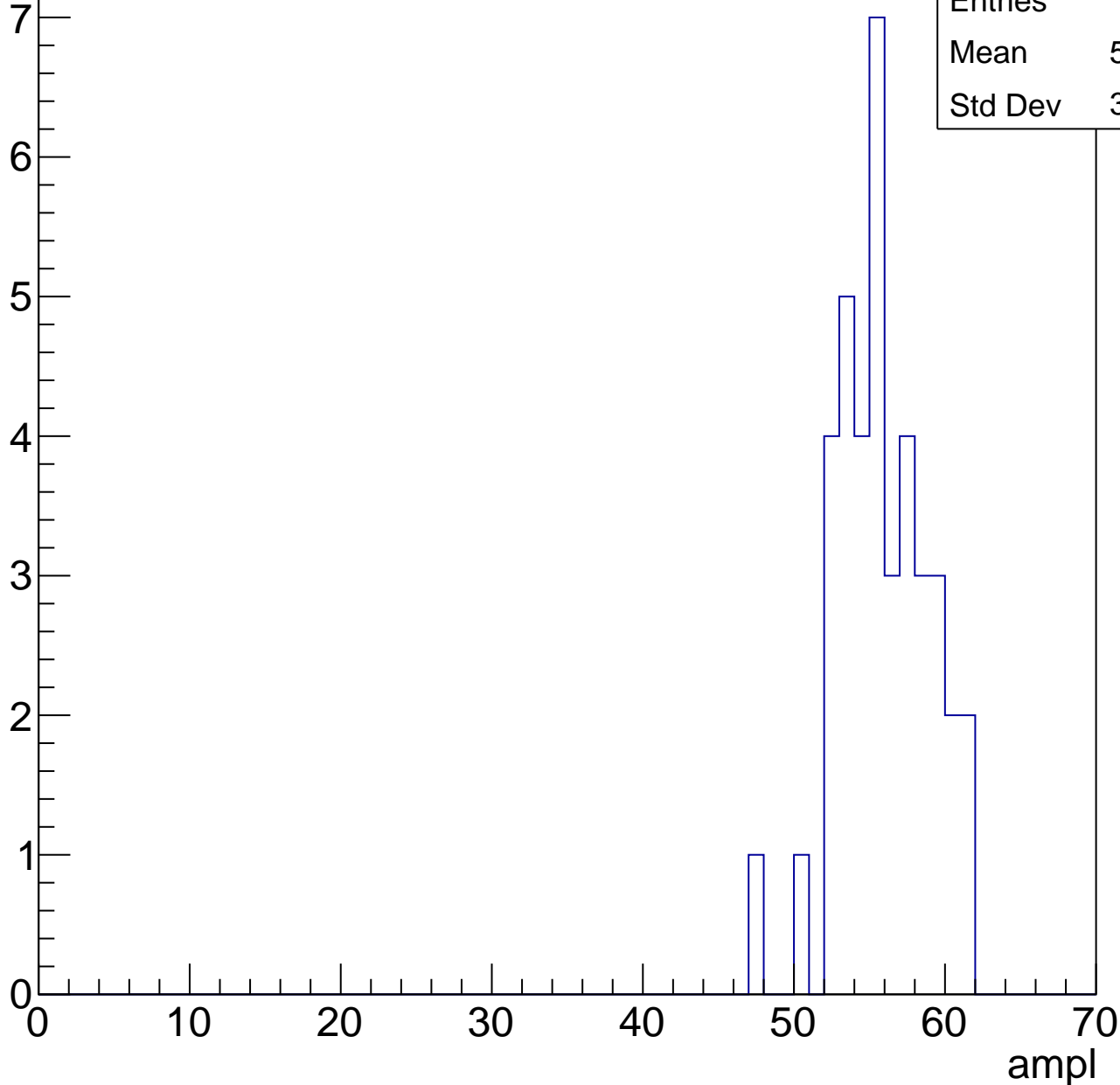


B1L003S, U16-ch21, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	55.38
Std Dev	3.035

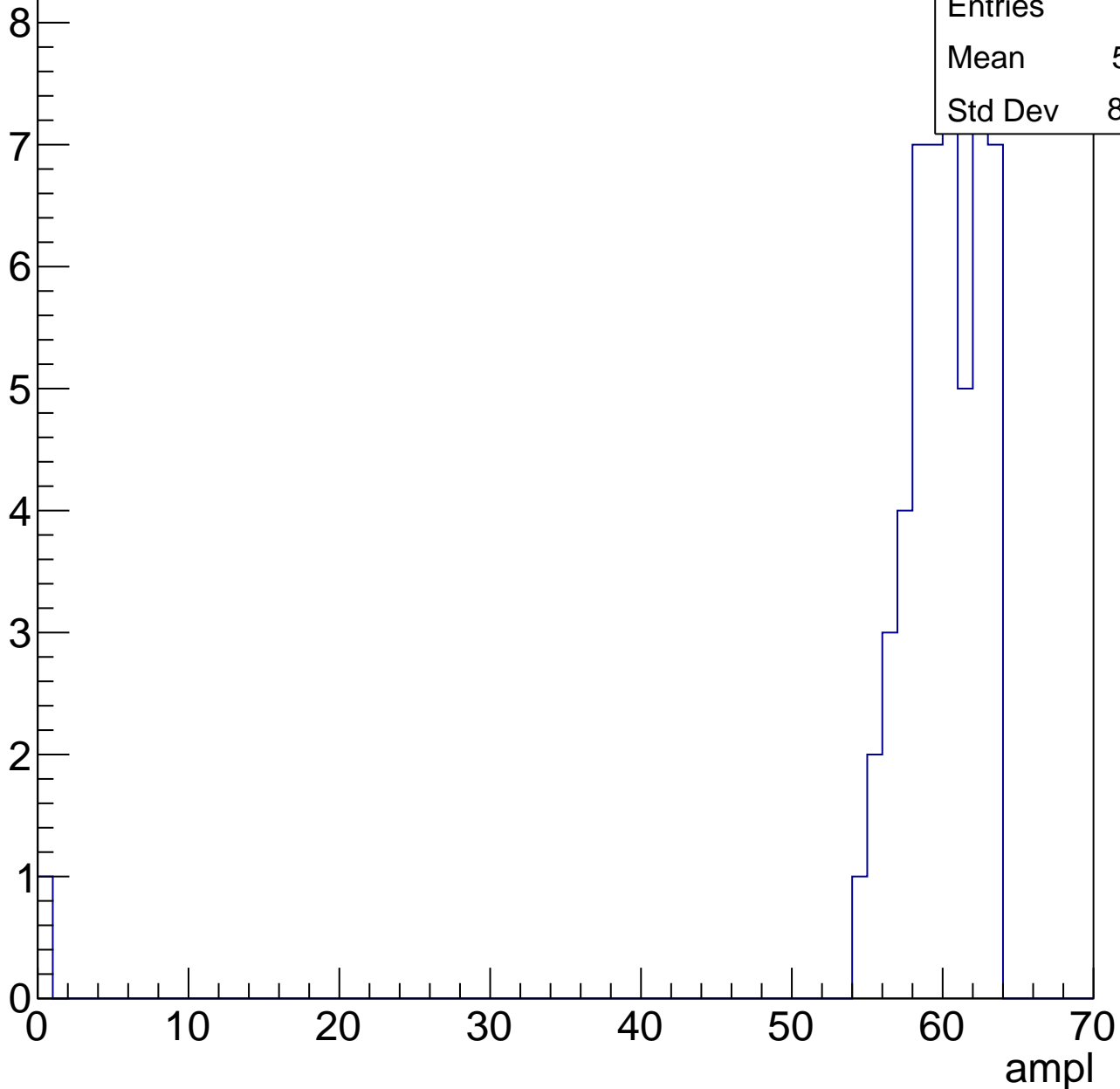


B1L003S, U16-ch21, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

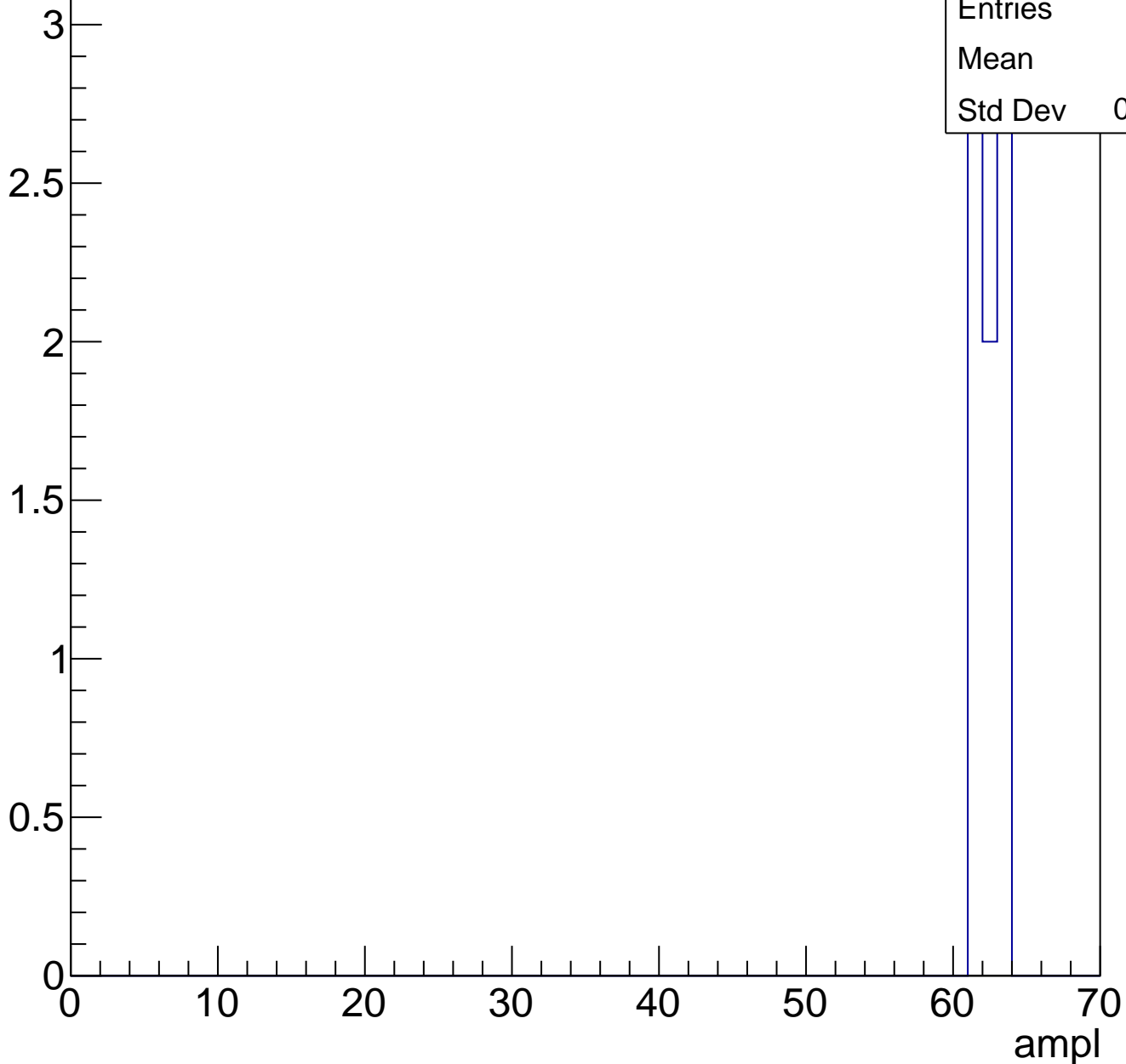
Entries	53
Mean	58.51
Std Dev	8.453



B1L003S, U16-ch21, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch21, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch22, adc0

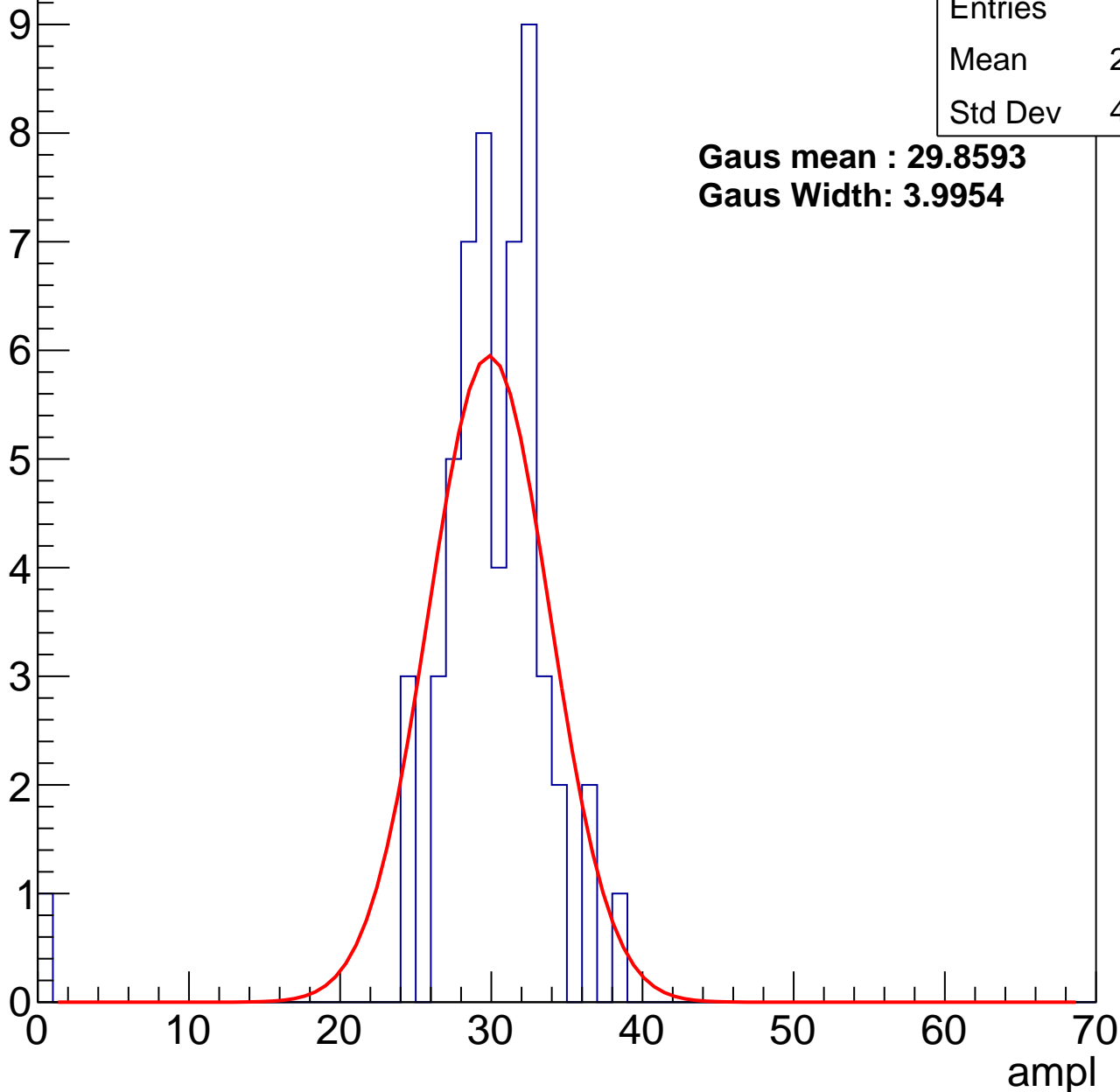
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.36
Std Dev	4.959

Gaus mean : 29.8593

Gaus Width: 3.9954



B1L003S, U16-ch22, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	36.91
Std Dev	3.726

Gaus mean : 37.9148

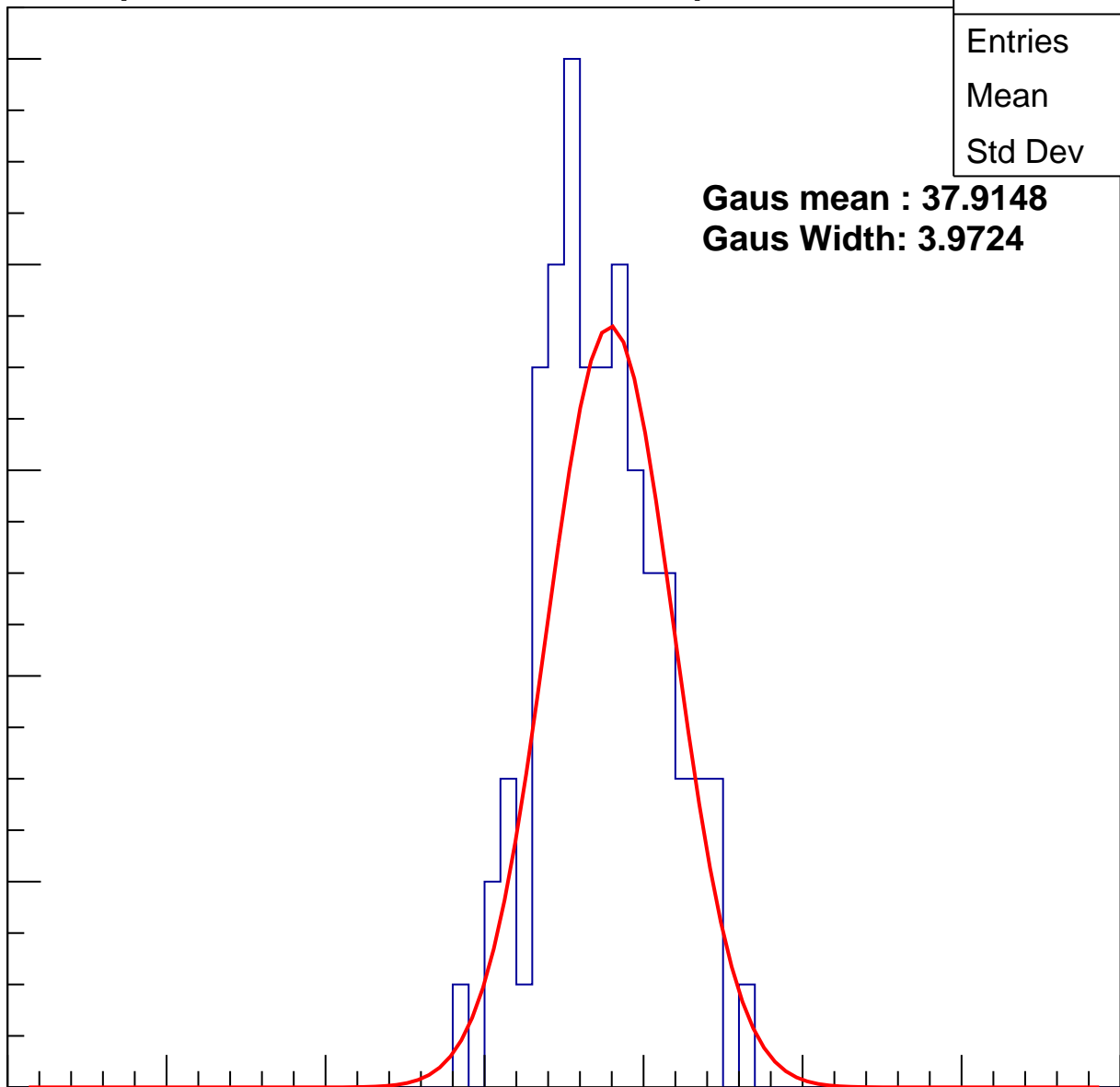
Gaus Width: 3.9724

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch22, adc2

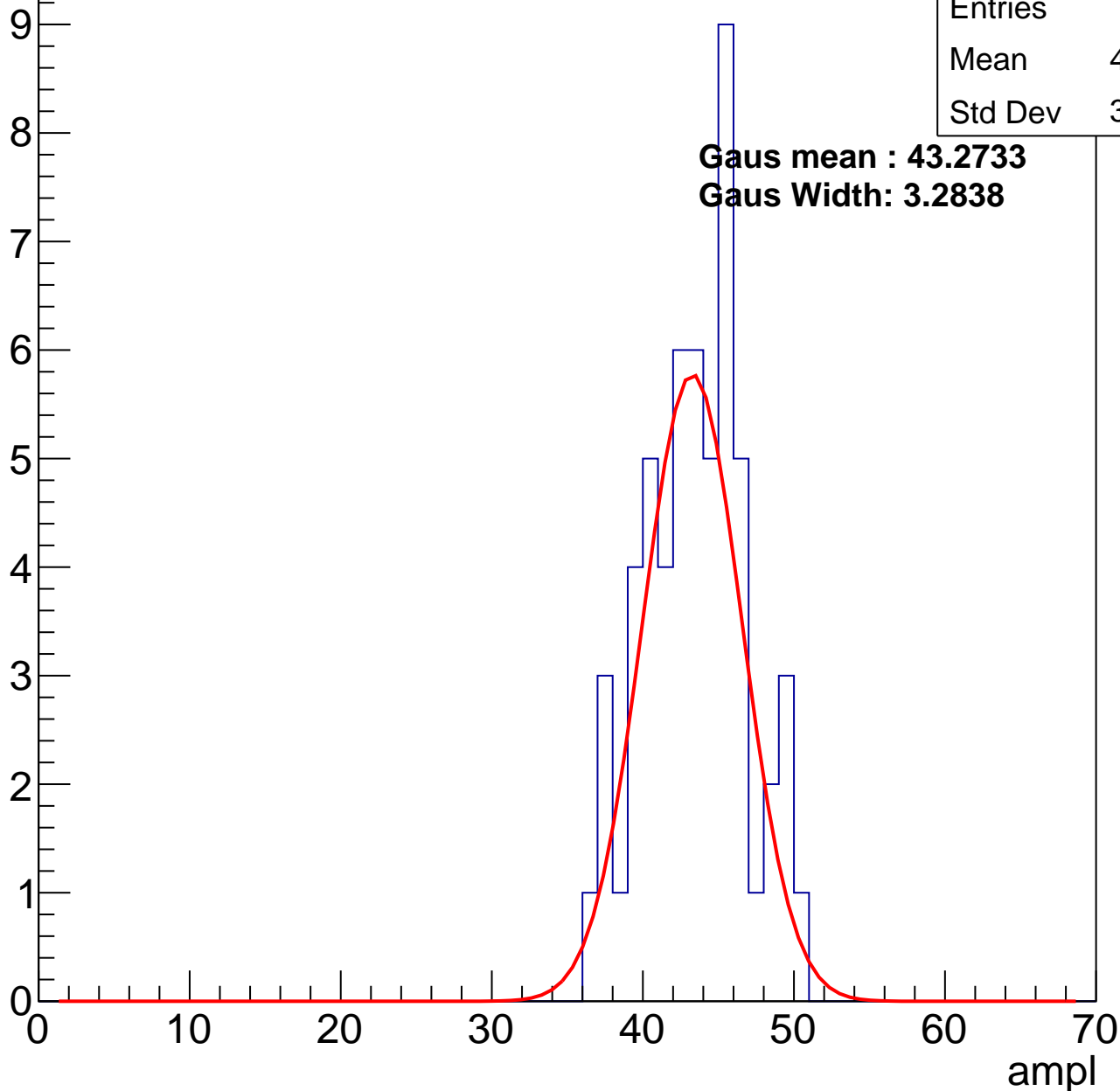
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.04
Std Dev	3.333

Gaus mean : 43.2733

Gaus Width: 3.2838

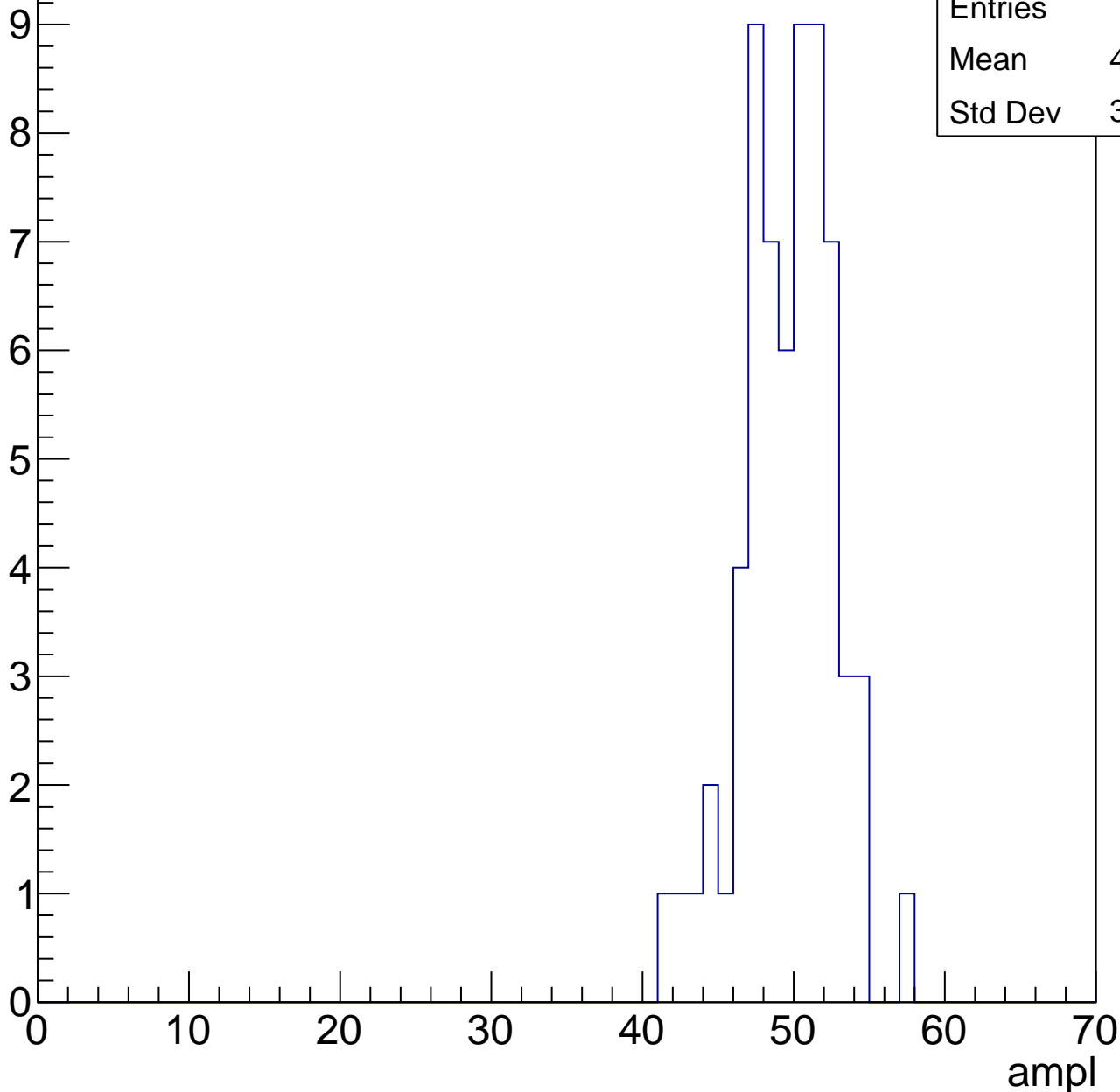


B1L003S, U16-ch22, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.17
Std Dev	3.029

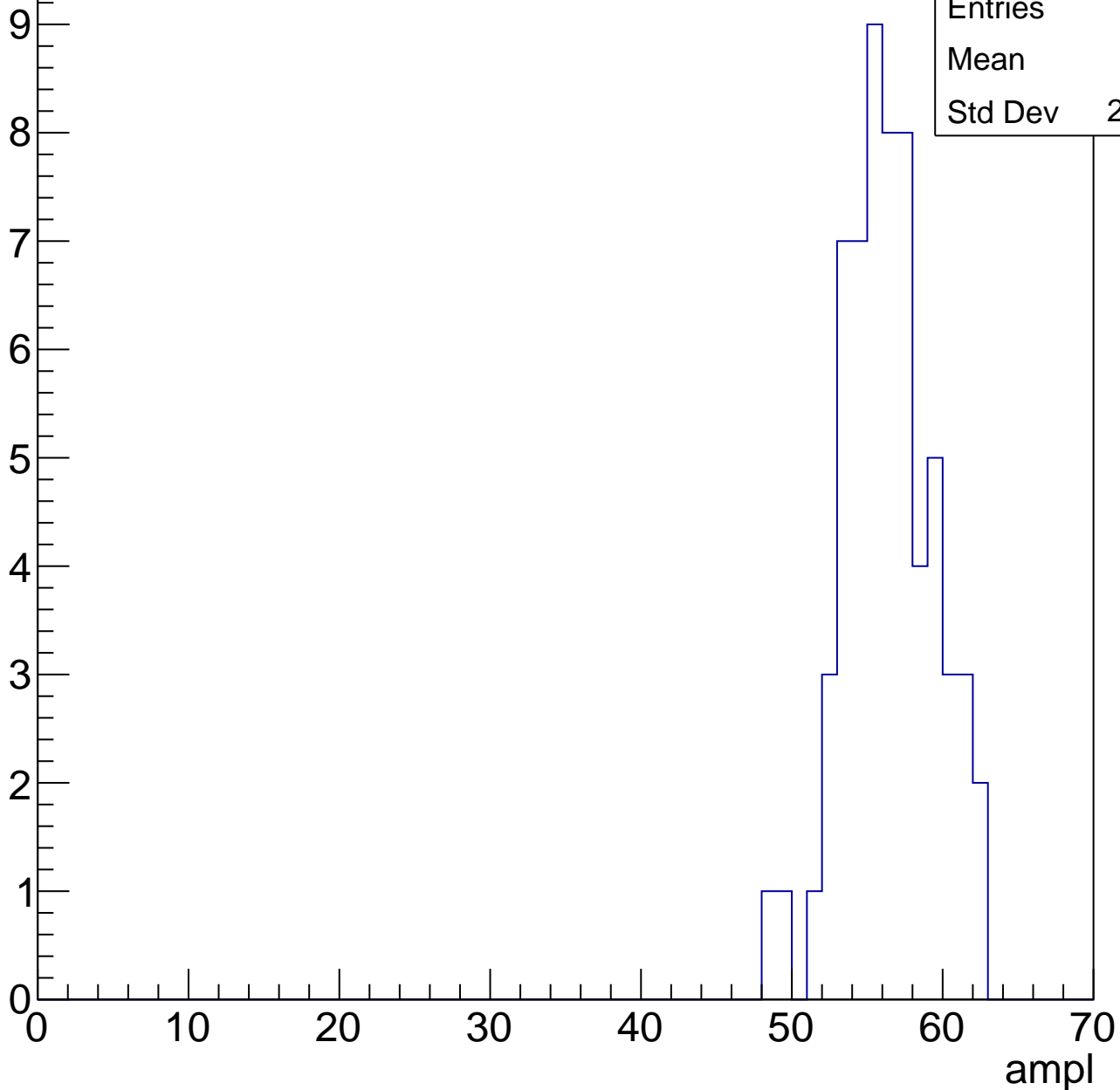


B1L003S, U16-ch22, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	55.9
Std Dev	2.977

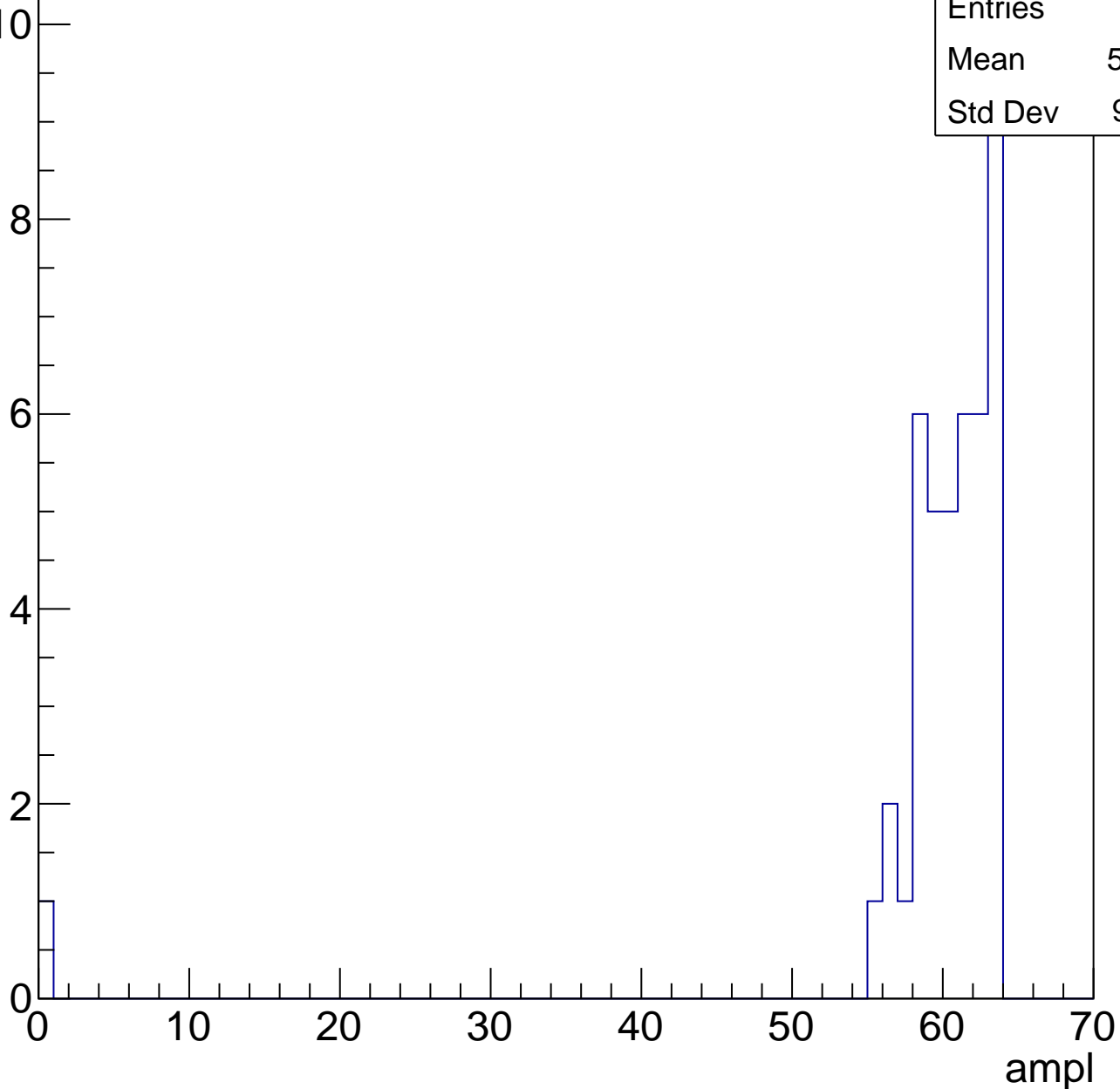


B1L003S, U16-ch22, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

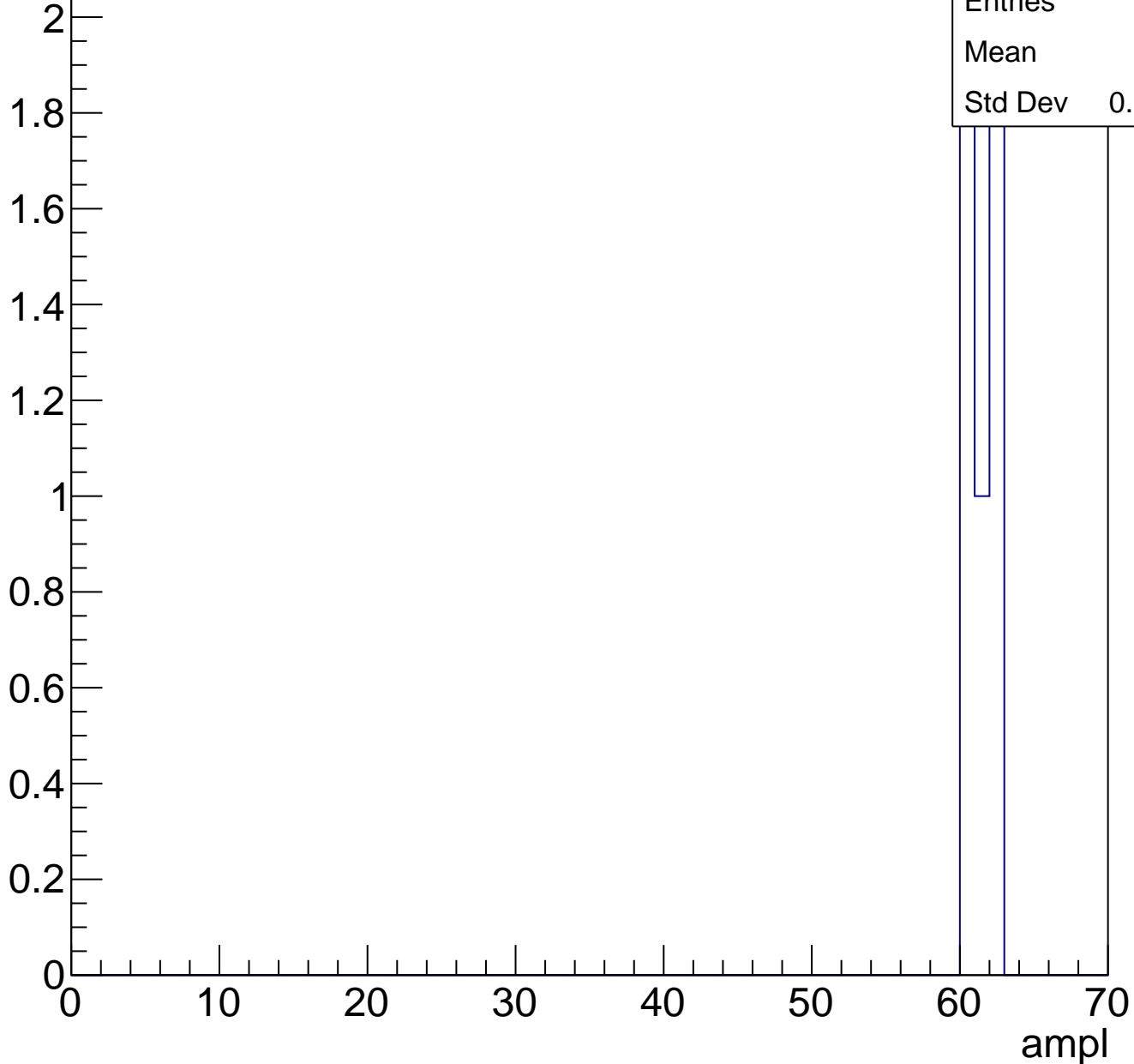
Entries	43
Mean	58.95
Std Dev	9.361



B1L003S, U16-ch22, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch22, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch23, adc0

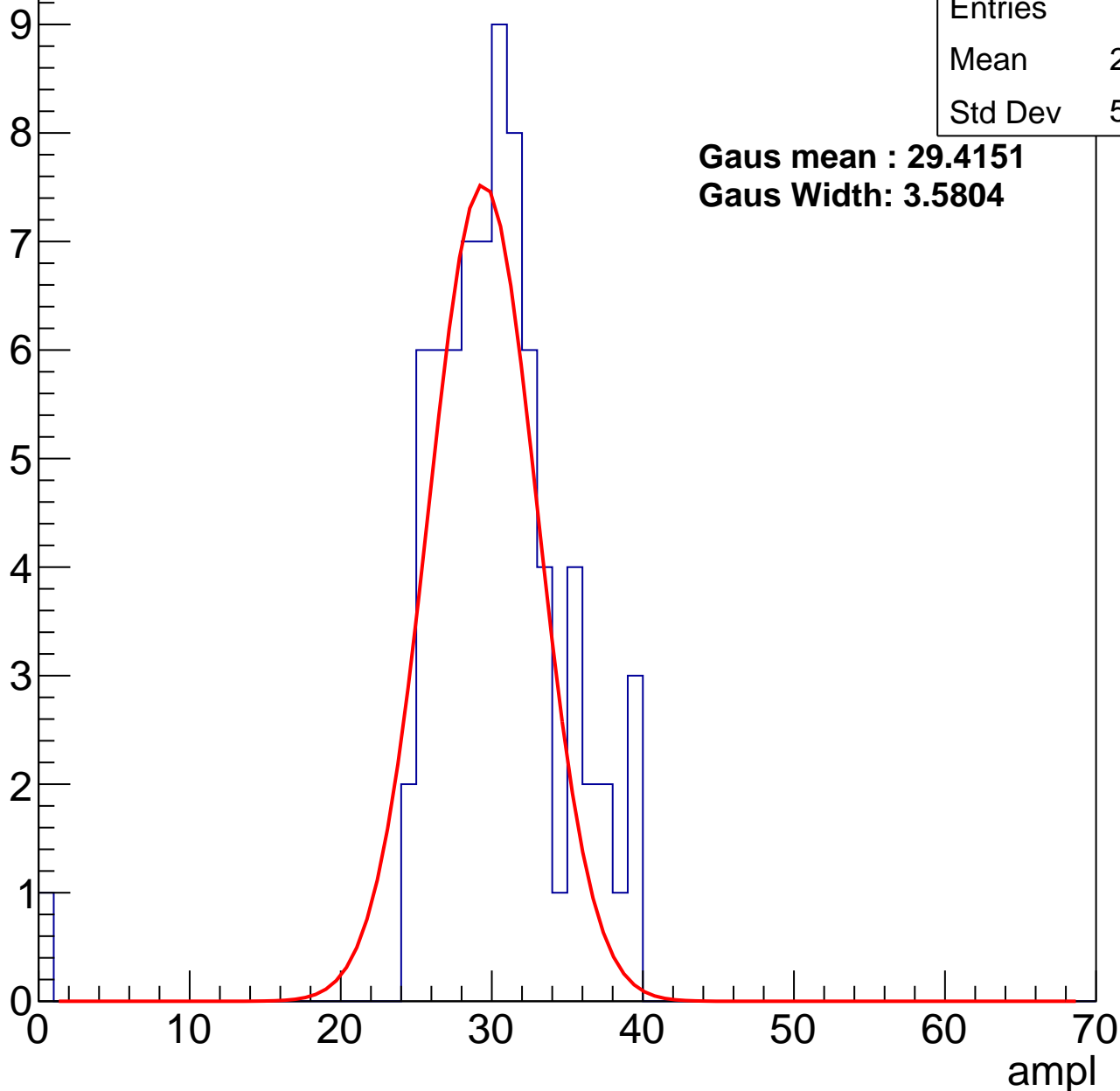
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.76
Std Dev	5.117

Gaus mean : 29.4151

Gaus Width: 3.5804



B1L003S, U16-ch23, adc1

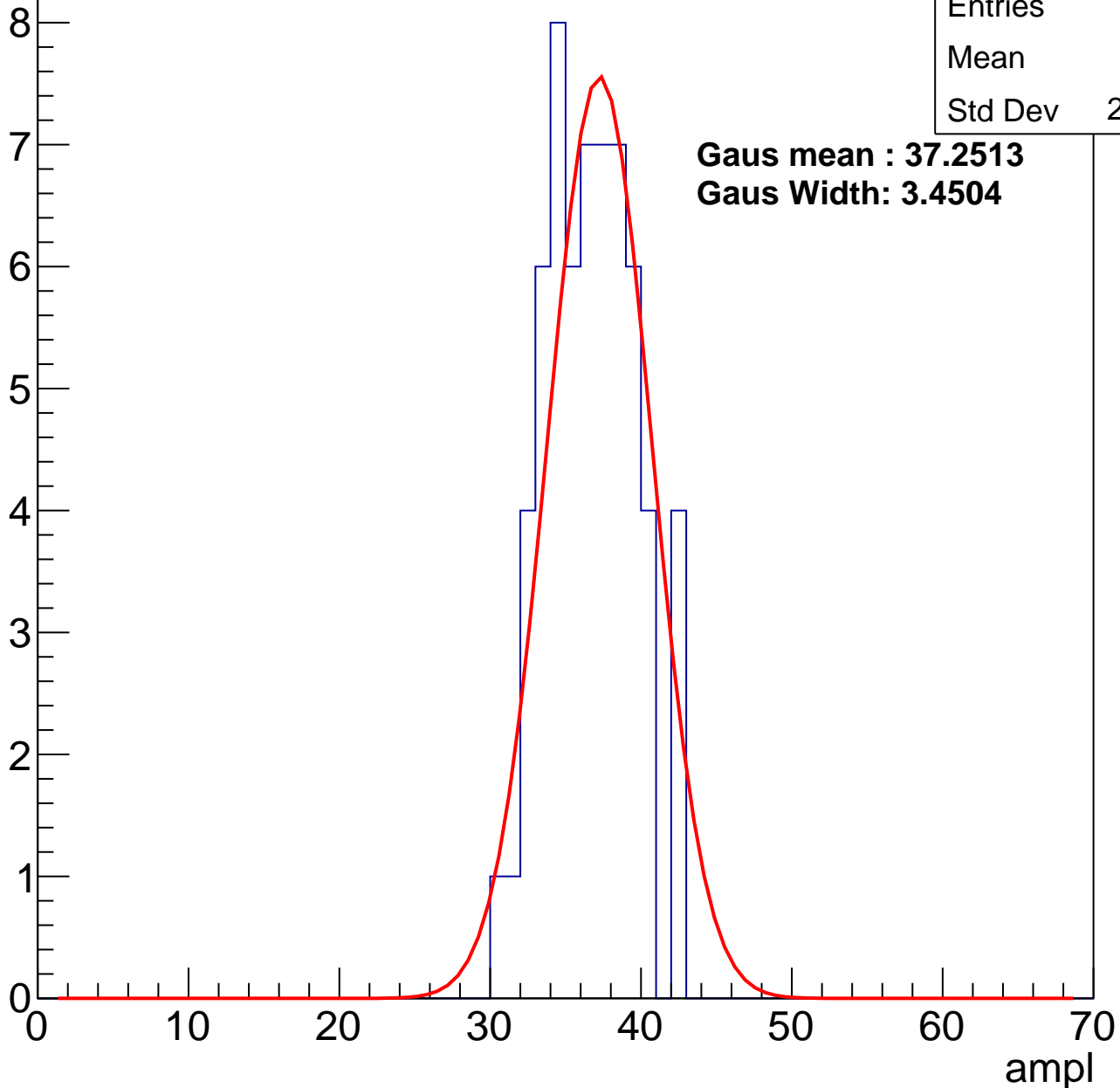
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.2
Std Dev	2.896

Gaus mean : 37.2513

Gaus Width: 3.4504



B1L003S, U16-ch23, adc2

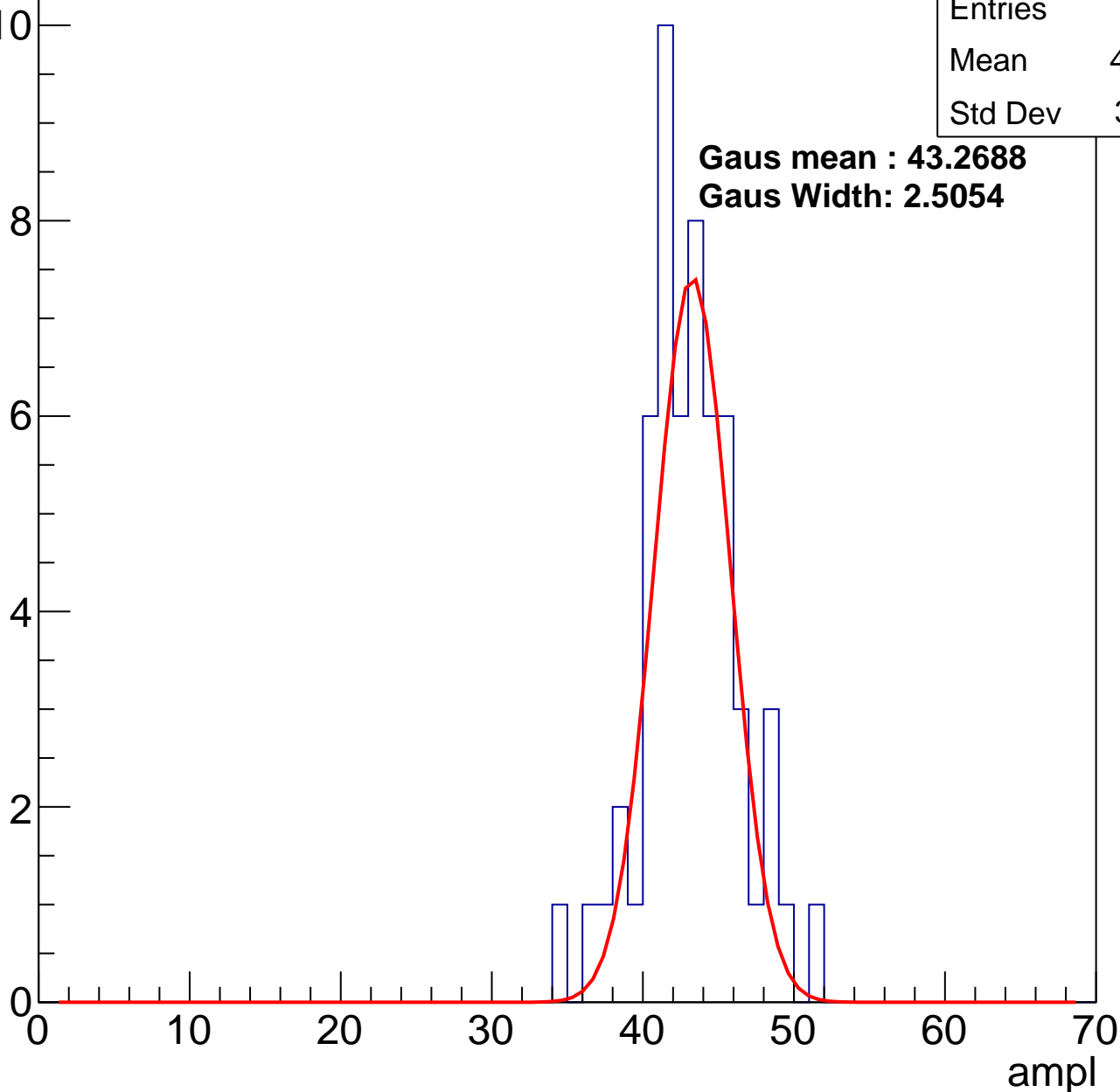
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.65
Std Dev	3.171

Gaus mean : 43.2688

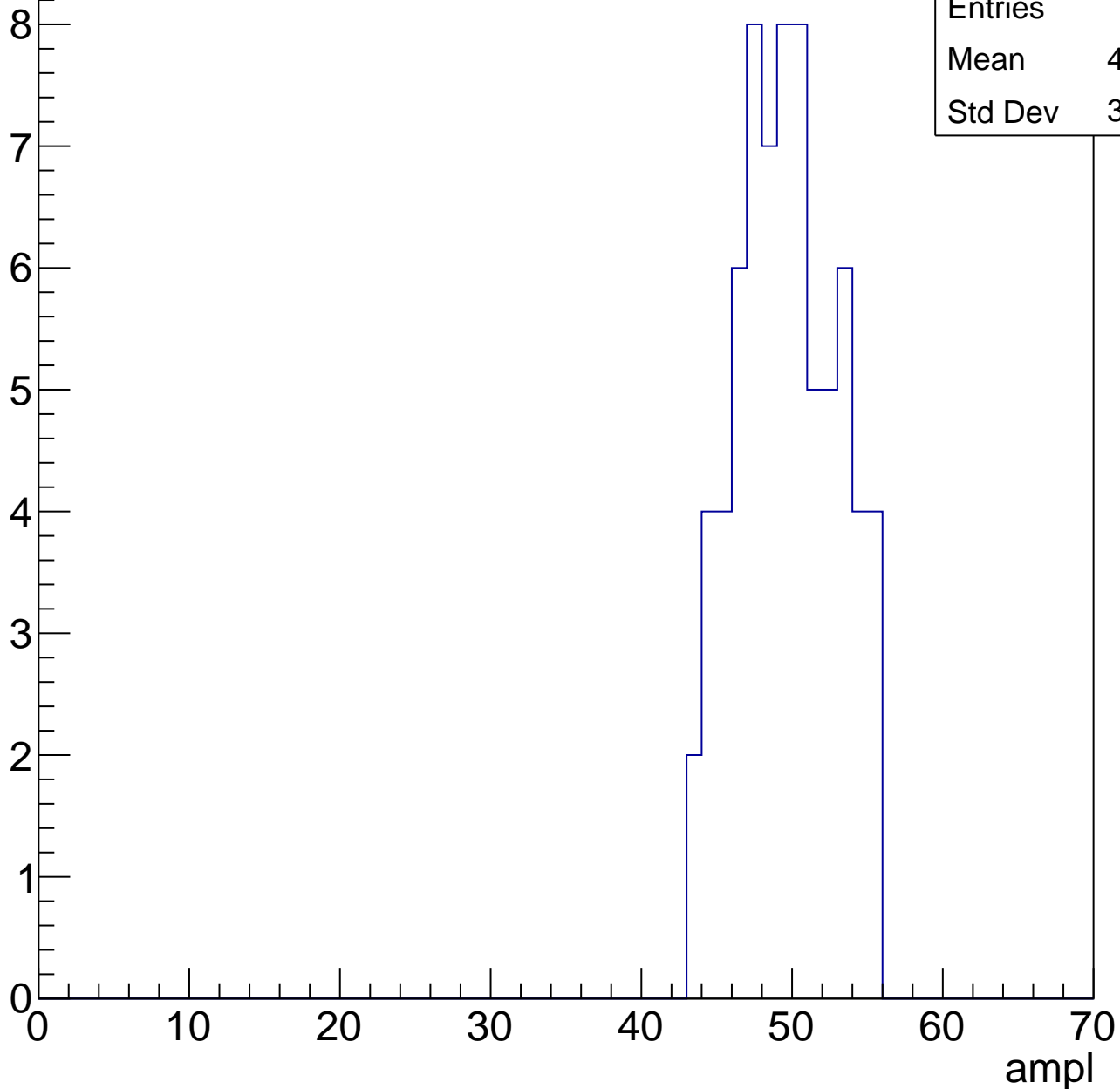
Gaus Width: 2.5054



B1L003S, U16-ch23, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



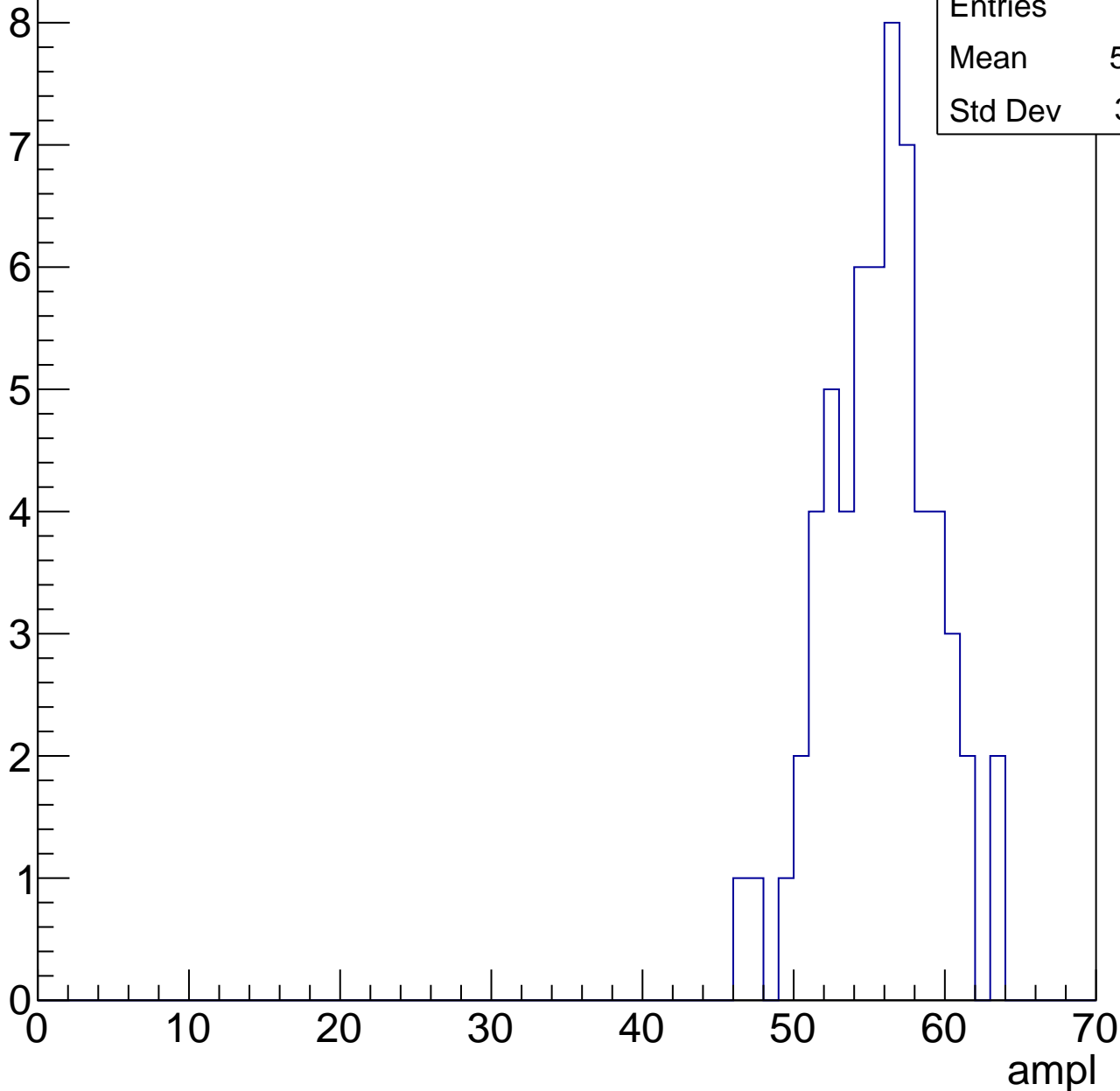
Entries	71
Mean	49.17
Std Dev	3.228

B1L003S, U16-ch23, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

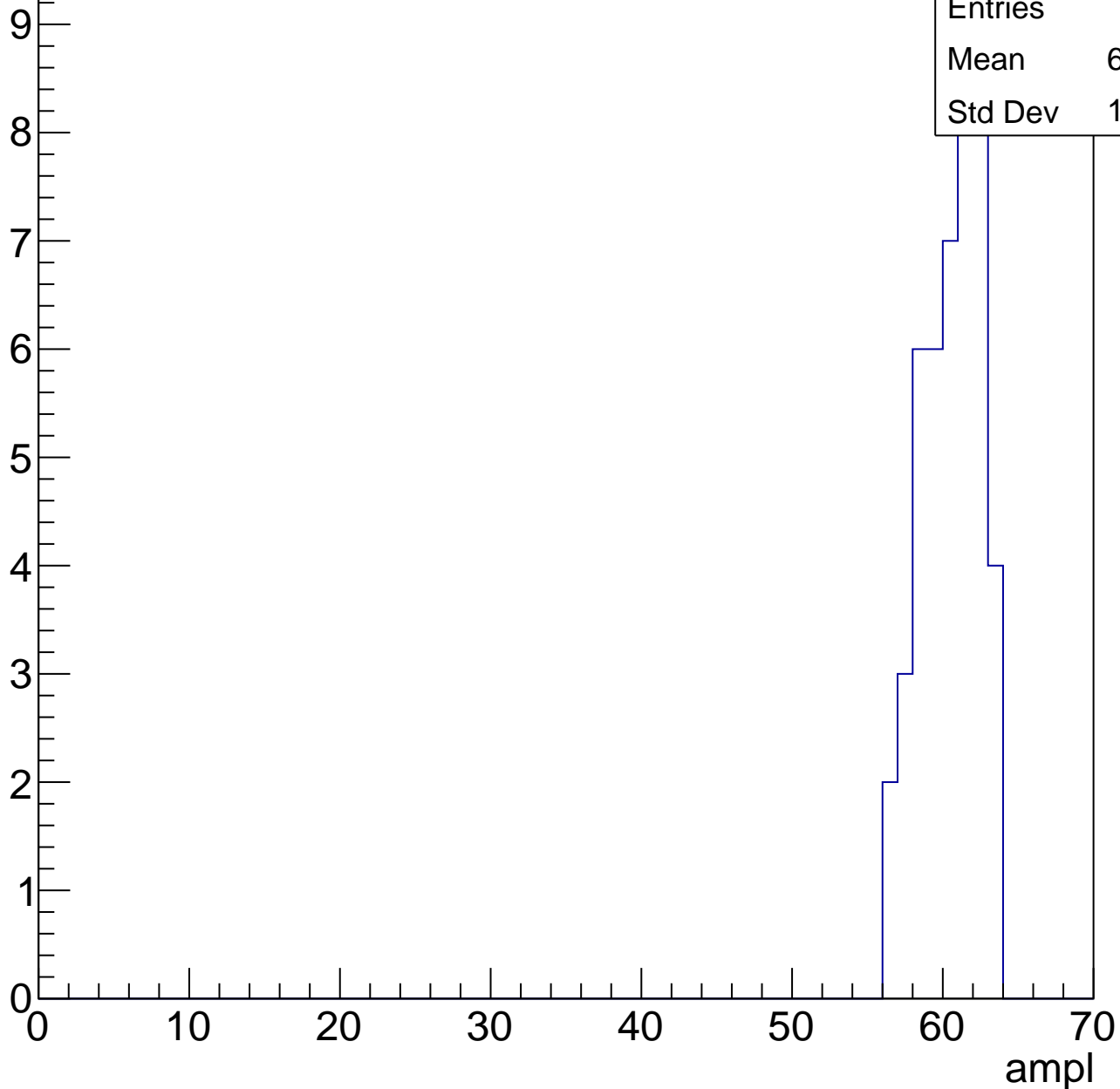
Entries	60
Mean	55.25
Std Dev	3.581



B1L003S, U16-ch23, adc5

calib_packv5_042523_0143.root, FC#13, port D2

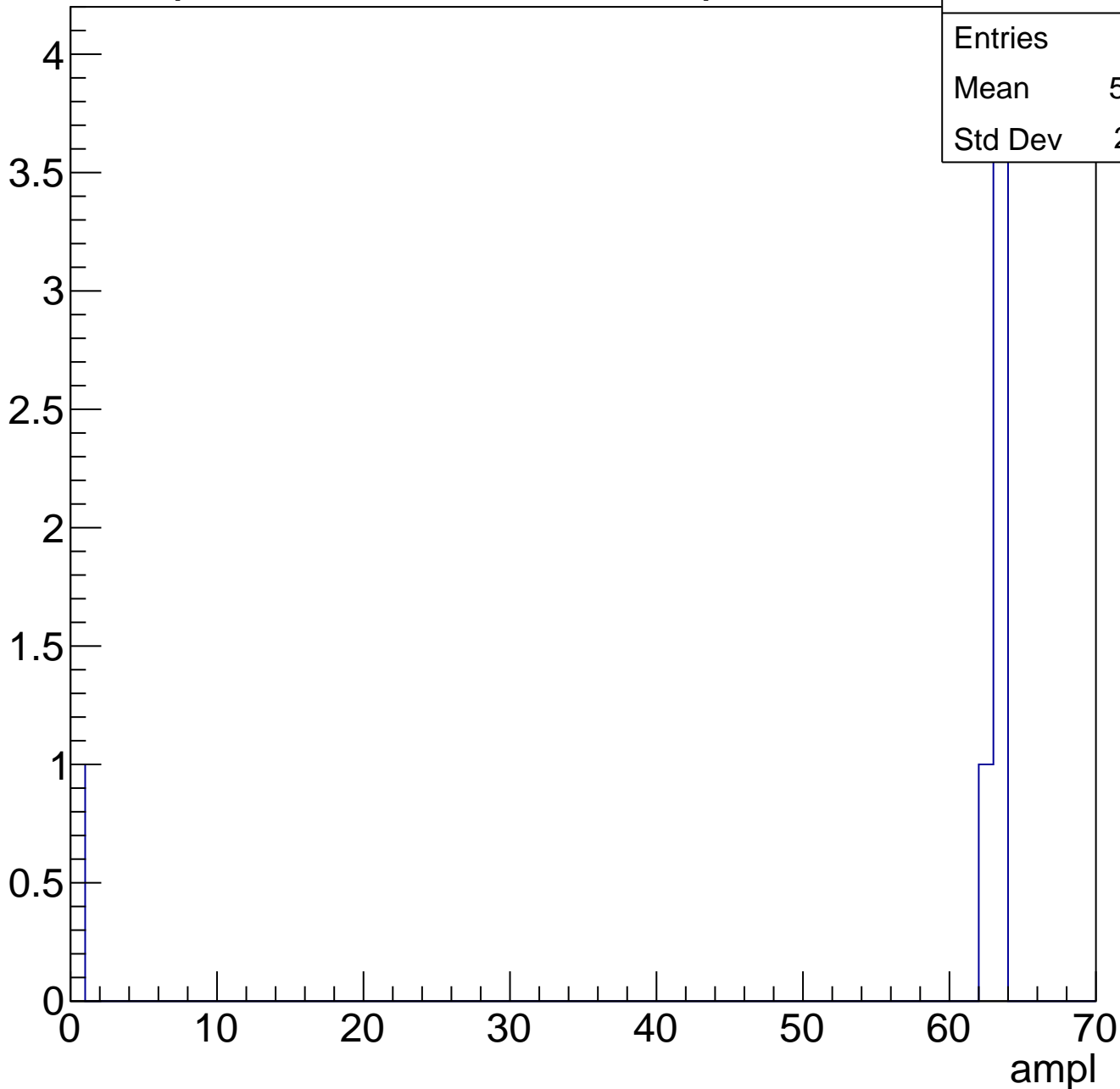
Entry



B1L003S, U16-ch23, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch23, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch24, adc0

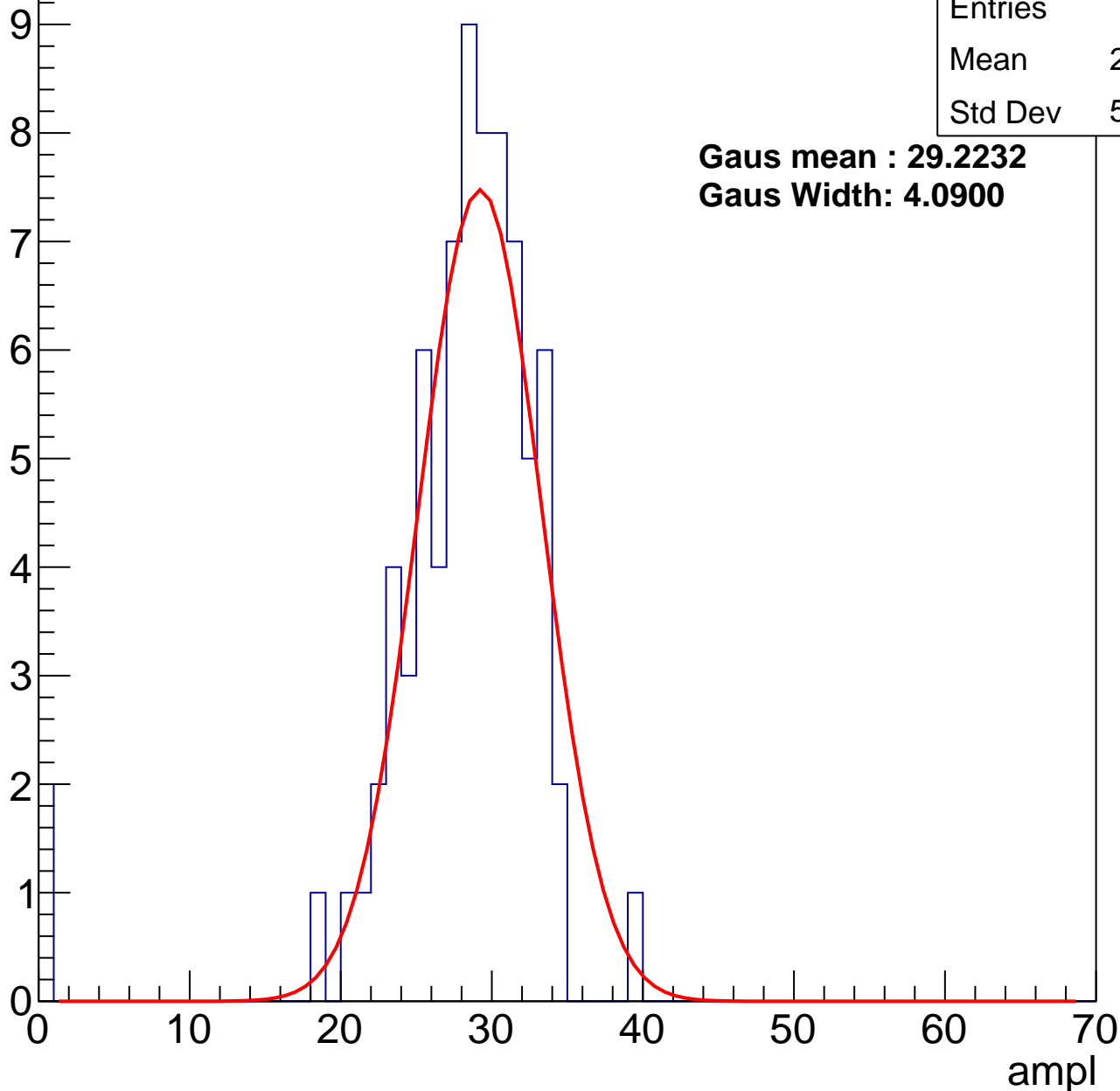
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	27.48
Std Dev	5.792

Gaus mean : 29.2232

Gaus Width: 4.0900



B1L003S, U16-ch24, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	58
Mean	35.79
Std Dev	3.072

Gaus mean : 36.0052

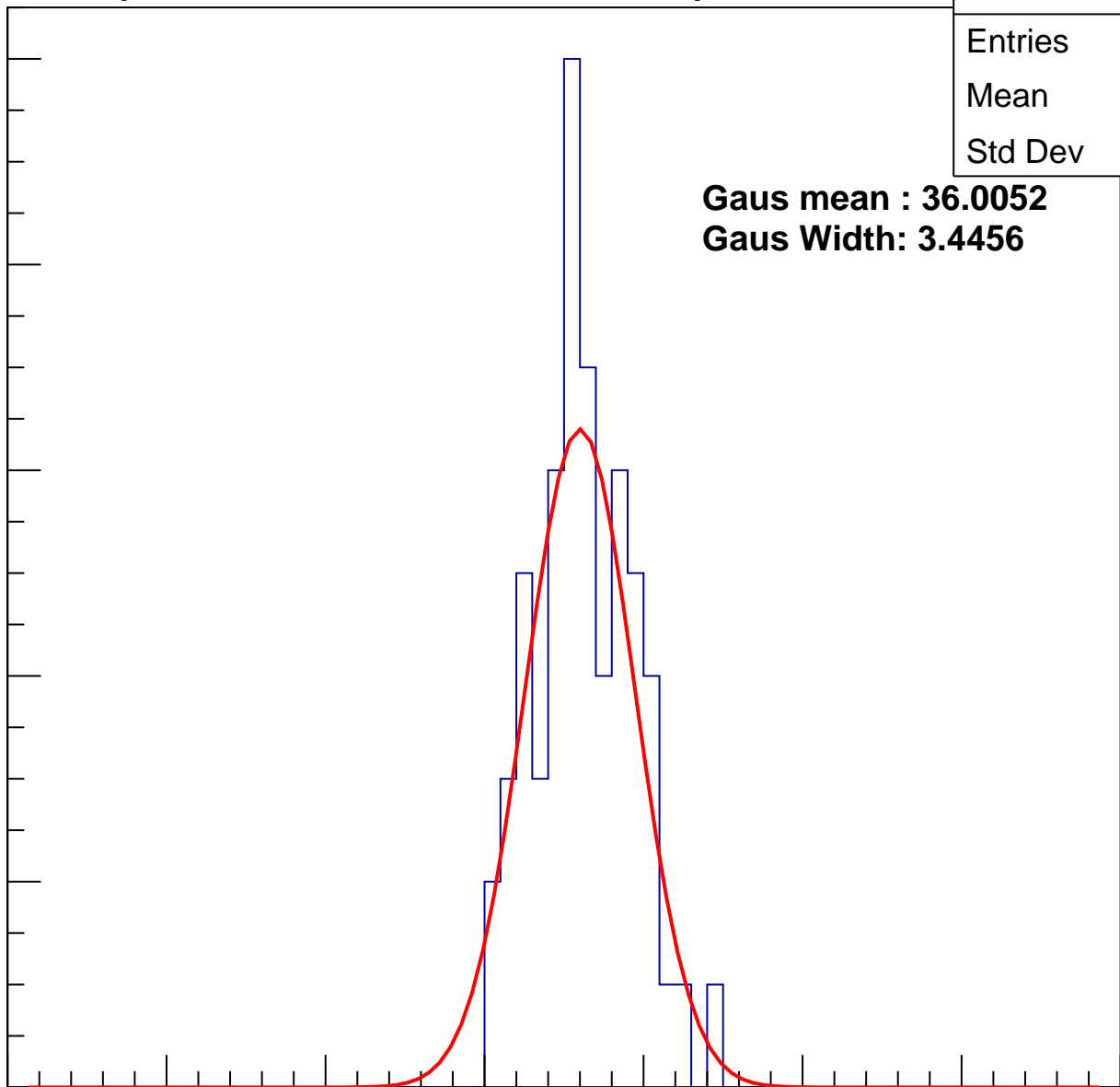
Gaus Width: 3.4456

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch24, adc2

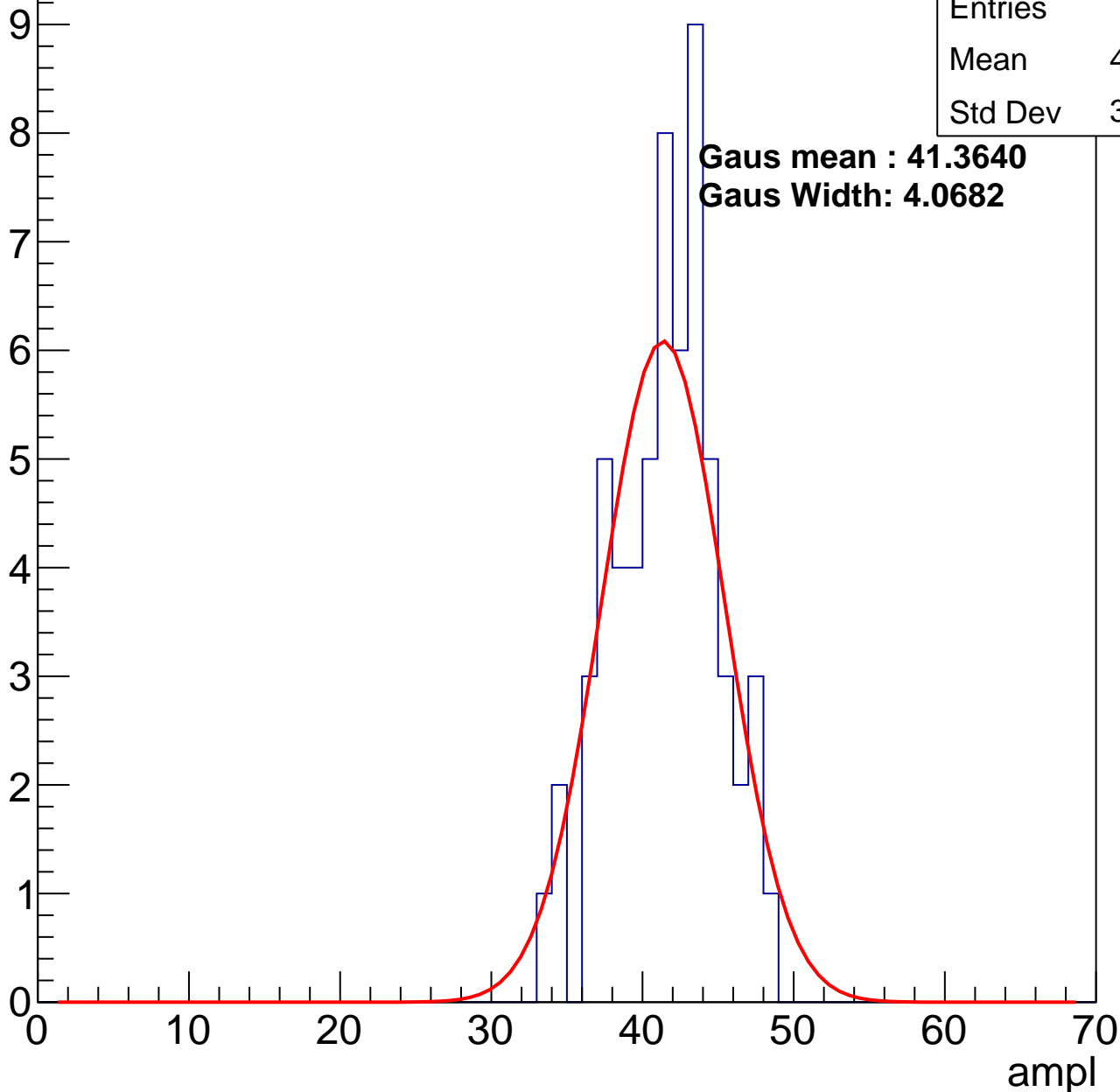
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	41.07
Std Dev	3.425

Gaus mean : 41.3640

Gaus Width: 4.0682

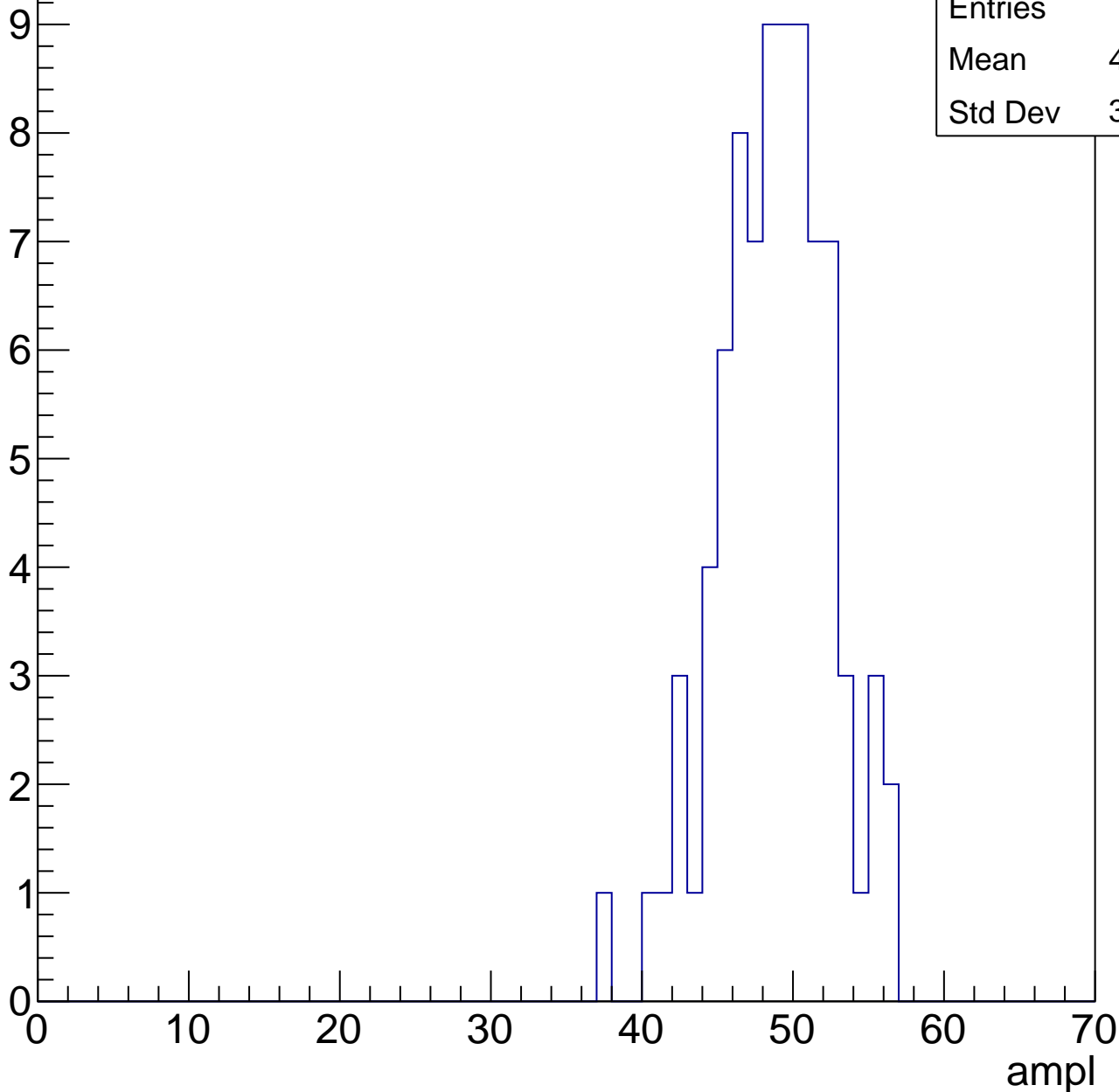


B1L003S, U16-ch24, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	48.34
Std Dev	3.696

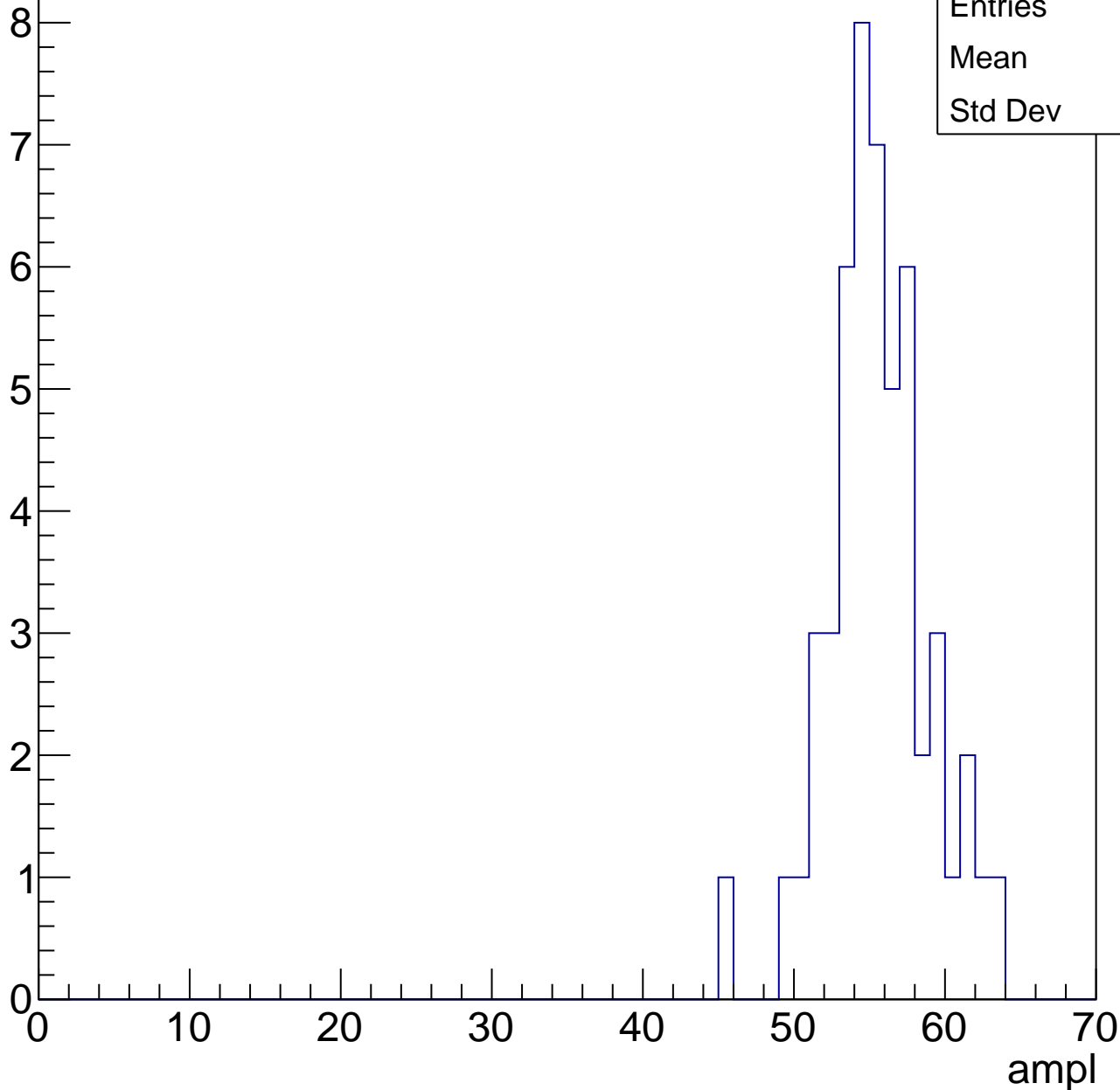


B1L003S, U16-ch24, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	55.1
Std Dev	3.35

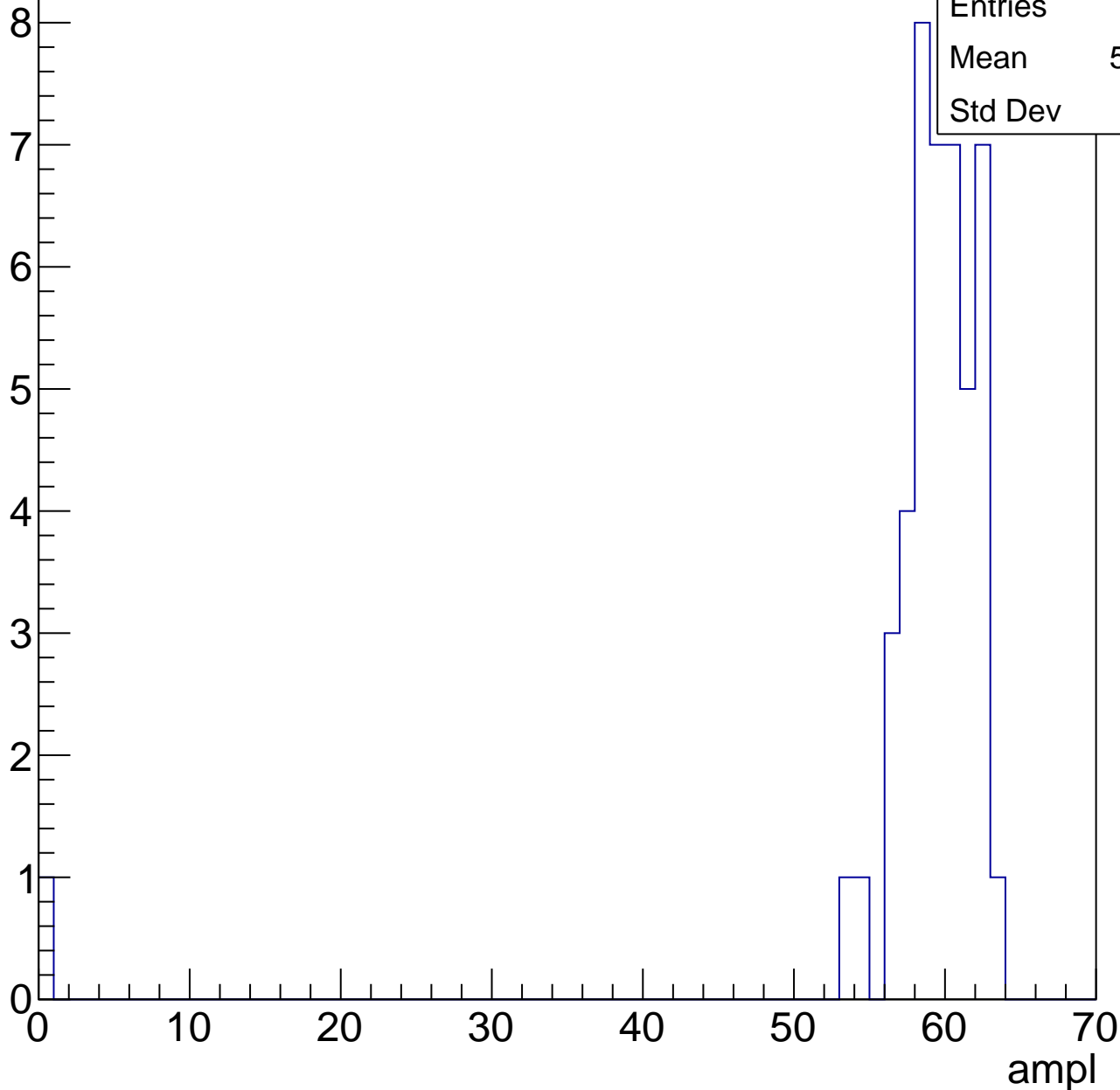


B1L003S, U16-ch24, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	57.82
Std Dev	8.99

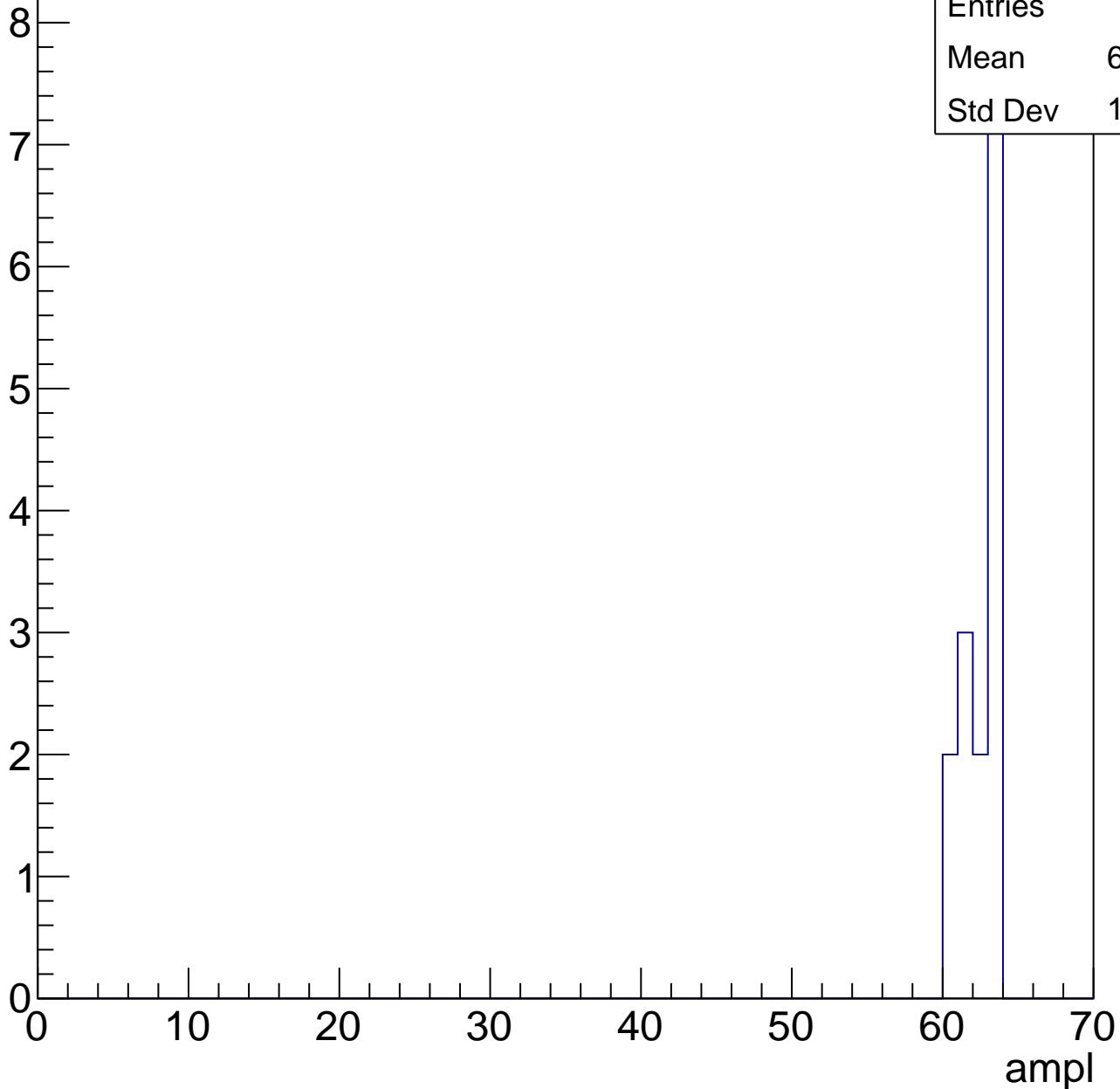


B1L003S, U16-ch24, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	62.07
Std Dev	1.123



B1L003S, U16-ch24, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch25, adc0

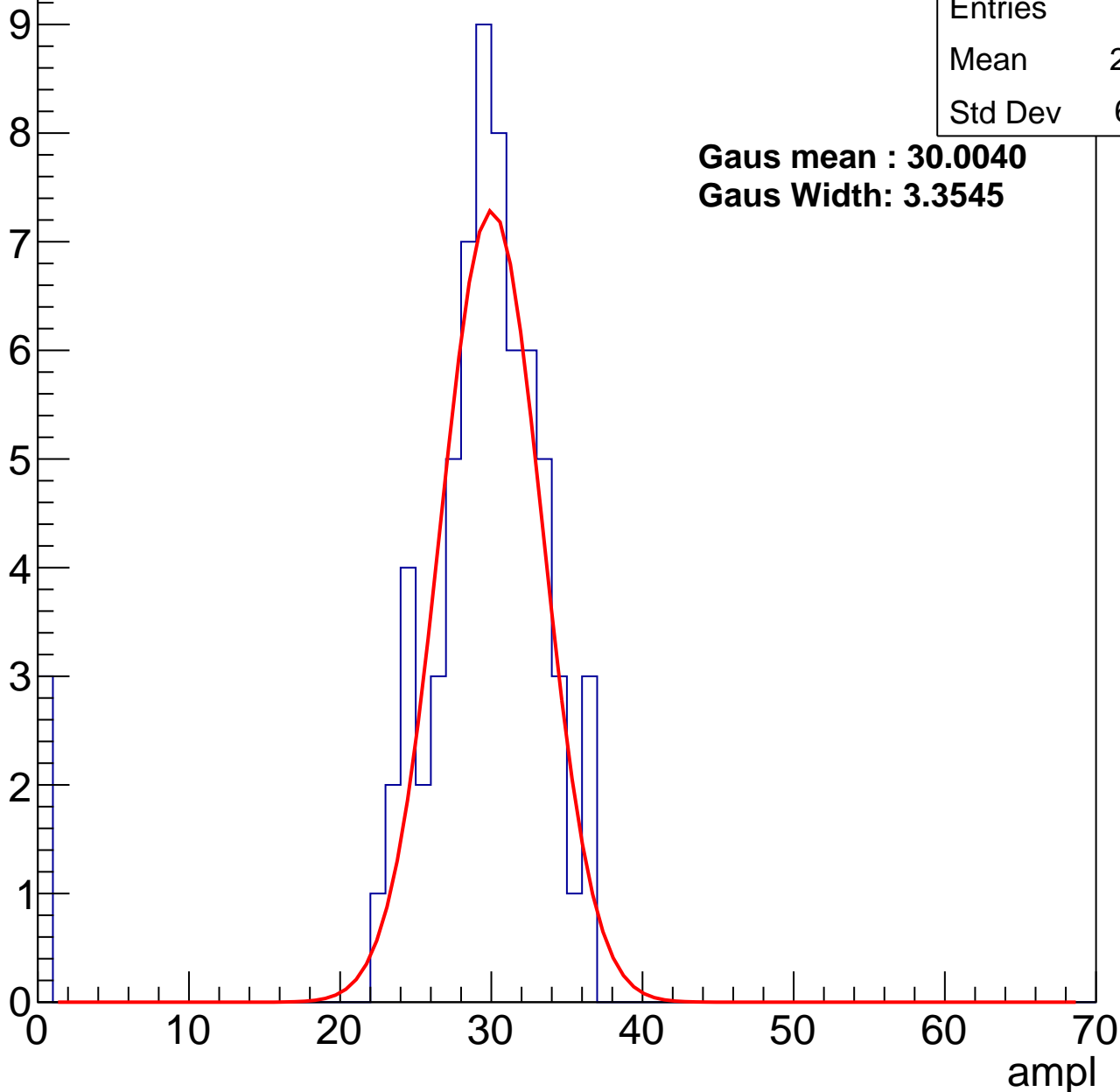
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.12
Std Dev	6.861

Gaus mean : 30.0040

Gaus Width: 3.3545



B1L003S, U16-ch25, adc1

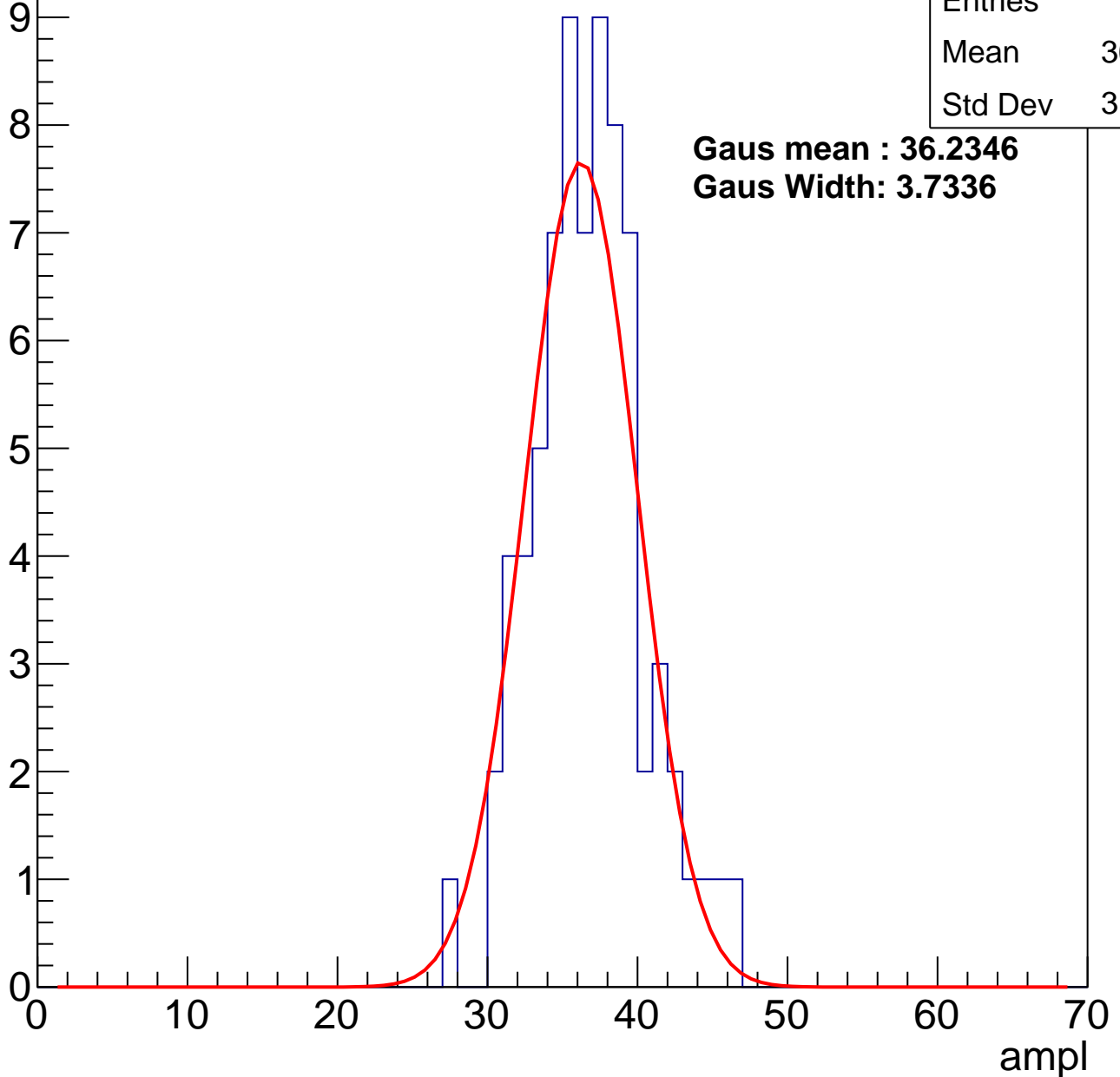
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	36.27
Std Dev	3.622

Gaus mean : 36.2346

Gaus Width: 3.7336



B1L003S, U16-ch25, adc2

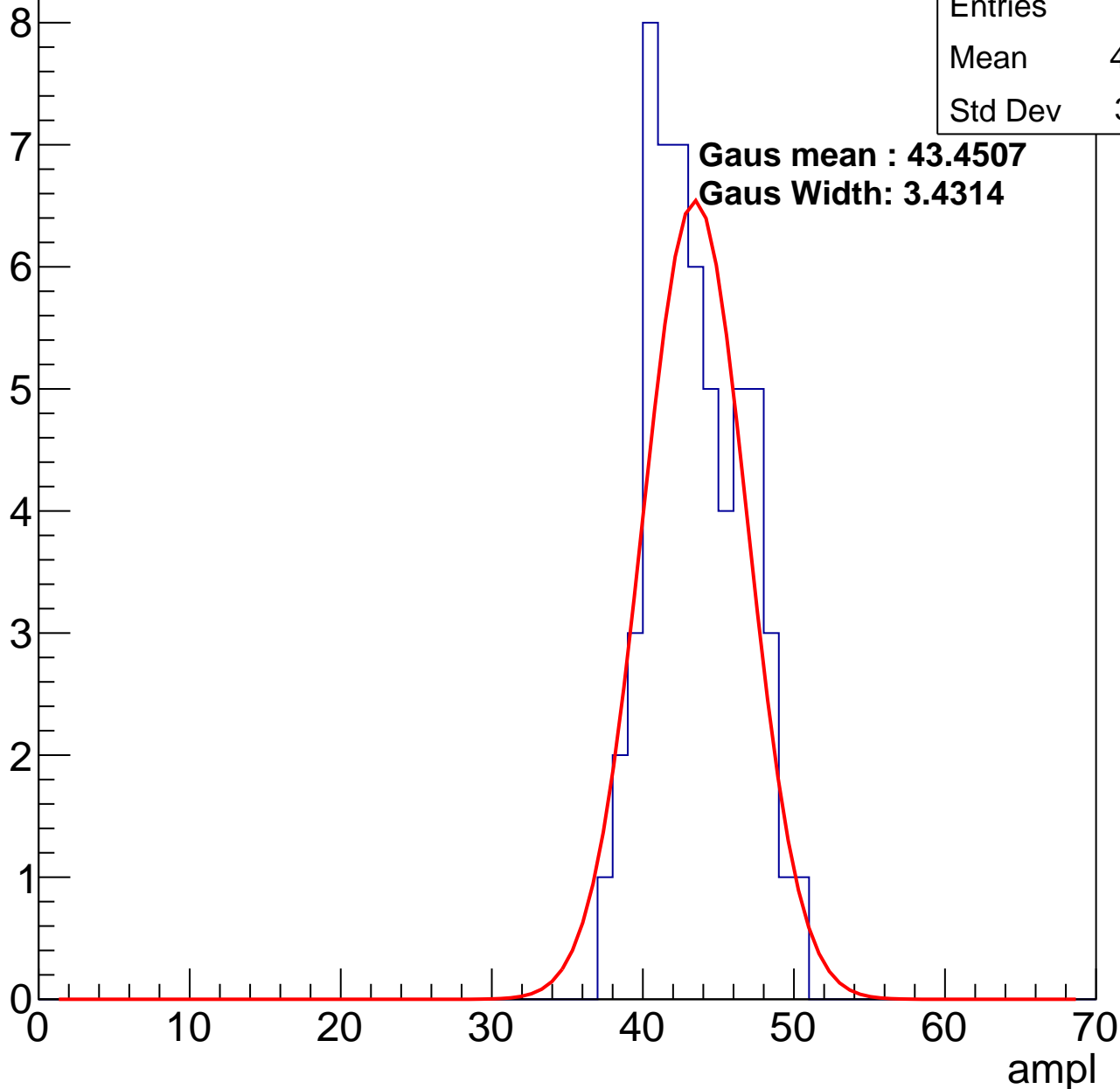
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.05
Std Dev	3.071

Gaus mean : 43.4507

Gaus Width: 3.4314

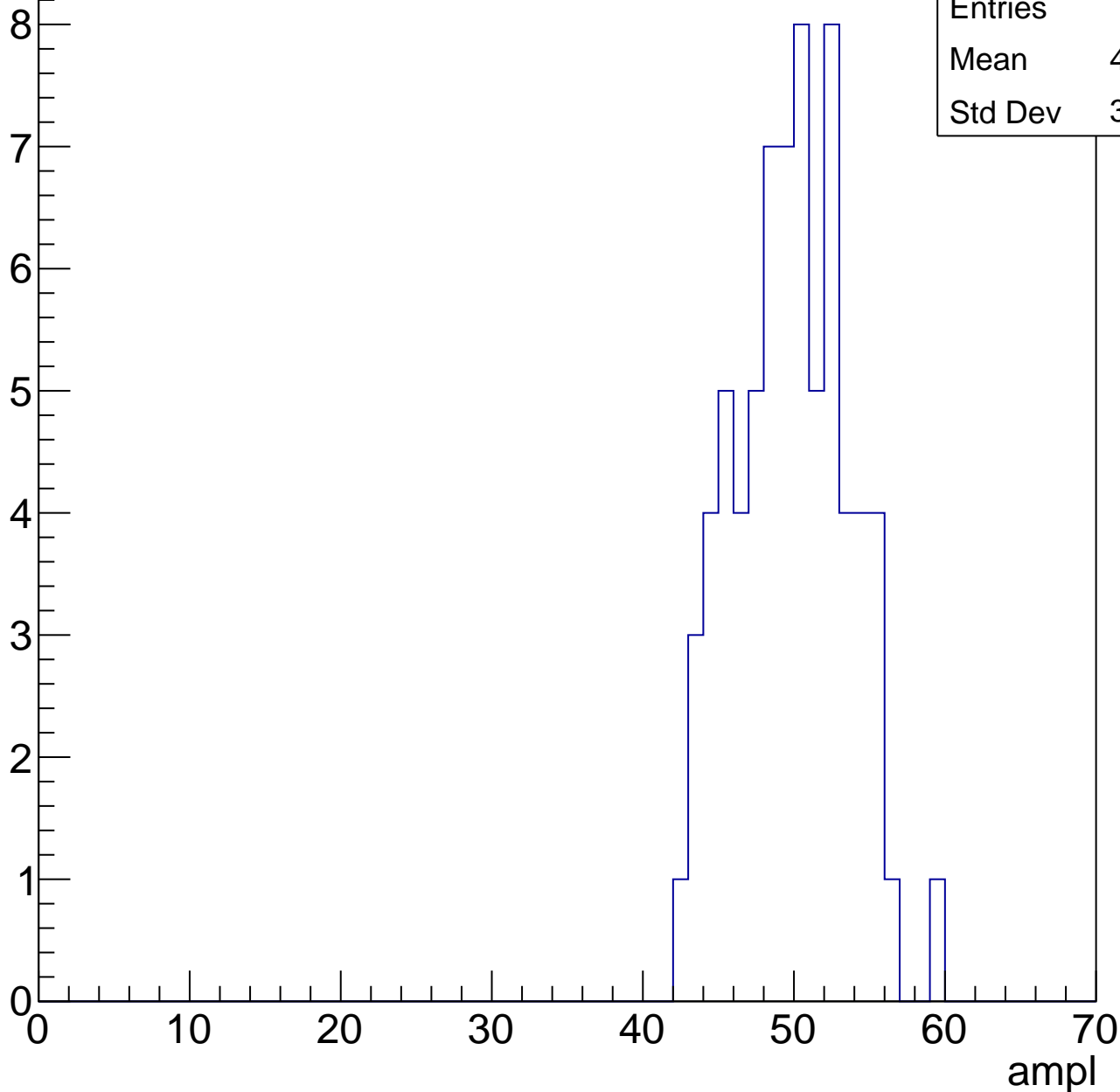


B1L003S, U16-ch25, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

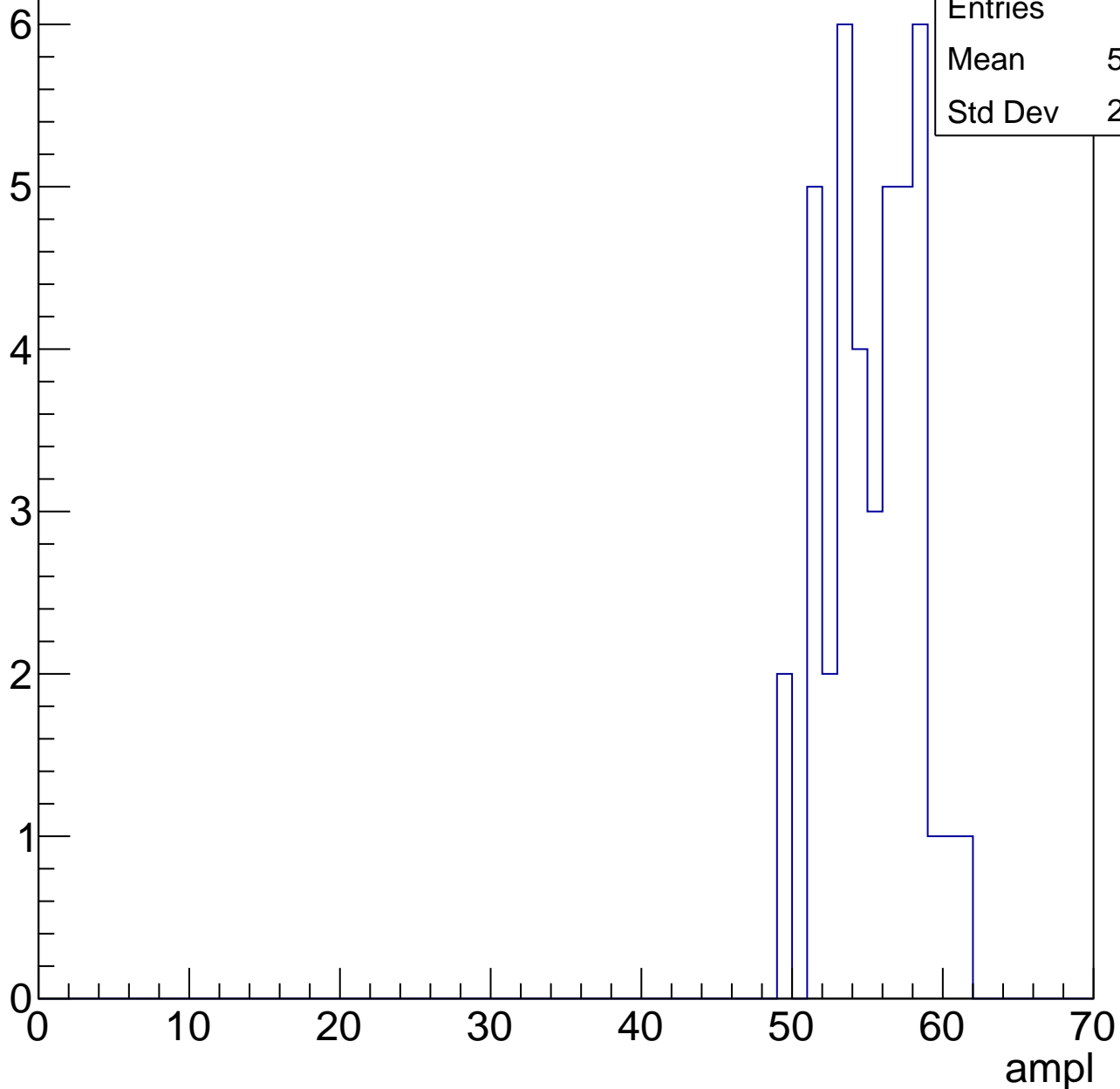
Entries	71
Mean	49.35
Std Dev	3.654



B1L003S, U16-ch25, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



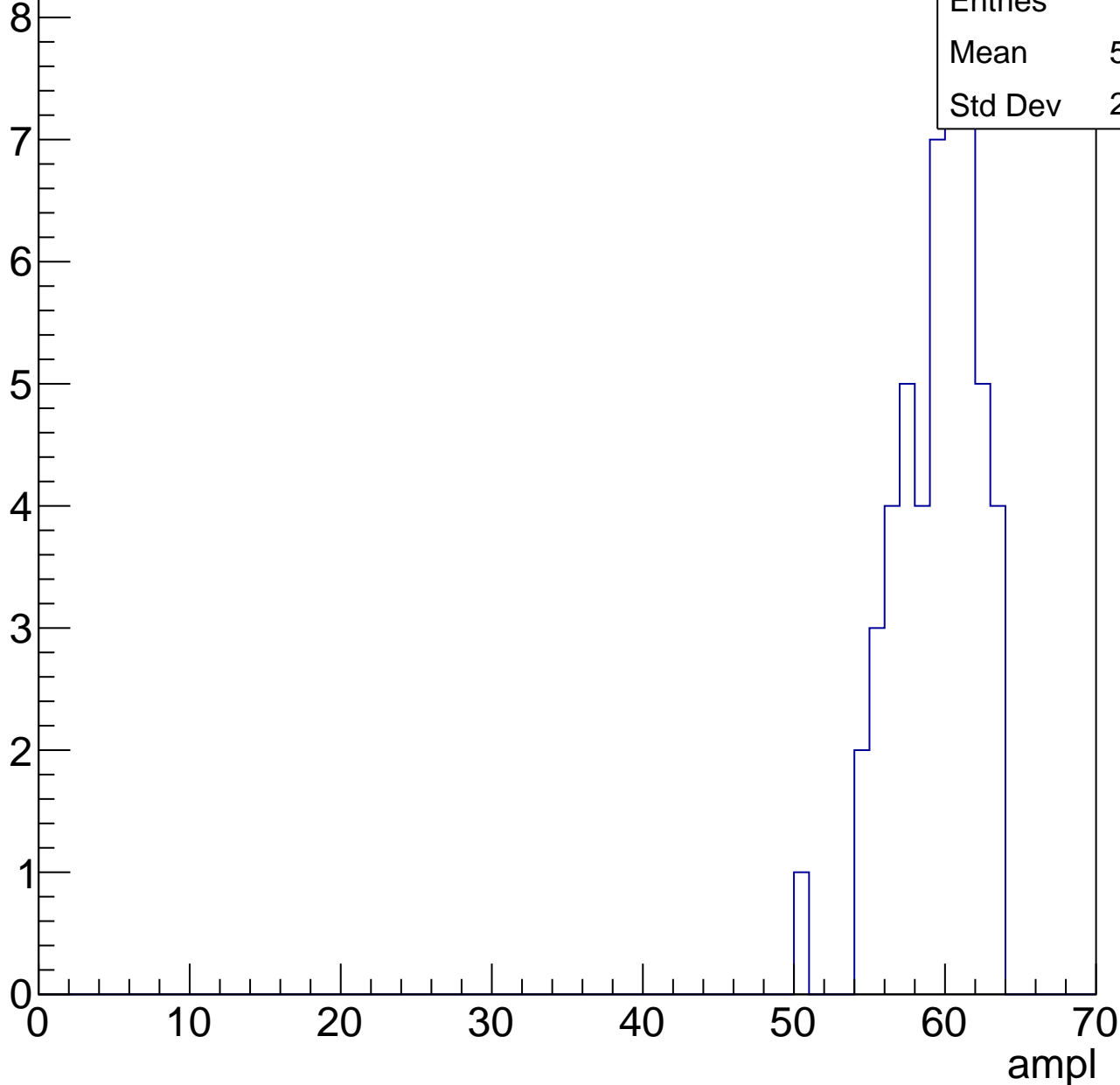
Entries	41
Mean	54.85
Std Dev	2.935

B1L003S, U16-ch25, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.96
Std Dev	2.758

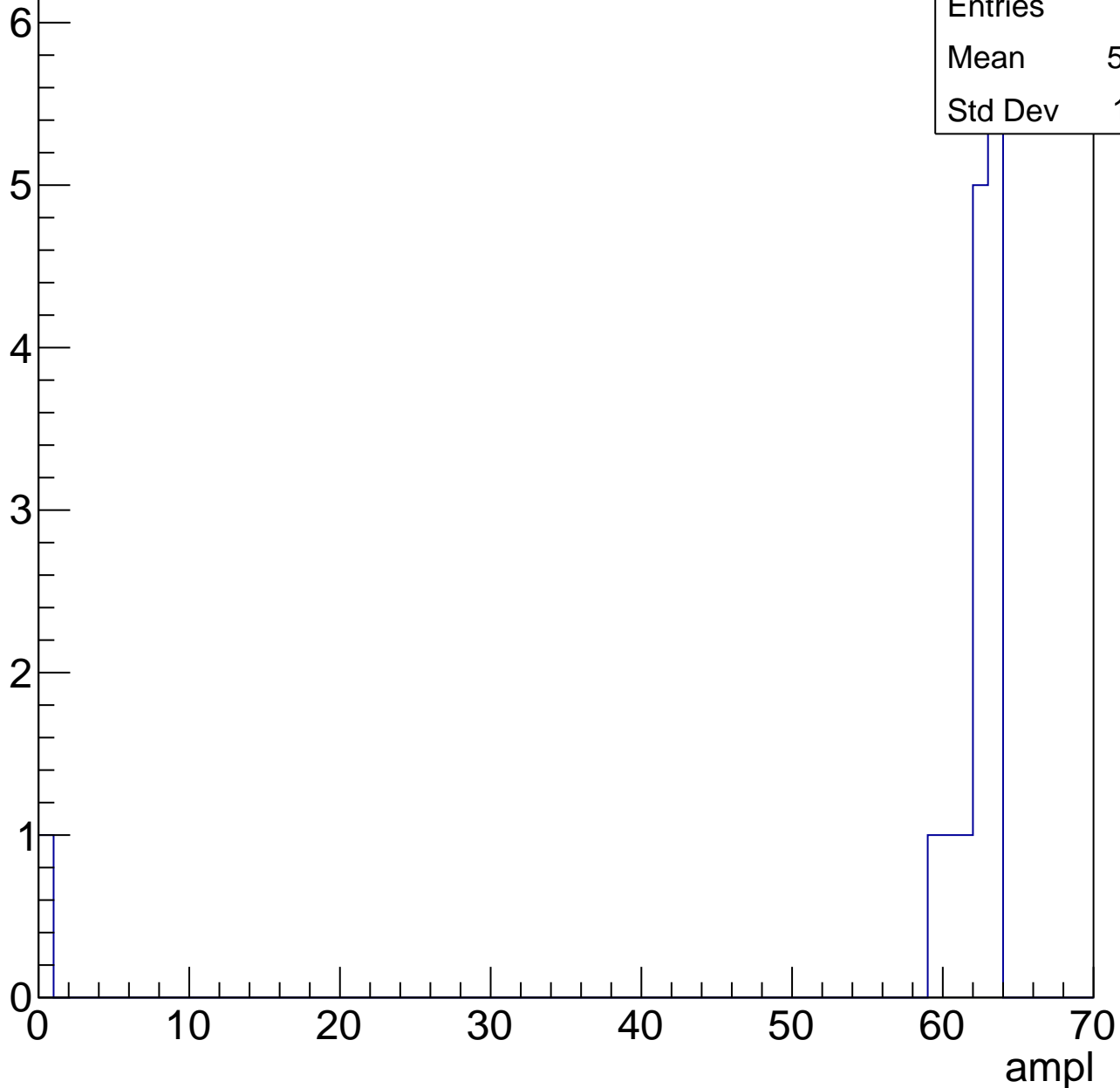


B1L003S, U16-ch25, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	57.87
Std Dev	15.51



B1L003S, U16-ch25, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch26, adc0

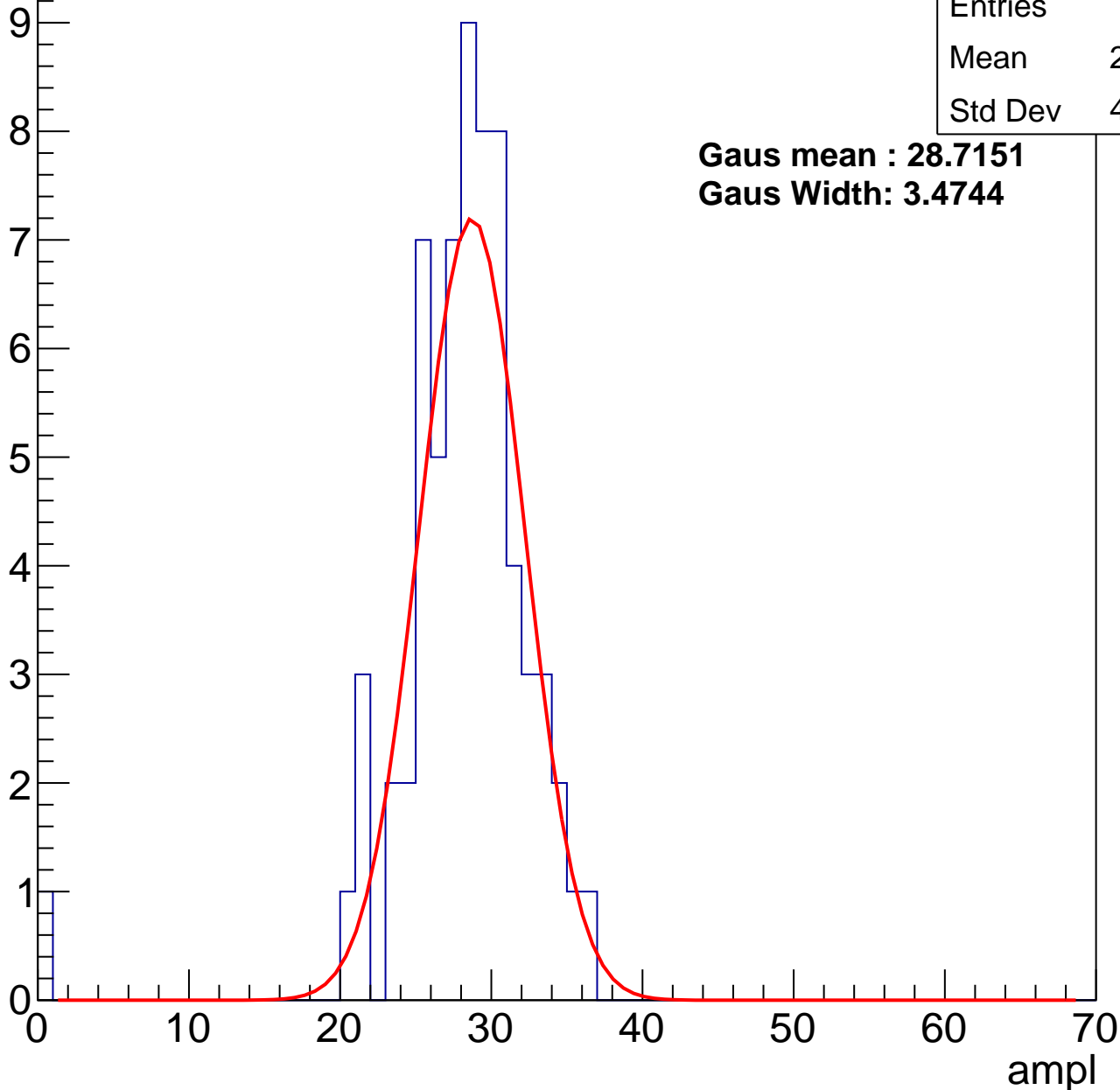
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	27.66
Std Dev	4.799

Gaus mean : 28.7151

Gaus Width: 3.4744



B1L003S, U16-ch26, adc1

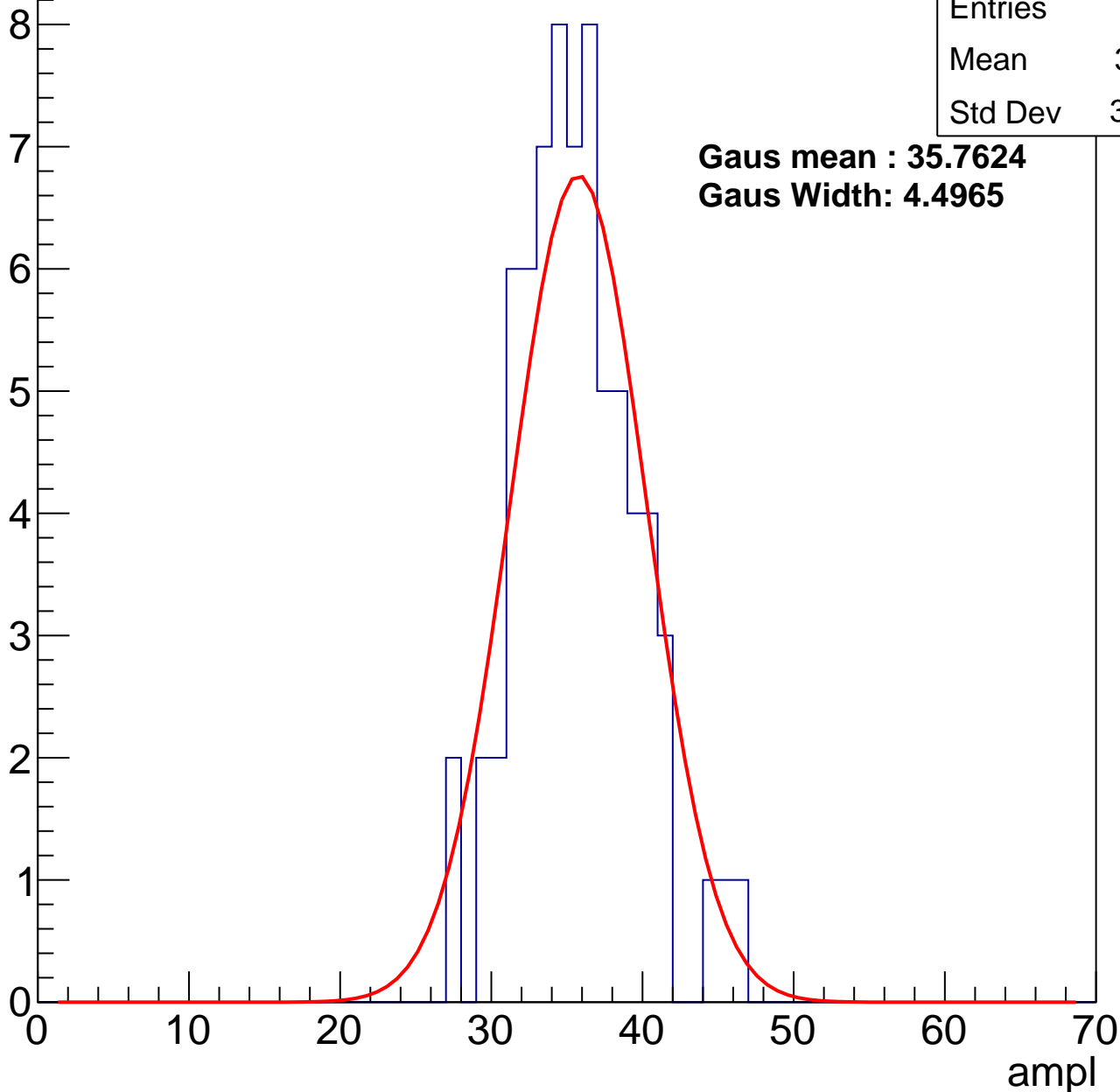
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.21
Std Dev	3.873

Gaus mean : 35.7624

Gaus Width: 4.4965



B1L003S, U16-ch26, adc2

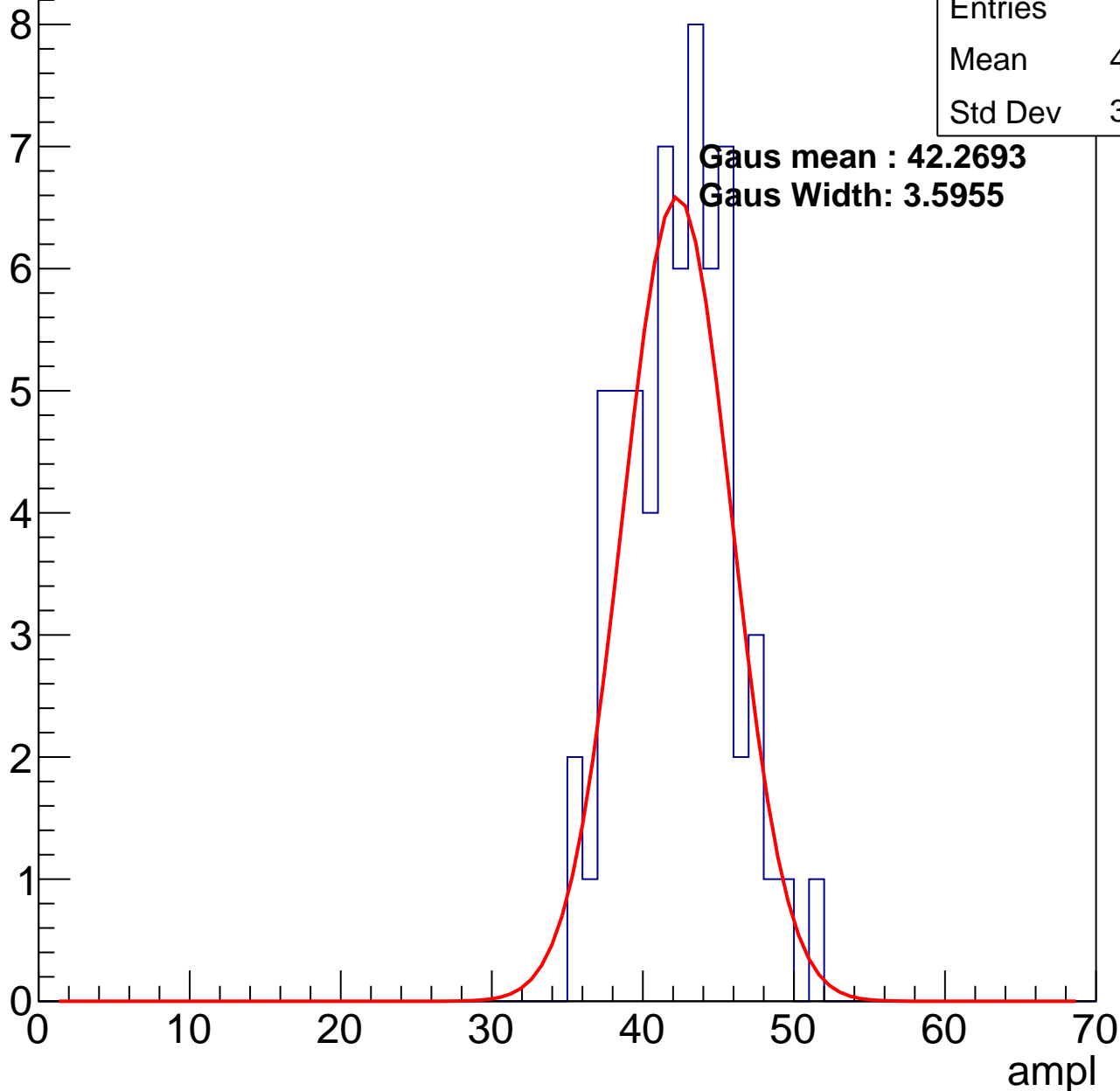
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.86
Std Dev	3.477

Gaus mean : 42.2693

Gaus Width: 3.5955

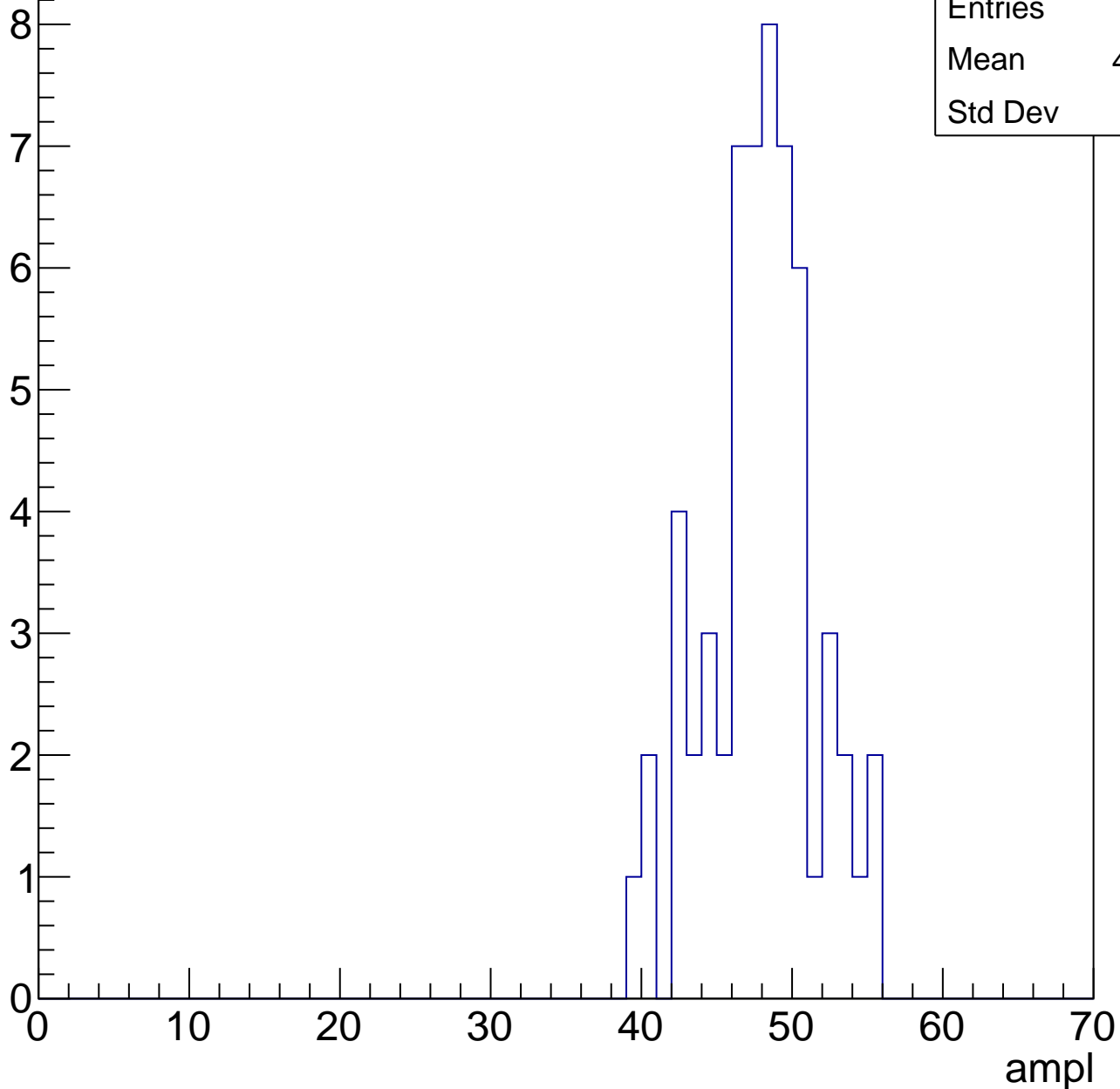


B1L003S, U16-ch26, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	47.41
Std Dev	3.61

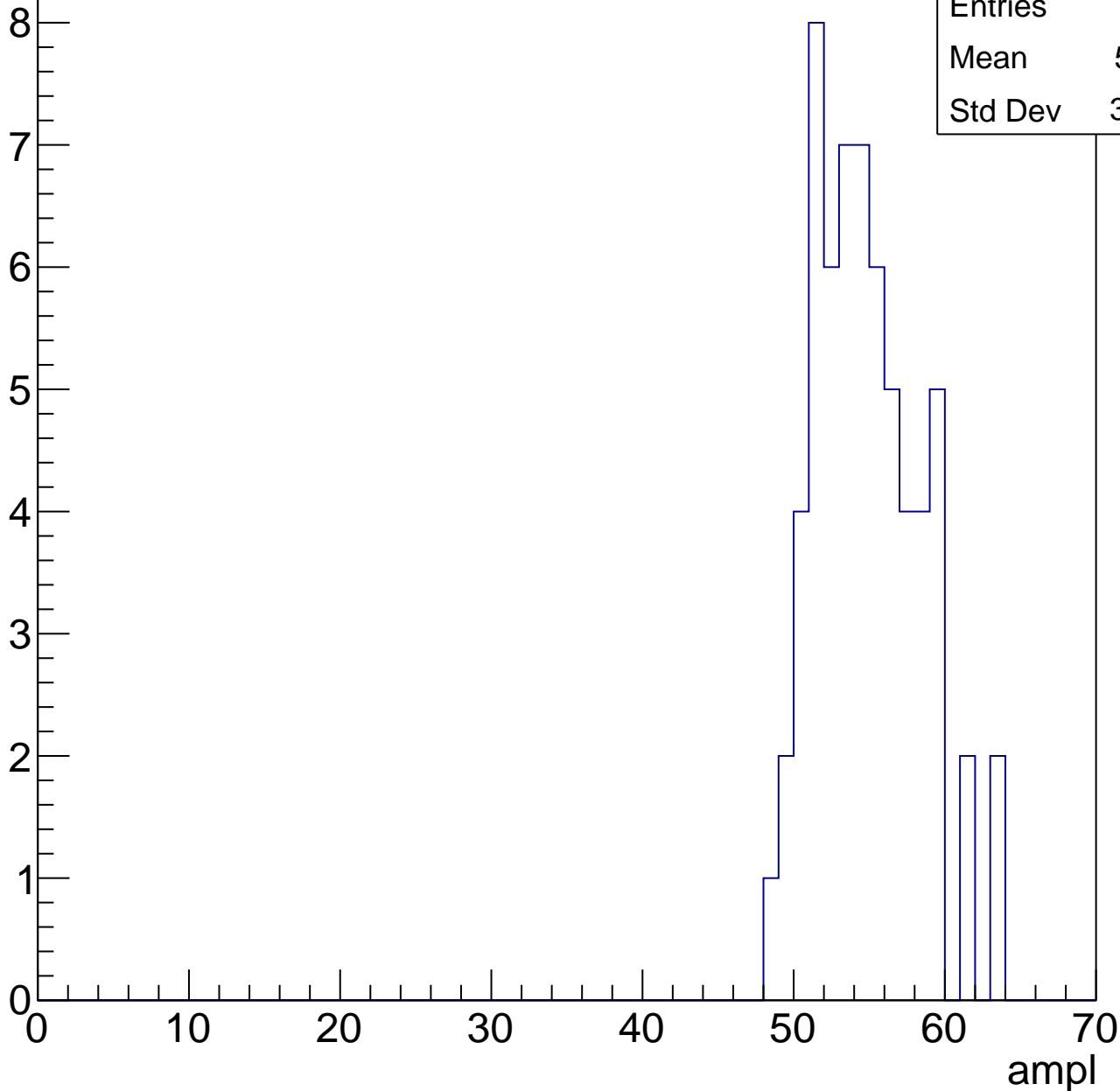


B1L003S, U16-ch26, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	54.41
Std Dev	3.458



B1L003S, U16-ch26, adc5

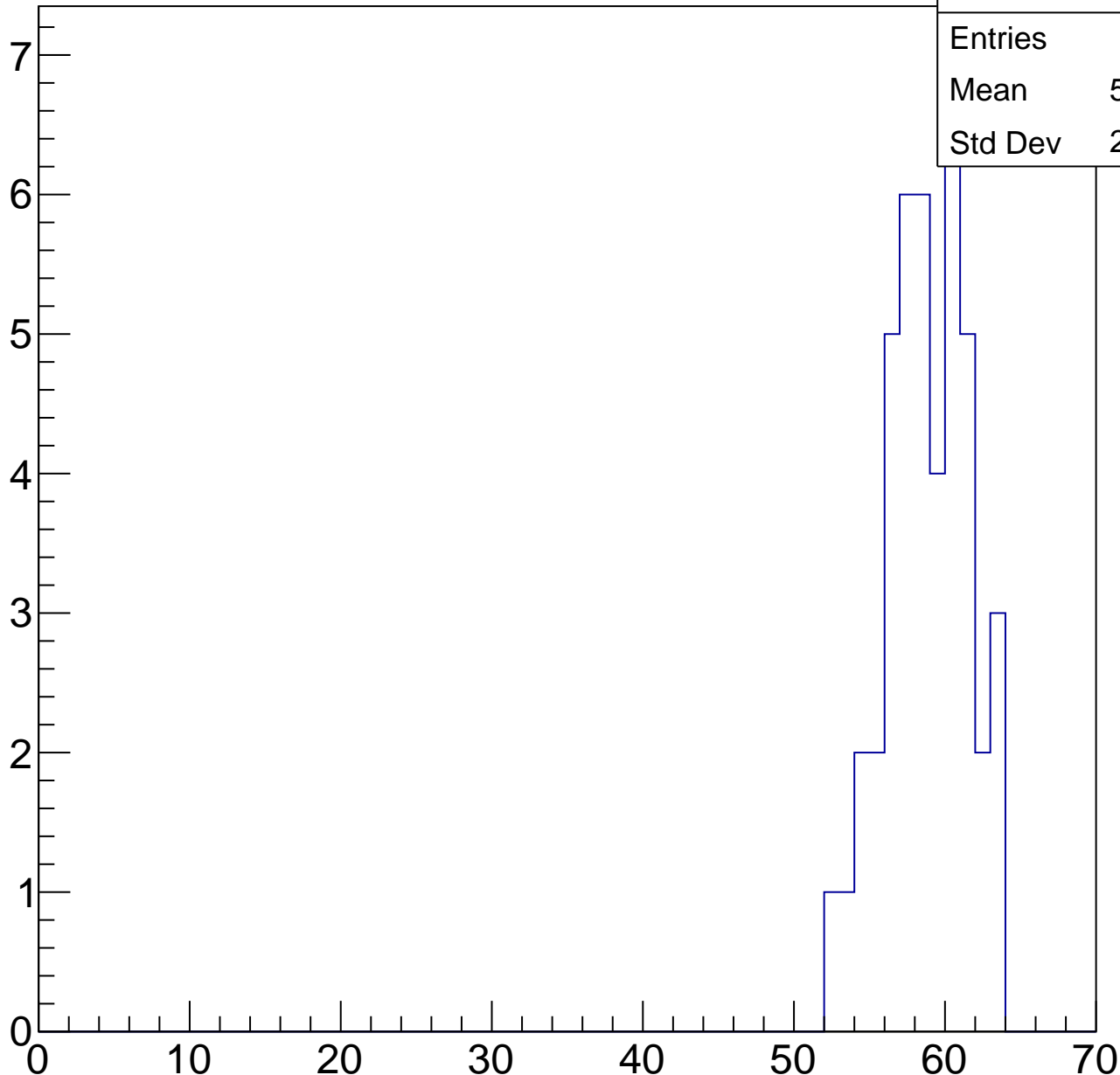
calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	58.34
Std Dev	2.679

ampl

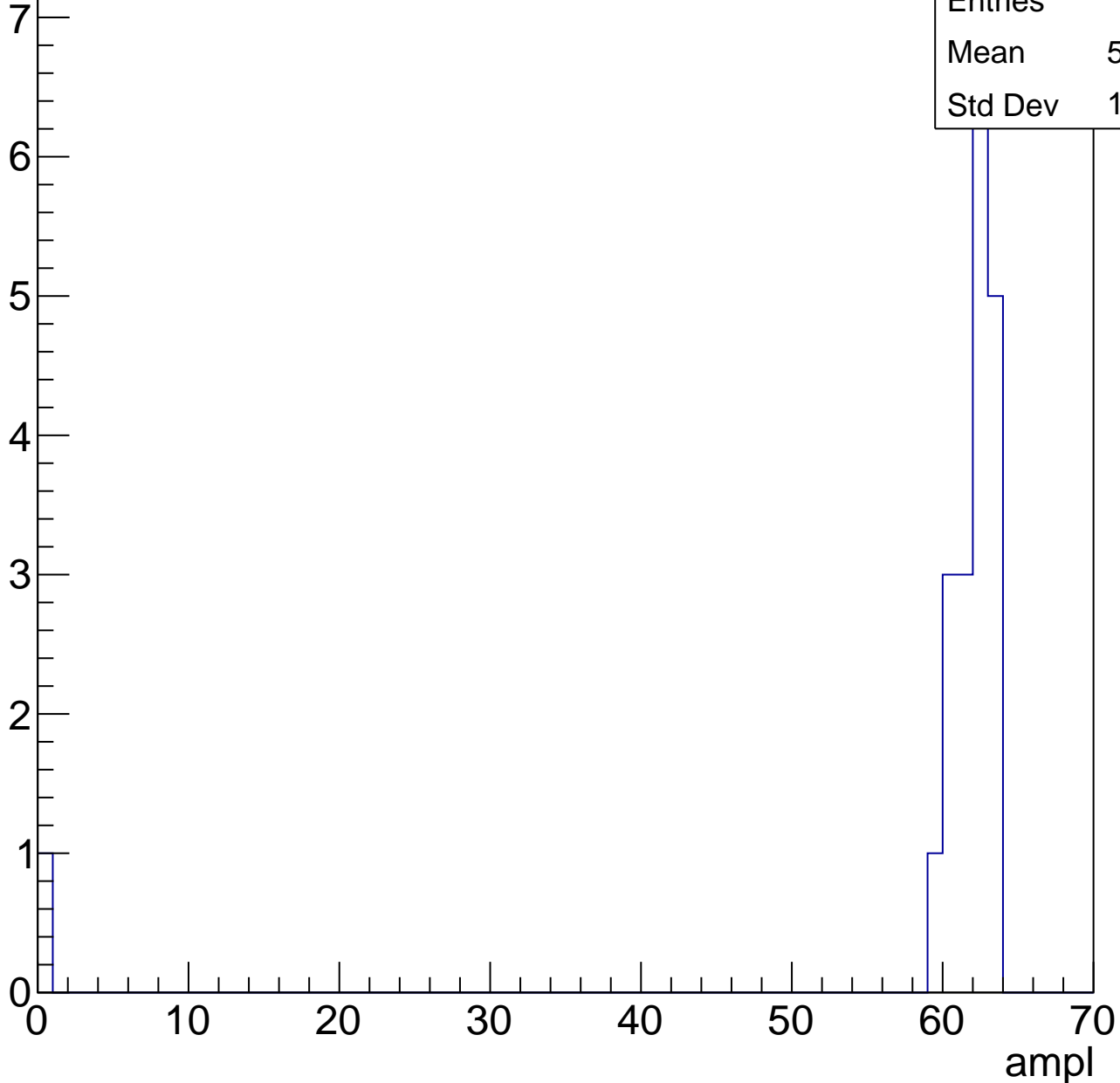


B1L003S, U16-ch26, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58.55
Std Dev	13.48



B1L003S, U16-ch26, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch27, adc0

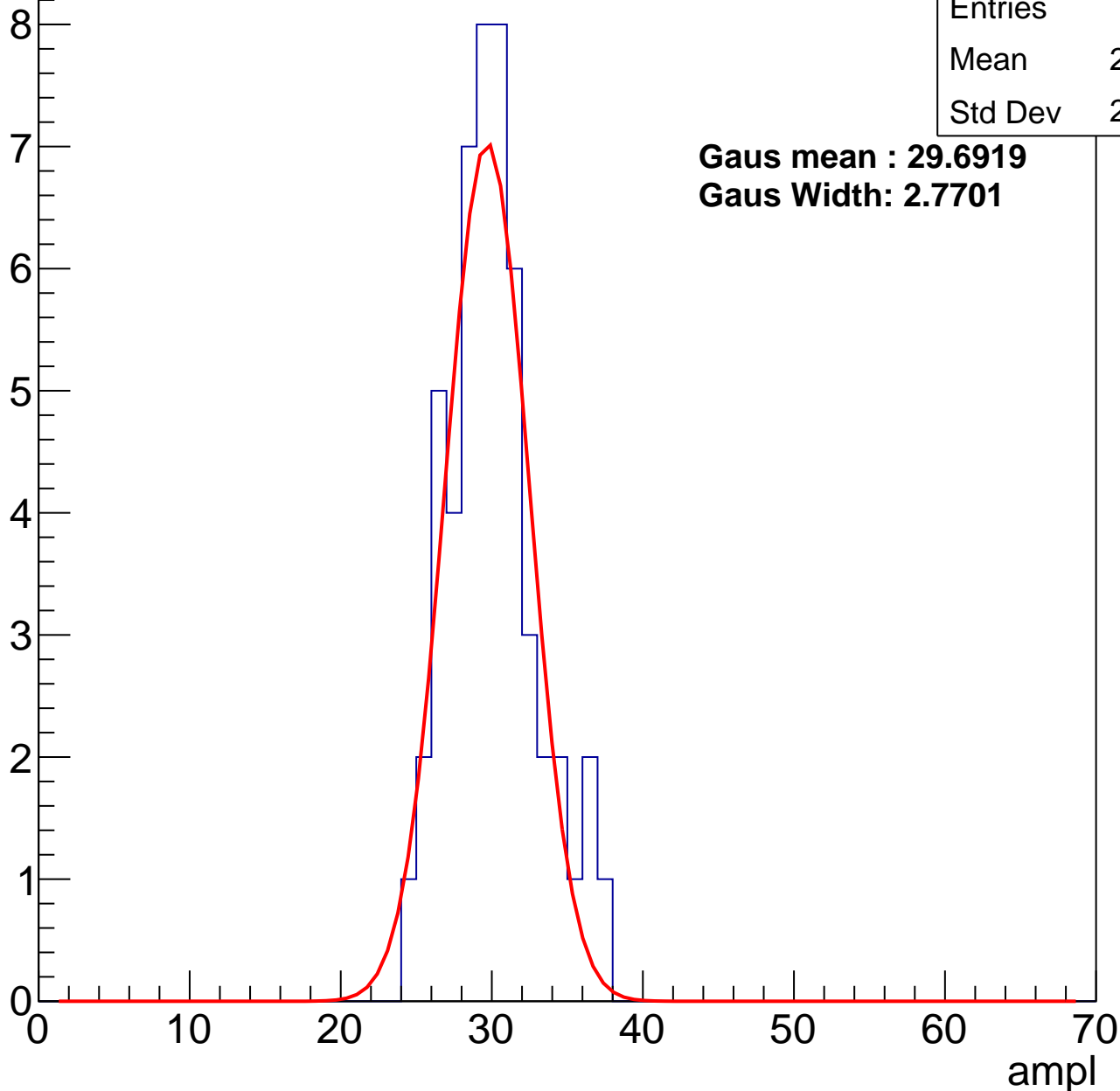
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	29.62
Std Dev	2.923

Gaus mean : 29.6919

Gaus Width: 2.7701



B1L003S, U16-ch27, adc1

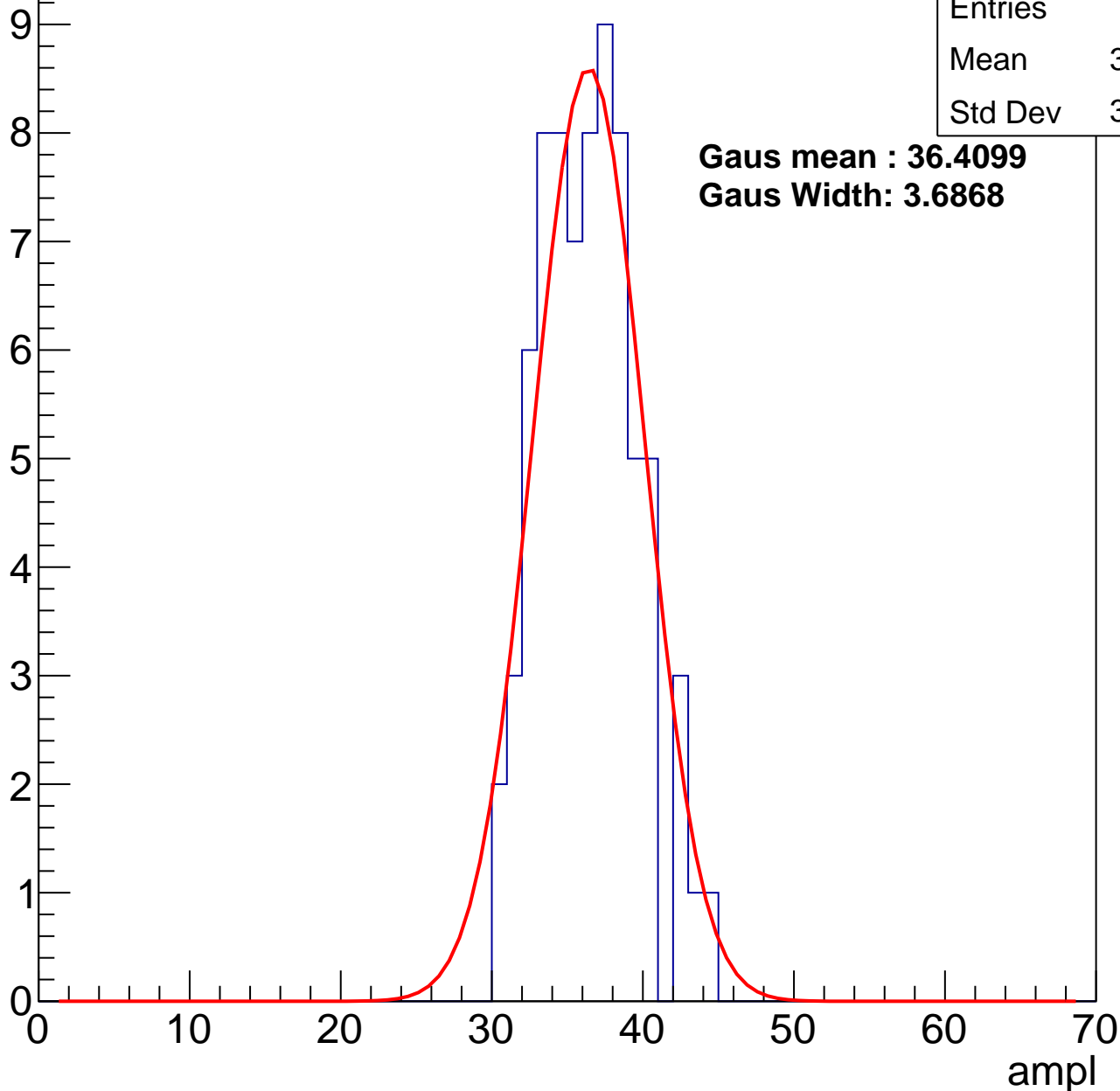
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.93
Std Dev	3.164

Gaus mean : 36.4099

Gaus Width: 3.6868



B1L003S, U16-ch27, adc2

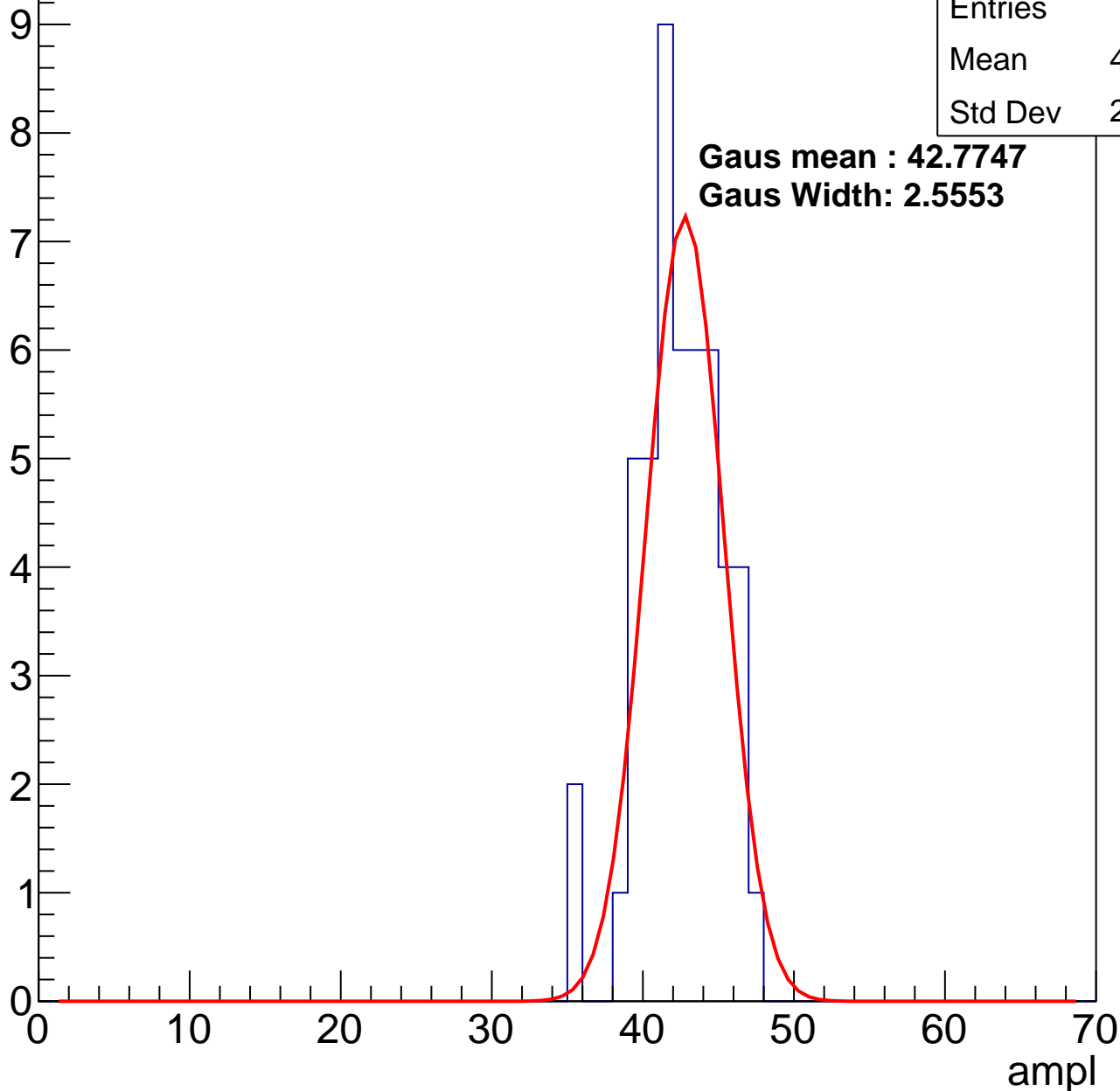
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	41.98
Std Dev	2.646

Gaus mean : 42.7747

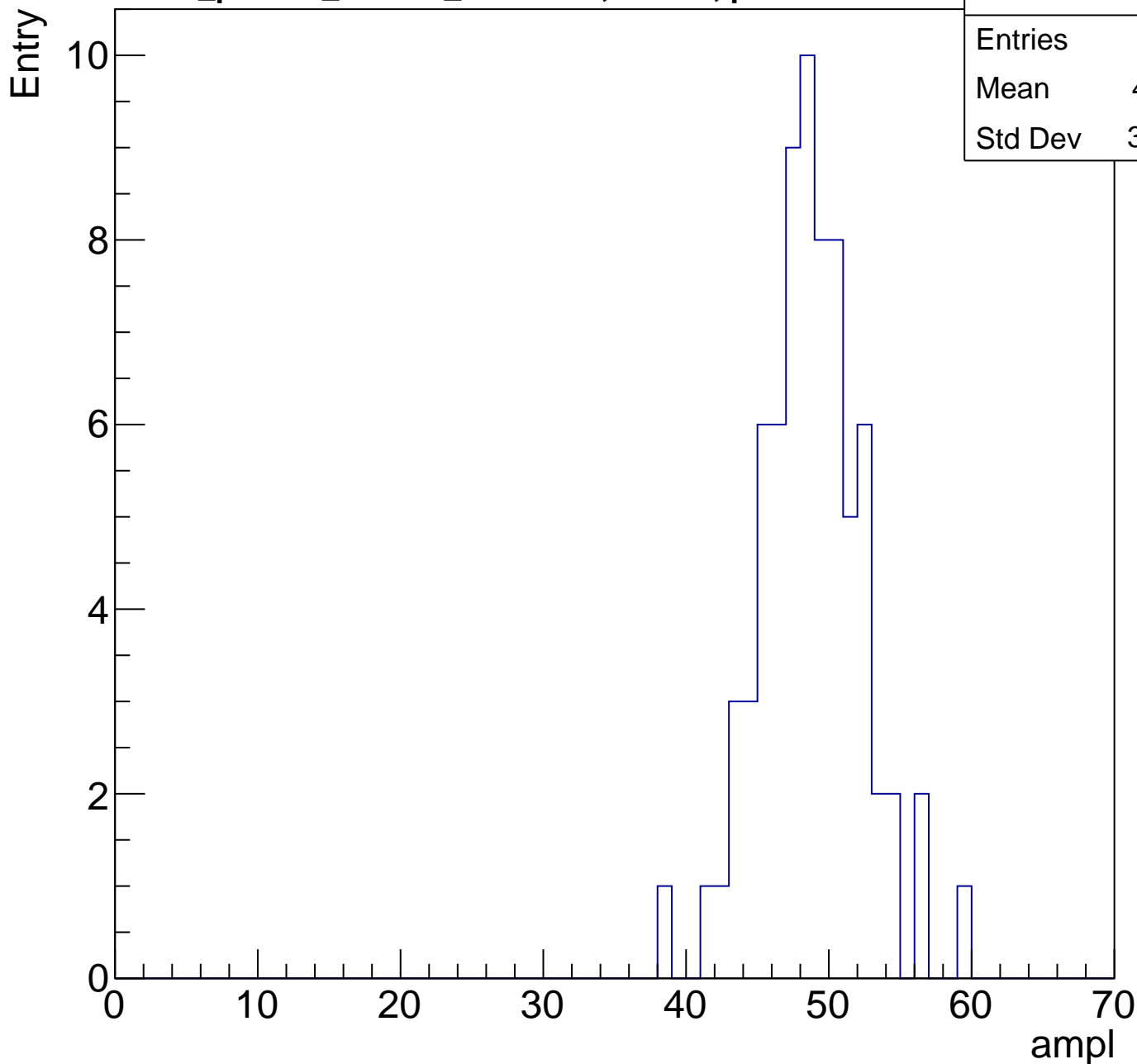
Gaus Width: 2.5553



B1L003S, U16-ch27, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	48.31
Std Dev	3.564

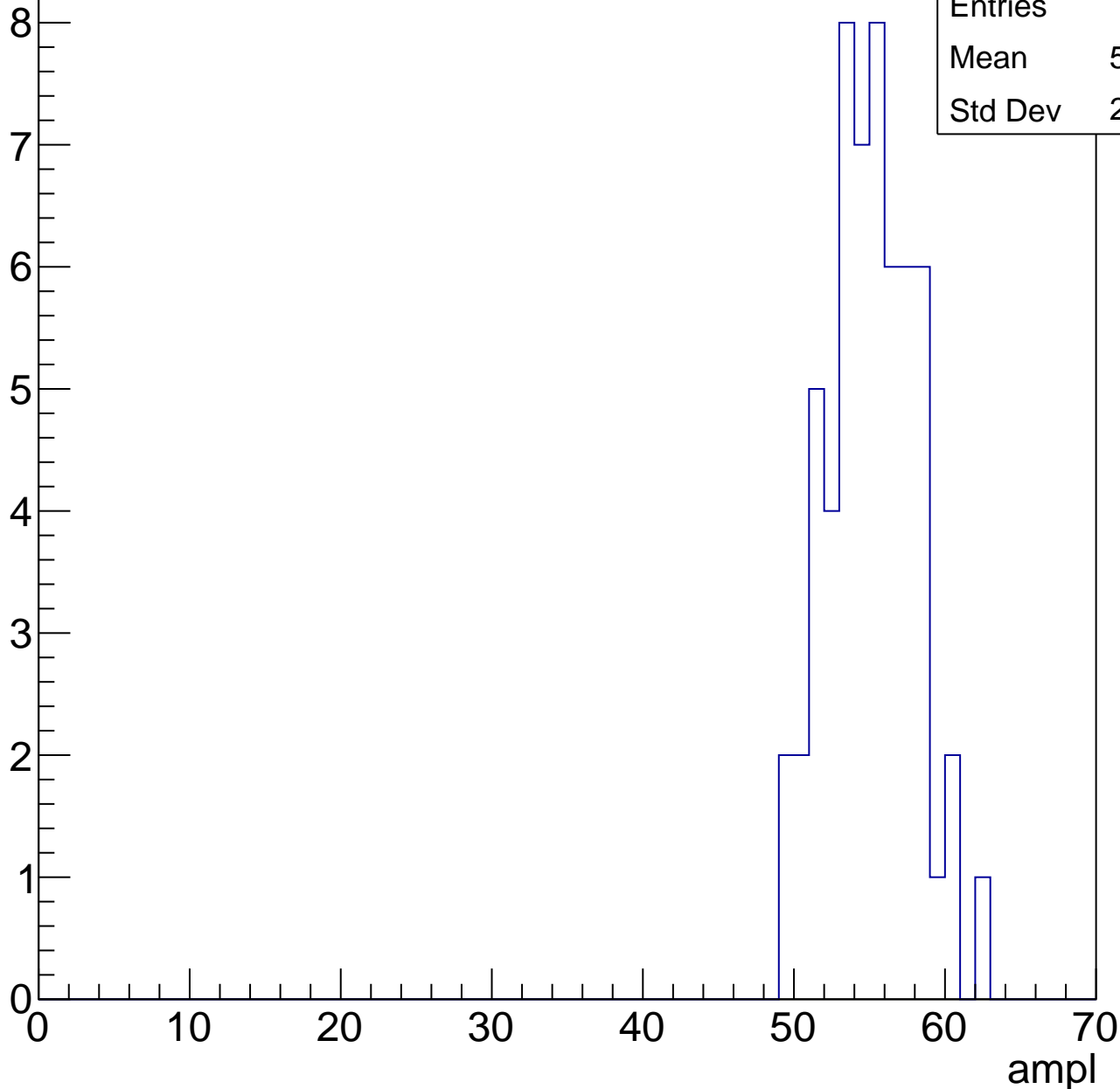


B1L003S, U16-ch27, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

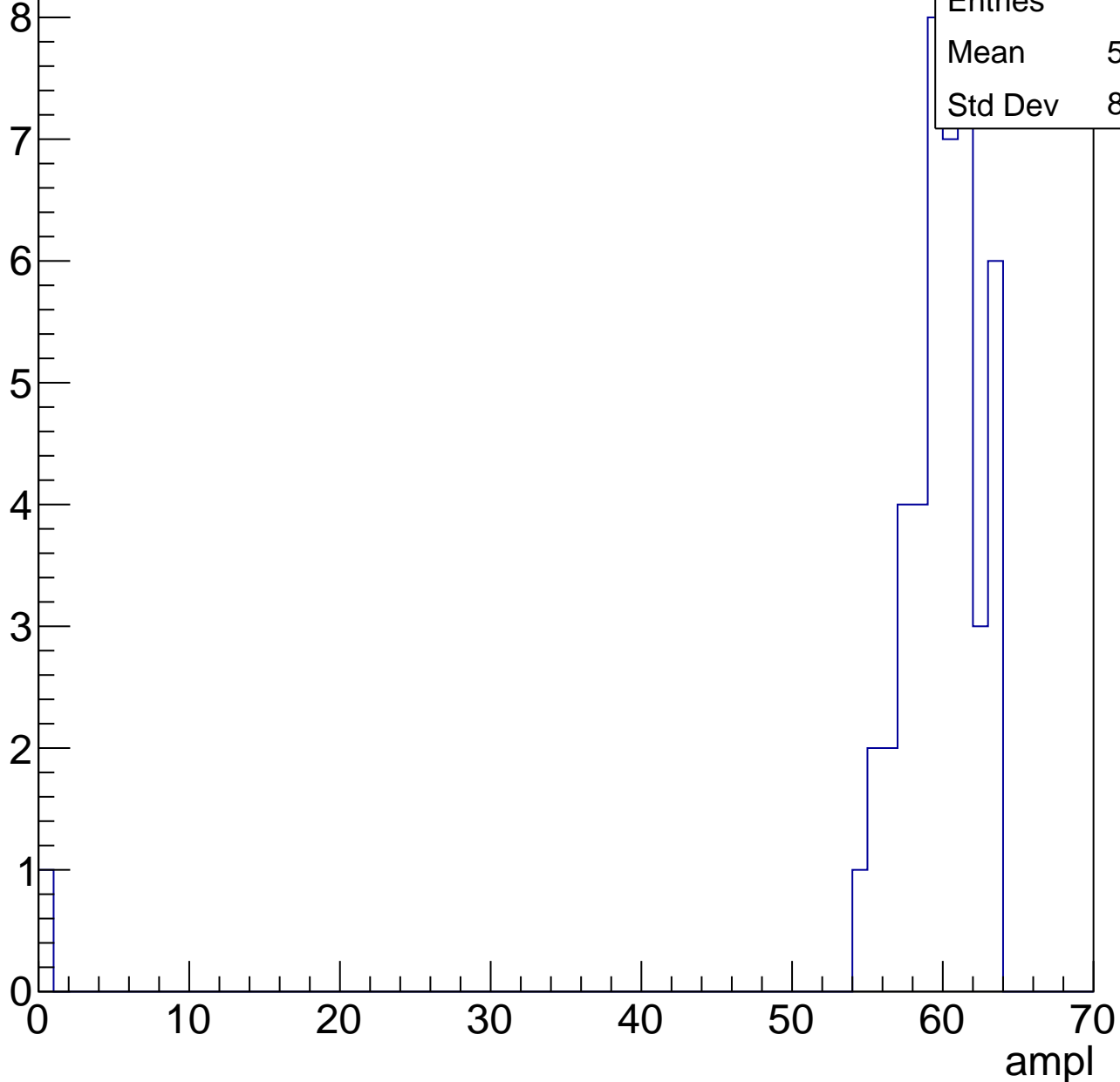
Entries	58
Mean	54.66
Std Dev	2.844



B1L003S, U16-ch27, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

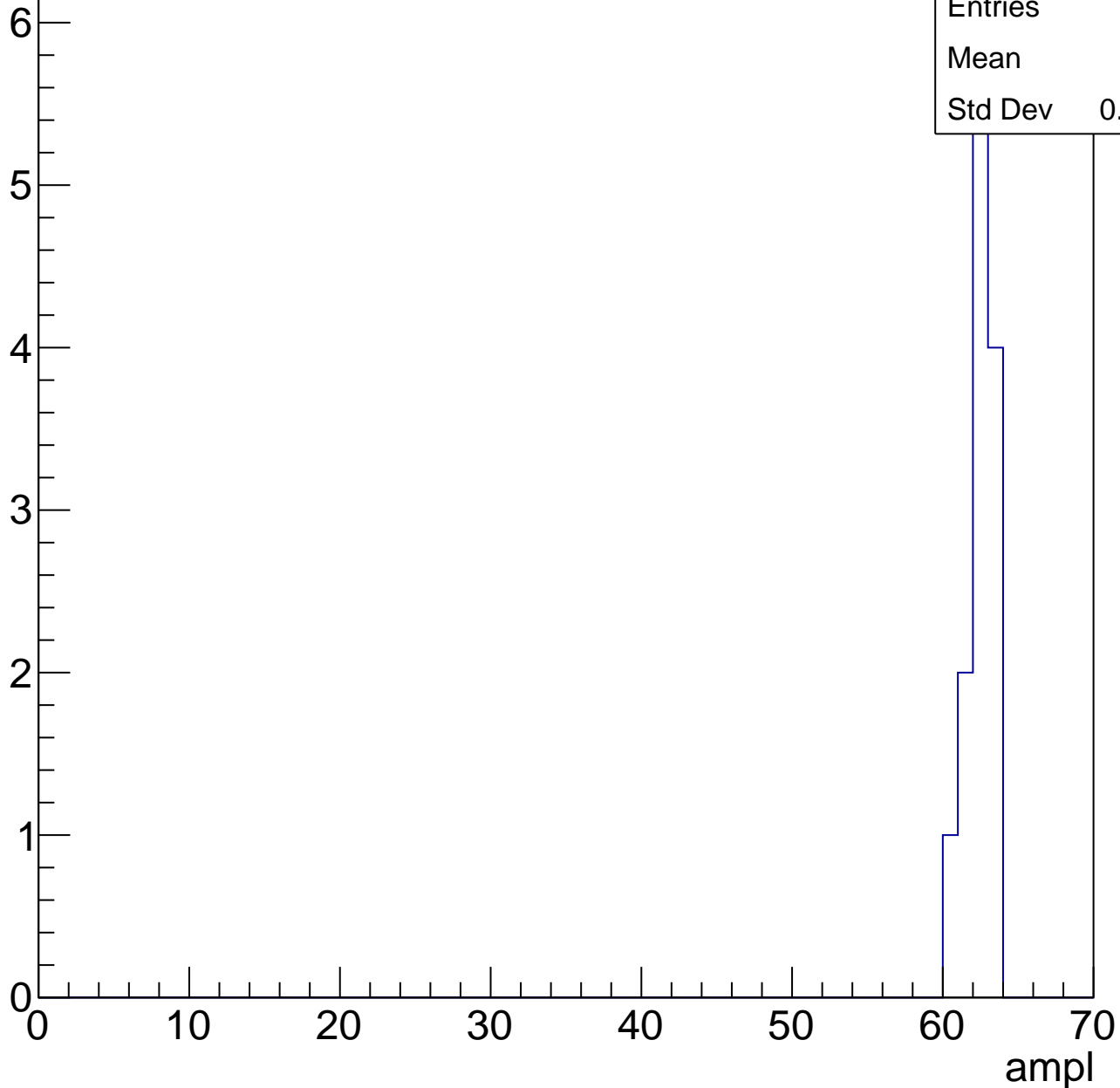


B1L003S, U16-ch27, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	62
Std Dev	0.8771



B1L003S, U16-ch27, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U16-ch28, adc0

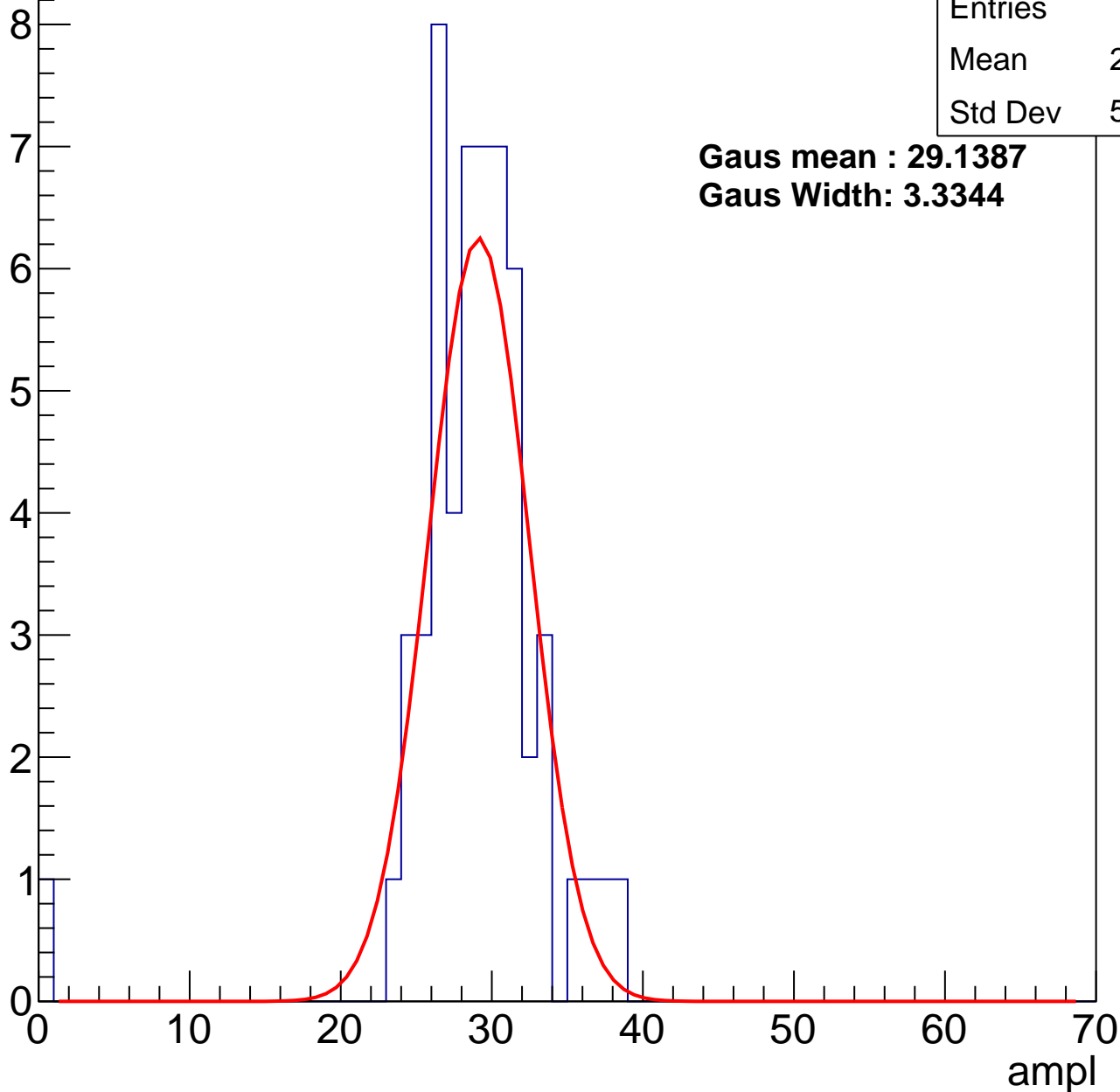
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	28.39
Std Dev	5.002

Gaus mean : 29.1387

Gaus Width: 3.3344



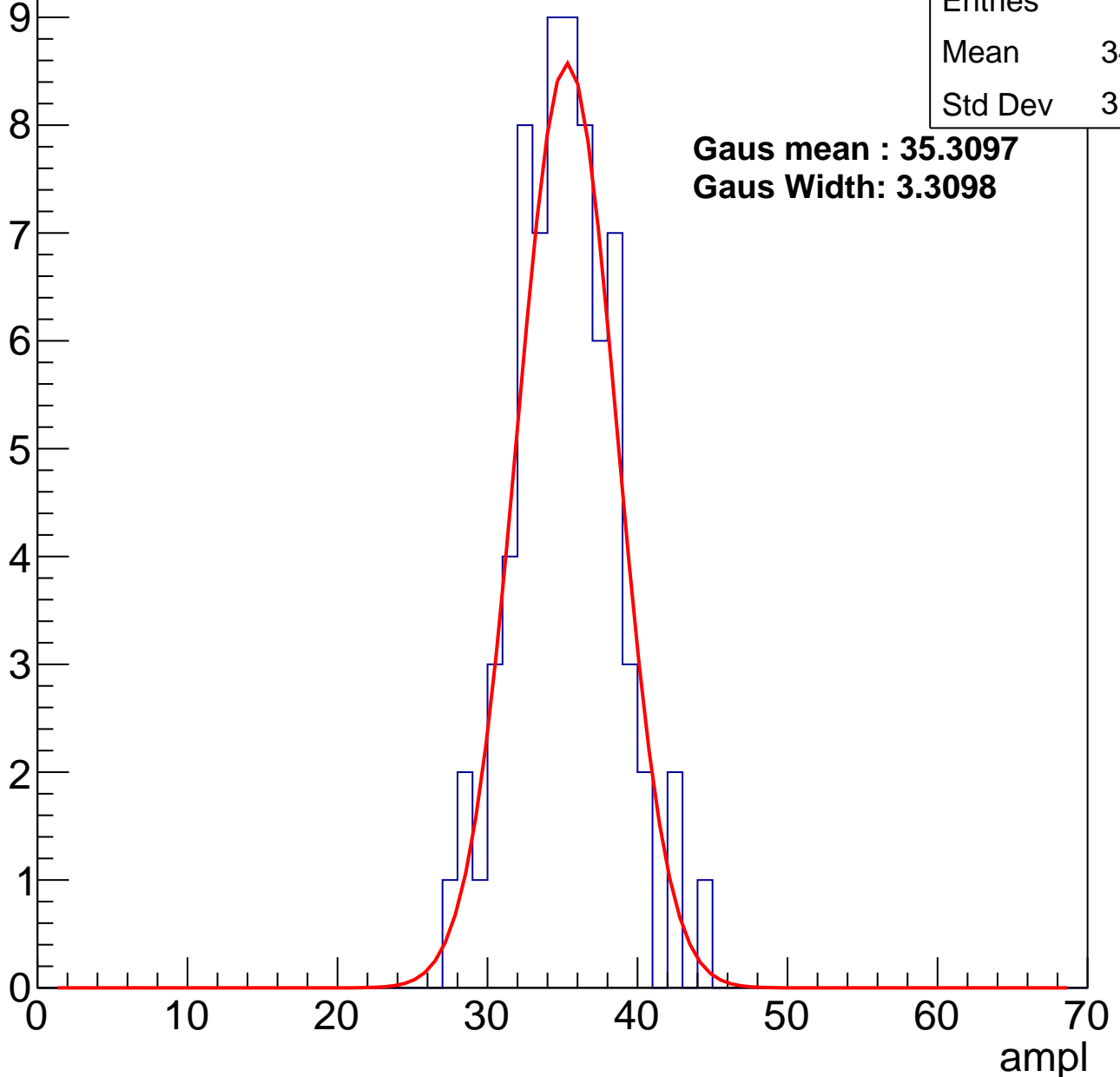
B1L003S, U16-ch28, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	34.73
Std Dev	3.336

Gaus mean : 35.3097
Gaus Width: 3.3098



B1L003S, U16-ch28, adc2

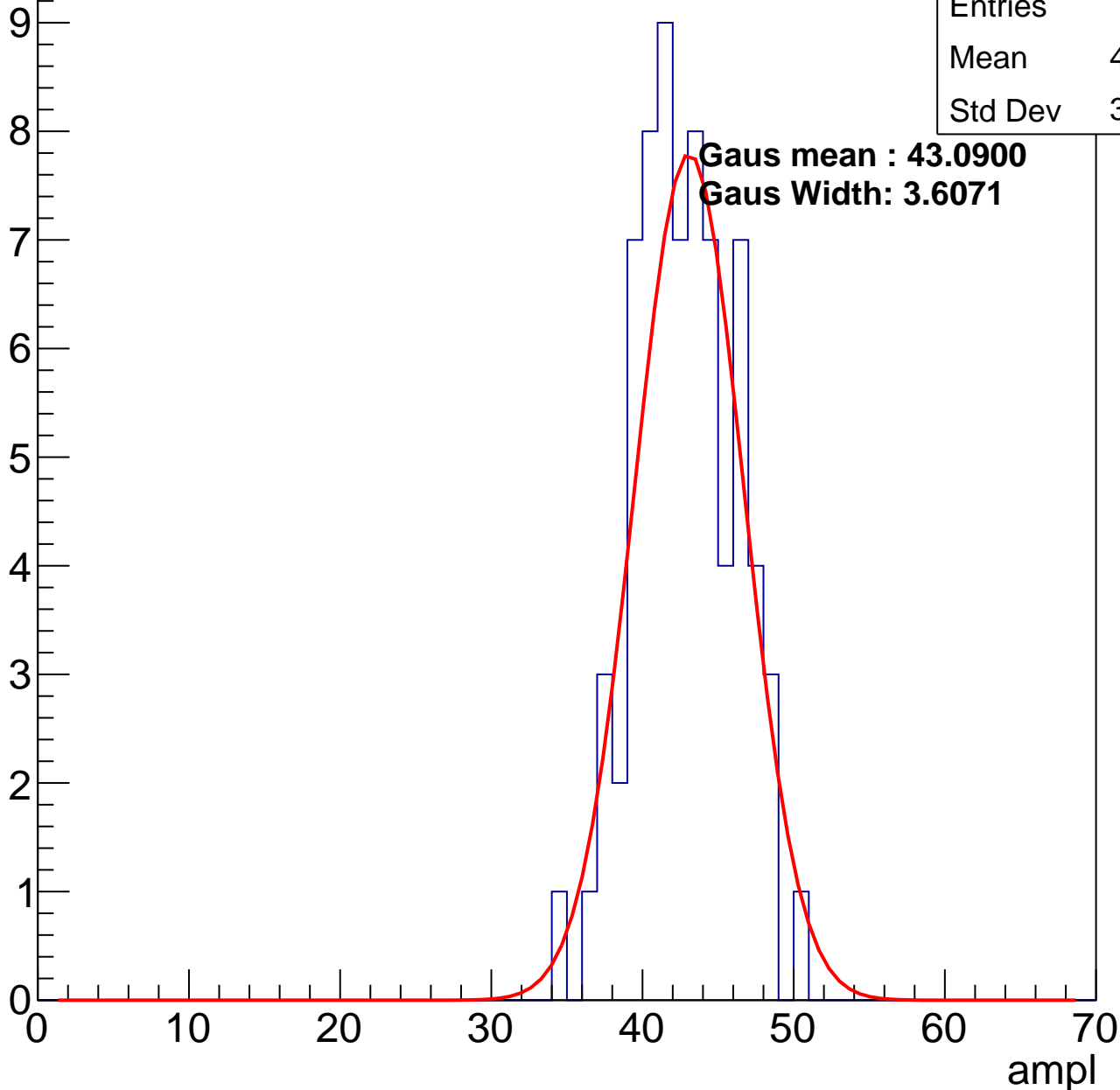
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.35
Std Dev	3.245

Gaus mean : 43.0900

Gaus Width: 3.6071

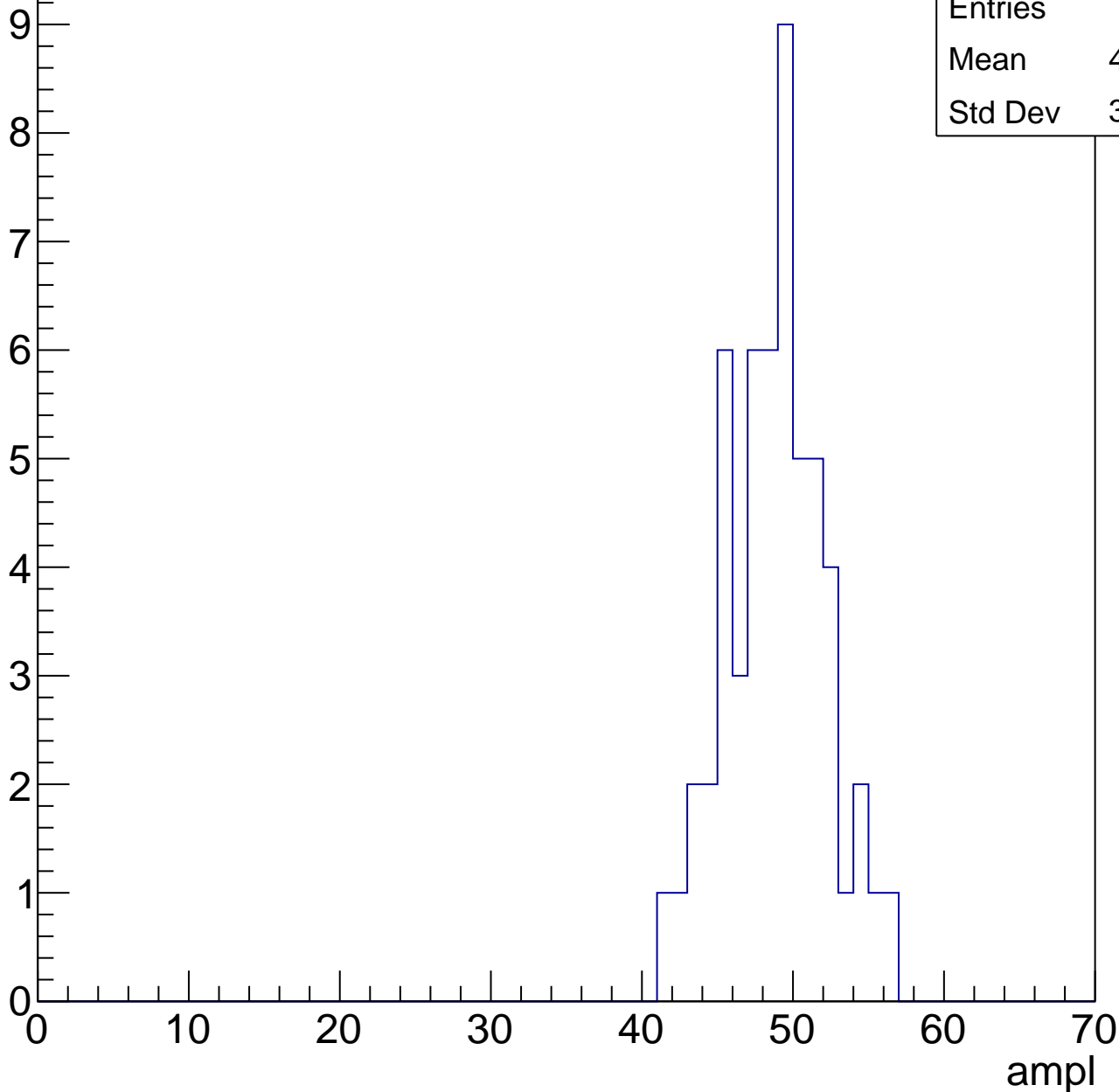


B1L003S, U16-ch28, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	48.38
Std Dev	3.245

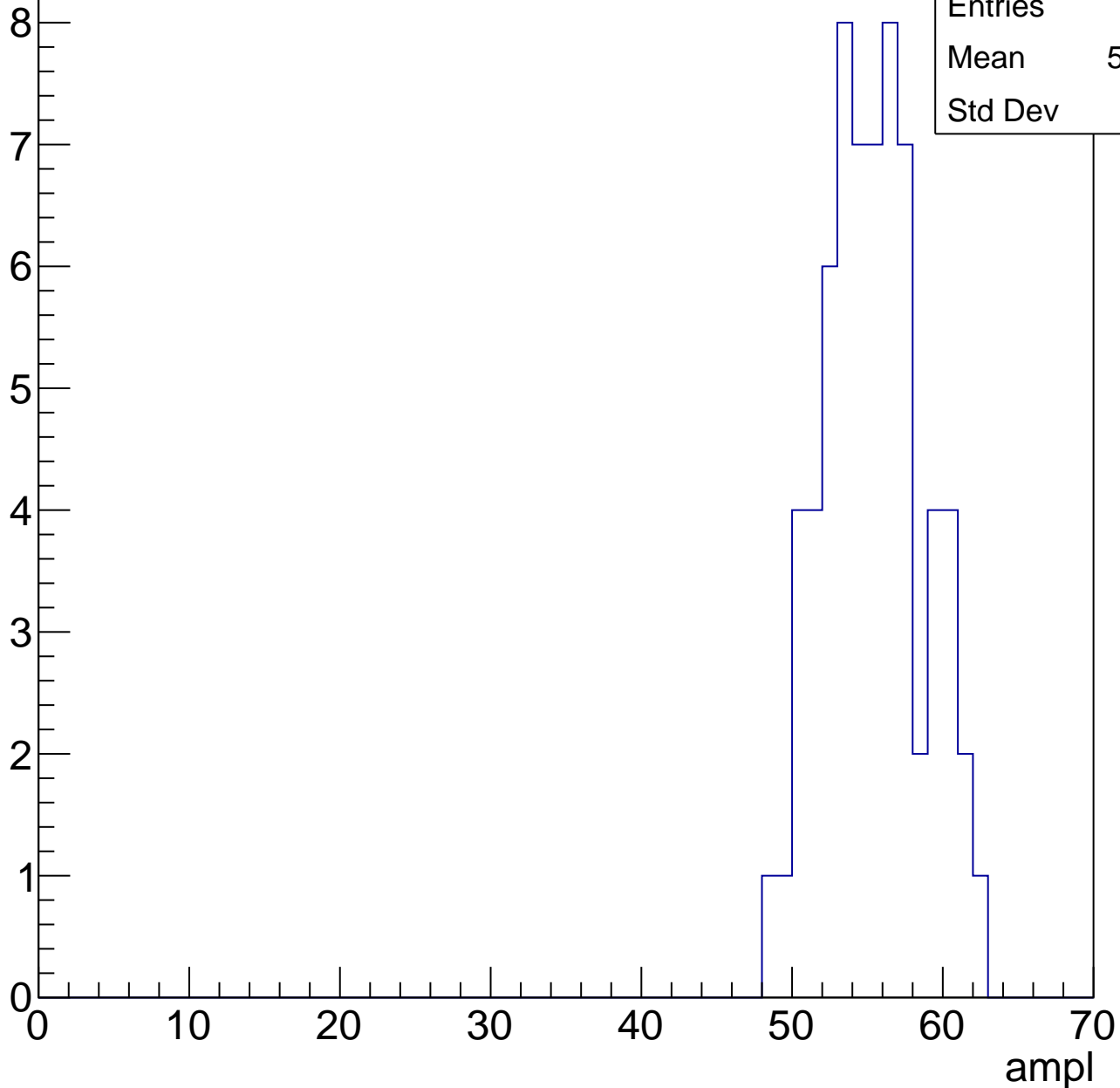


B1L003S, U16-ch28, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	54.89
Std Dev	3.21

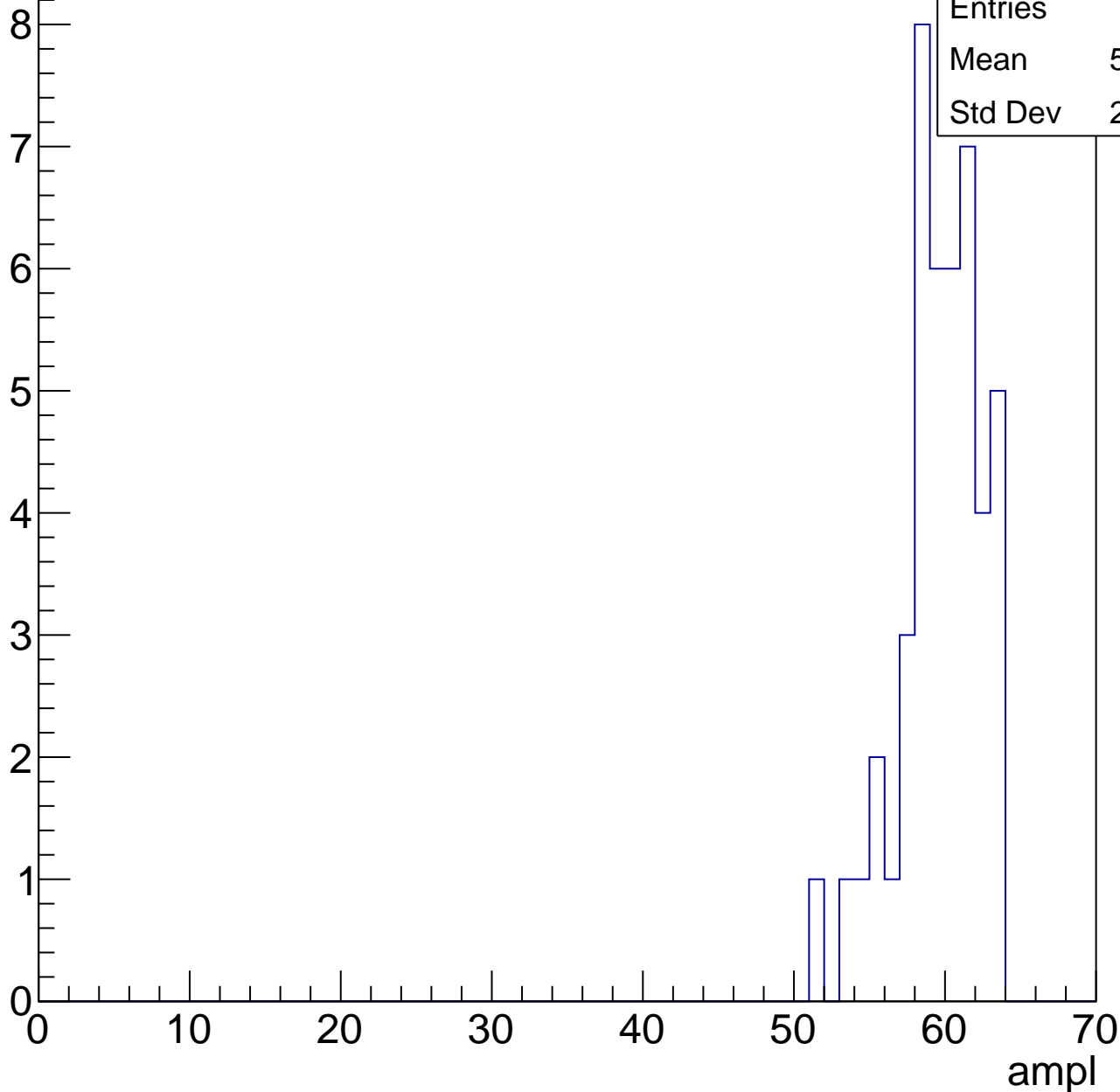


B1L003S, U16-ch28, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.18
Std Dev	2.727

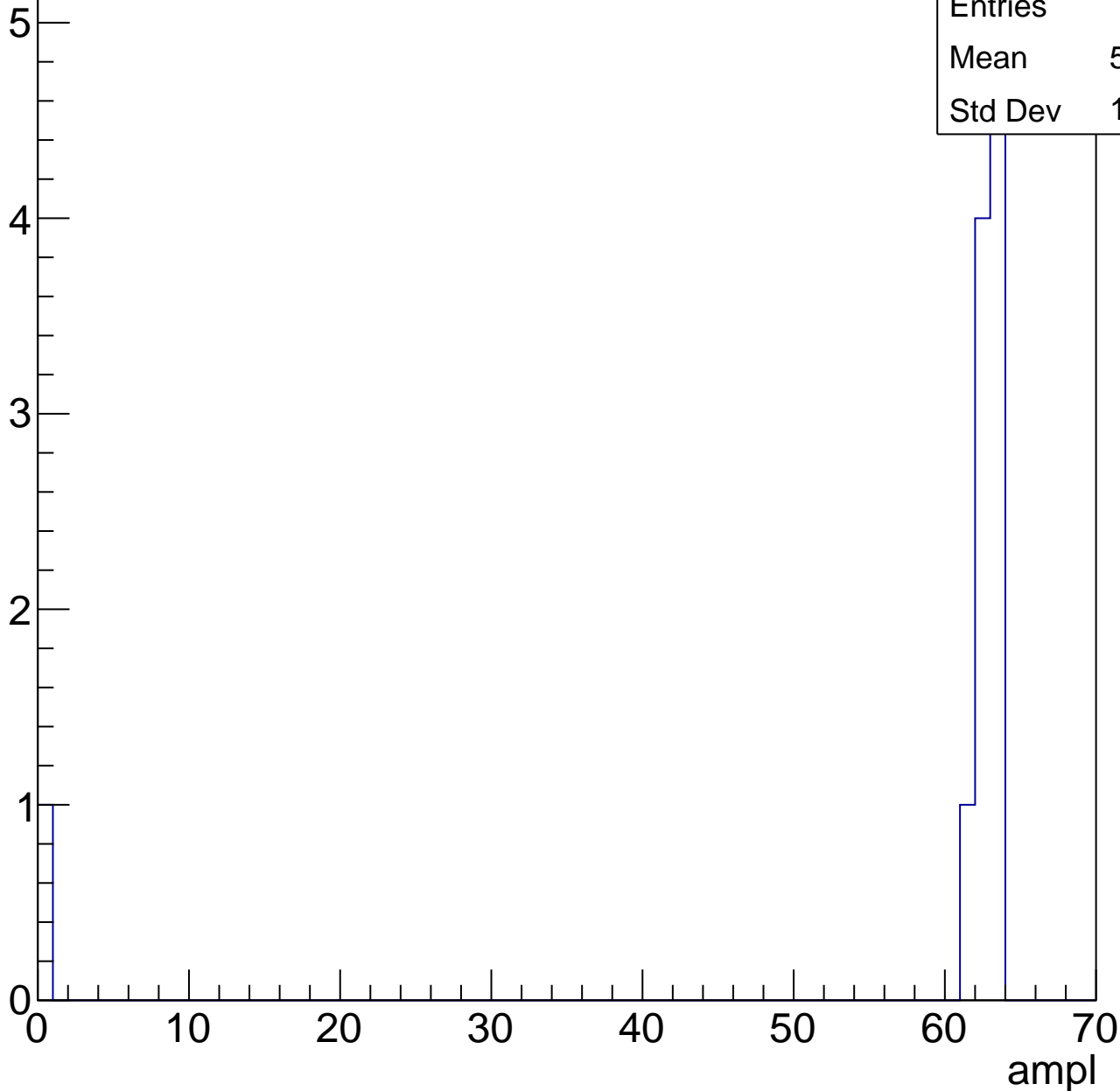


B1L003S, U16-ch28, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

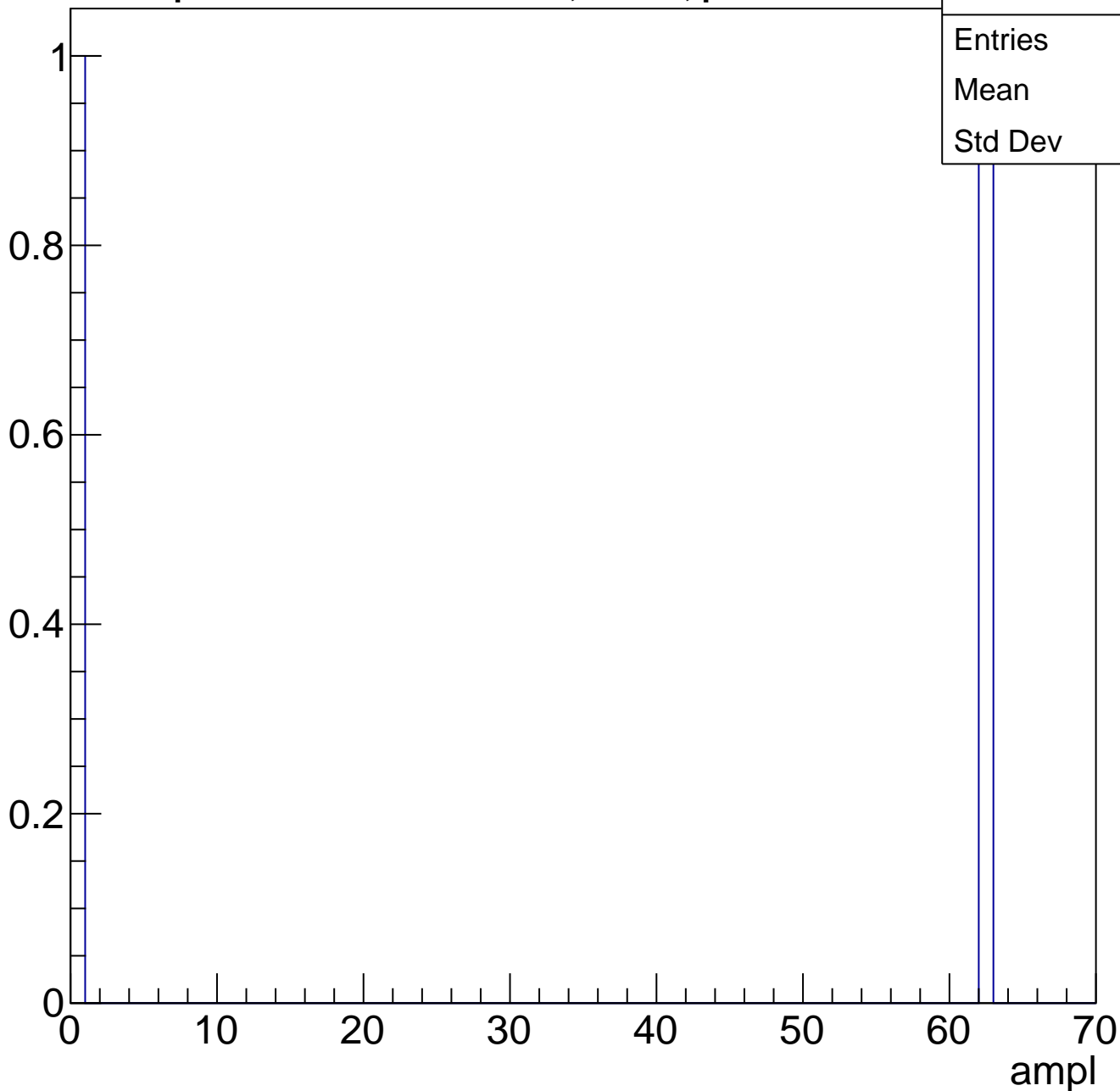
Entries	11
Mean	56.73
Std Dev	17.95



B1L003S, U16-ch28, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch29, adc0

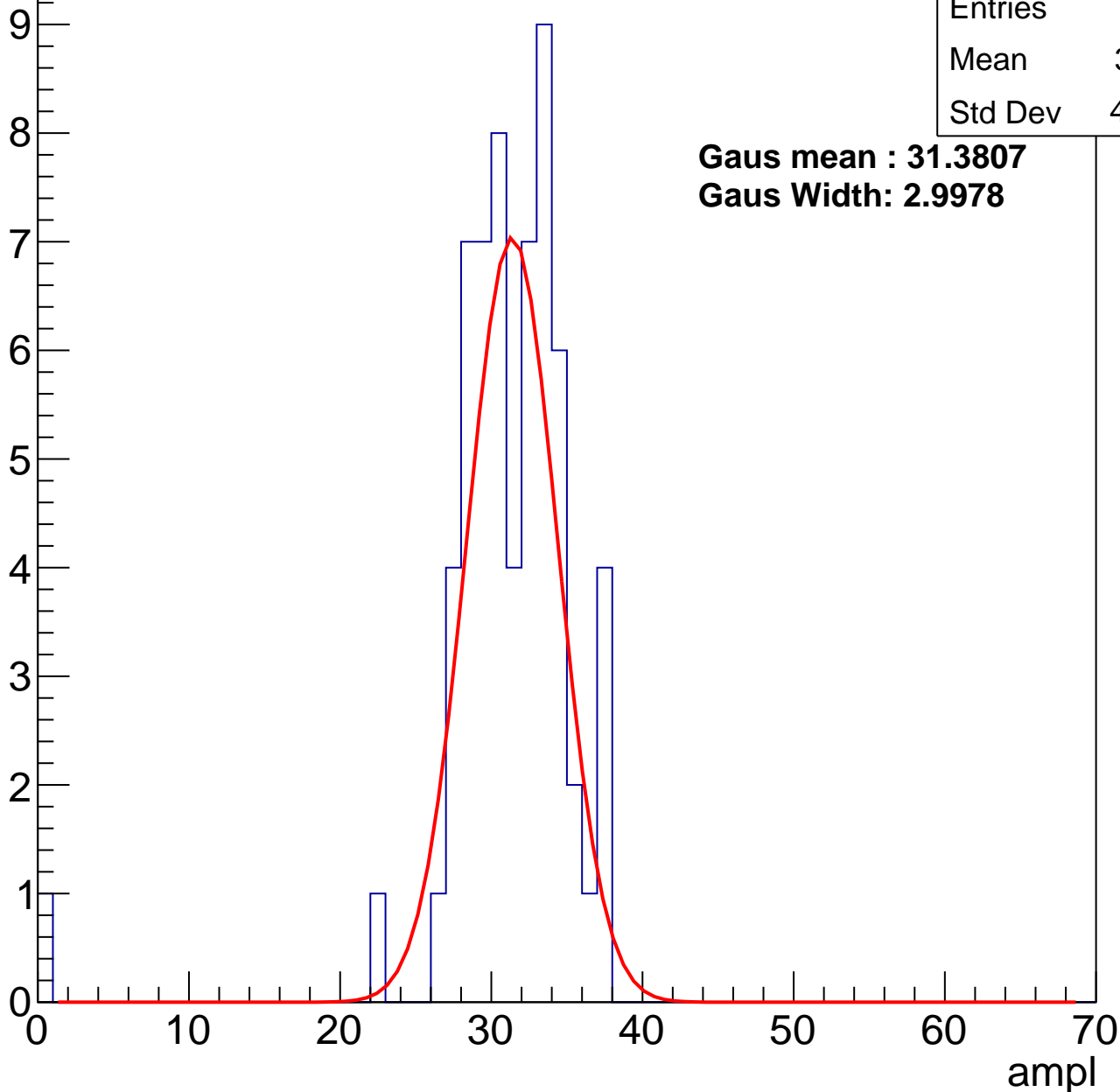
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.61
Std Dev	4.949

Gaus mean : 31.3807

Gaus Width: 2.9978



B1L003S, U16-ch29, adc1

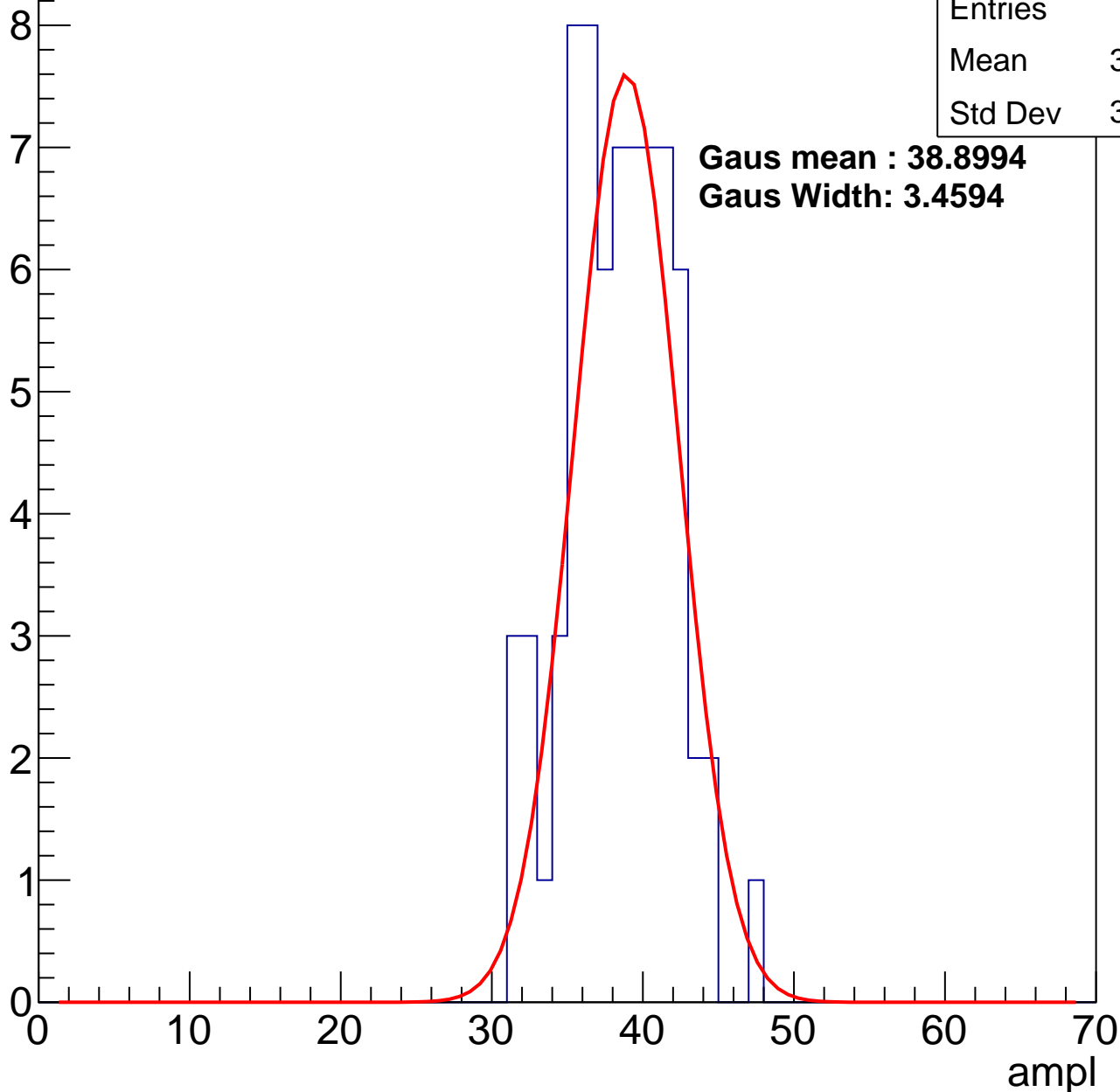
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	37.93
Std Dev	3.429

Gaus mean : 38.8994

Gaus Width: 3.4594



B1L003S, U16-ch29, adc2

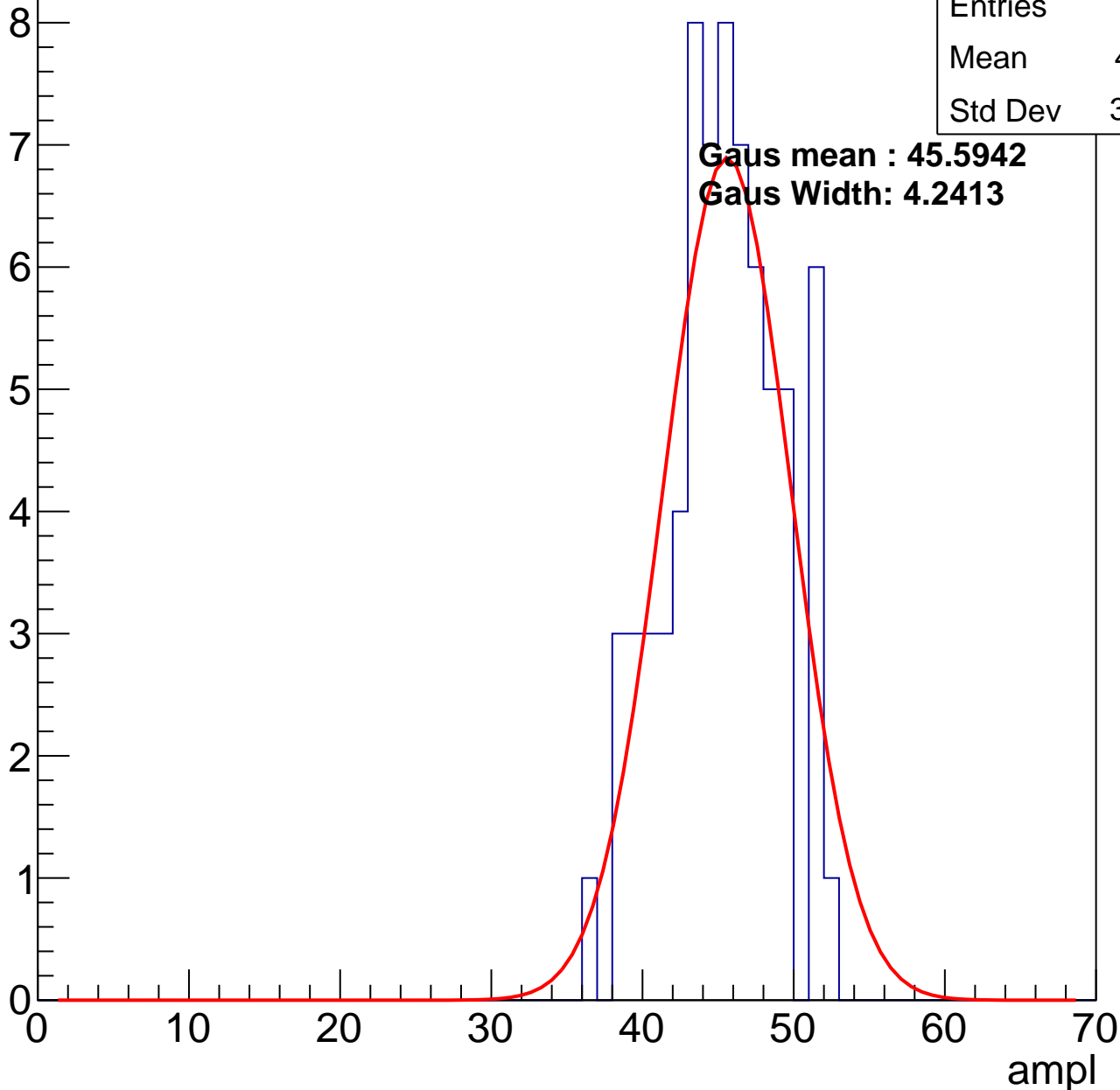
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	44.81
Std Dev	3.689

Gaus mean : 45.5942

Gaus Width: 4.2413



B1L003S, U16-ch29, adc3

calib_packv5_042523_0143.root, FC#13, port D2

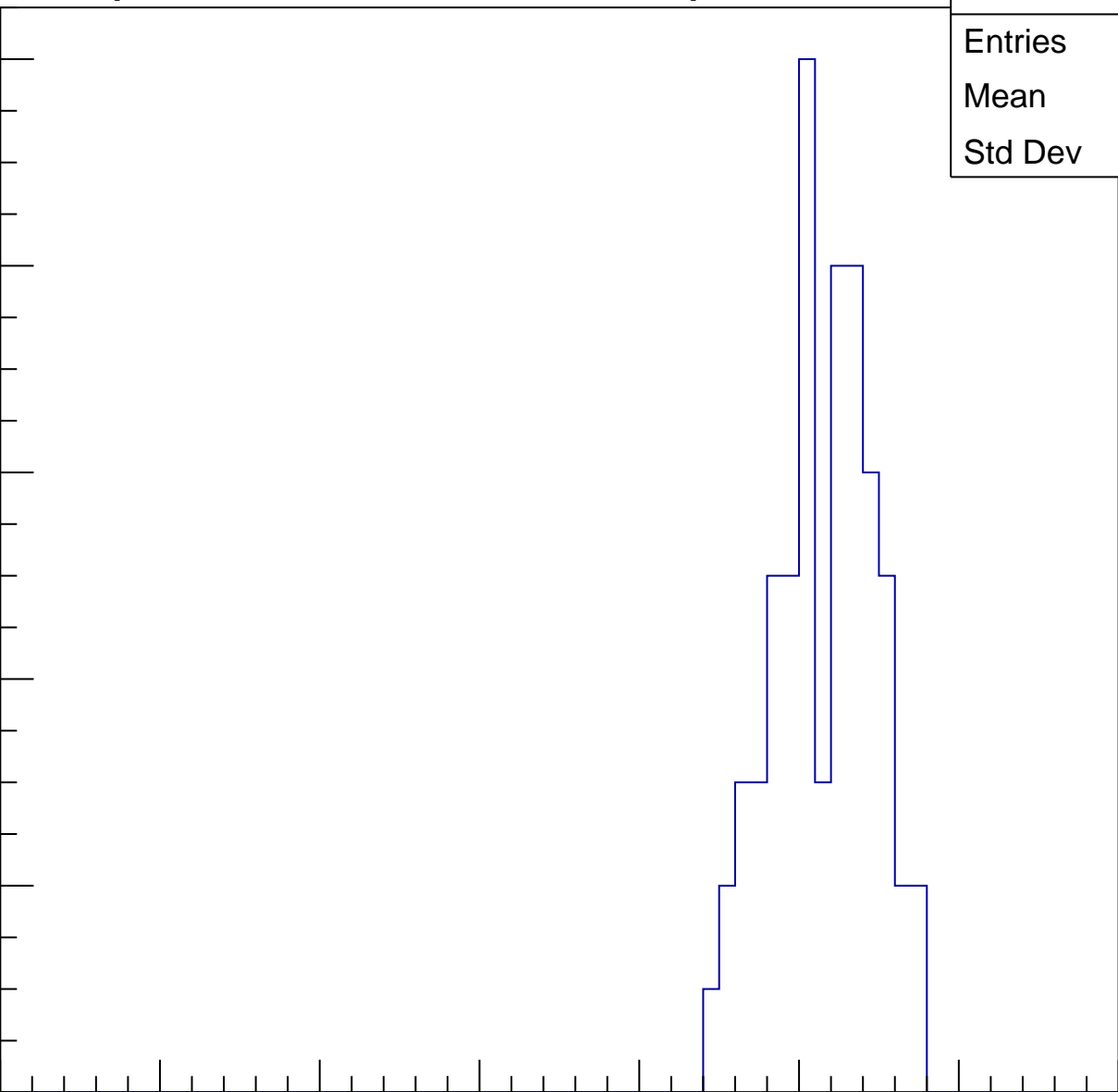
Entries	63
Mean	51.05
Std Dev	3.124

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch29, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	56.72
Std Dev	2.336

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

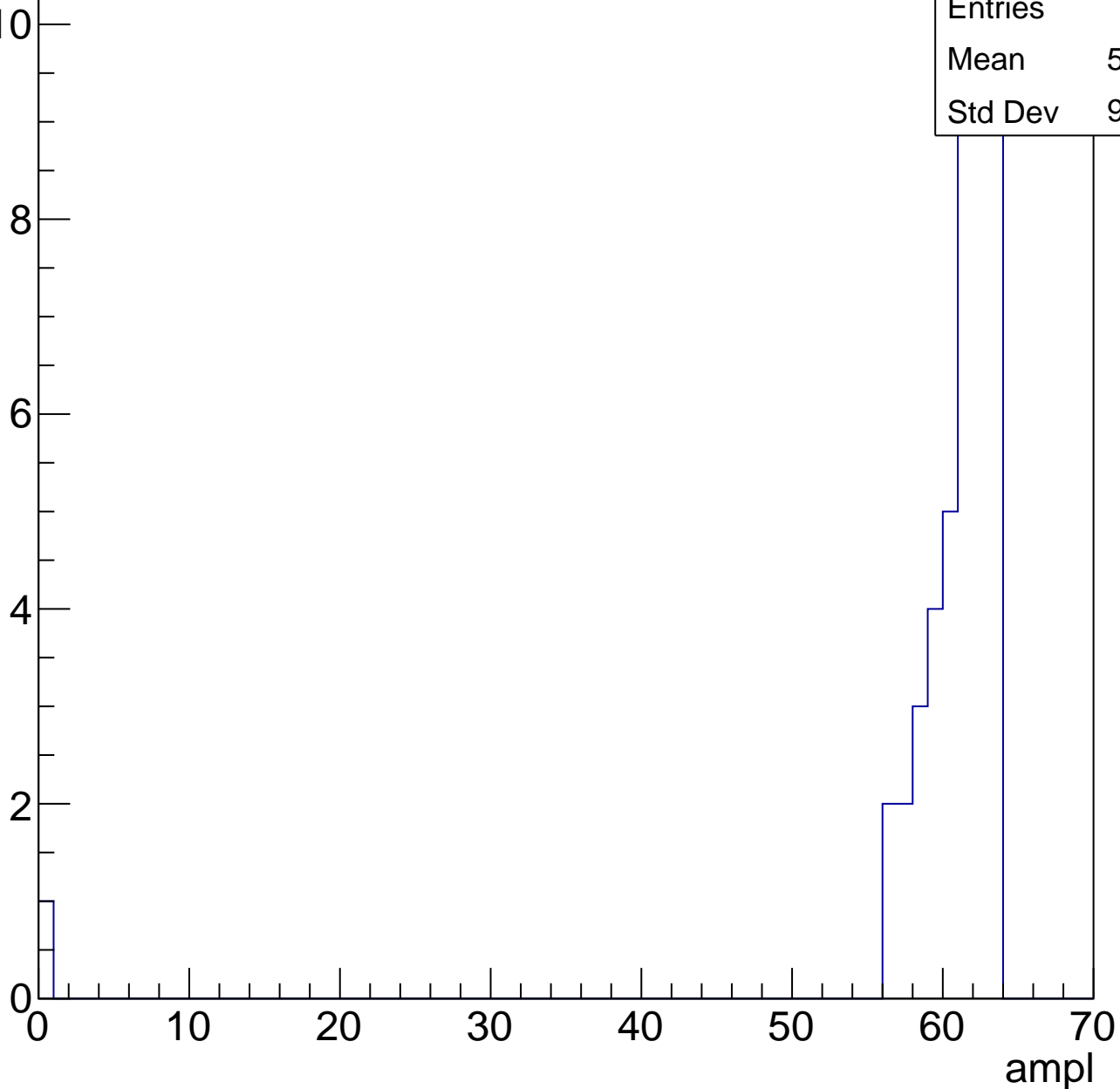
ampl

B1L003S, U16-ch29, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.38
Std Dev	9.163



B1L003S, U16-ch29, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch29, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch30, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	30.54
Std Dev	4.853

Gaus mean : 31.4414

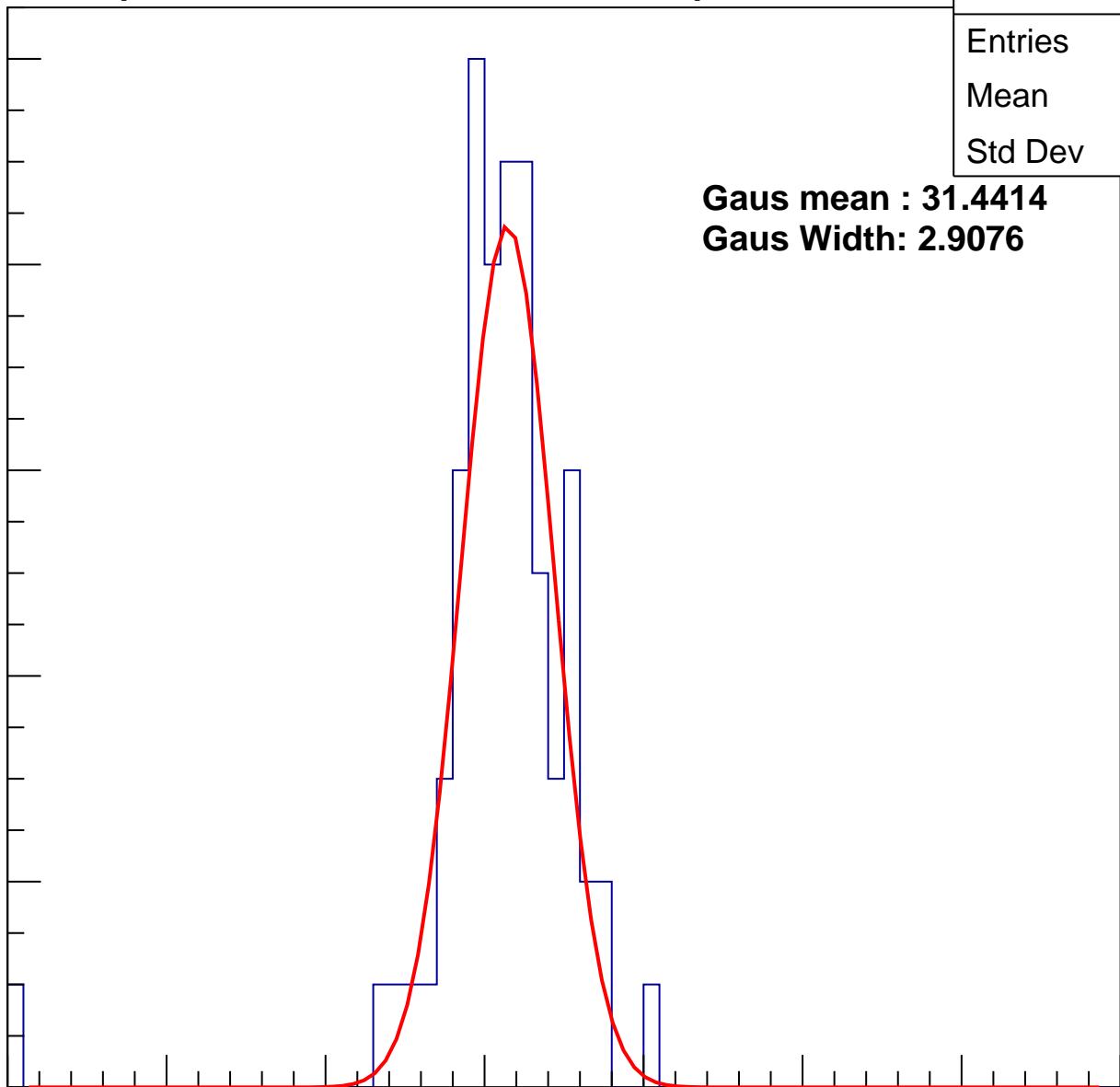
Gaus Width: 2.9076

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch30, adc1

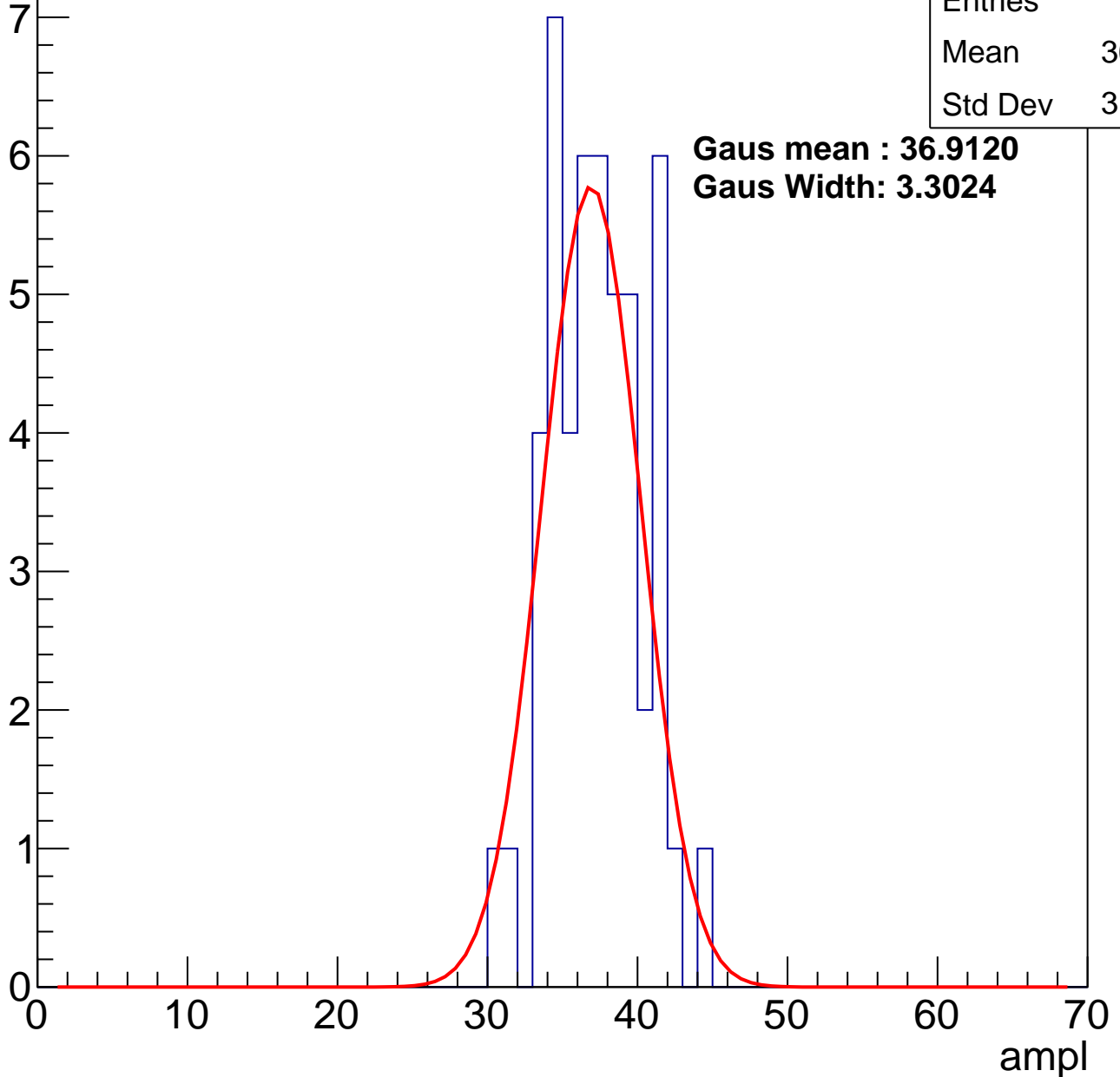
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	36.86
Std Dev	3.017

Gaus mean : 36.9120

Gaus Width: 3.3024



B1L003S, U16-ch30, adc2

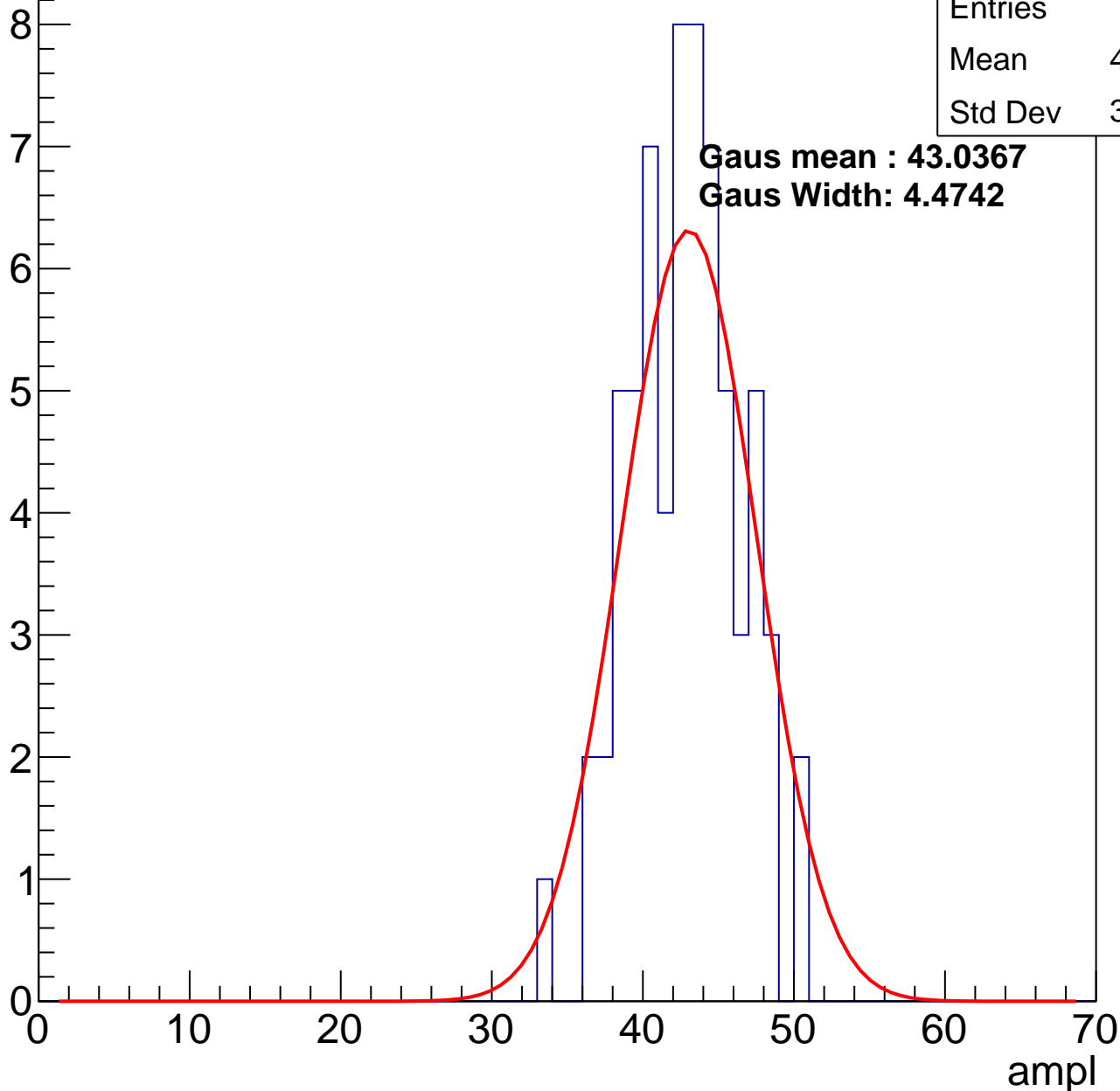
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.36
Std Dev	3.544

Gaus mean : 43.0367

Gaus Width: 4.4742



B1L003S, U16-ch30, adc3

calib_packv5_042523_0143.root, FC#13, port D2

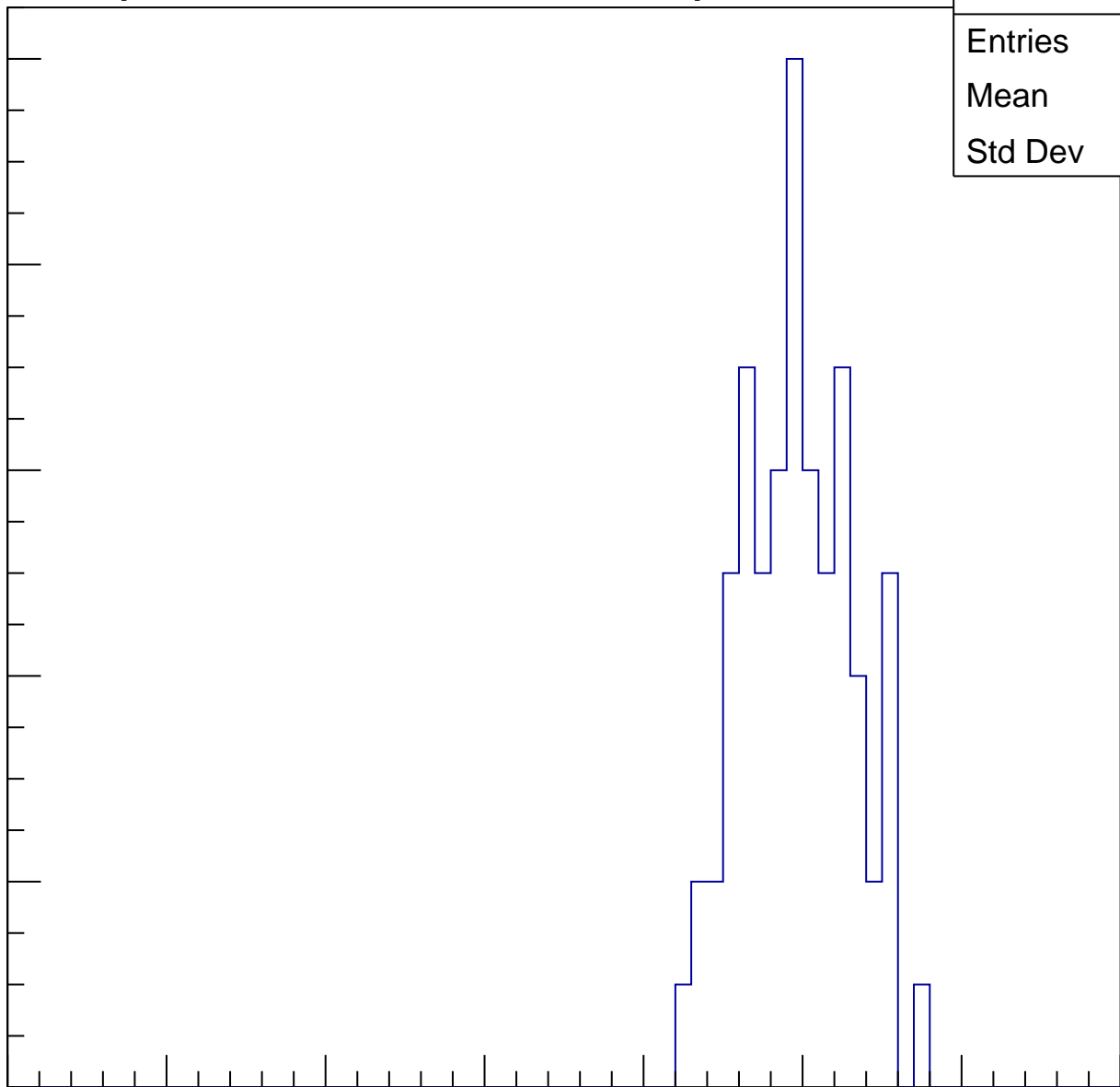
Entries	68
Mean	49.22
Std Dev	3.395

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

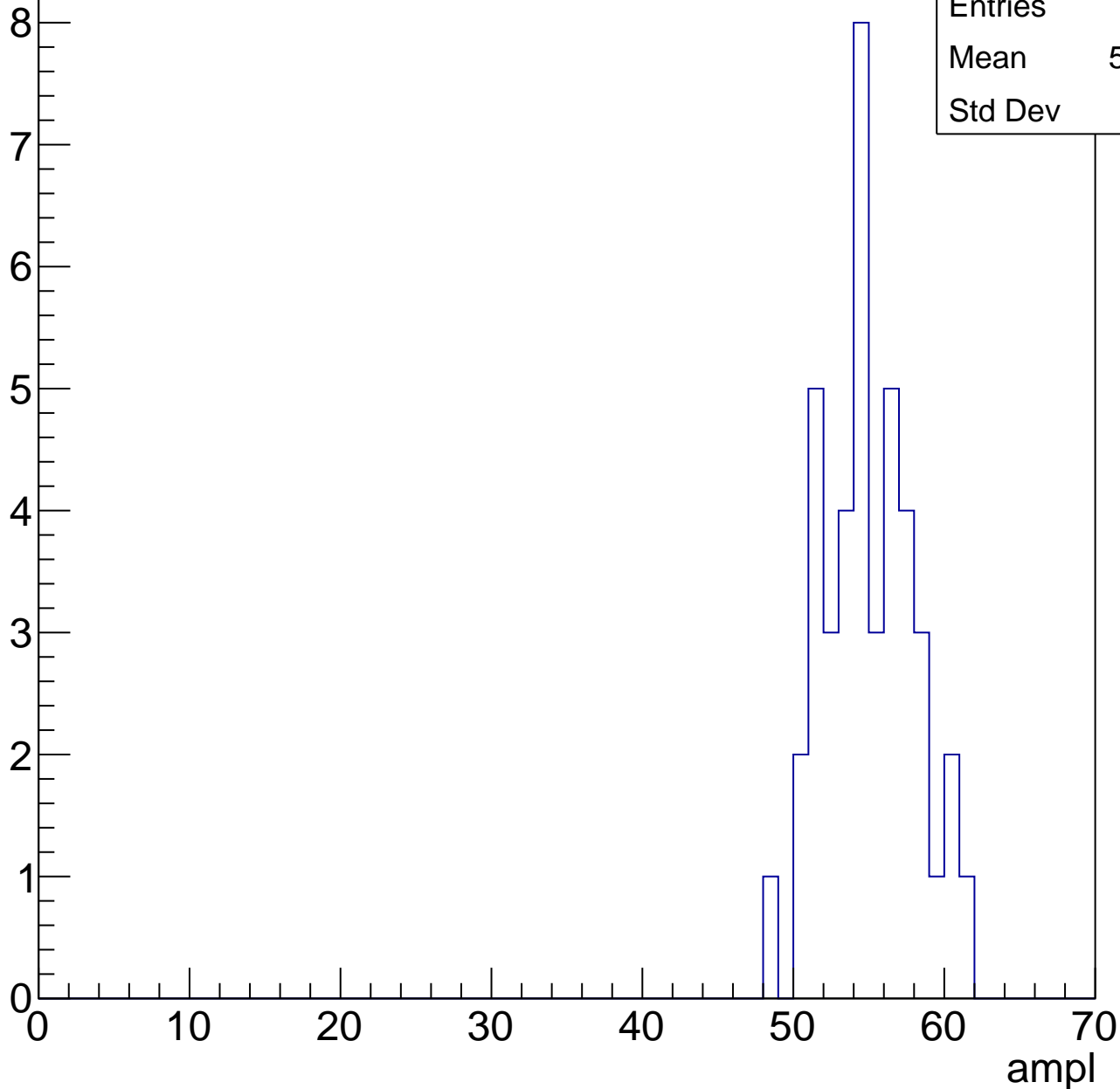


B1L003S, U16-ch30, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	54.52
Std Dev	2.97

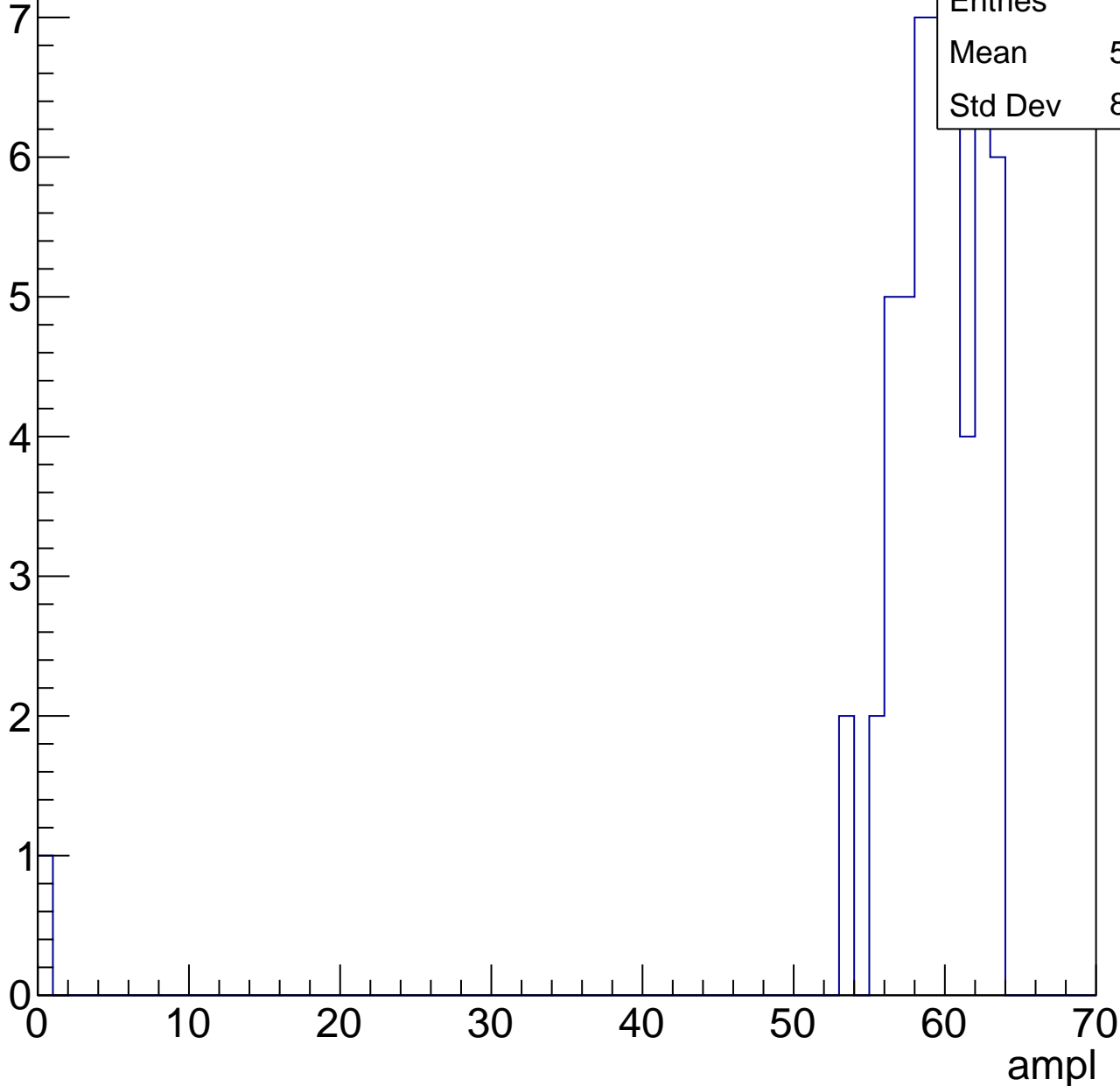


B1L003S, U16-ch30, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.04
Std Dev	8.456

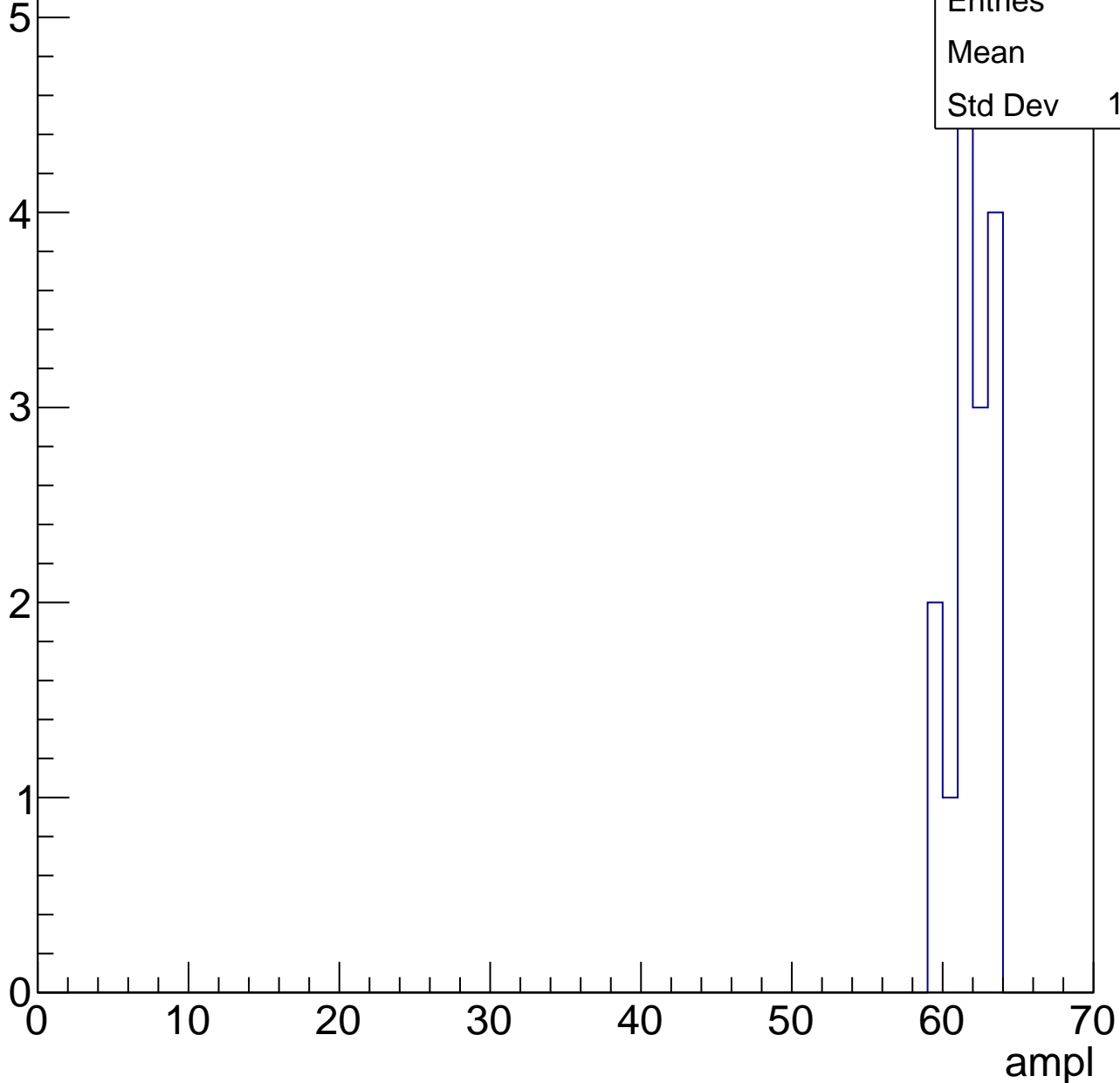


B1L003S, U16-ch30, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

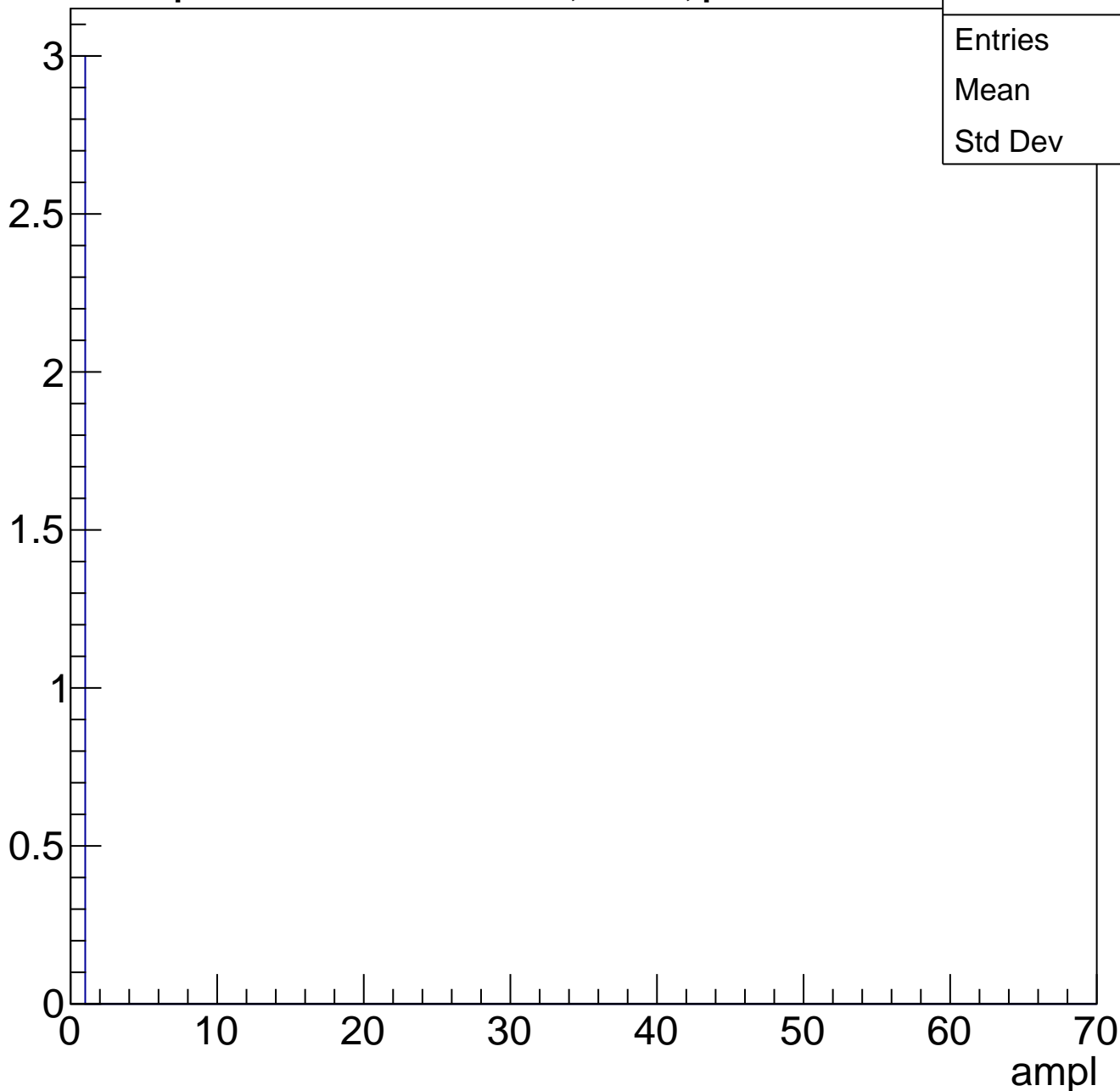
Entries	15
Mean	61.4
Std Dev	1.306



B1L003S, U16-ch30, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U16-ch31, adc0

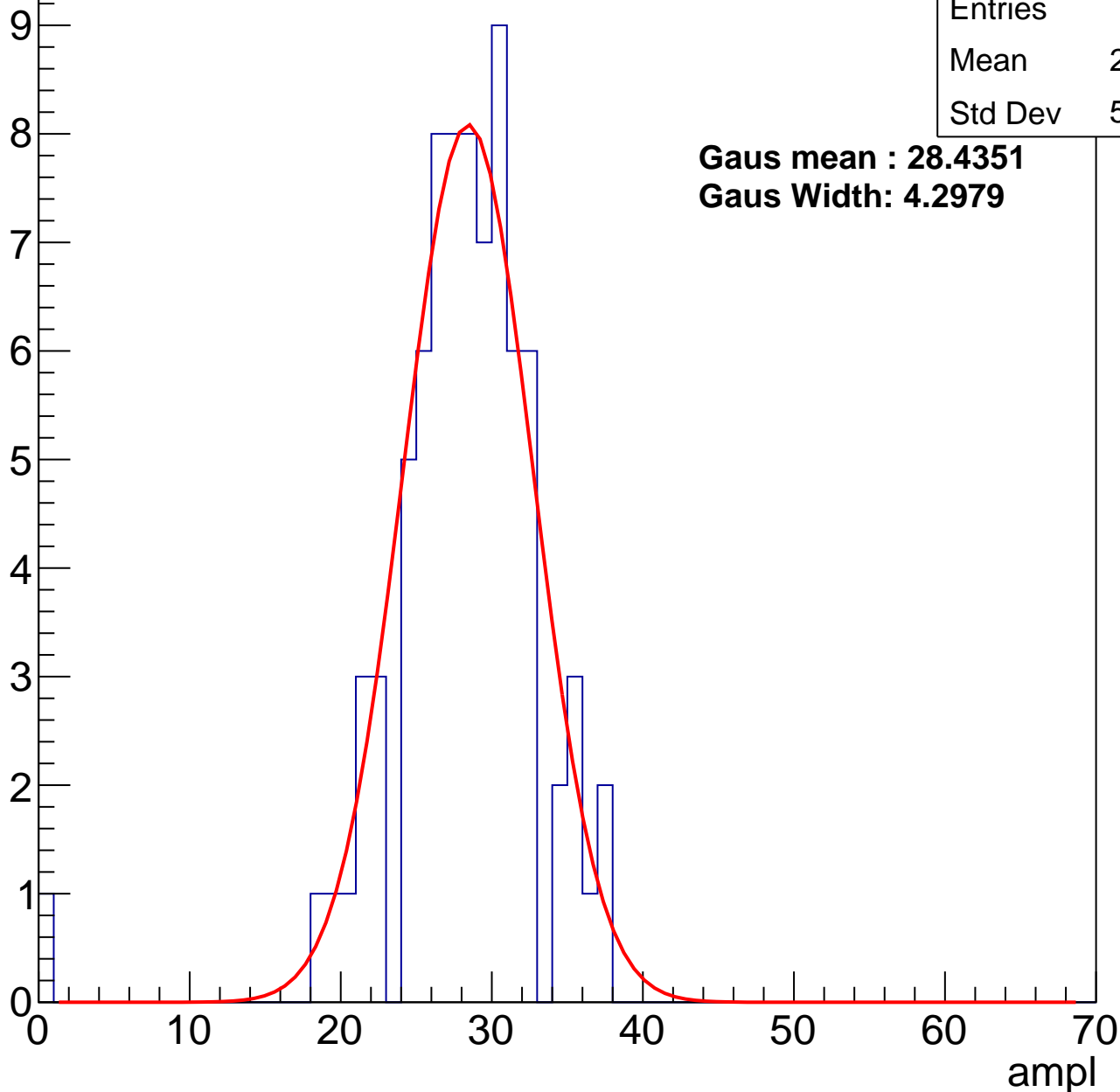
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	27.63
Std Dev	5.073

Gaus mean : 28.4351

Gaus Width: 4.2979



B1L003S, U16-ch31, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	34.92
Std Dev	3.271

Gaus mean : 35.8342

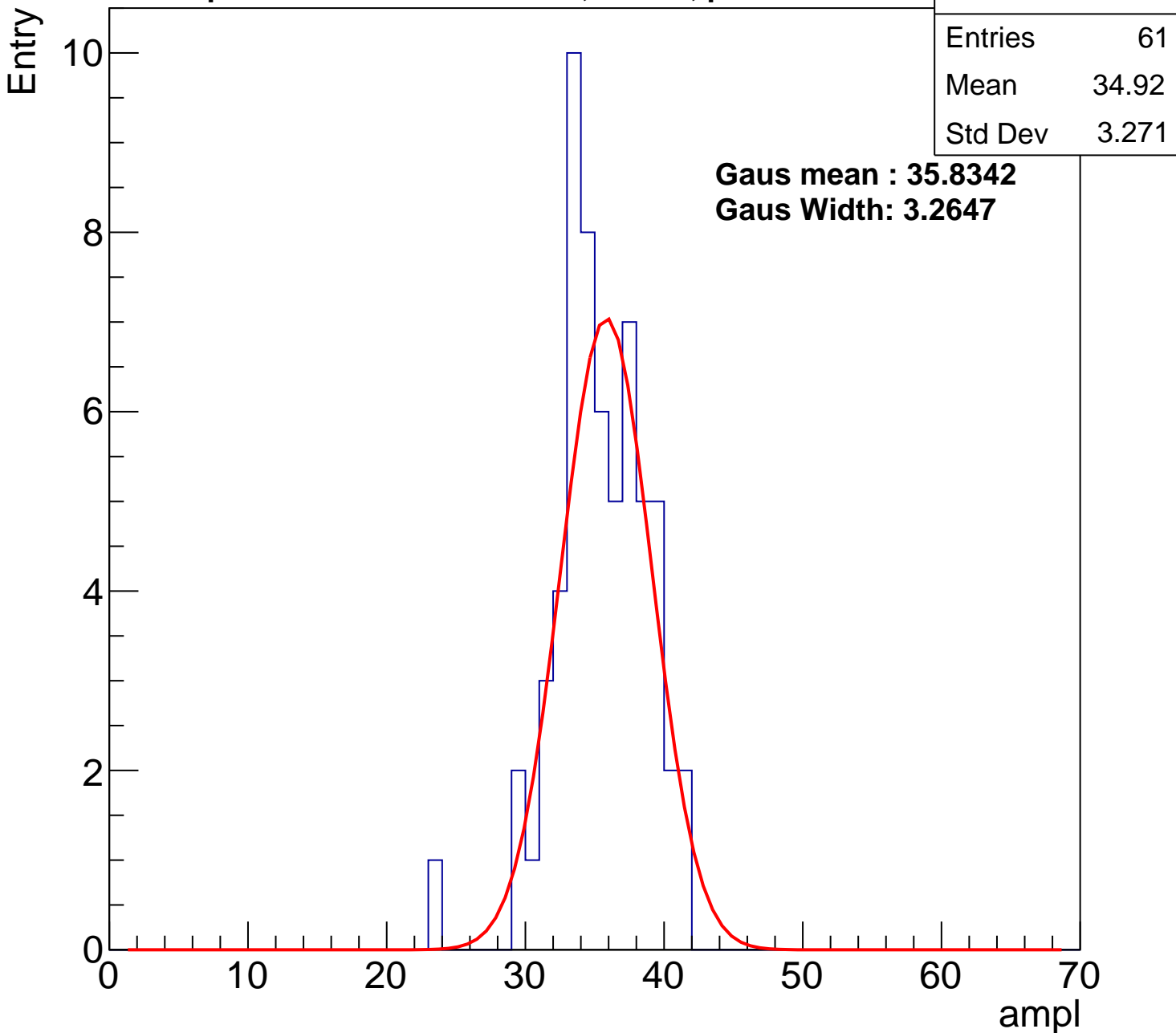
Gaus Width: 3.2647

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch31, adc2

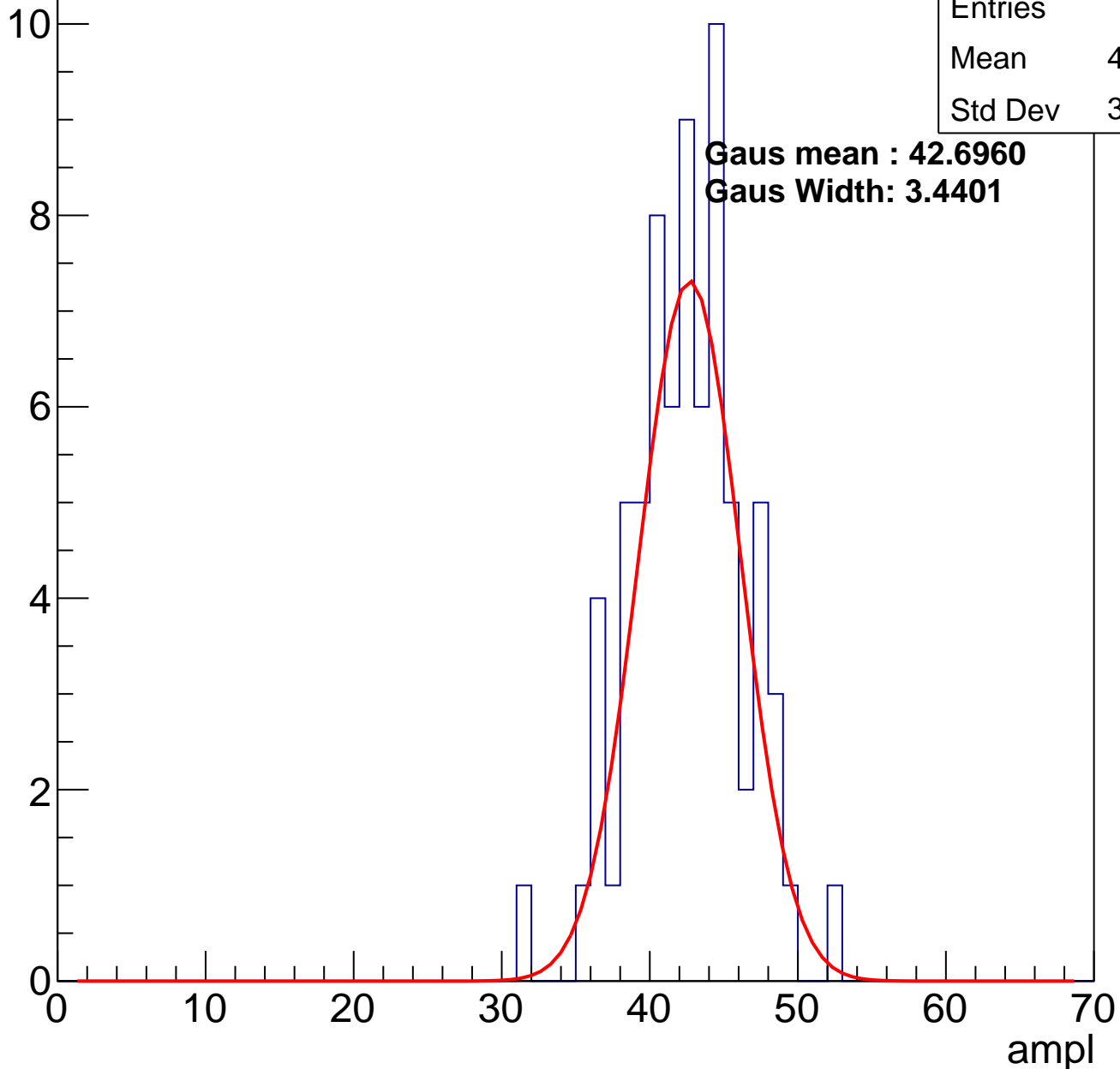
calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	42.07
Std Dev	3.713

Gaus mean : 42.6960

Gaus Width: 3.4401

Entry

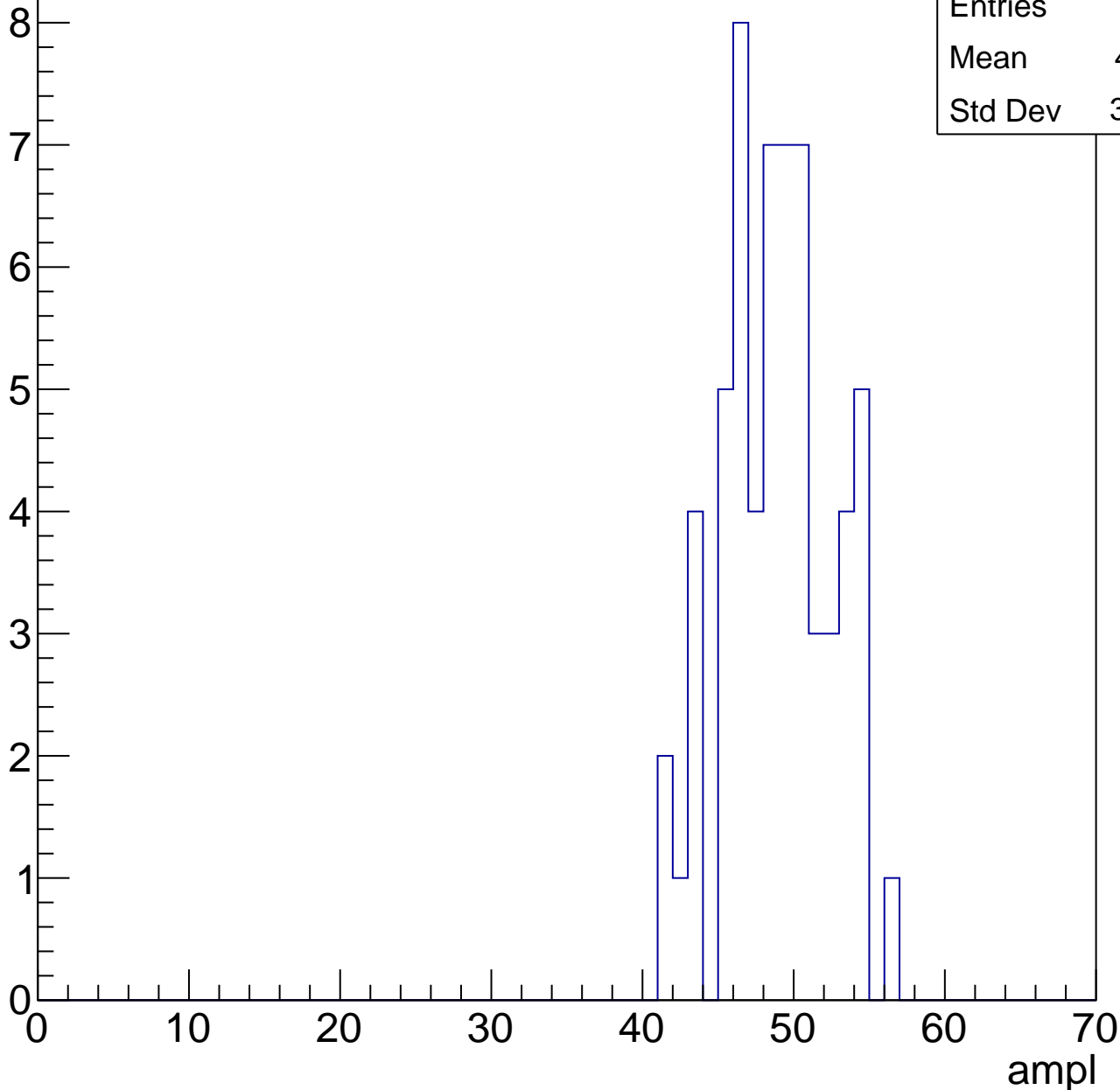


B1L003S, U16-ch31, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.41
Std Dev	3.527

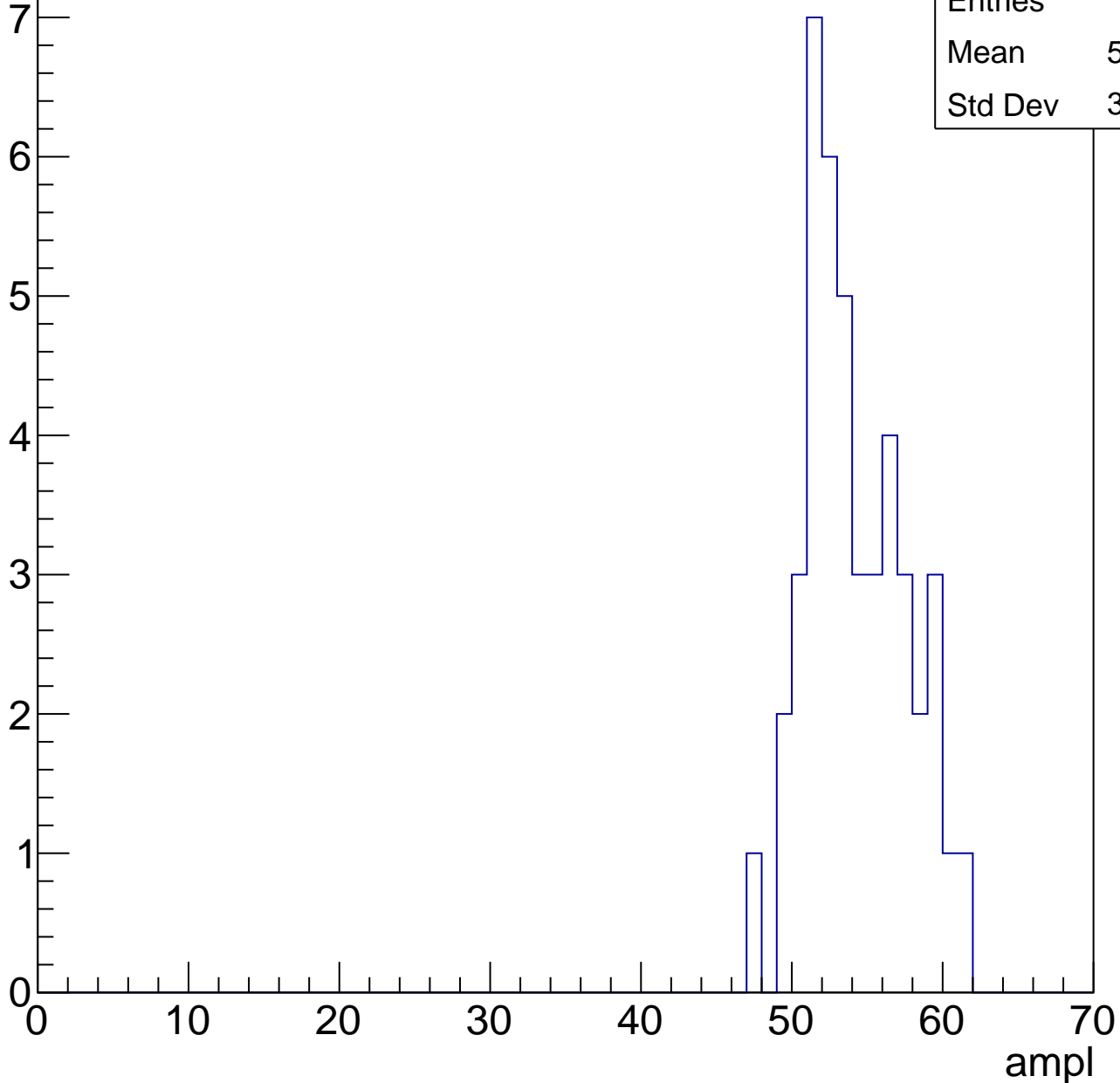


B1L003S, U16-ch31, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

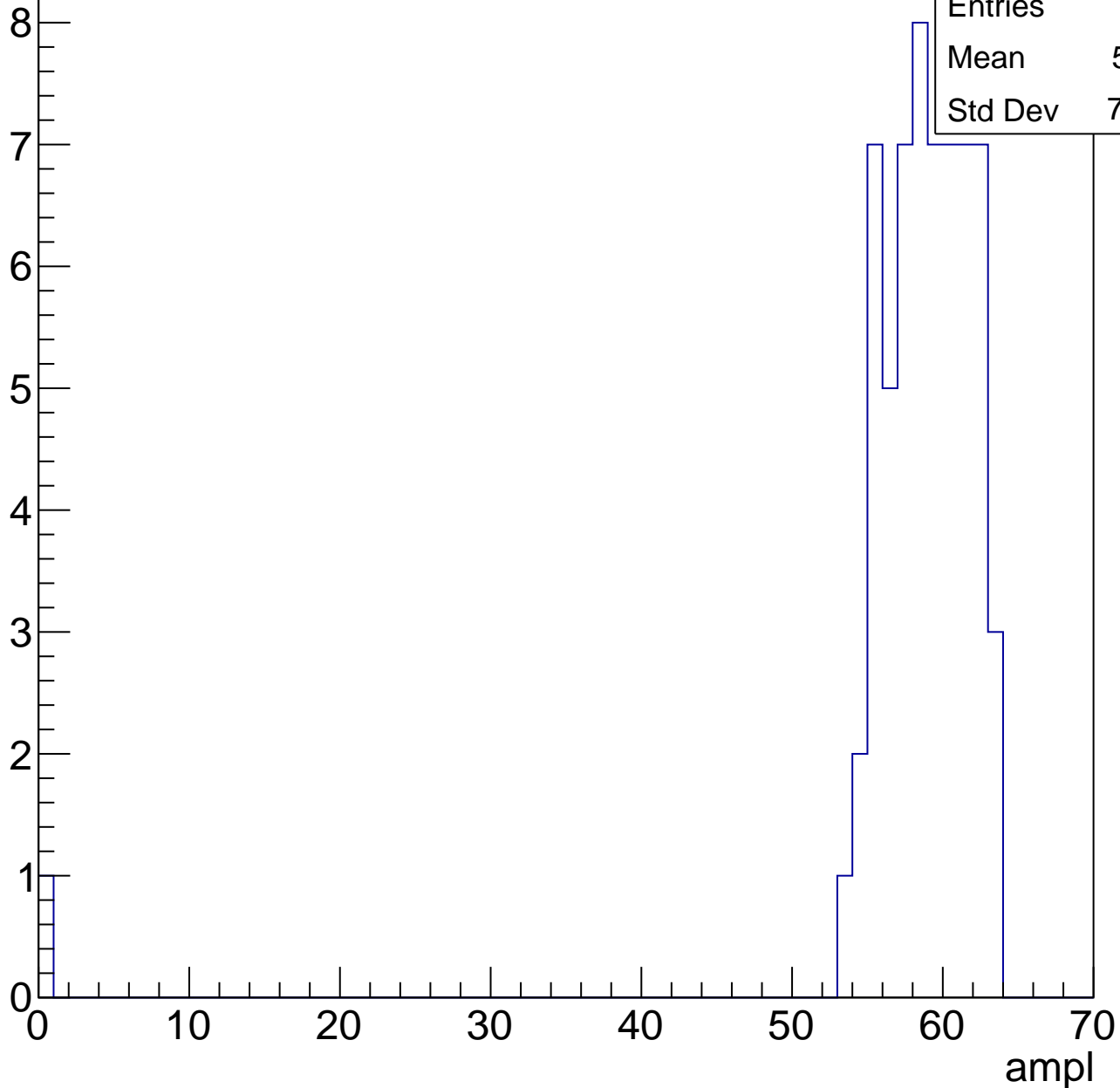
Entries	44
Mean	53.75
Std Dev	3.283



B1L003S, U16-ch31, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



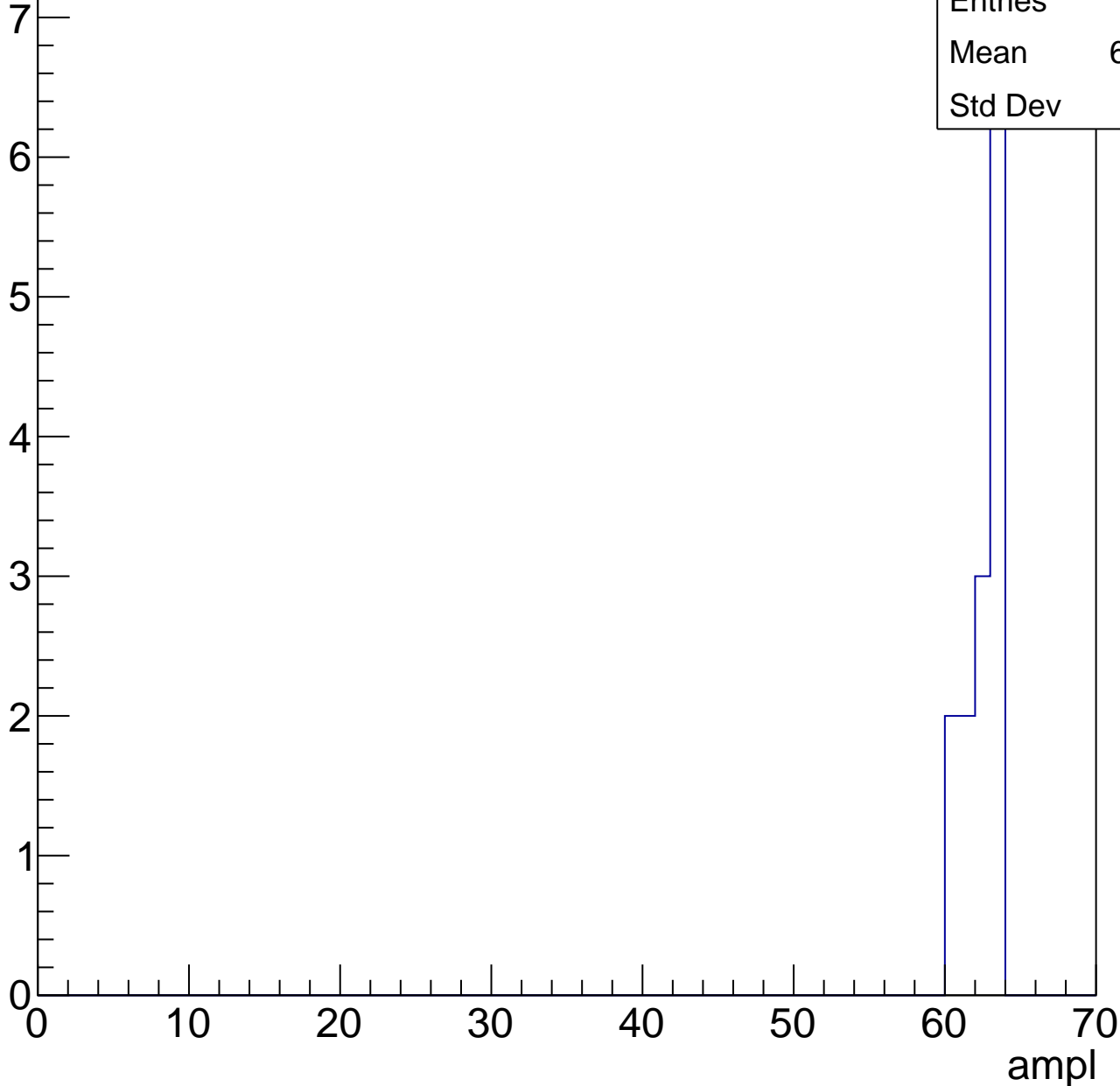
Entries	62
Mean	57.61
Std Dev	7.815

B1L003S, U16-ch31, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	62.07
Std Dev	1.1



B1L003S, U16-ch31, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch32, adc0

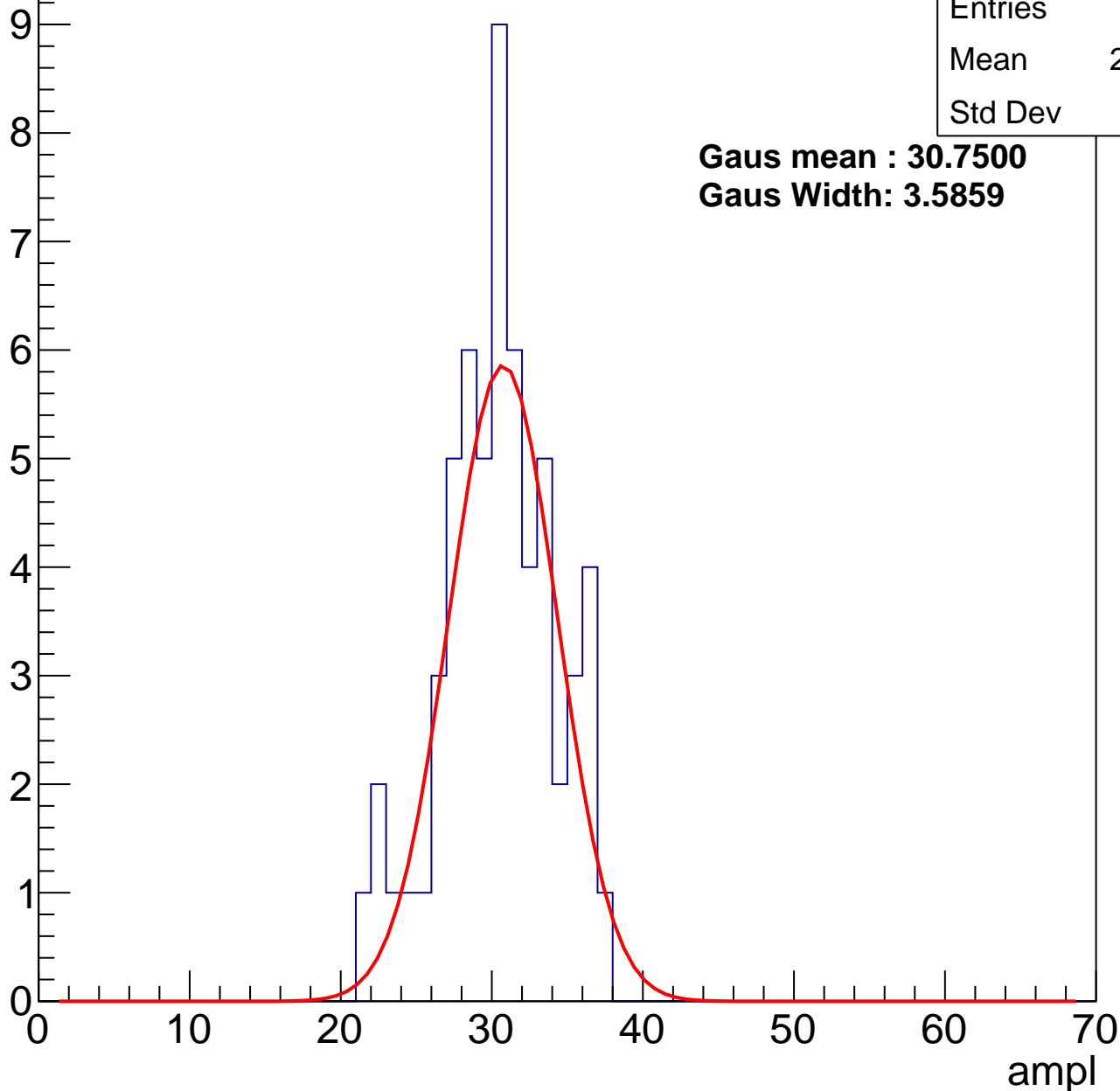
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.93
Std Dev	3.7

Gaus mean : 30.7500

Gaus Width: 3.5859



B1L003S, U16-ch32, adc1

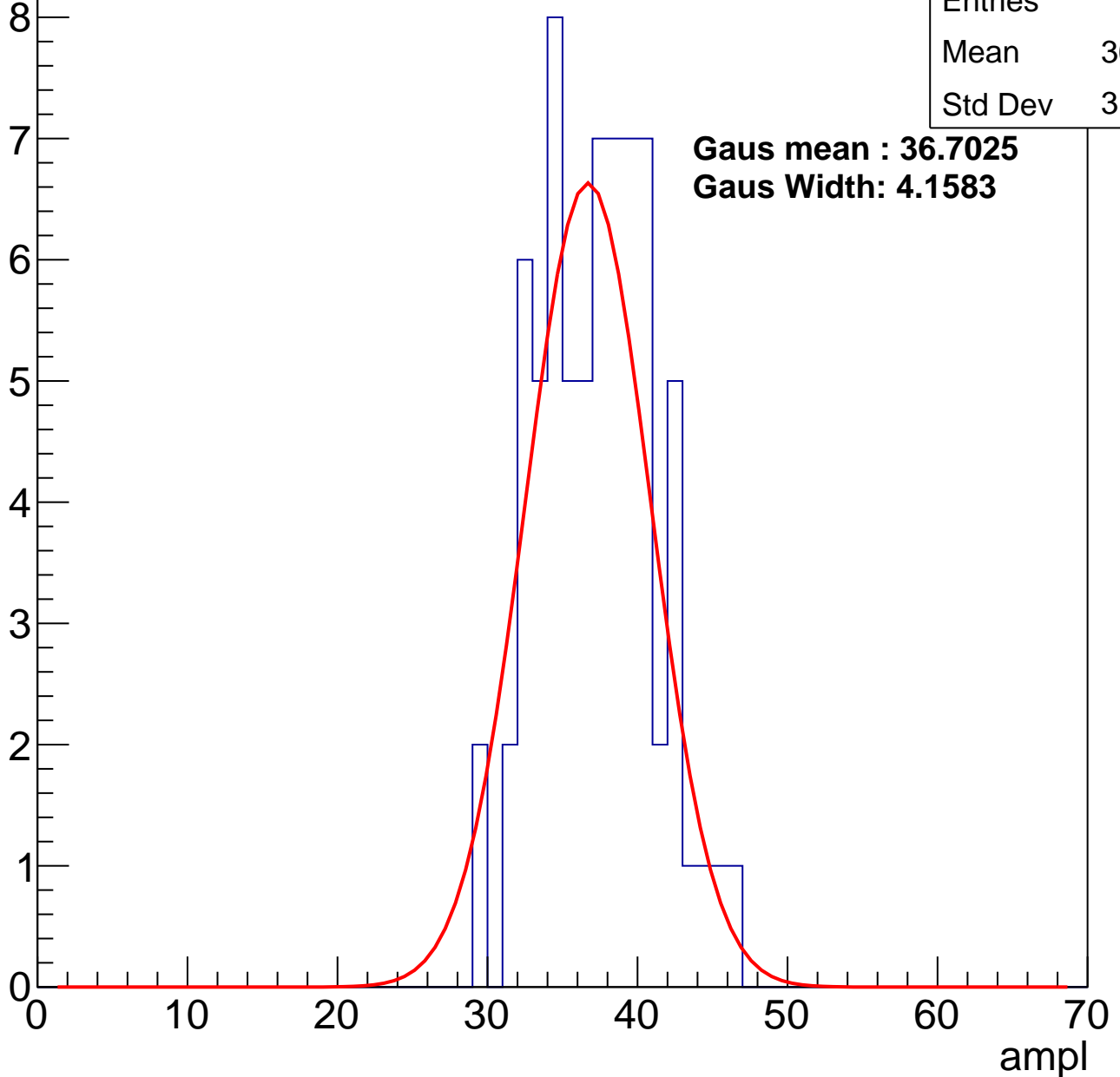
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.83
Std Dev	3.745

Gaus mean : 36.7025

Gaus Width: 4.1583



B1L003S, U16-ch32, adc2

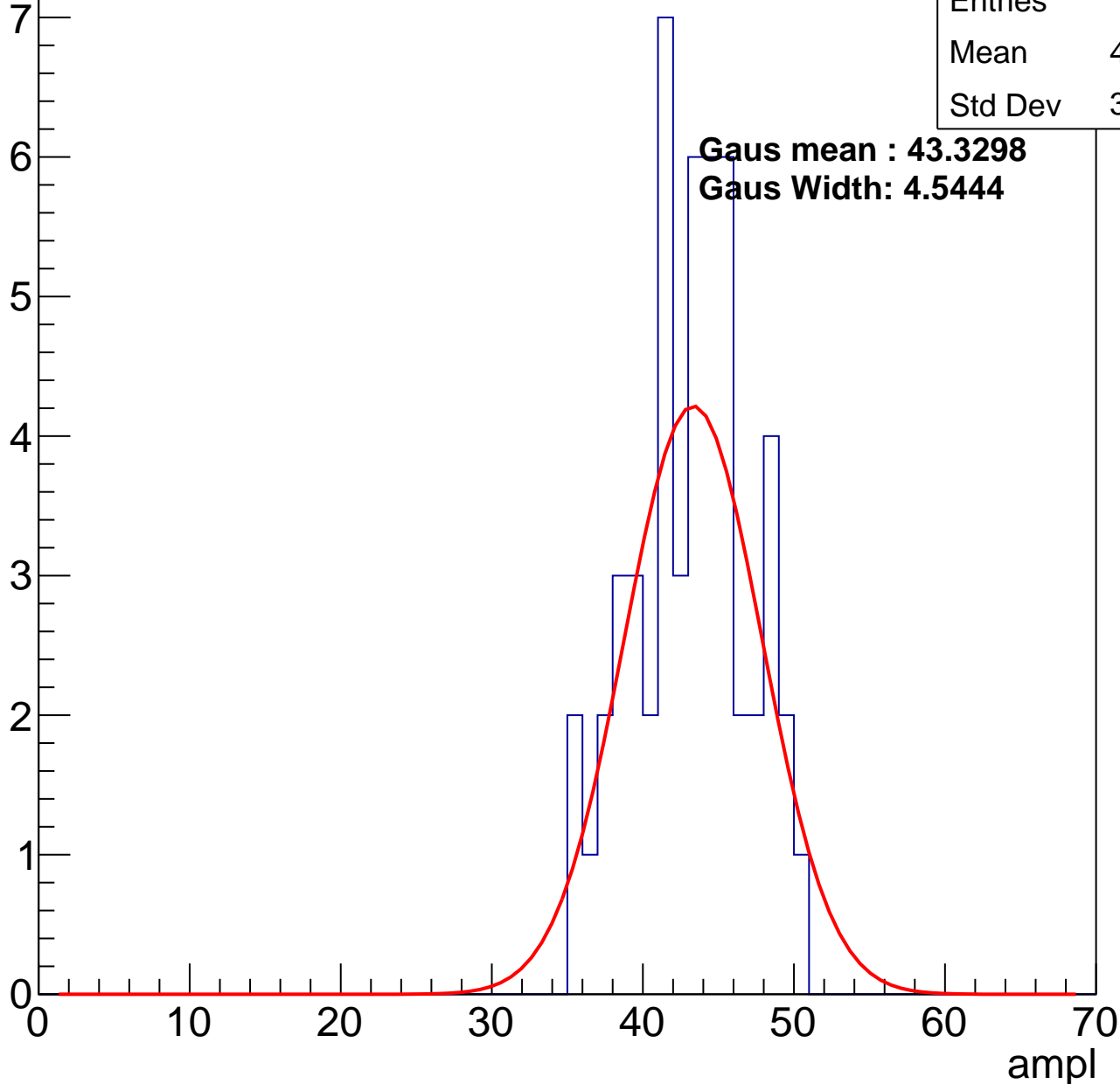
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.73
Std Dev	3.716

Gaus mean : 43.3298

Gaus Width: 4.5444

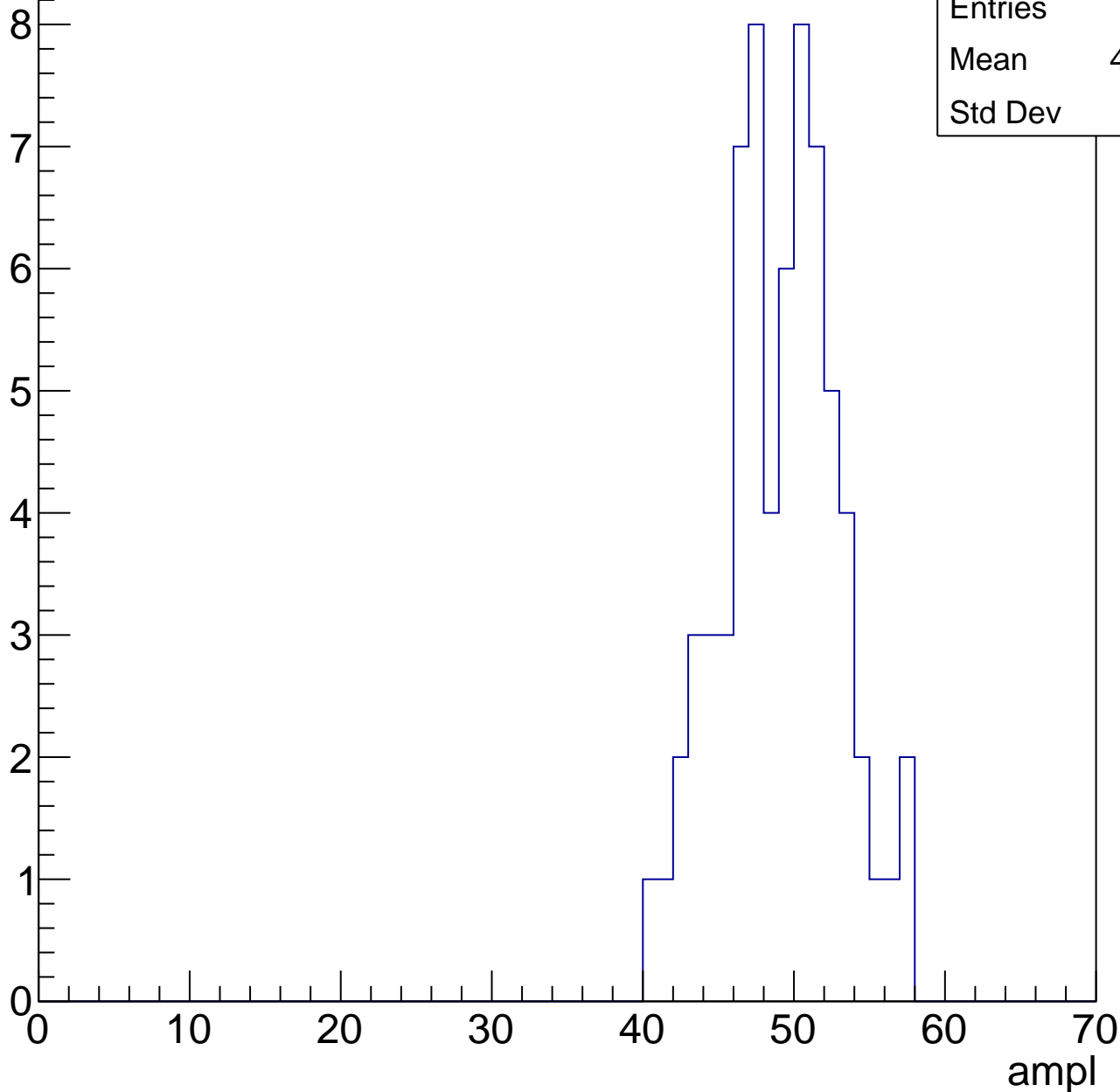


B1L003S, U16-ch32, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	48.63
Std Dev	3.78



B1L003S, U16-ch32, adc4

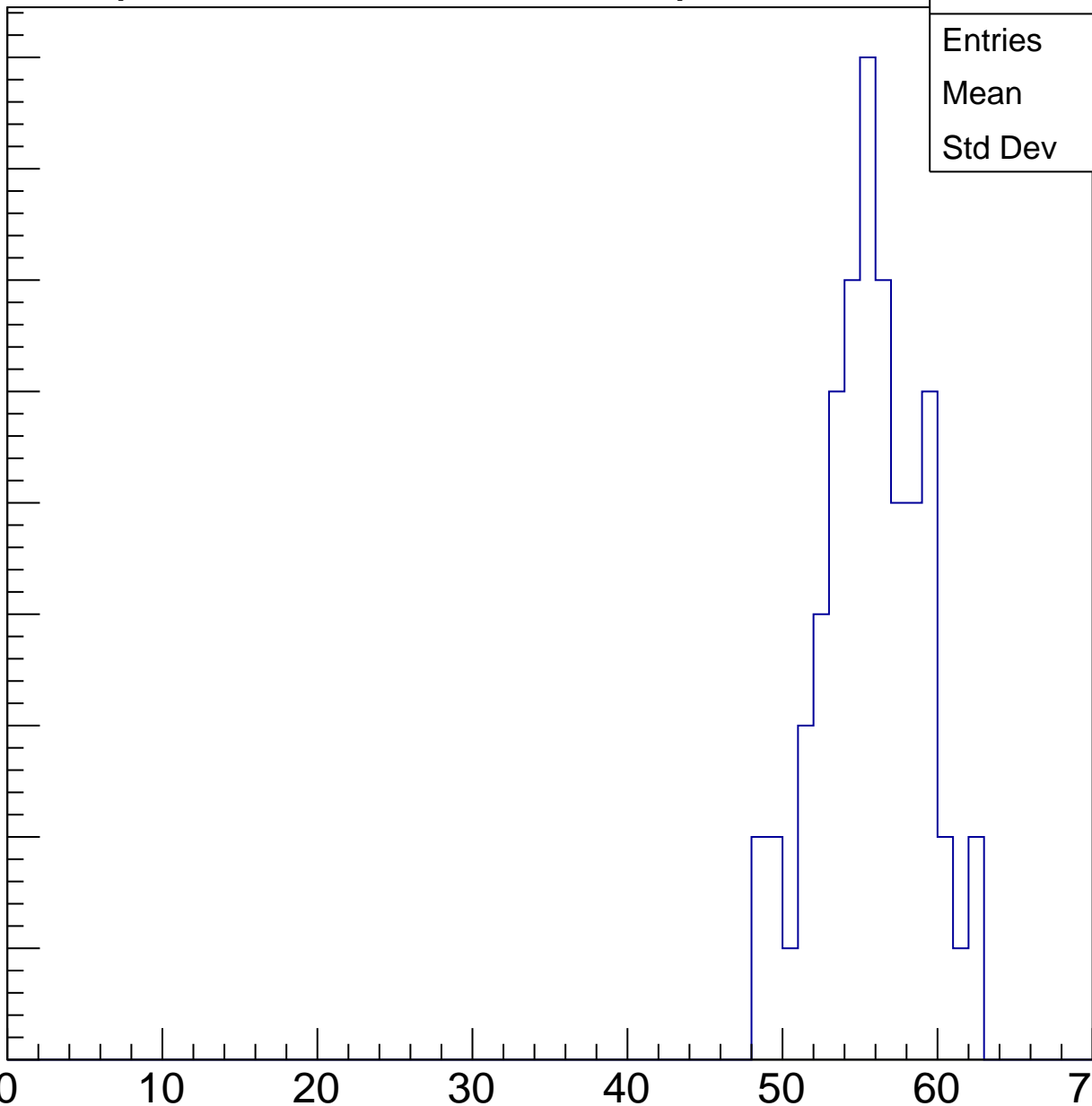
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	55.19
Std Dev	3.262

ampl

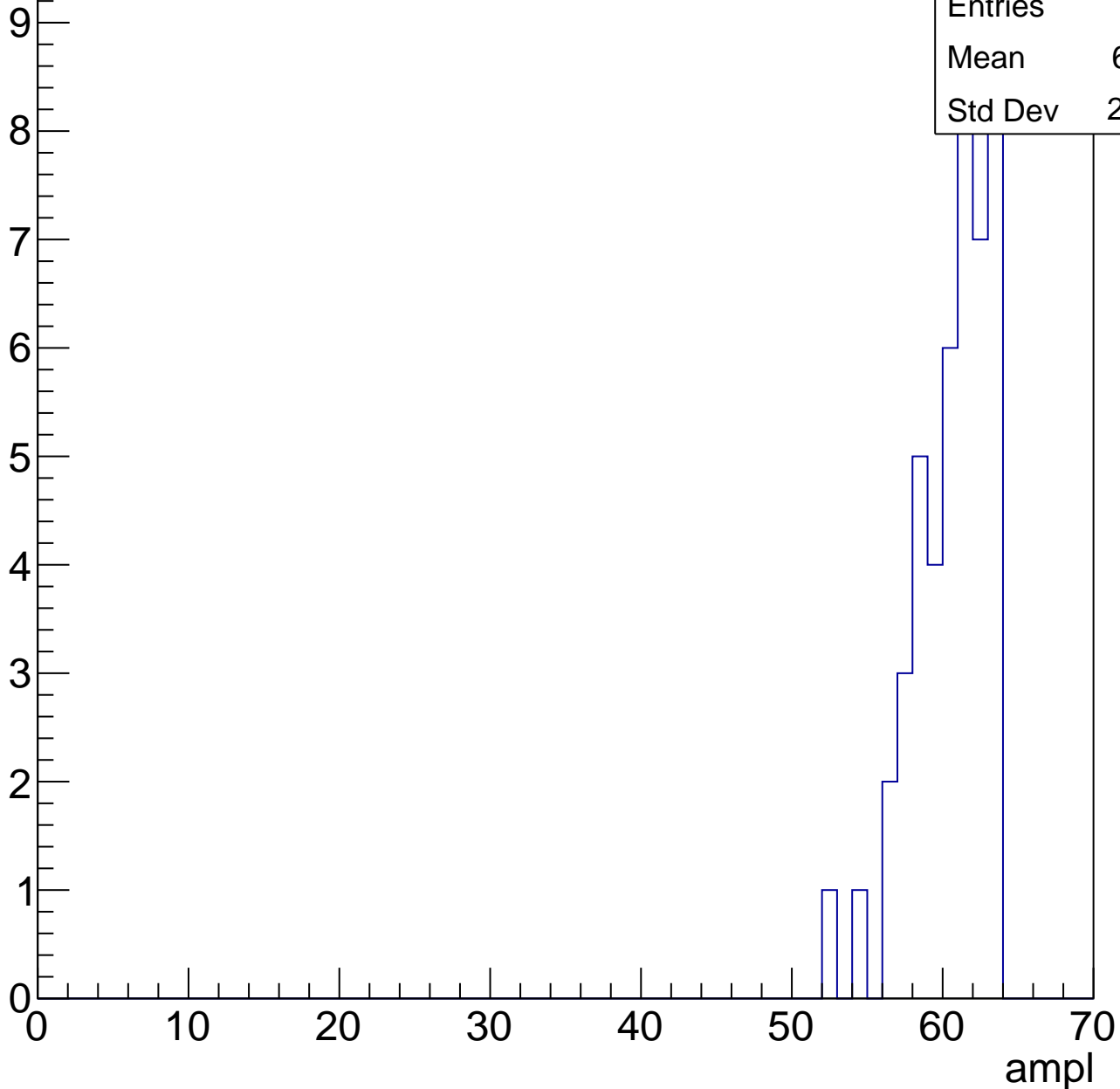


B1L003S, U16-ch32, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

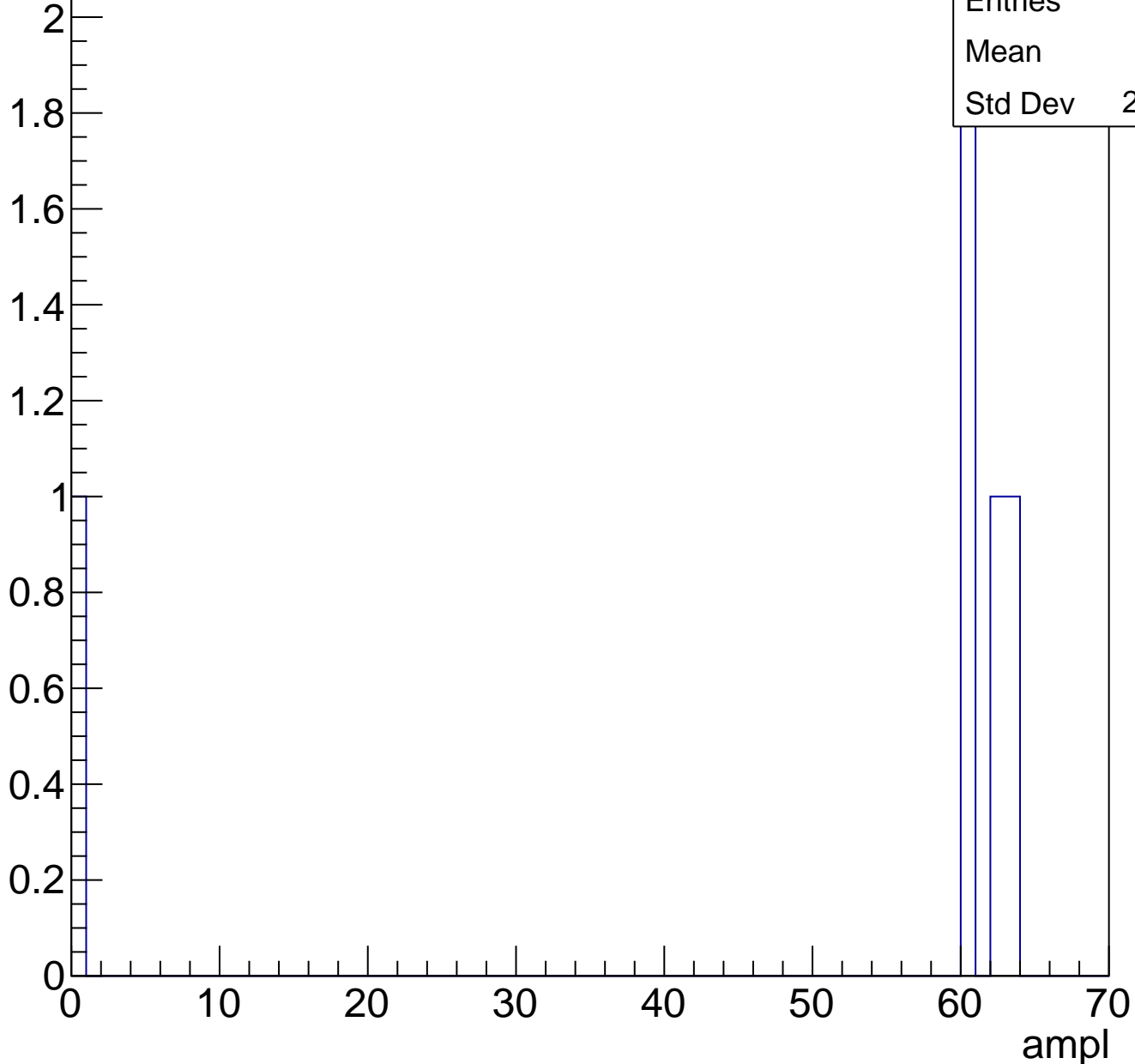
Entries	47
Mean	60.11
Std Dev	2.528



B1L003S, U16-ch32, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	49
Std Dev	24.53

B1L003S, U16-ch32, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch33, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	28.52
Std Dev	6.57

Gaus mean : 30.4733

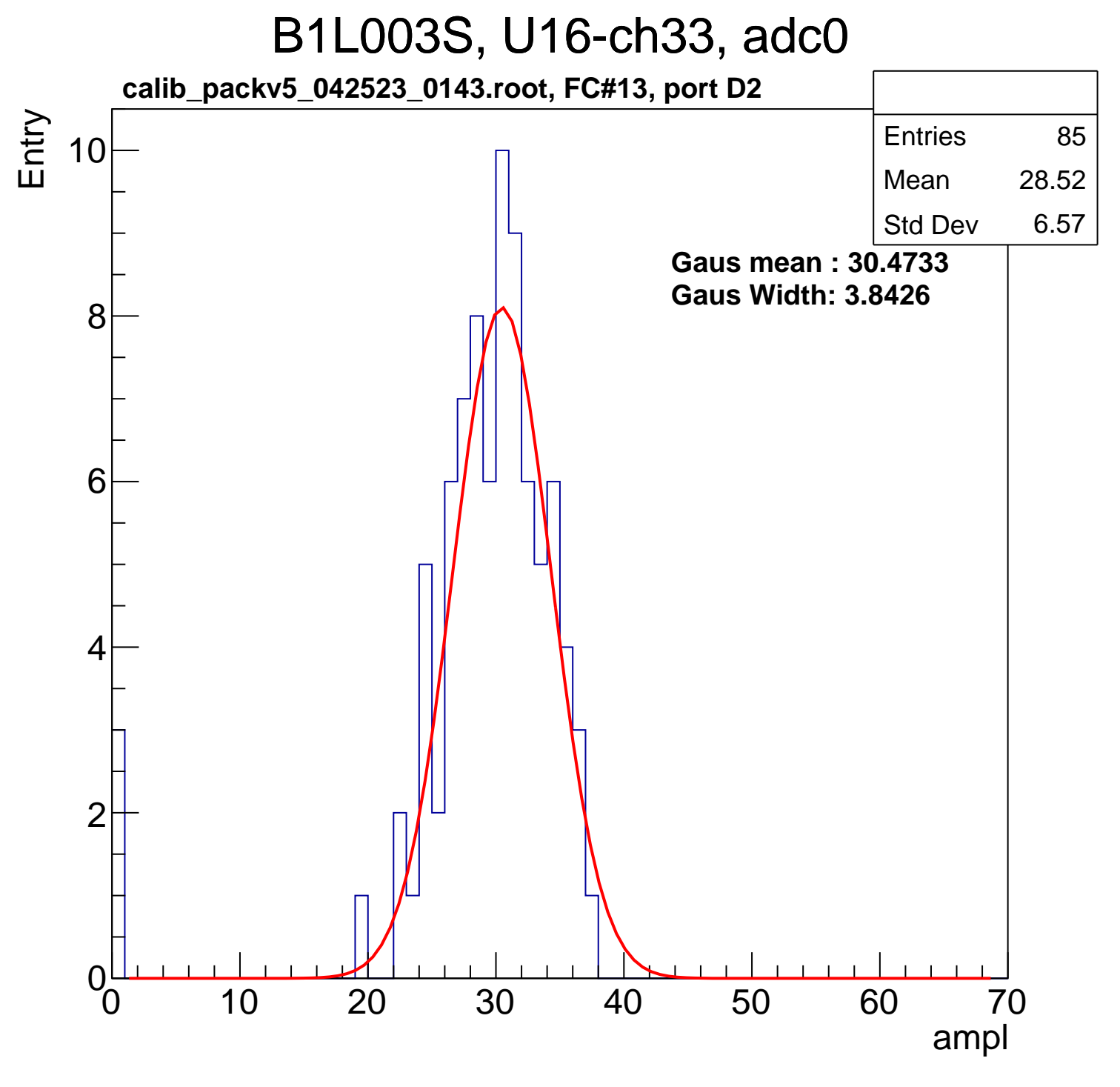
Gaus Width: 3.8426

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch33, adc1

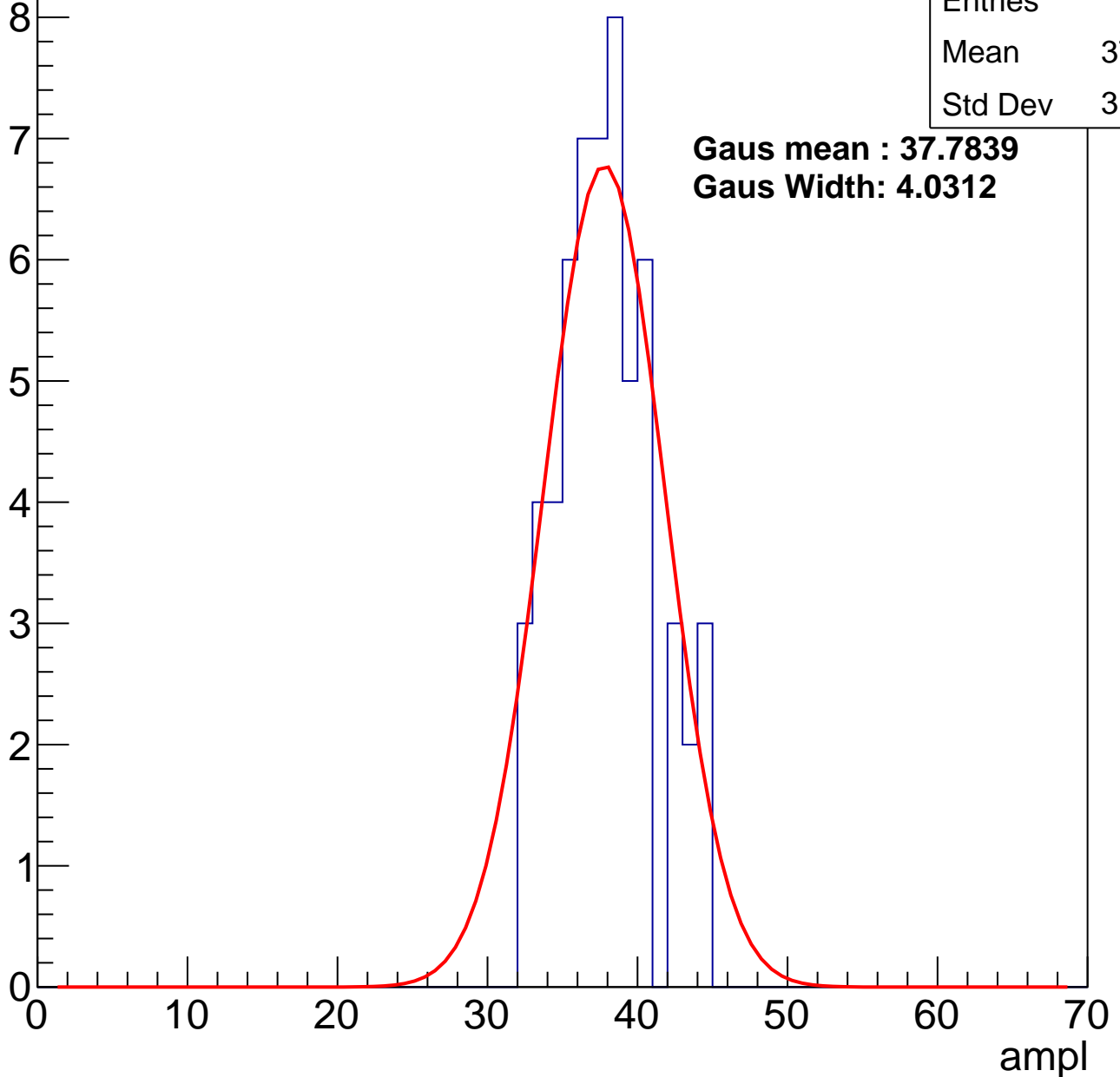
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	37.38
Std Dev	3.145

Gaus mean : 37.7839

Gaus Width: 4.0312



B1L003S, U16-ch33, adc2

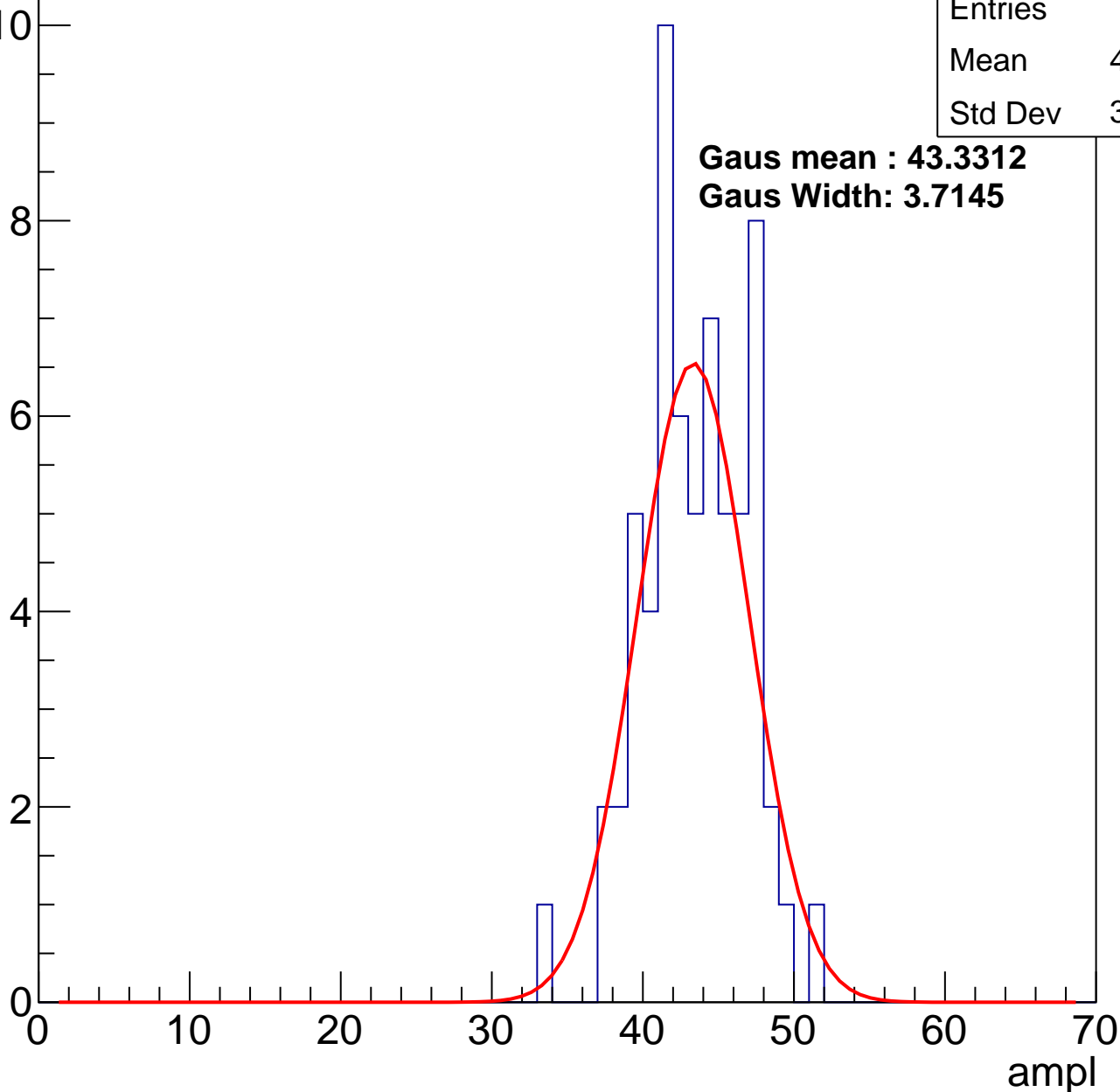
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.97
Std Dev	3.386

Gaus mean : 43.3312

Gaus Width: 3.7145

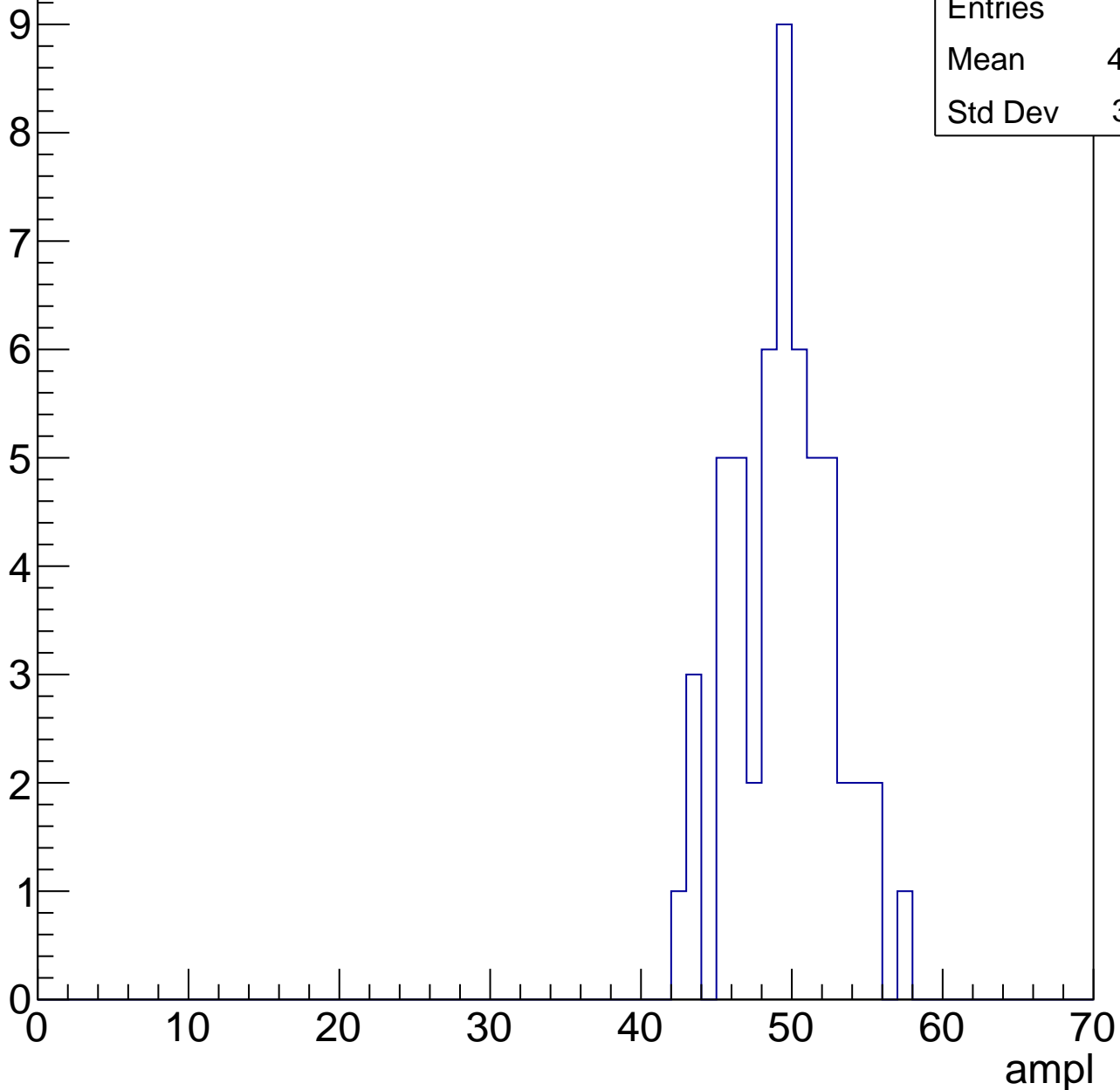


B1L003S, U16-ch33, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.98
Std Dev	3.291

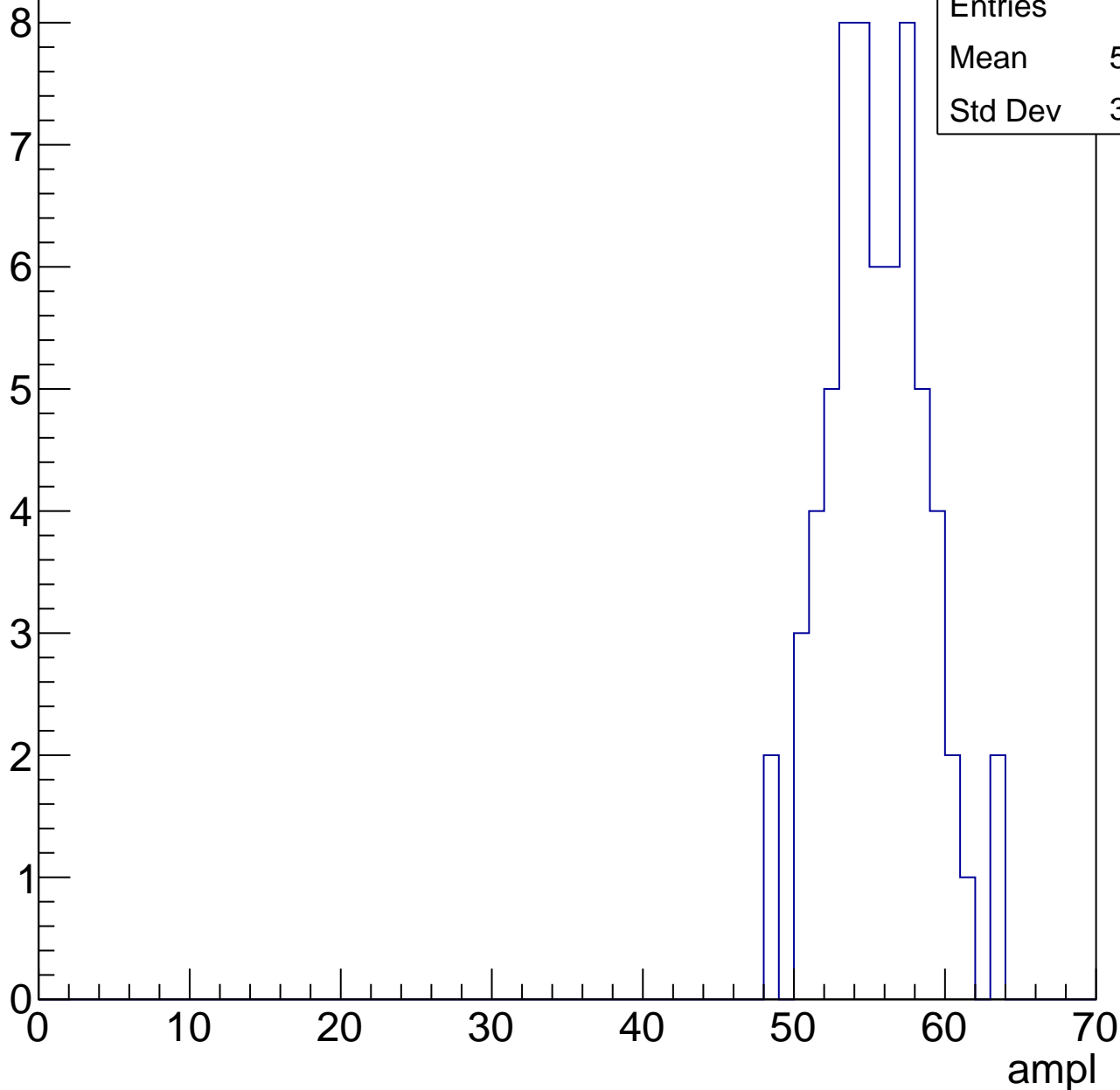


B1L003S, U16-ch33, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.02
Std Dev	3.267

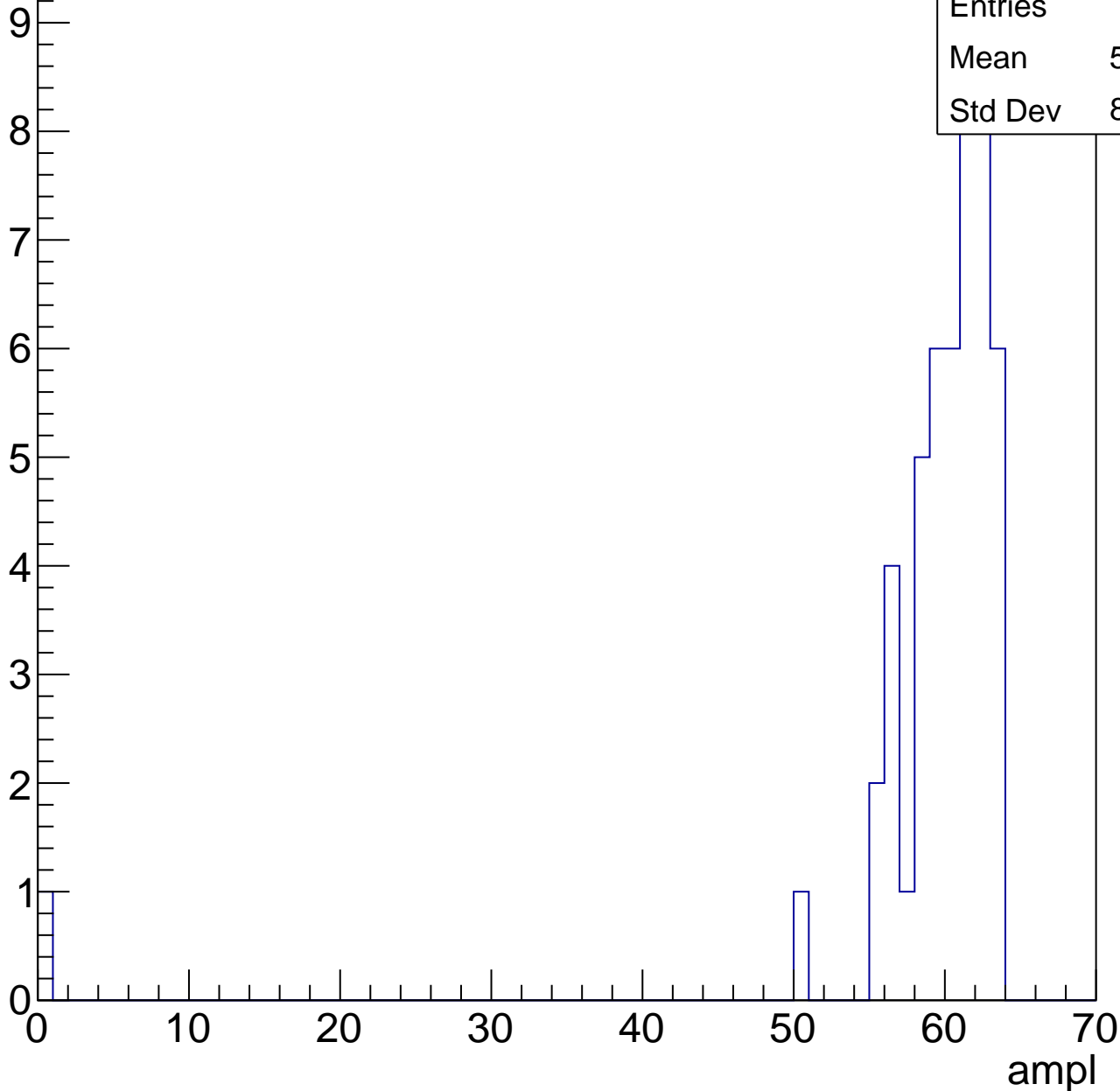


B1L003S, U16-ch33, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.55
Std Dev	8.857



B1L003S, U16-ch33, adc6

calib_packv5_042523_0143.root, FC#13, port D2

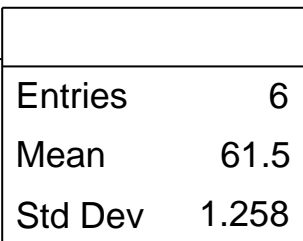
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch33, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch34, adc0

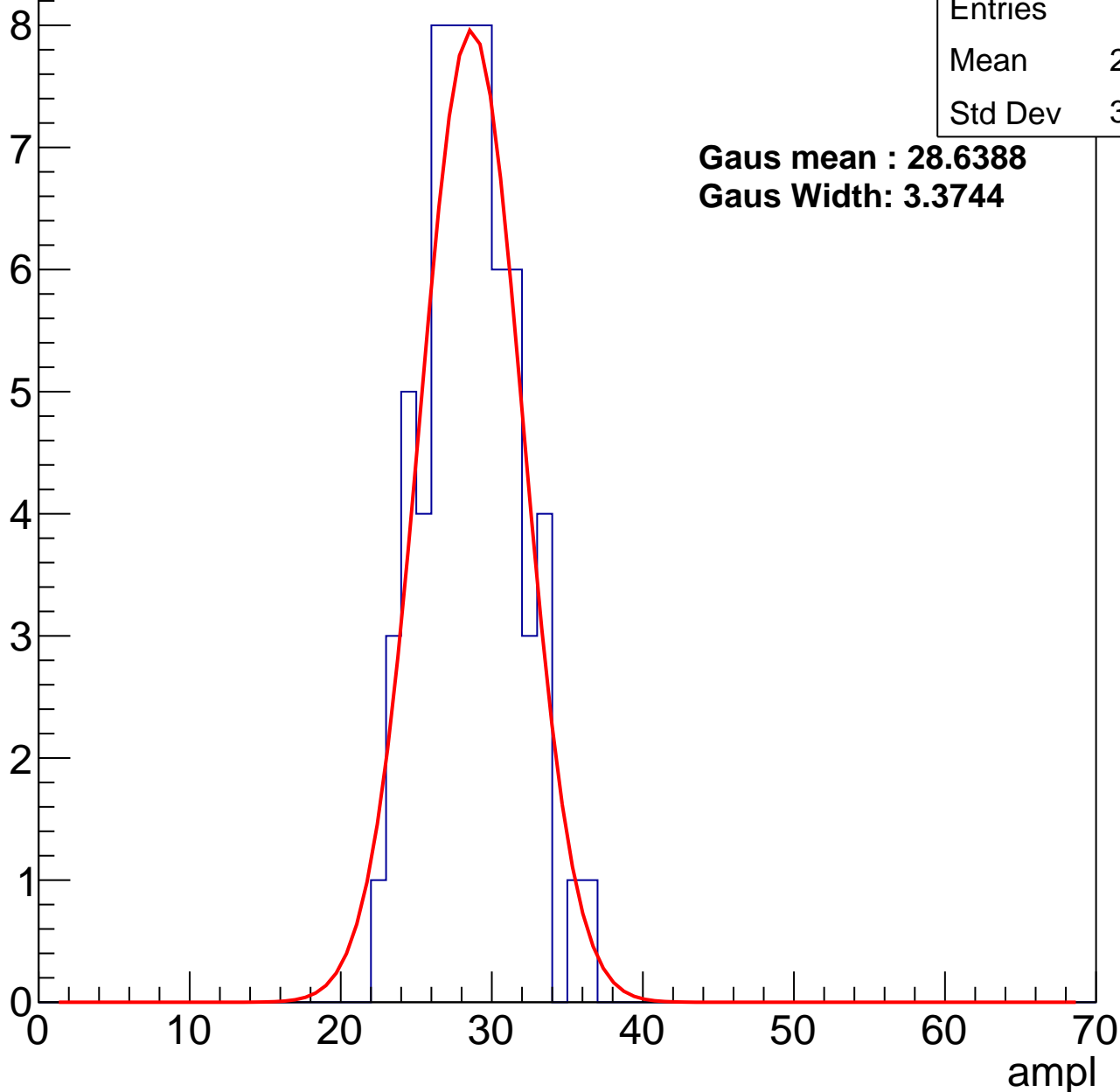
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.12
Std Dev	3.048

Gaus mean : 28.6388

Gaus Width: 3.3744



B1L003S, U16-ch34, adc1

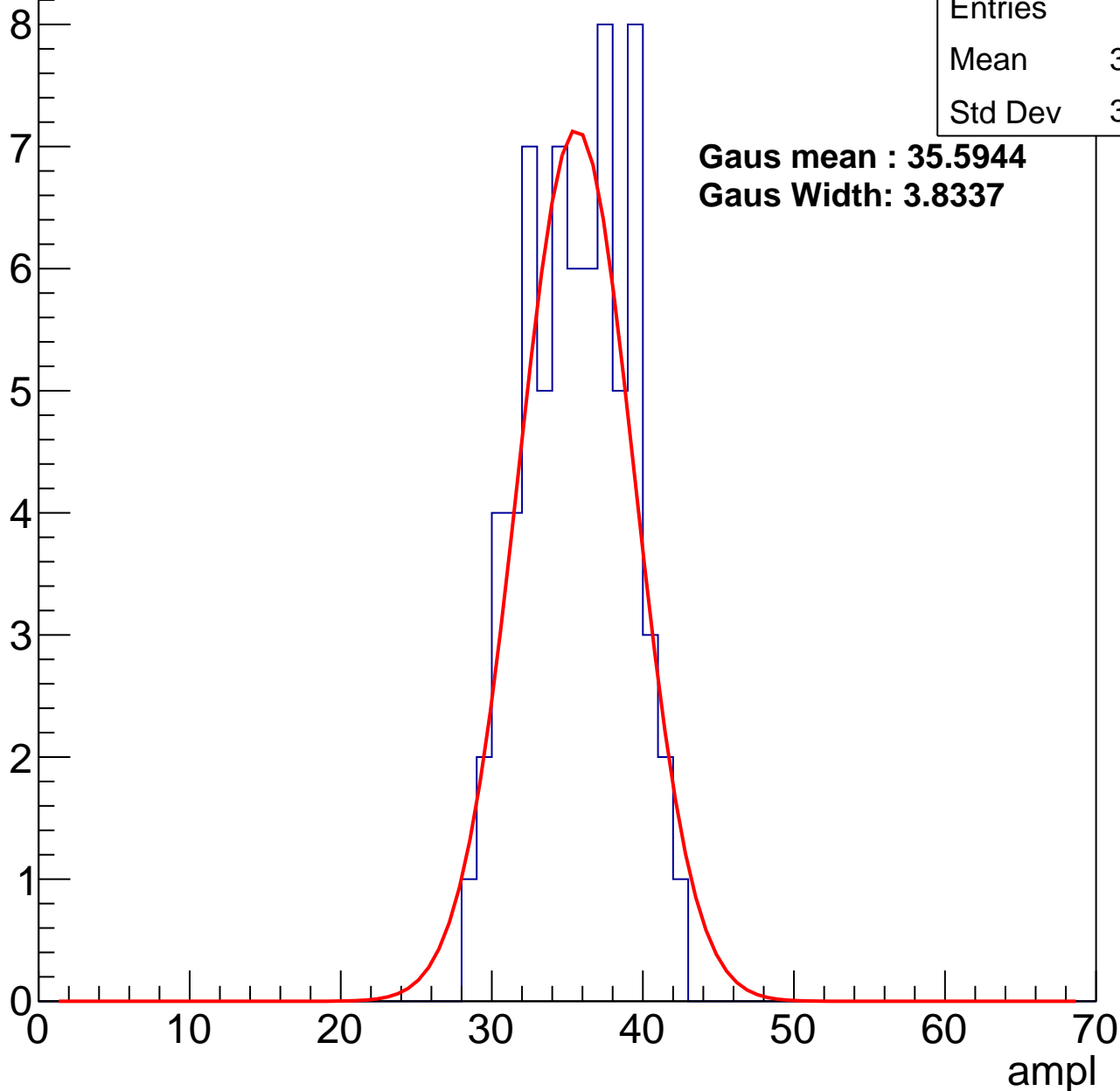
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.14
Std Dev	3.363

Gaus mean : 35.5944

Gaus Width: 3.8337



B1L003S, U16-ch34, adc2

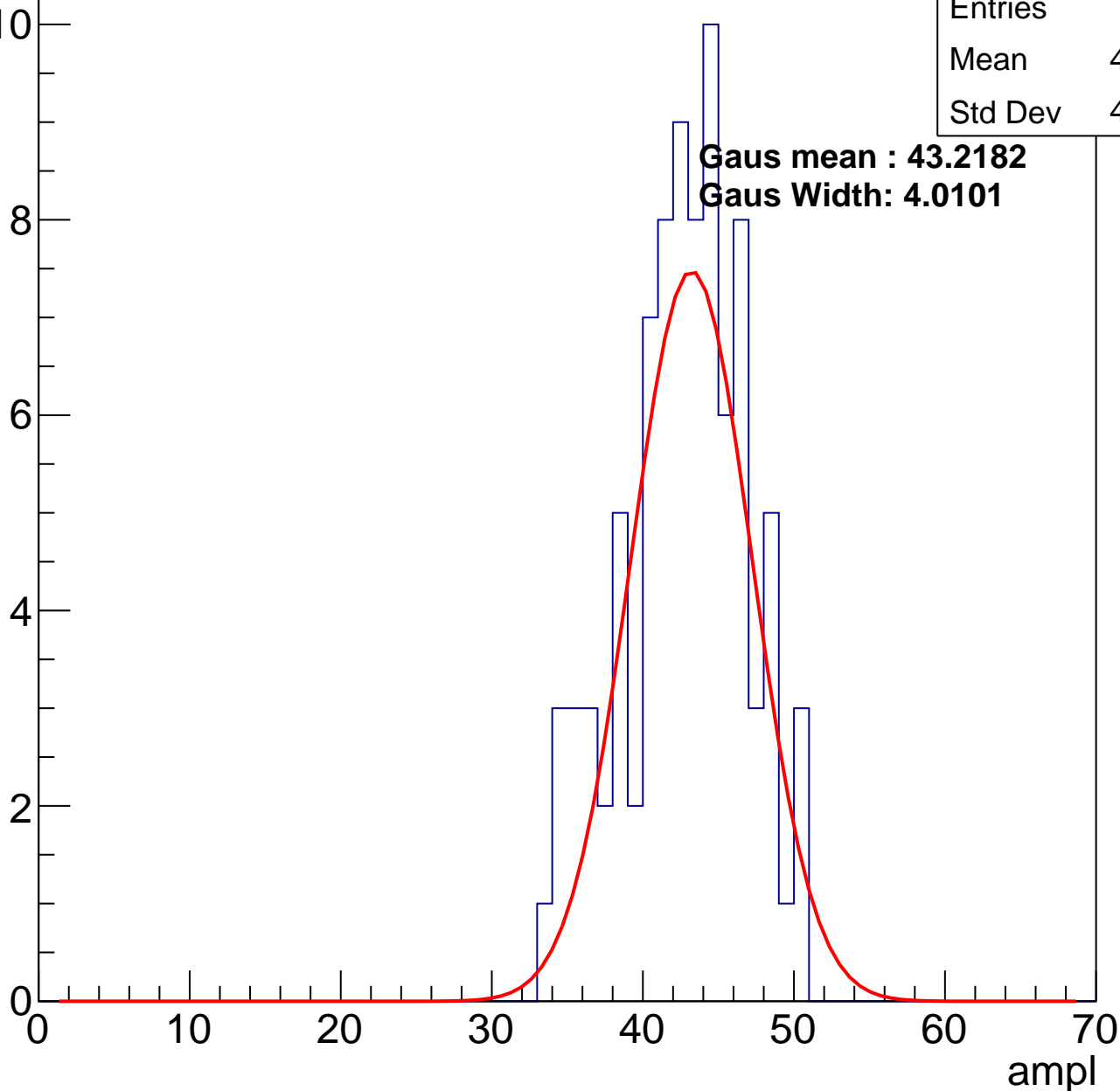
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	87
Mean	42.28
Std Dev	4.056

Gaus mean : 43.2182

Gaus Width: 4.0101

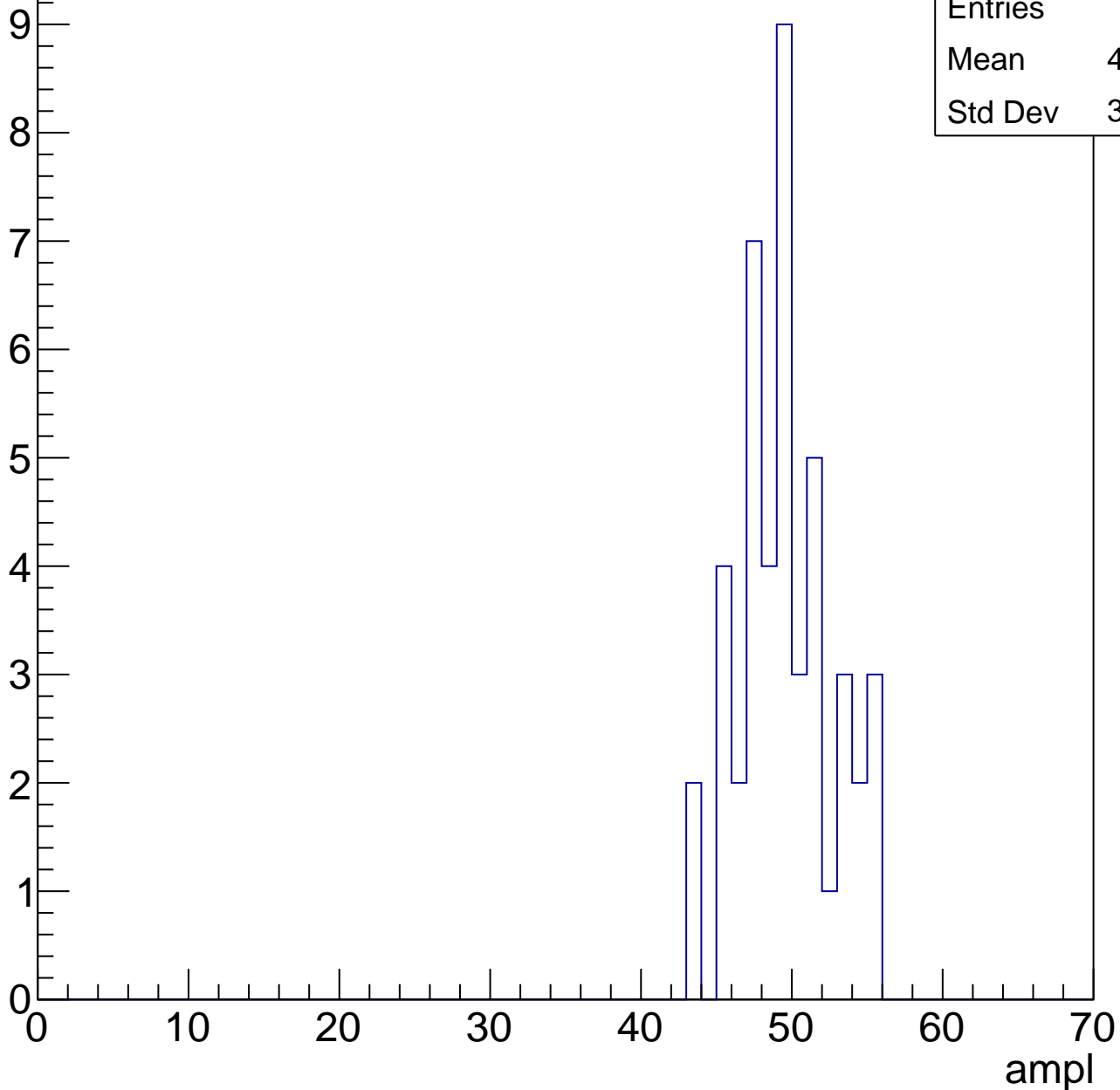


B1L003S, U16-ch34, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	49.09
Std Dev	3.068

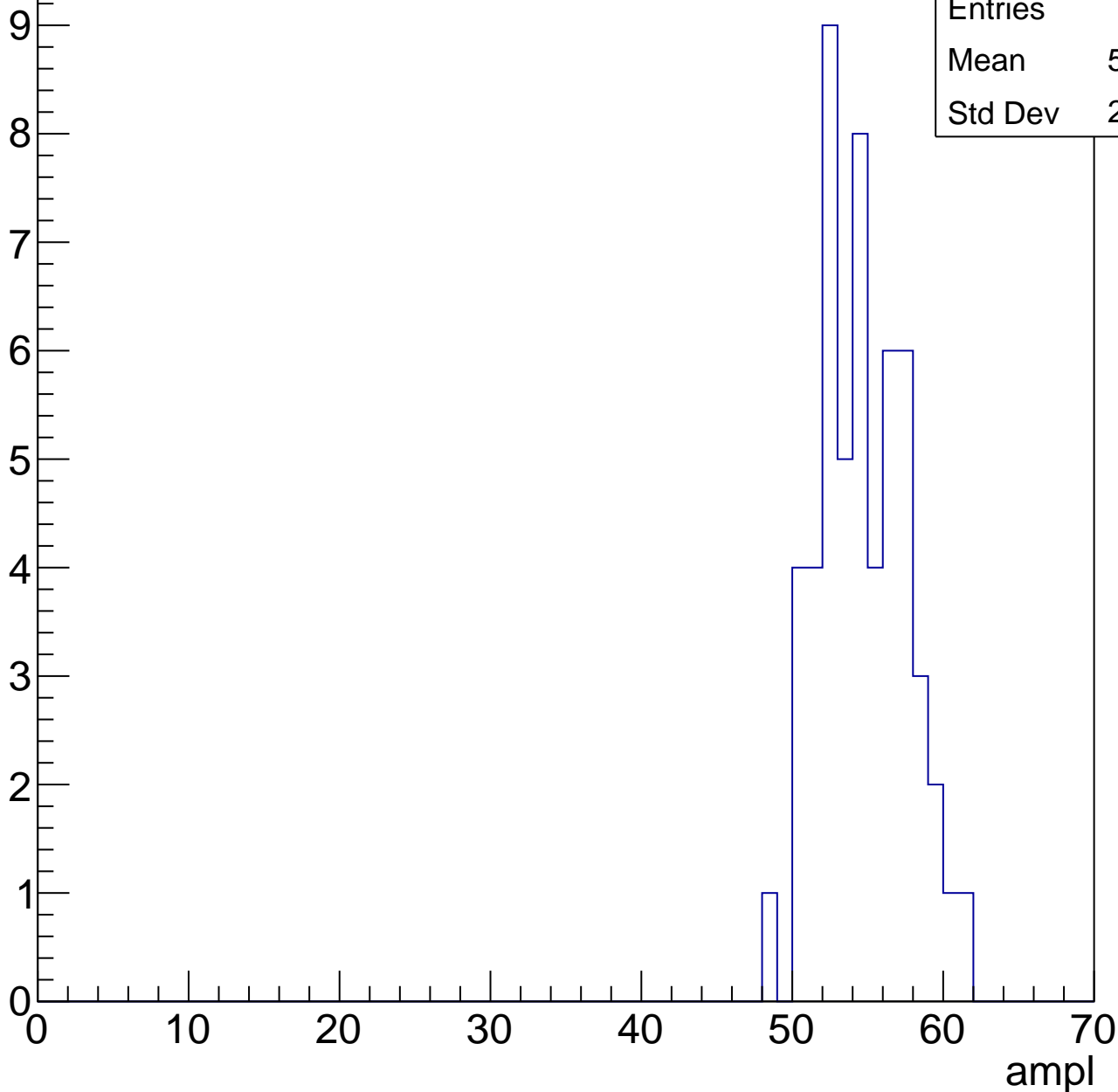


B1L003S, U16-ch34, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	54.22
Std Dev	2.852



B1L003S, U16-ch34, adc5

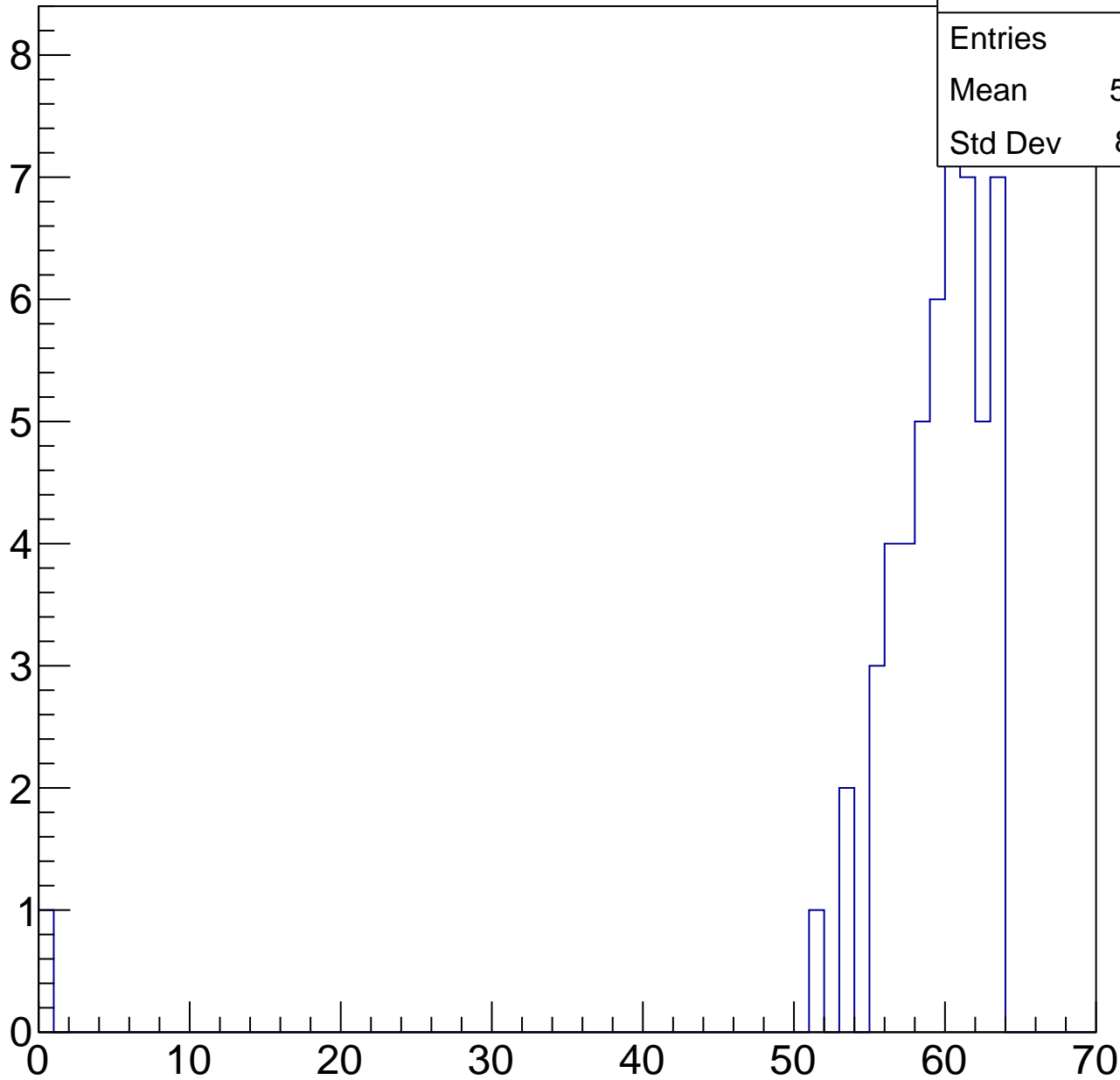
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.04
Std Dev	8.541

ampl

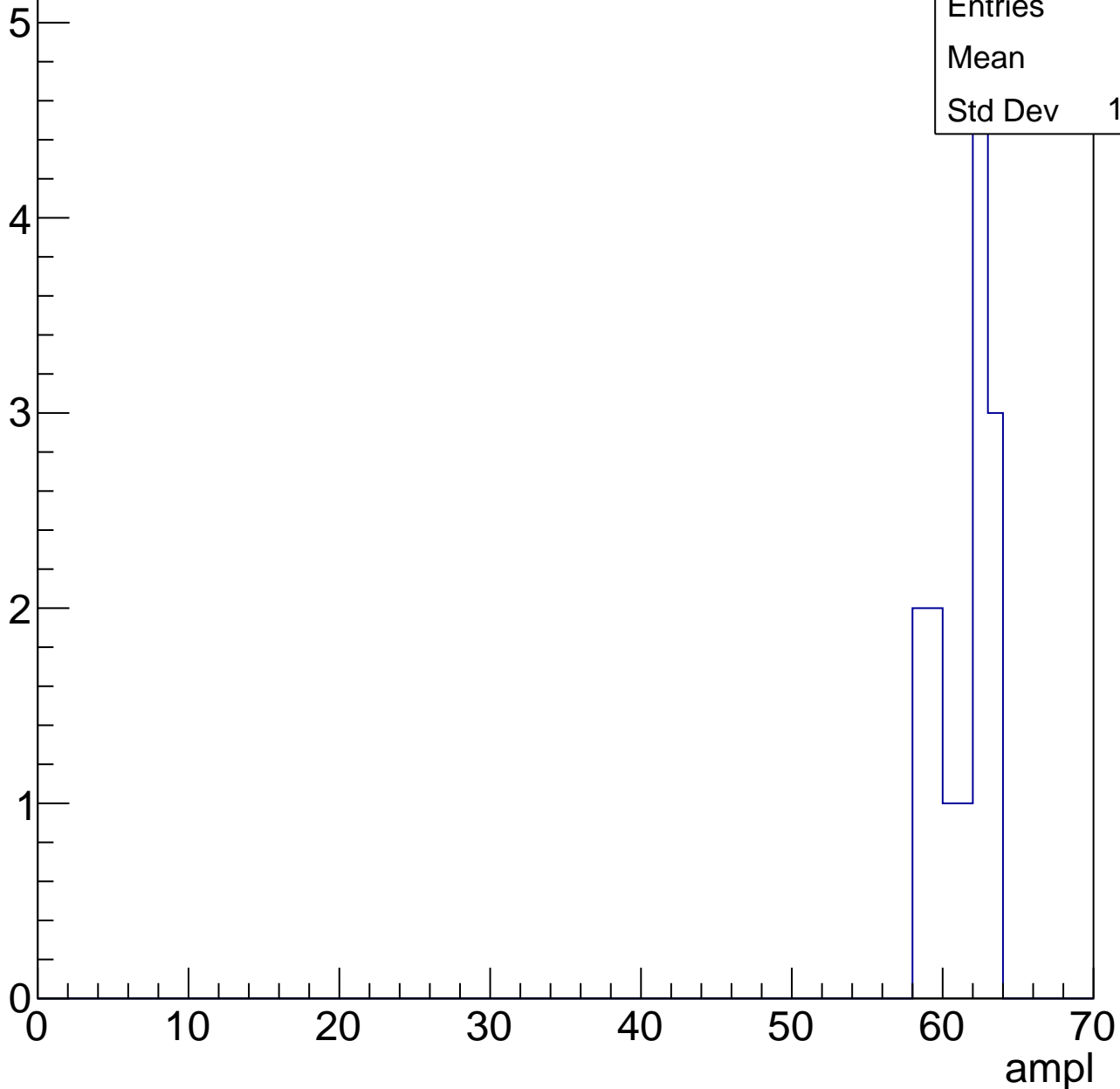


B1L003S, U16-ch34, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

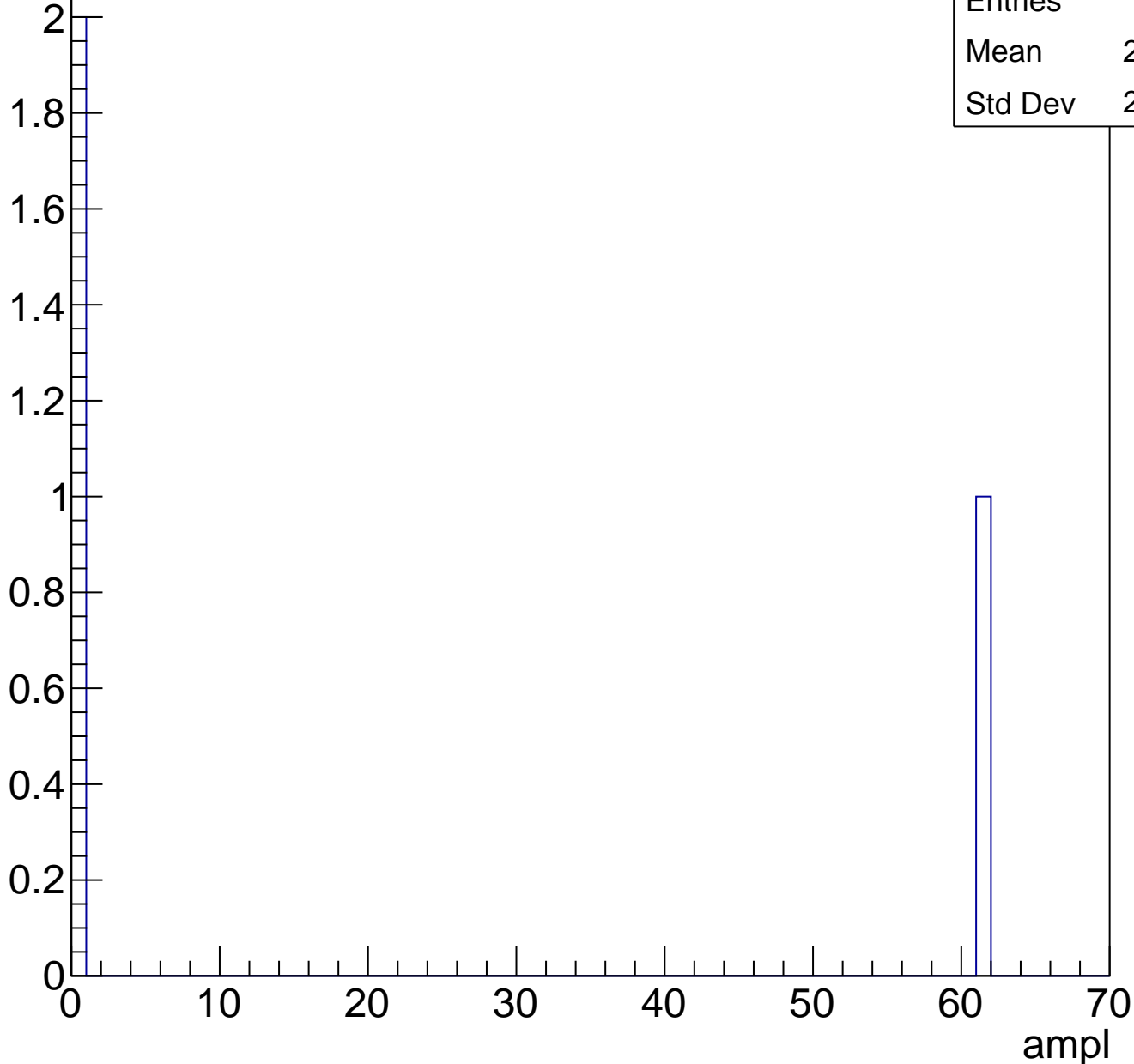
Entries	14
Mean	61
Std Dev	1.773



B1L003S, U16-ch34, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch35, adc0

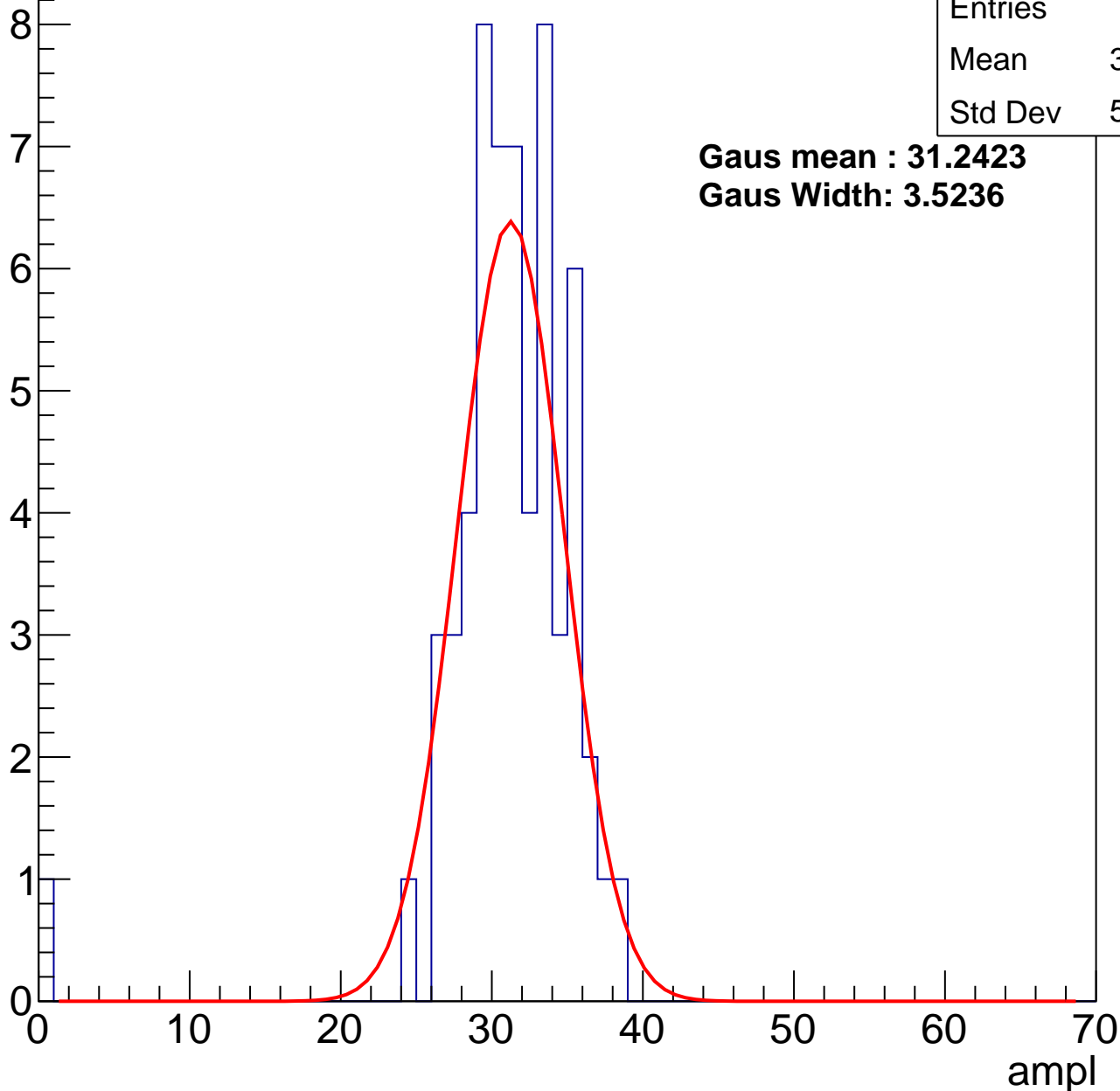
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	30.59
Std Dev	5.029

Gaus mean : 31.2423

Gaus Width: 3.5236



B1L003S, U16-ch35, adc1

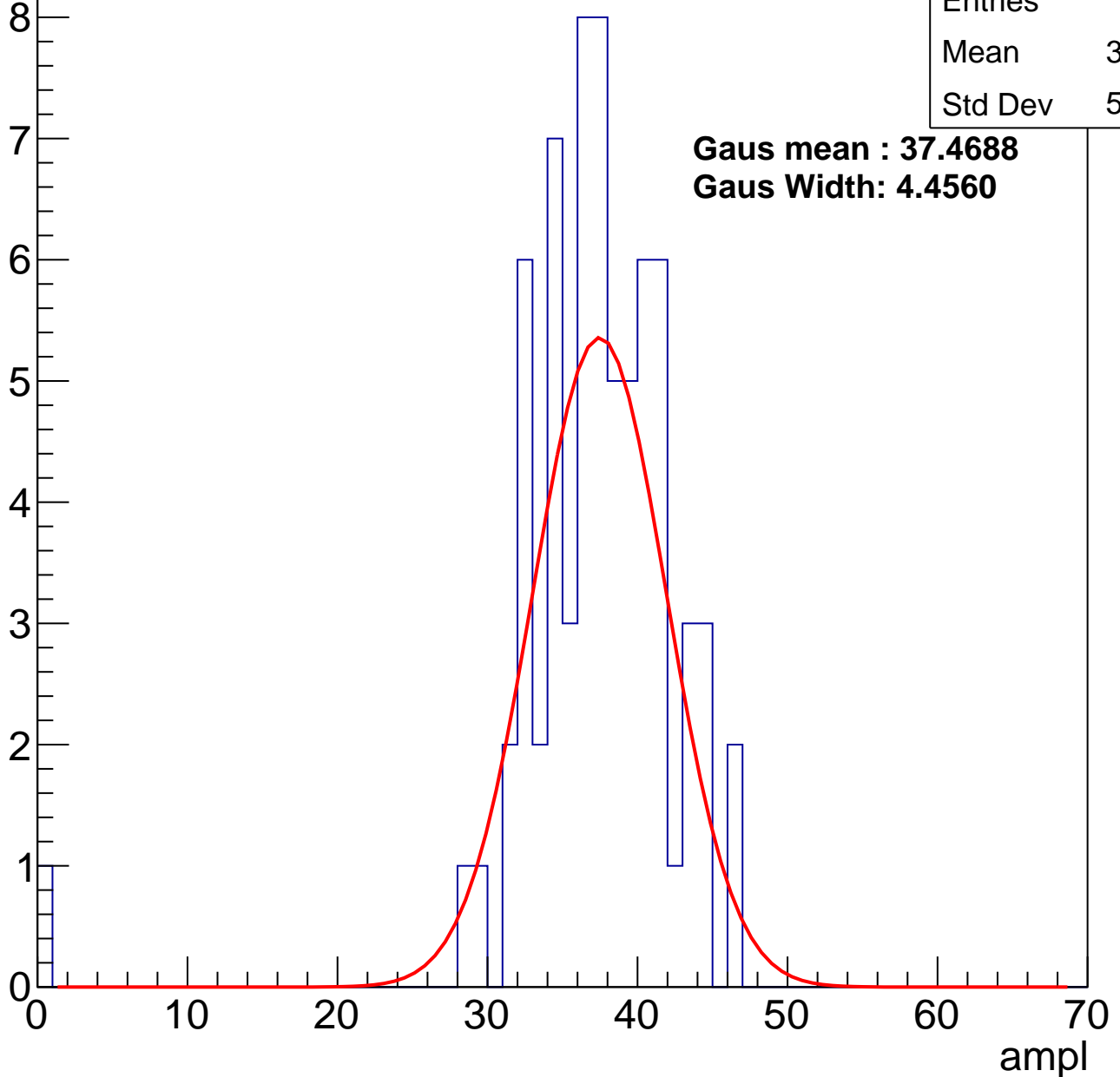
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.71
Std Dev	5.931

Gaus mean : 37.4688

Gaus Width: 4.4560



B1L003S, U16-ch35, adc2

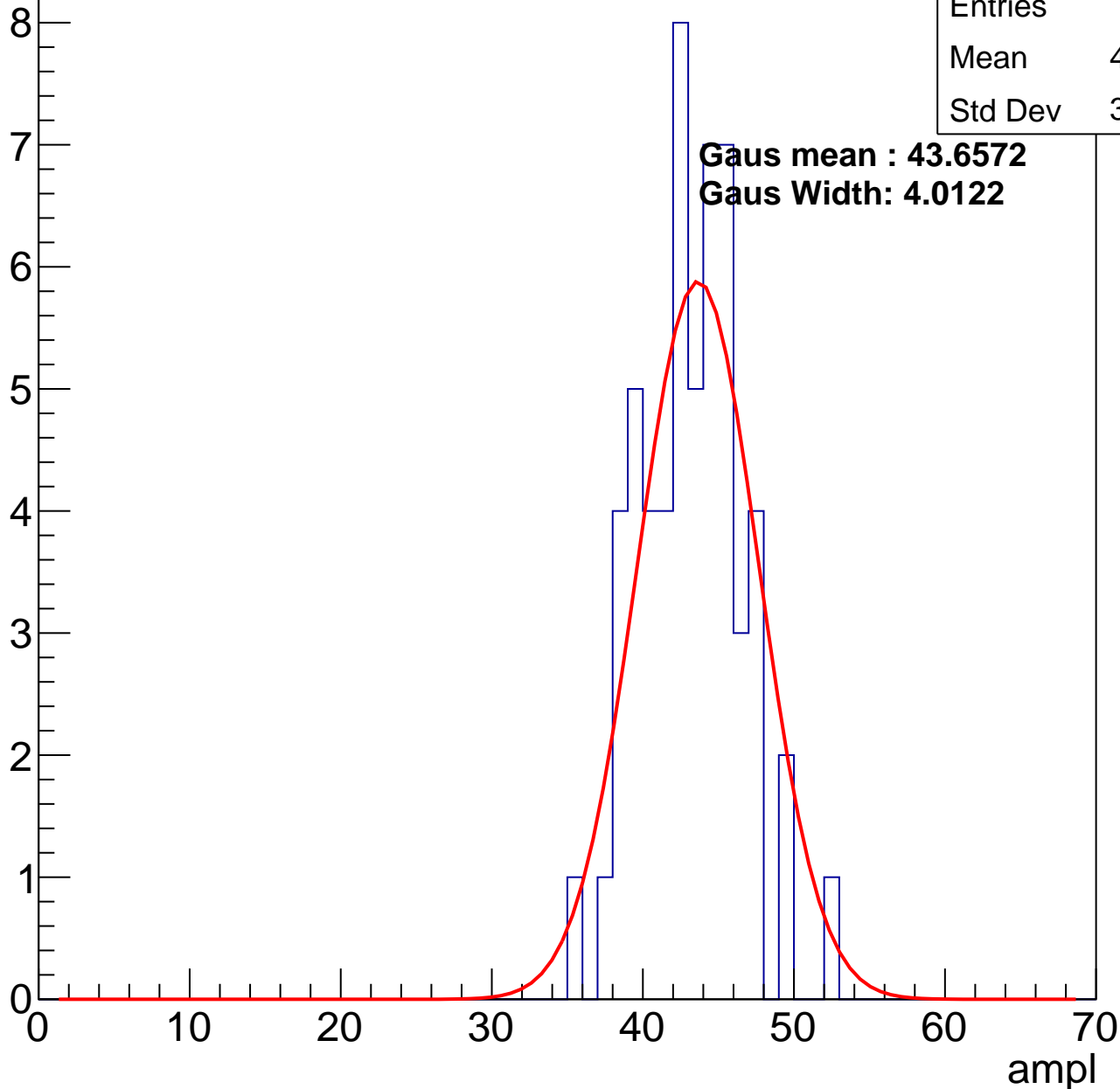
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.73
Std Dev	3.314

Gaus mean : 43.6572

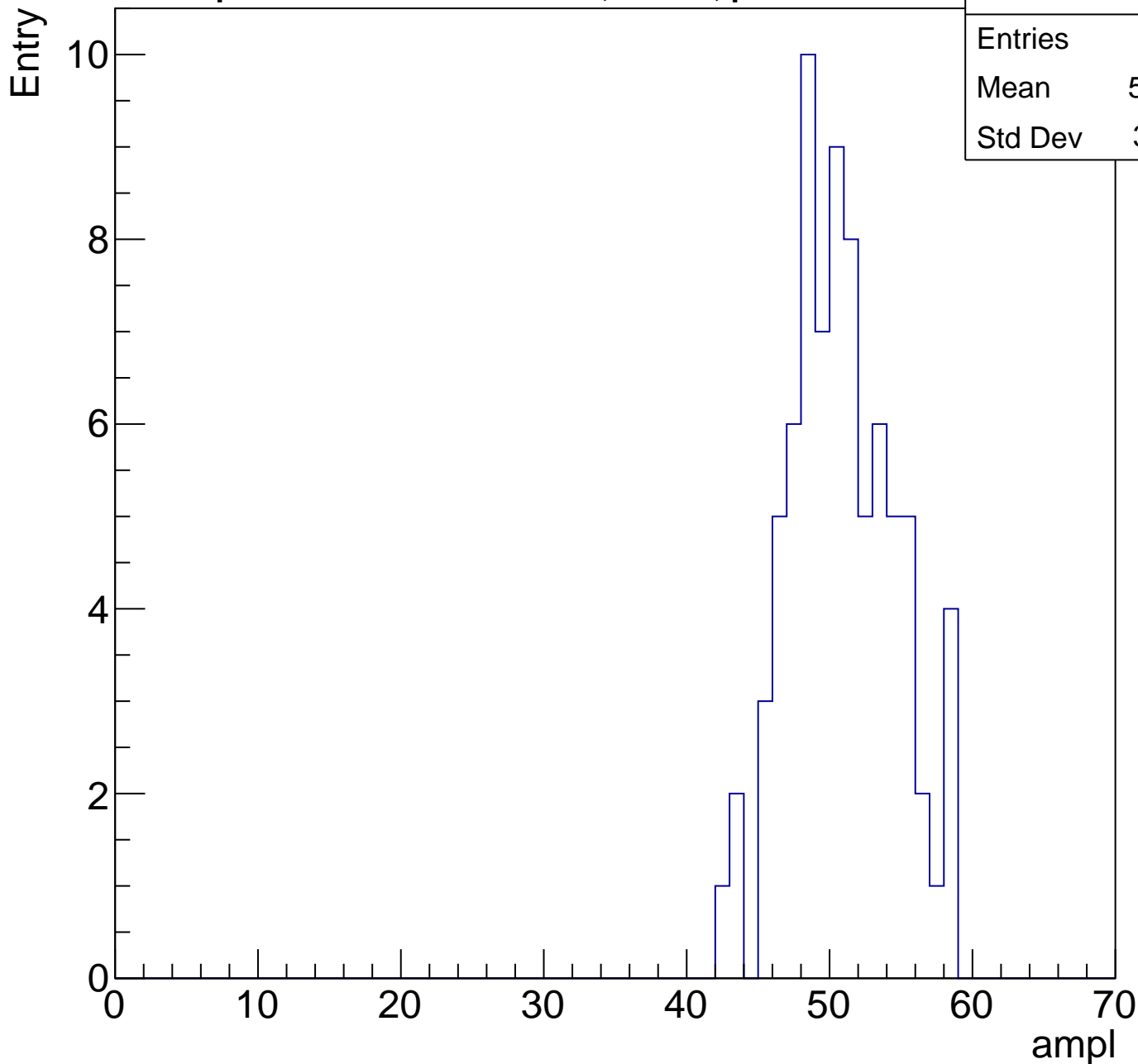
Gaus Width: 4.0122



B1L003S, U16-ch35, adc3

calib_packv5_042523_0143.root, FC#13, port D2

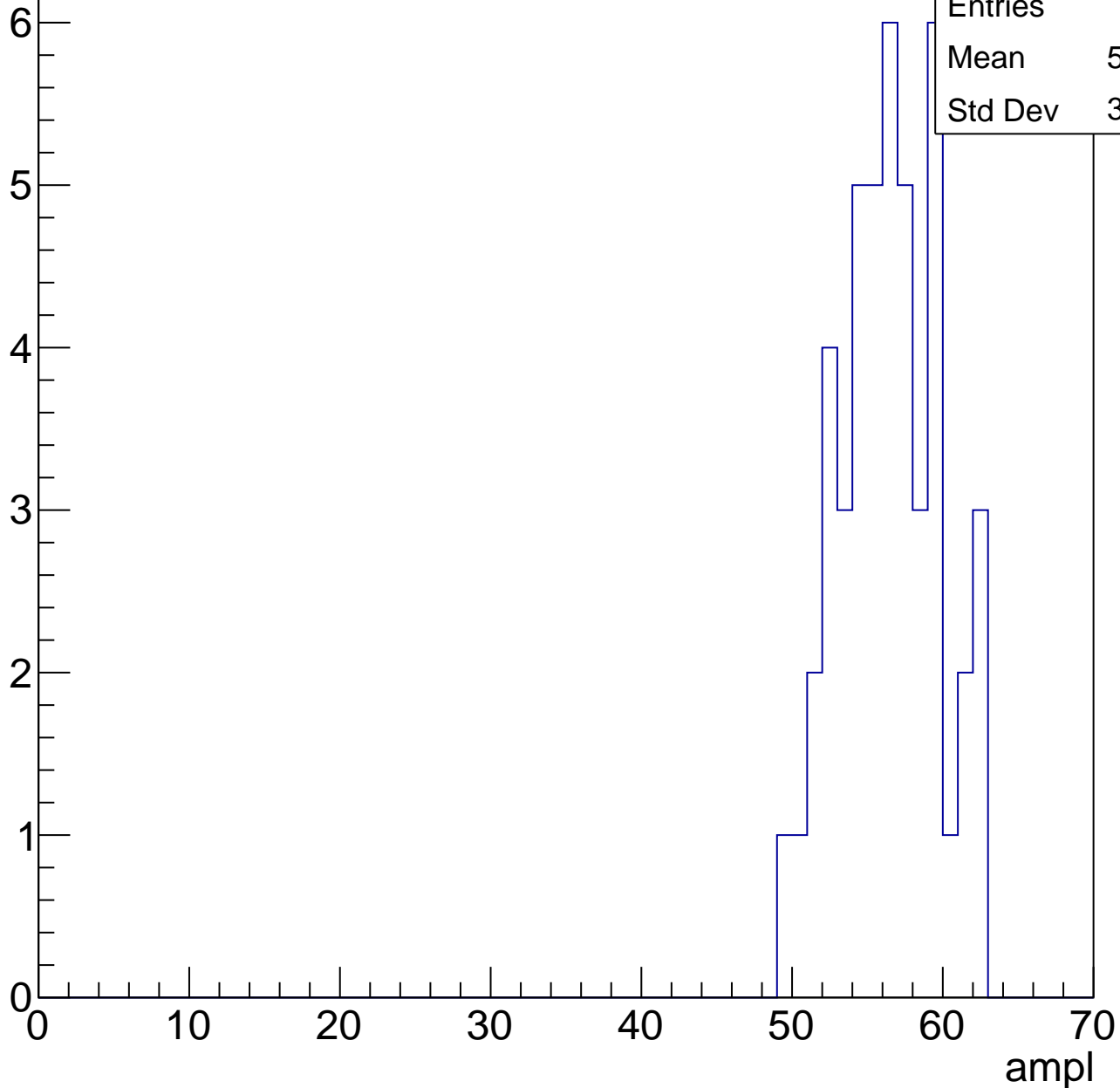
Entries	79
Mean	50.38
Std Dev	3.681



B1L003S, U16-ch35, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

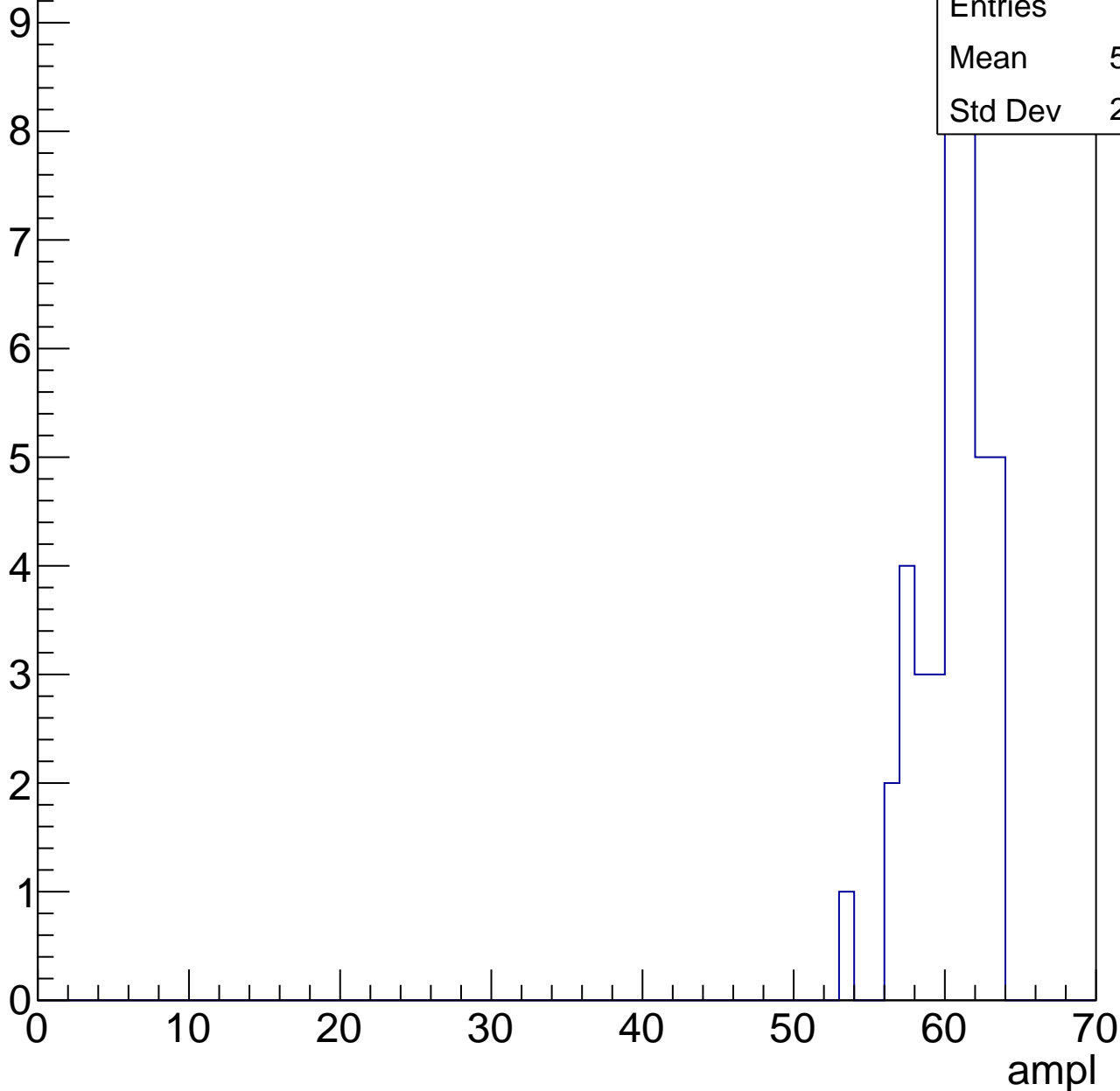


B1L003S, U16-ch35, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	59.92
Std Dev	2.263

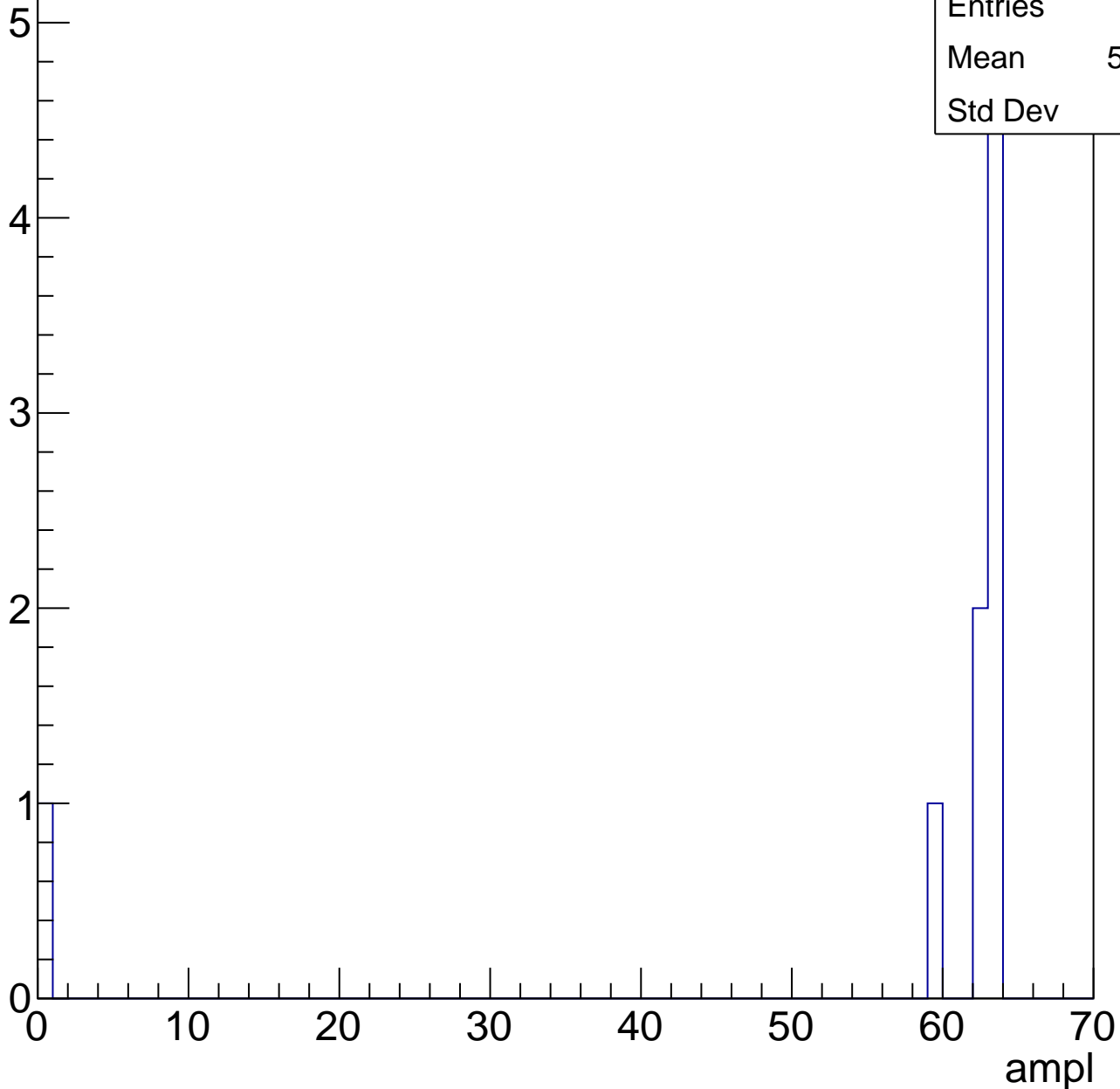


B1L003S, U16-ch35, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	55.33
Std Dev	19.6



B1L003S, U16-ch35, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch36, adc0

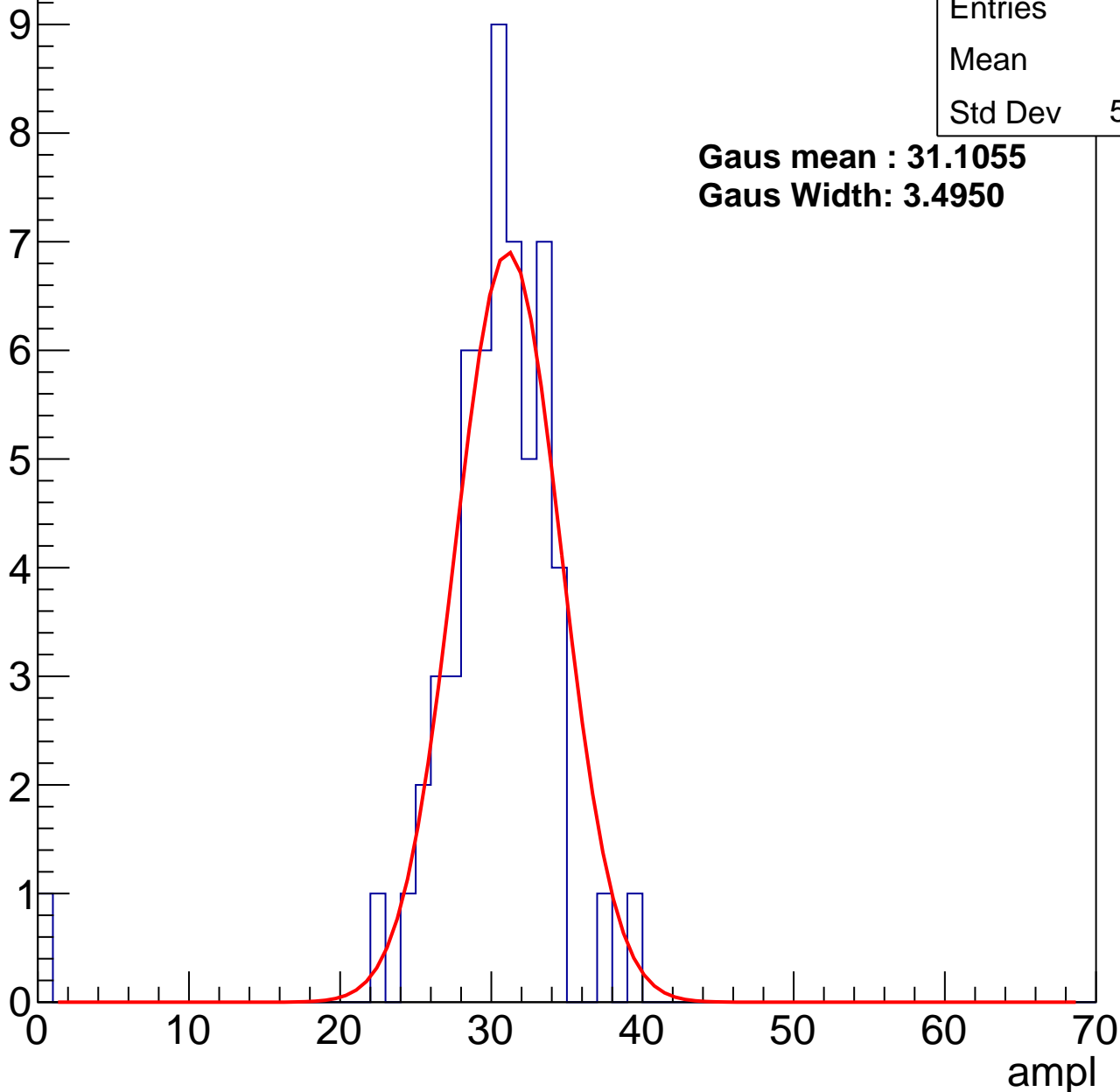
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.6
Std Dev	5.008

Gaus mean : 31.1055

Gaus Width: 3.4950



B1L003S, U16-ch36, adc1

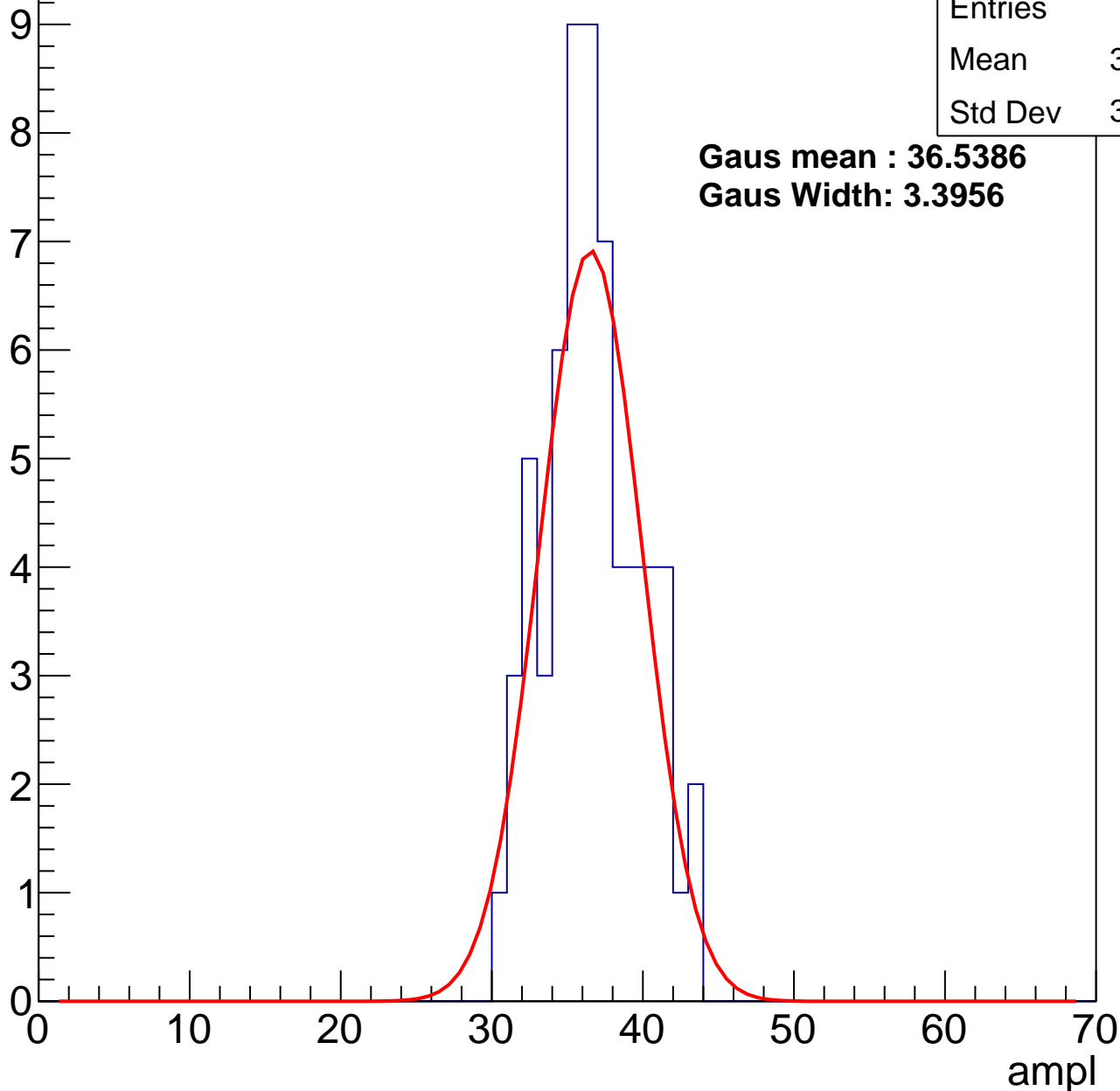
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.19
Std Dev	3.126

Gaus mean : 36.5386

Gaus Width: 3.3956



B1L003S, U16-ch36, adc2

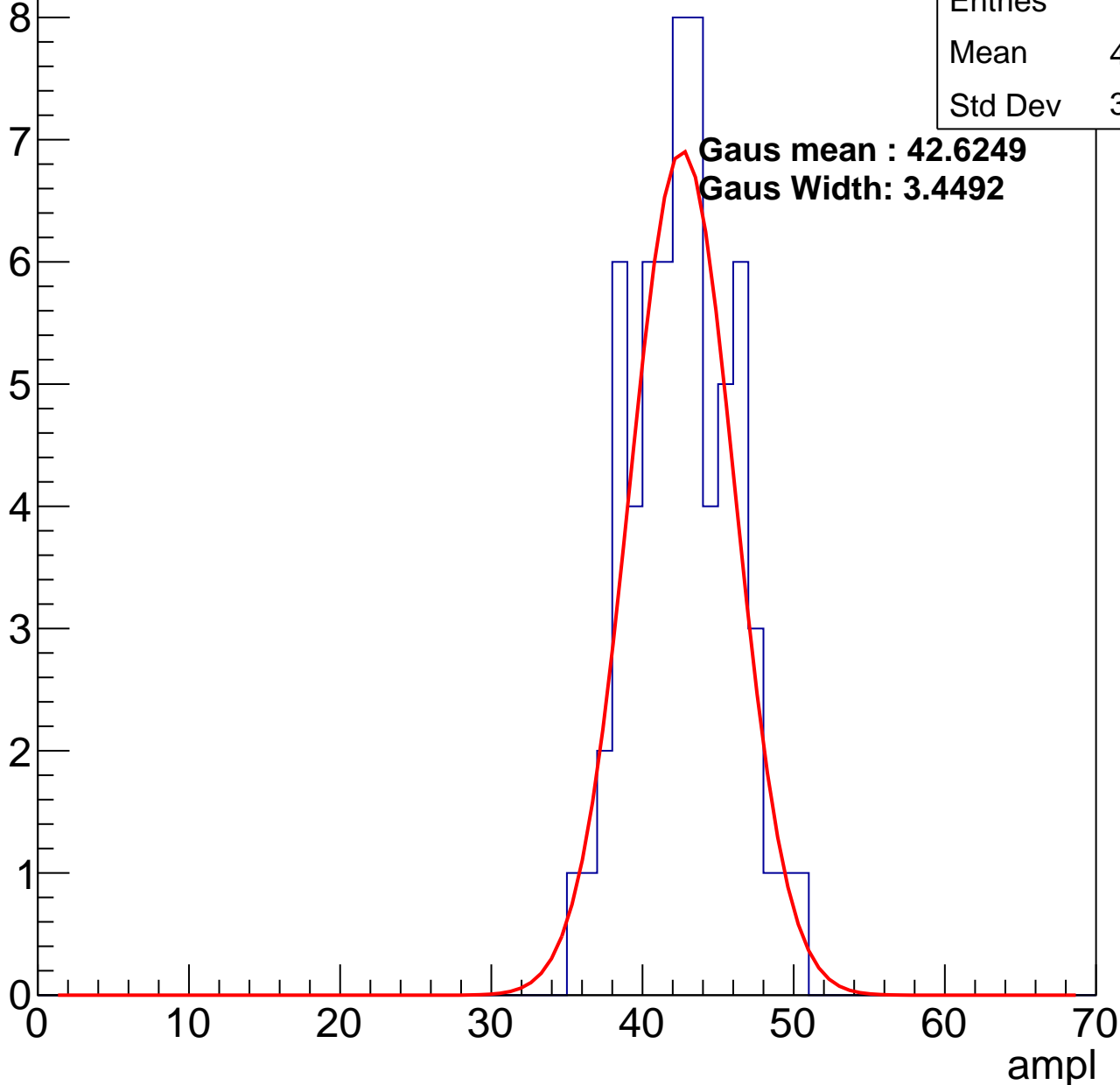
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.22
Std Dev	3.292

Gaus mean : 42.6249

Gaus Width: 3.4492

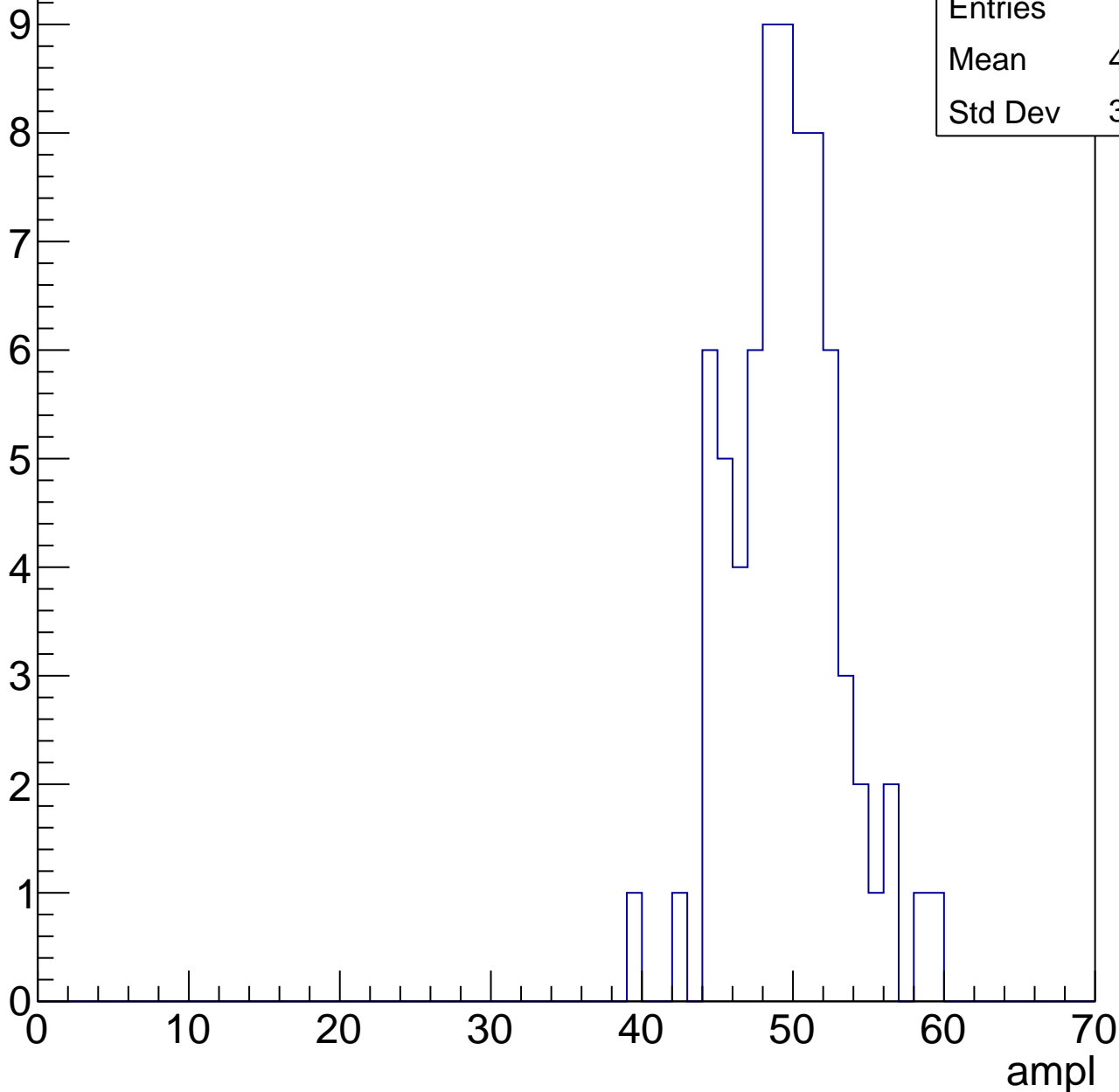


B1L003S, U16-ch36, adc3

calib_packv5_042523_0143.root, FC#13, port D2

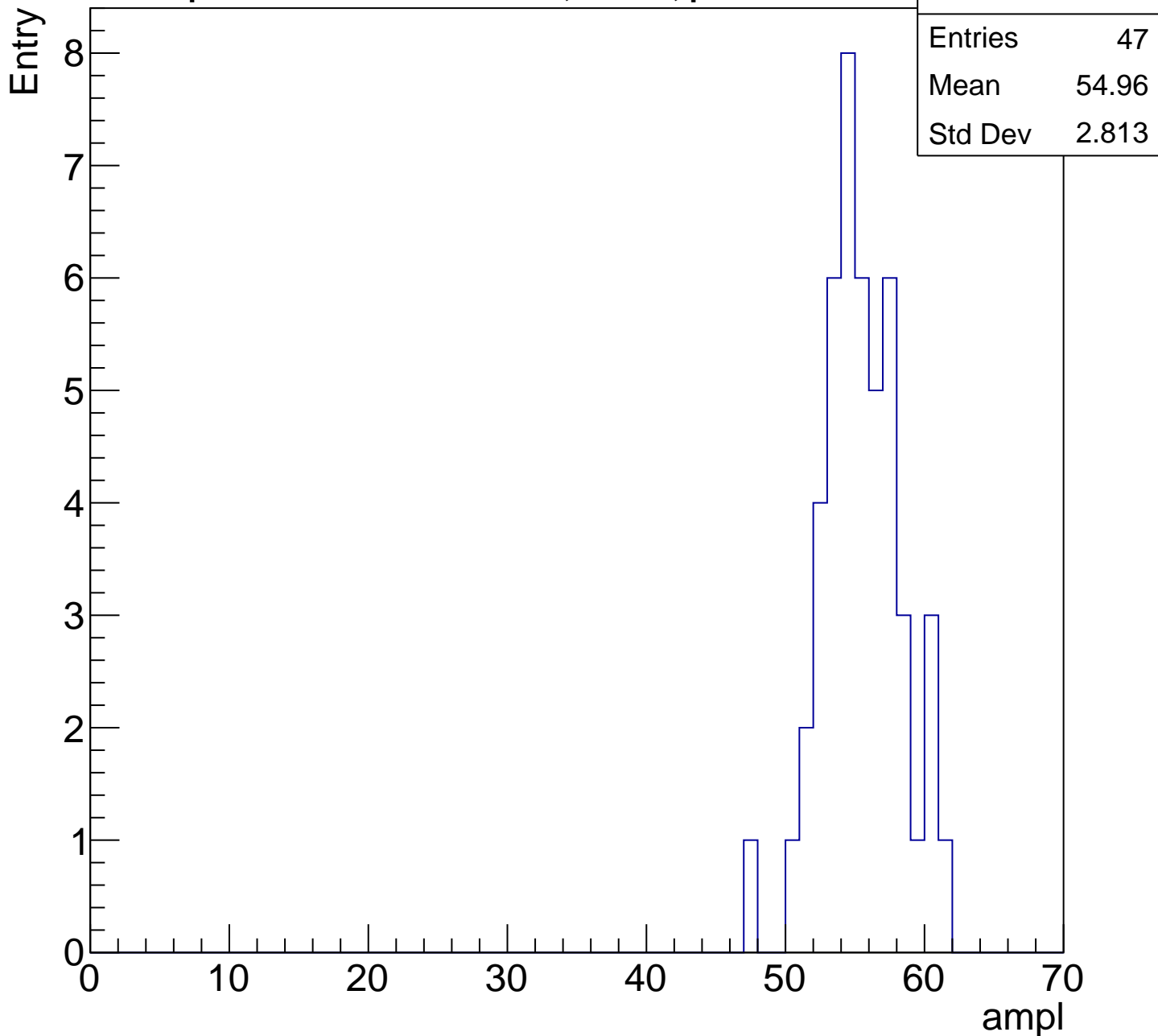
Entry

Entries	73
Mean	49.04
Std Dev	3.617



B1L003S, U16-ch36, adc4

calib_packv5_042523_0143.root, FC#13, port D2

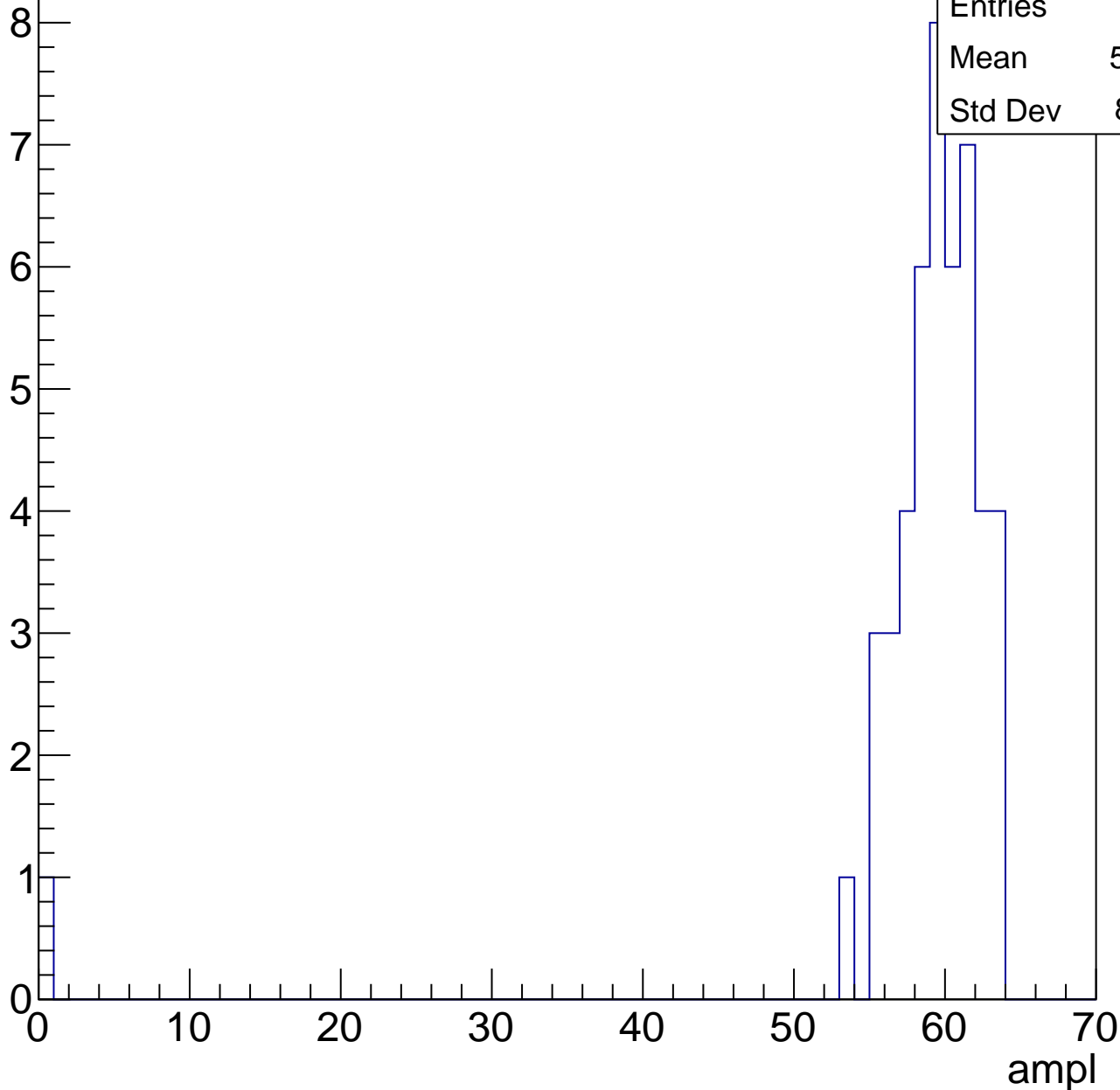


B1L003S, U16-ch36, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	57.89
Std Dev	8.861

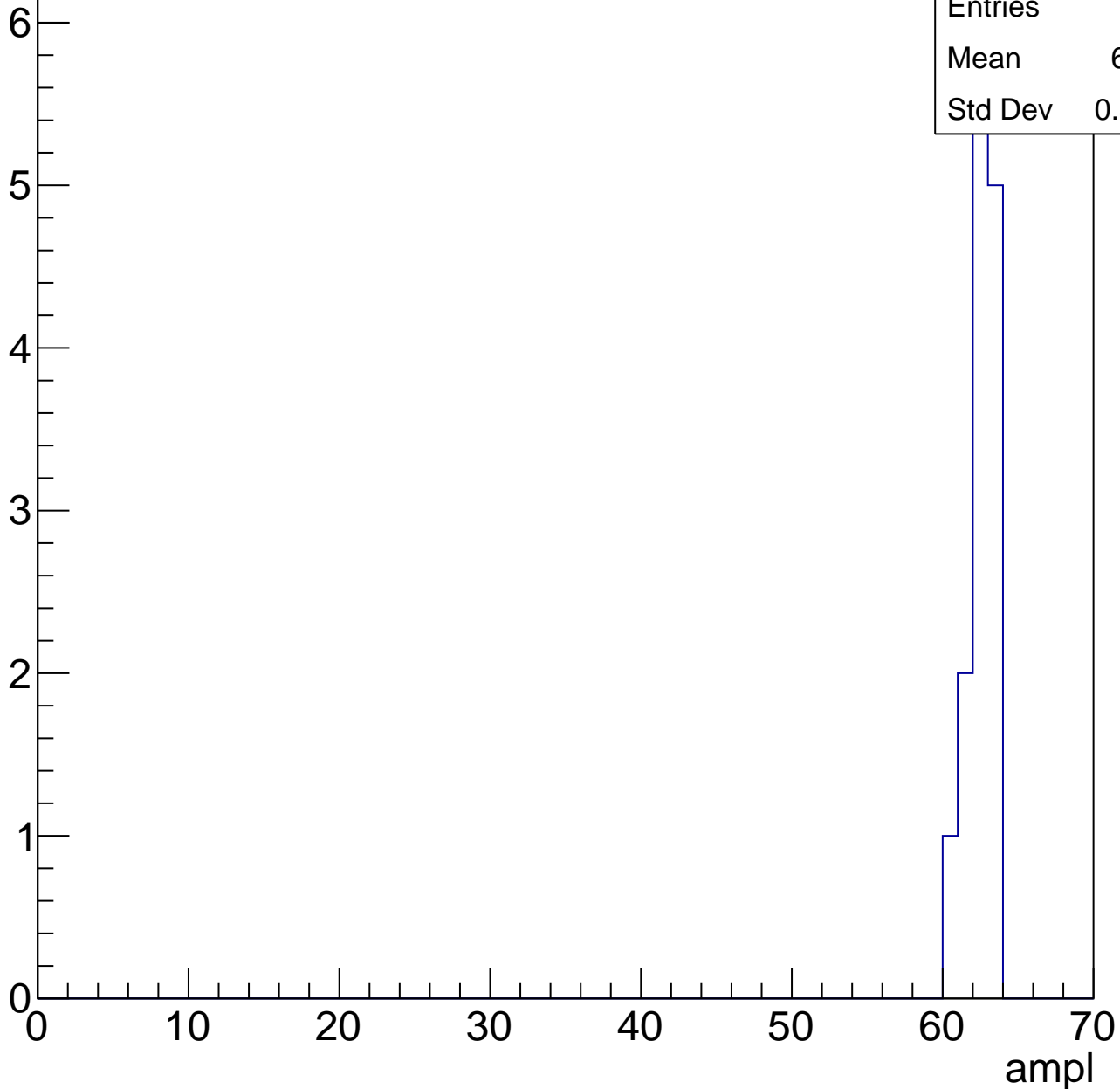


B1L003S, U16-ch36, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	62.07
Std Dev	0.8835



B1L003S, U16-ch36, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U16-ch37, adc0

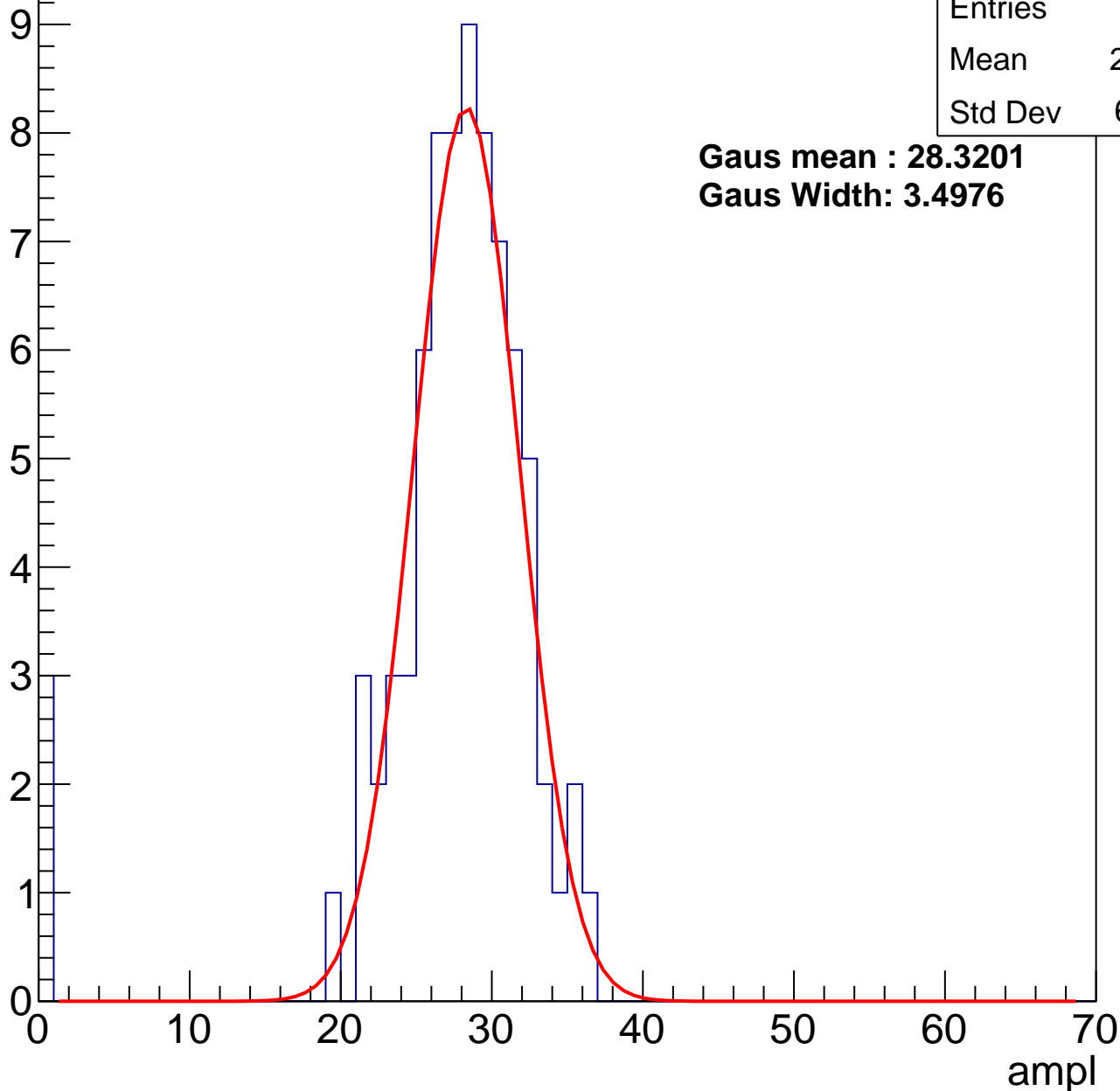
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	26.76
Std Dev	6.371

Gaus mean : 28.3201

Gaus Width: 3.4976



B1L003S, U16-ch37, adc1

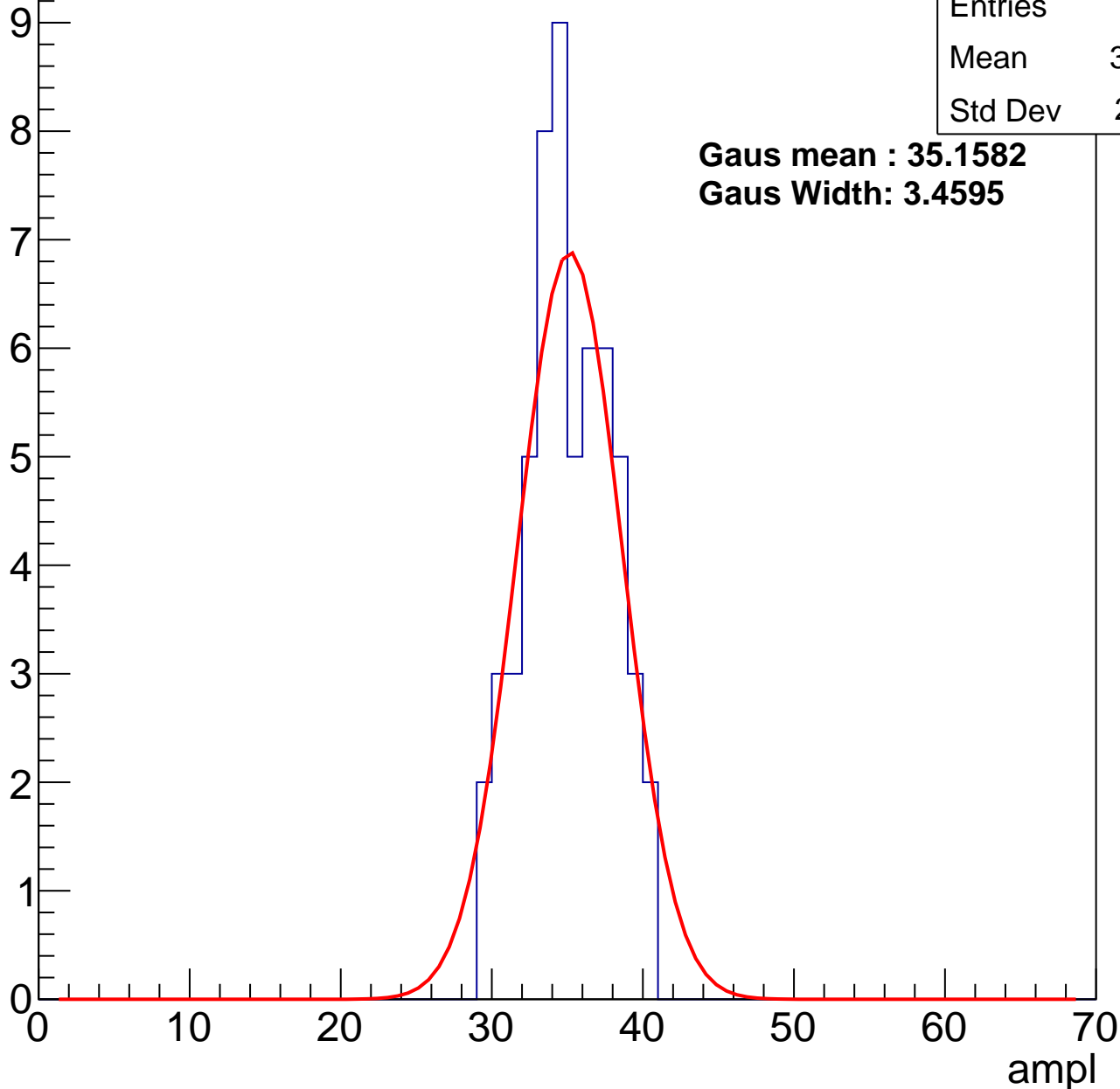
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	34.58
Std Dev	2.791

Gaus mean : 35.1582

Gaus Width: 3.4595



B1L003S, U16-ch37, adc2

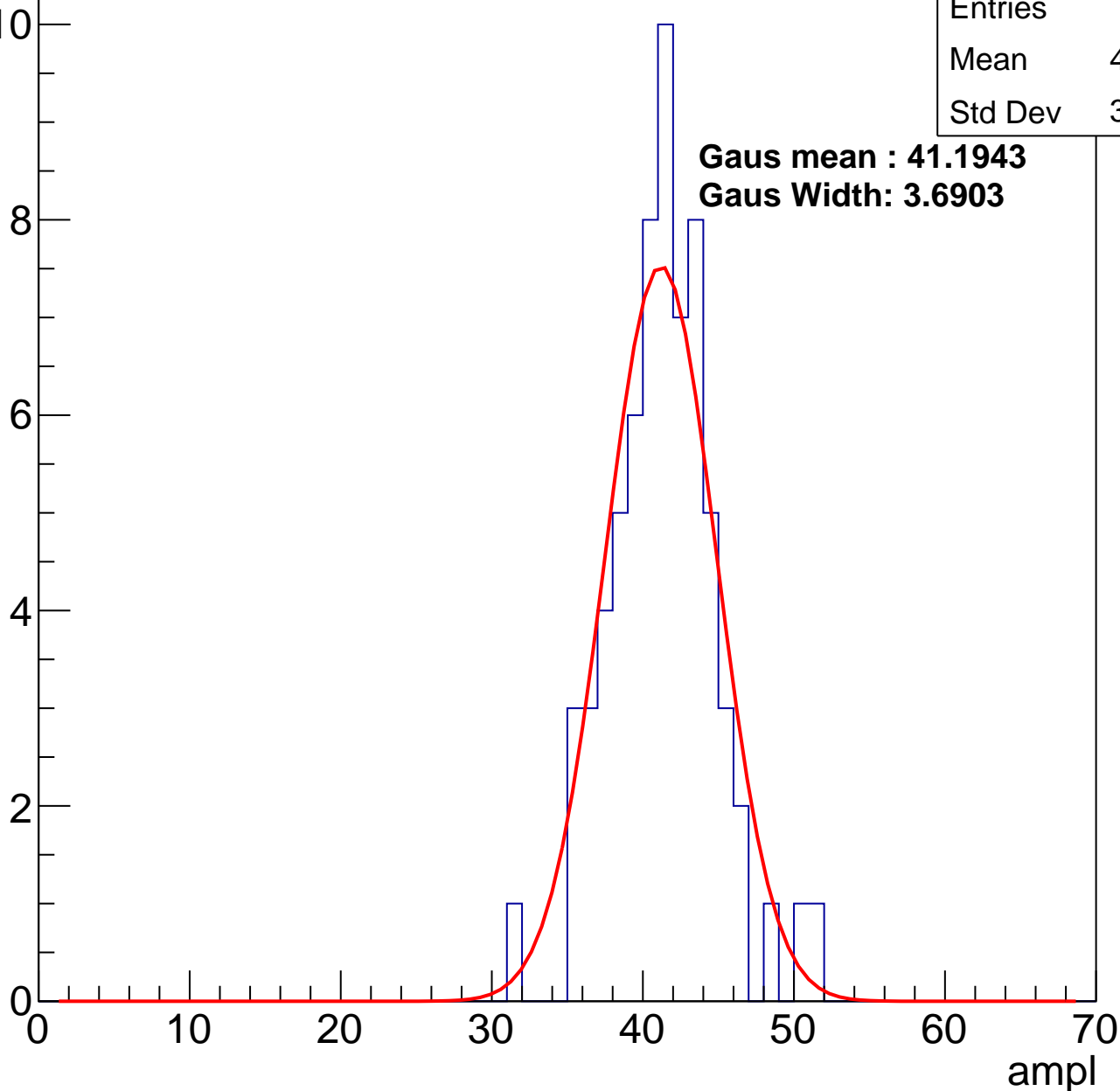
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	40.88
Std Dev	3.504

Gaus mean : 41.1943

Gaus Width: 3.6903

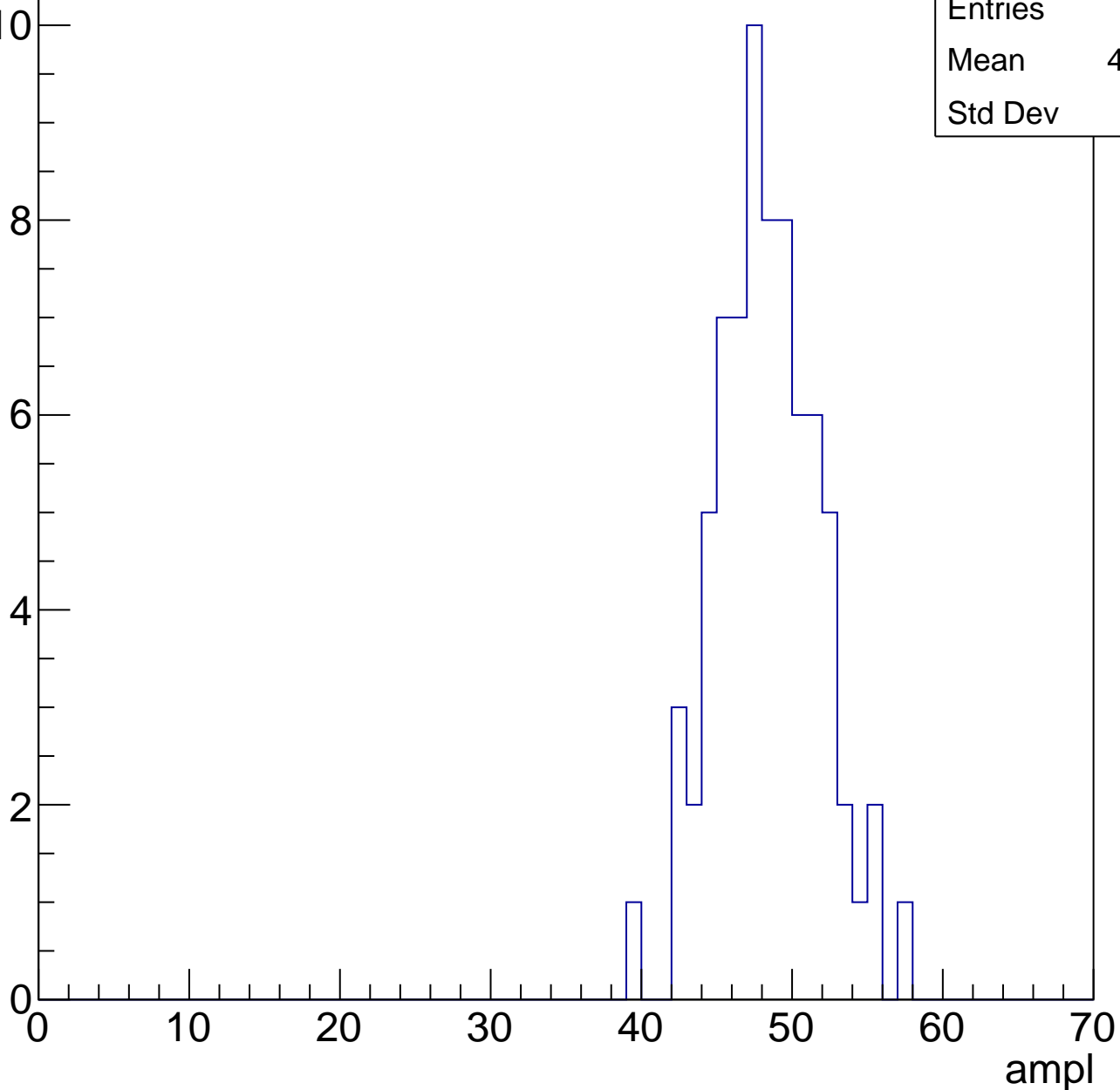


B1L003S, U16-ch37, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	47.93
Std Dev	3.39

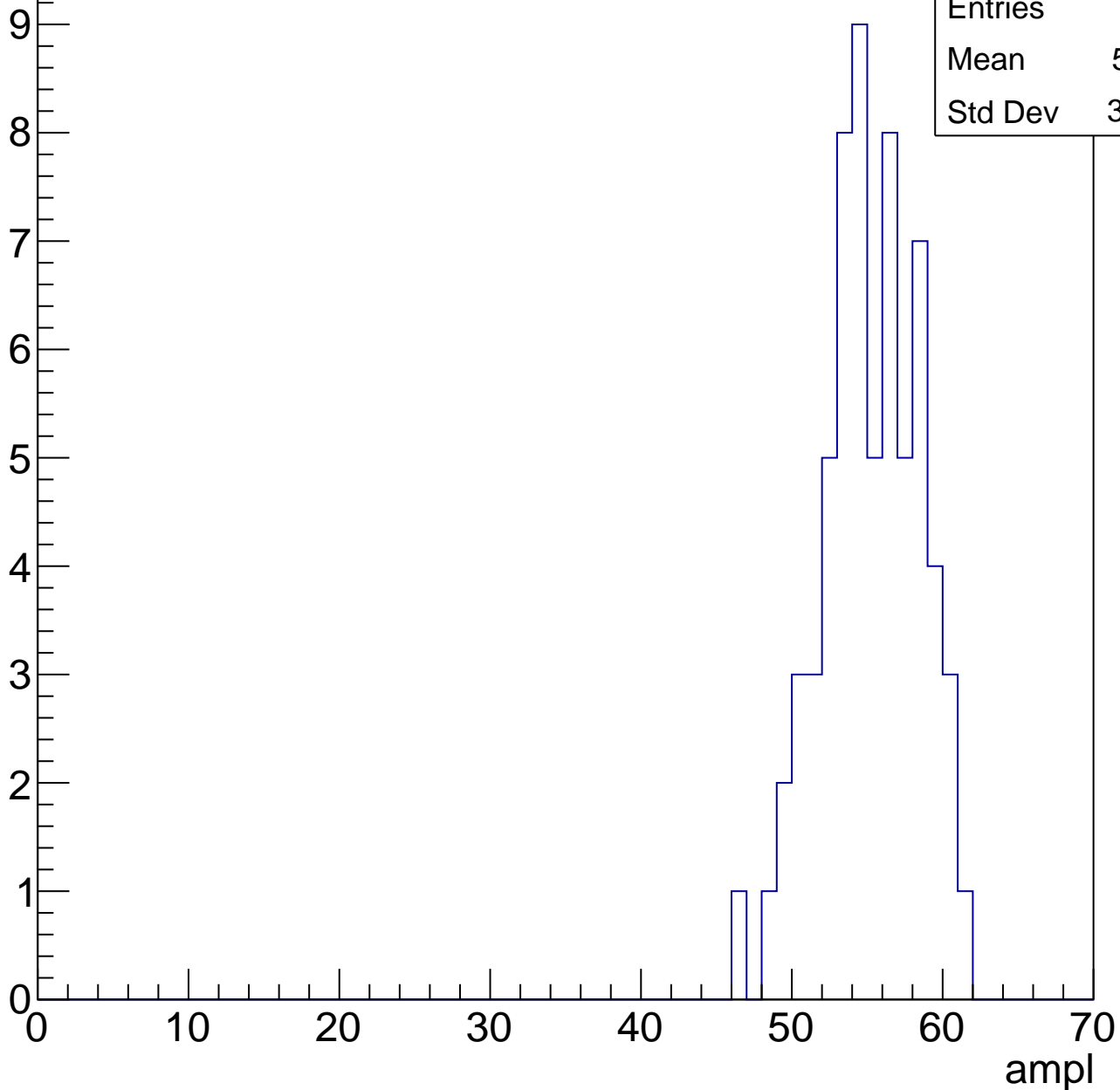


B1L003S, U16-ch37, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.71
Std Dev	3.214



B1L003S, U16-ch37, adc5

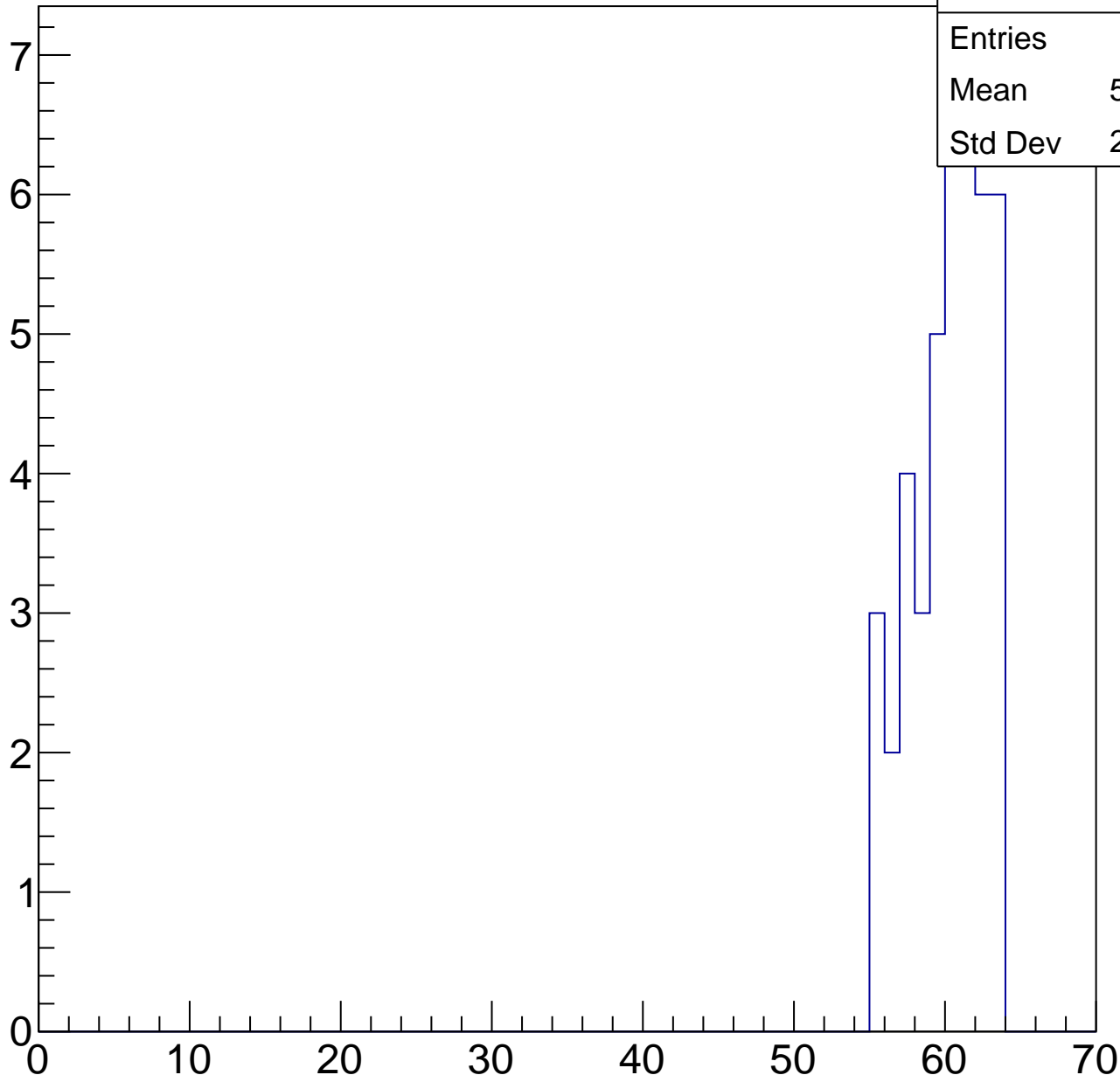
calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	43
Mean	59.79
Std Dev	2.378

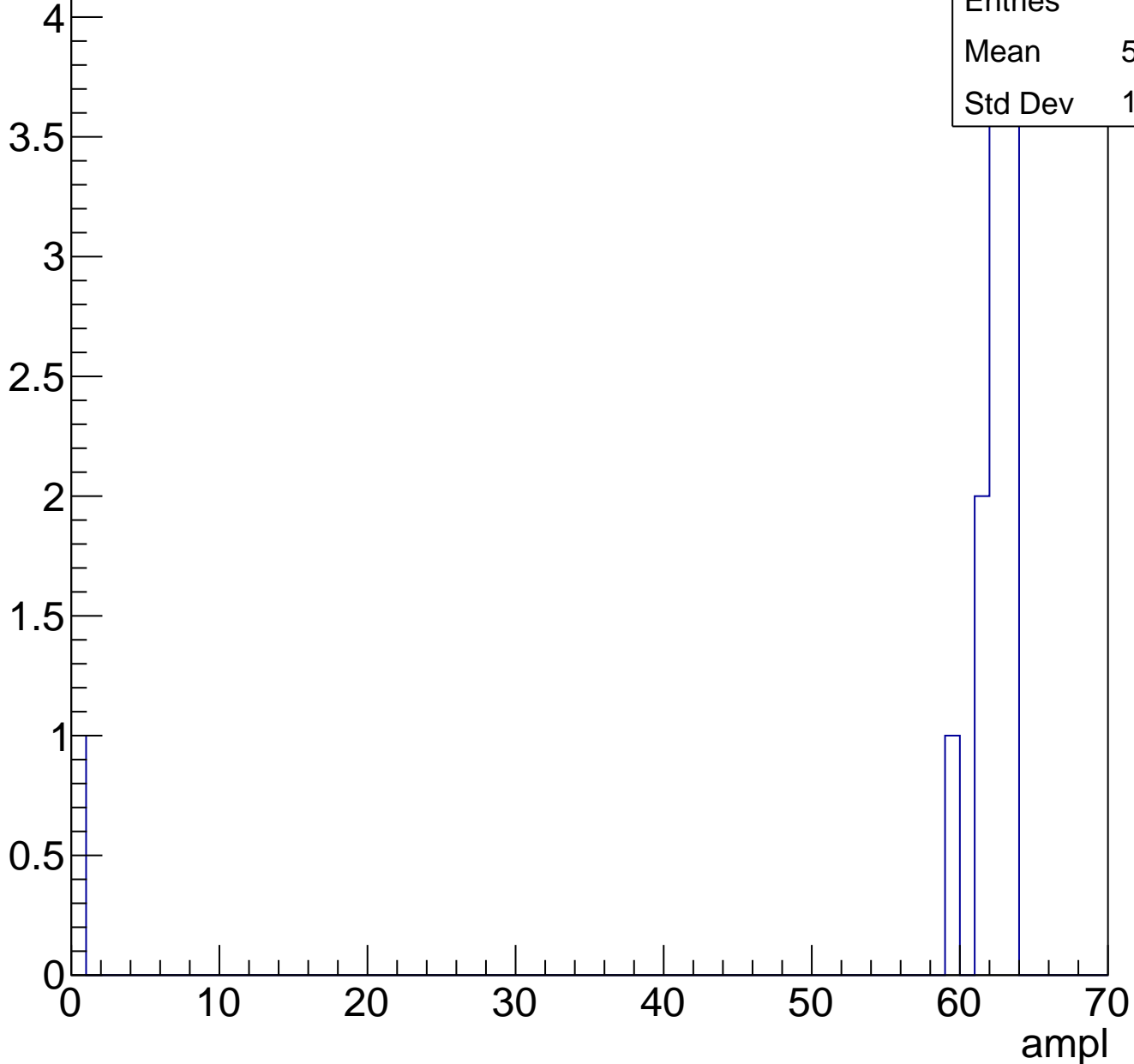
ampl



B1L003S, U16-ch37, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch37, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch38, adc0

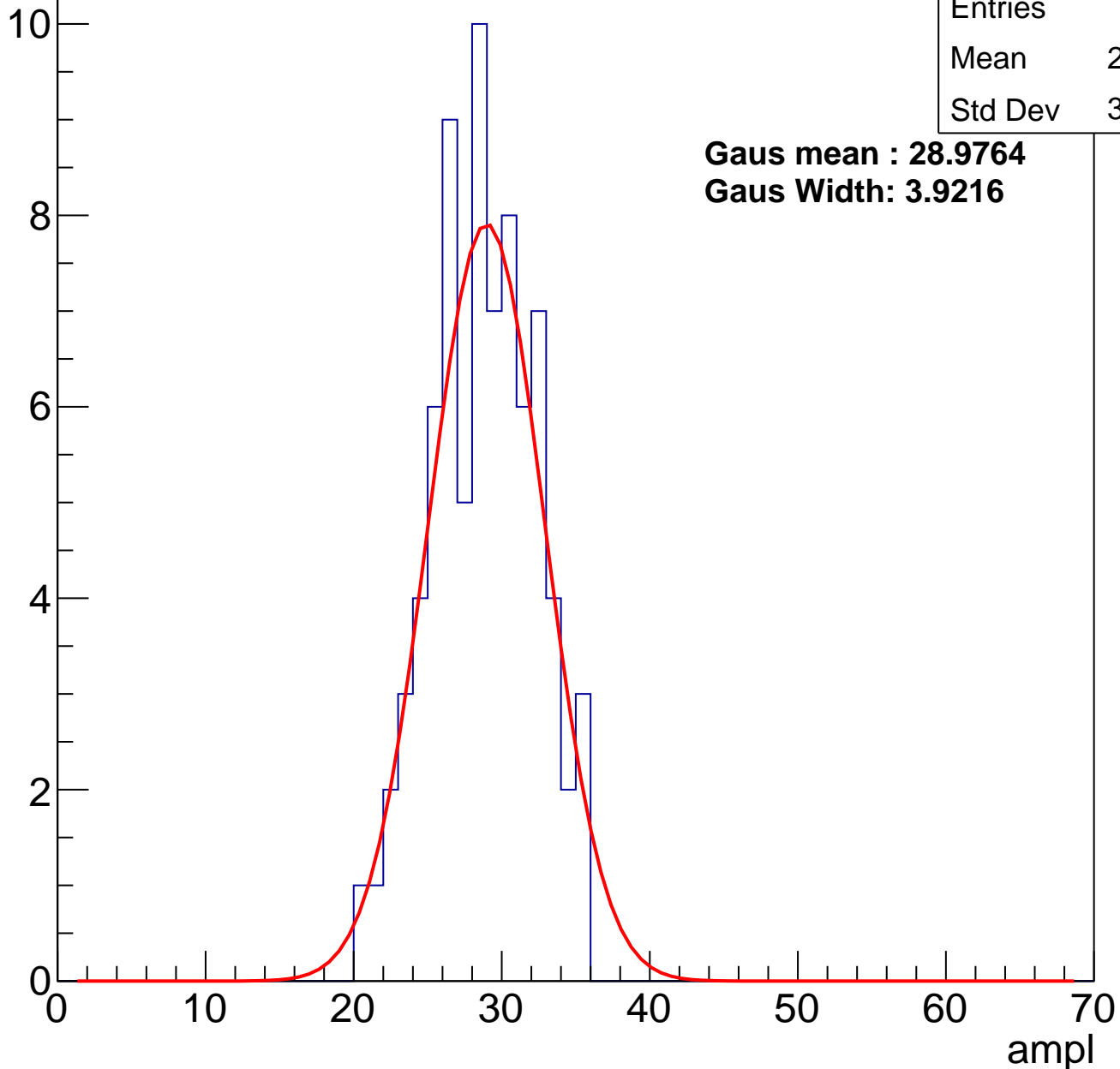
calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	28.29
Std Dev	3.465

Gaus mean : 28.9764

Gaus Width: 3.9216

Entry



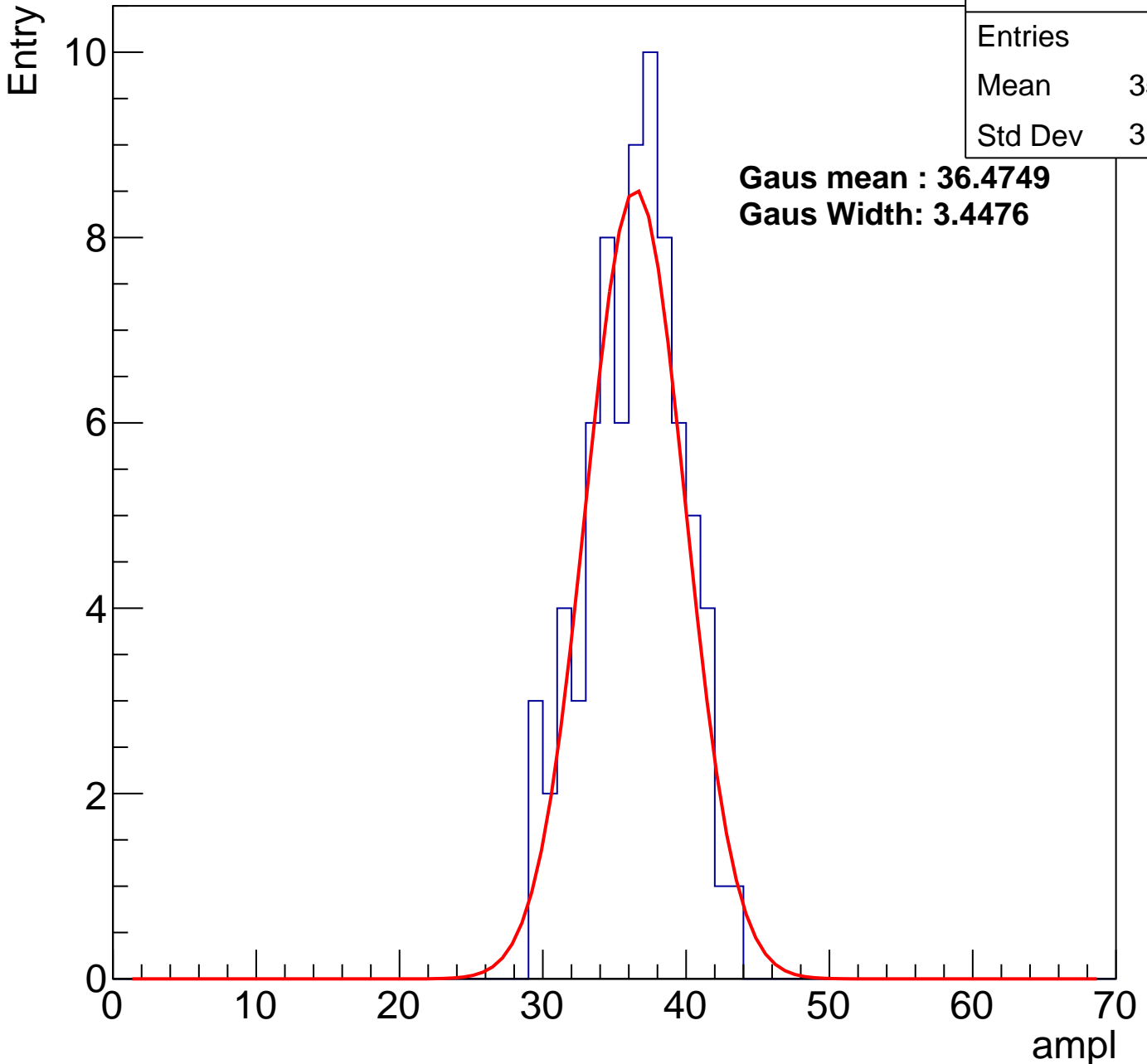
B1L003S, U16-ch38, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	35.89
Std Dev	3.283

Gaus mean : 36.4749

Gaus Width: 3.4476



B1L003S, U16-ch38, adc2

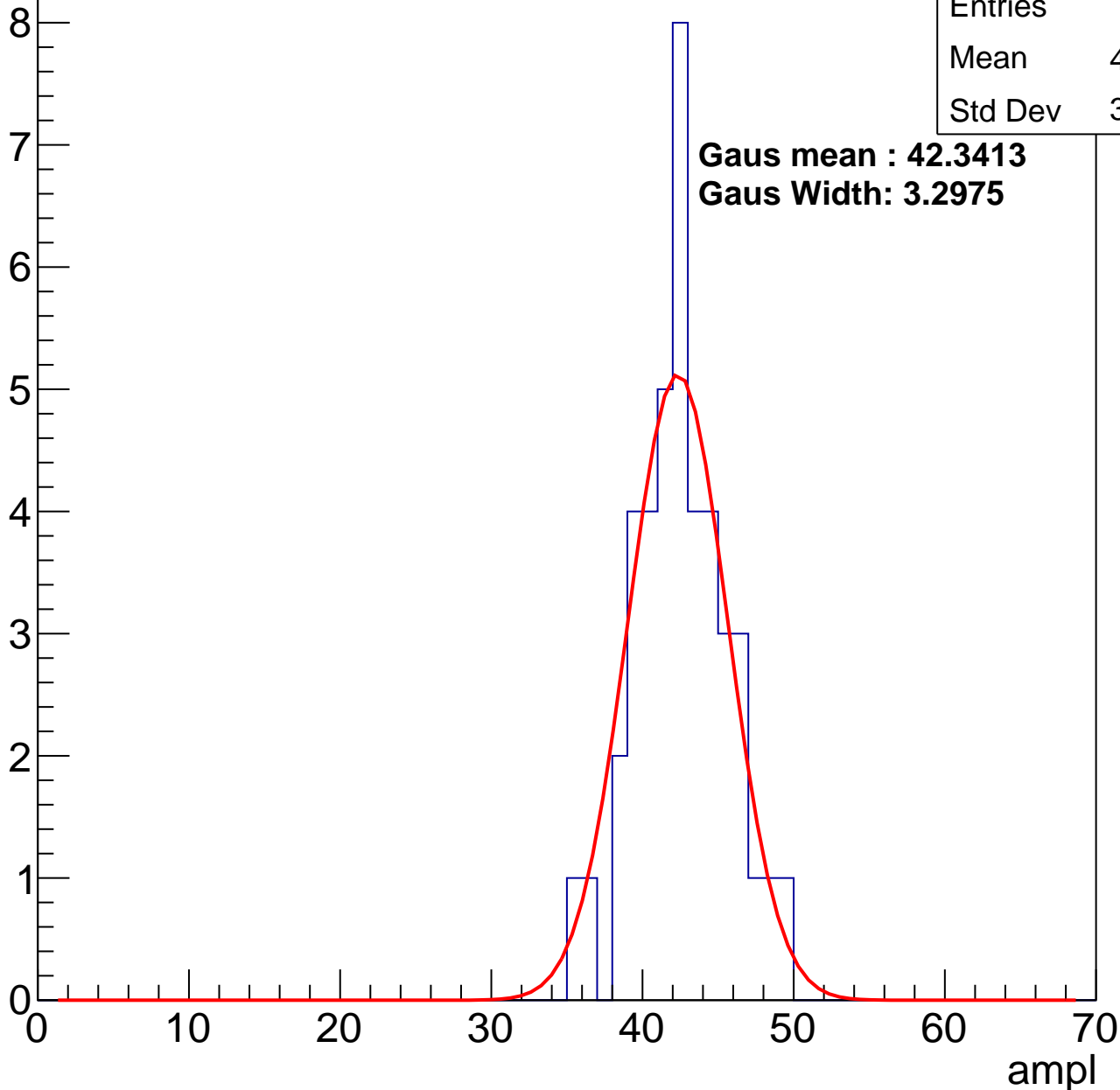
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	42.12
Std Dev	3.002

Gaus mean : 42.3413

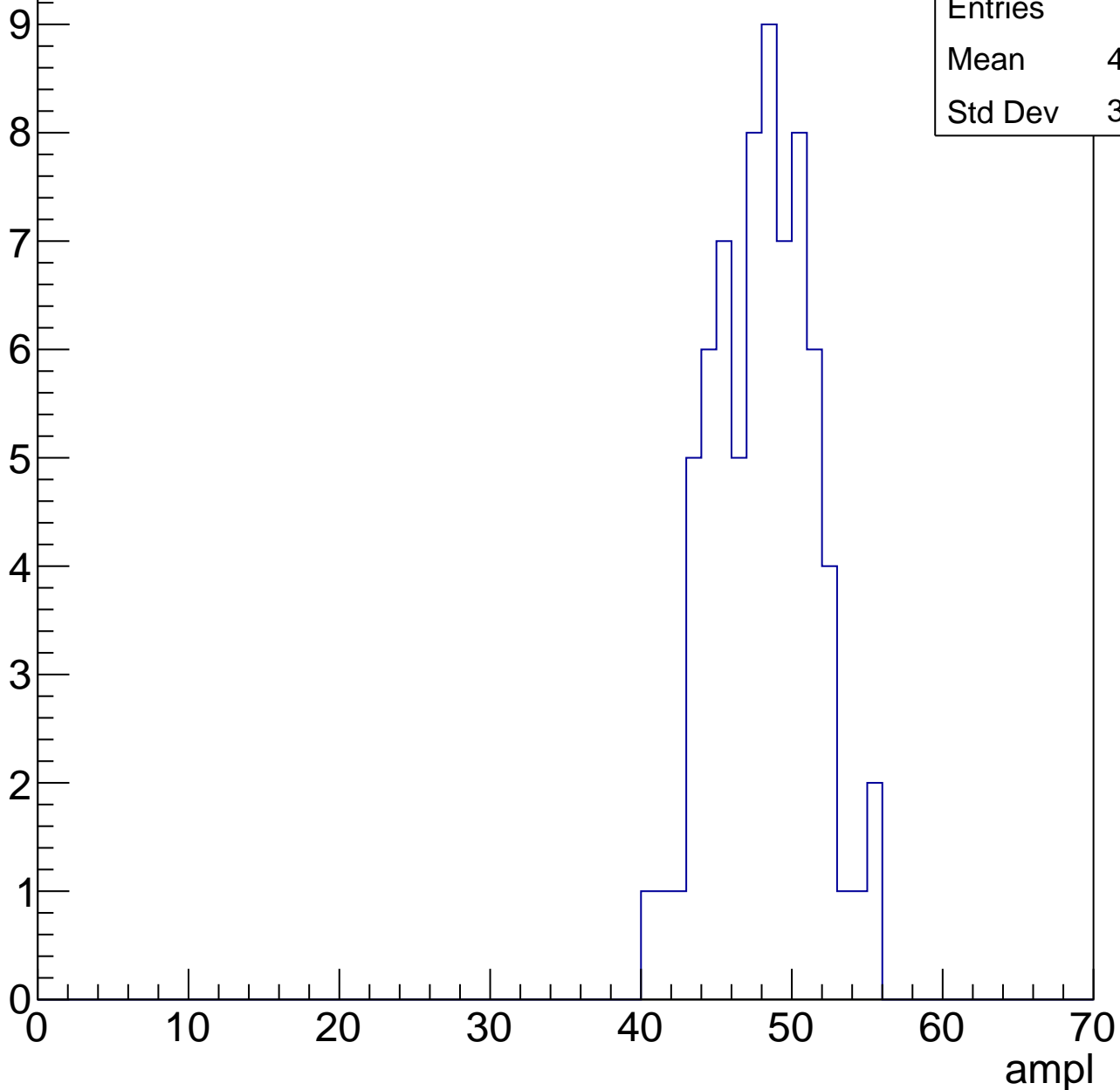
Gaus Width: 3.2975



B1L003S, U16-ch38, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

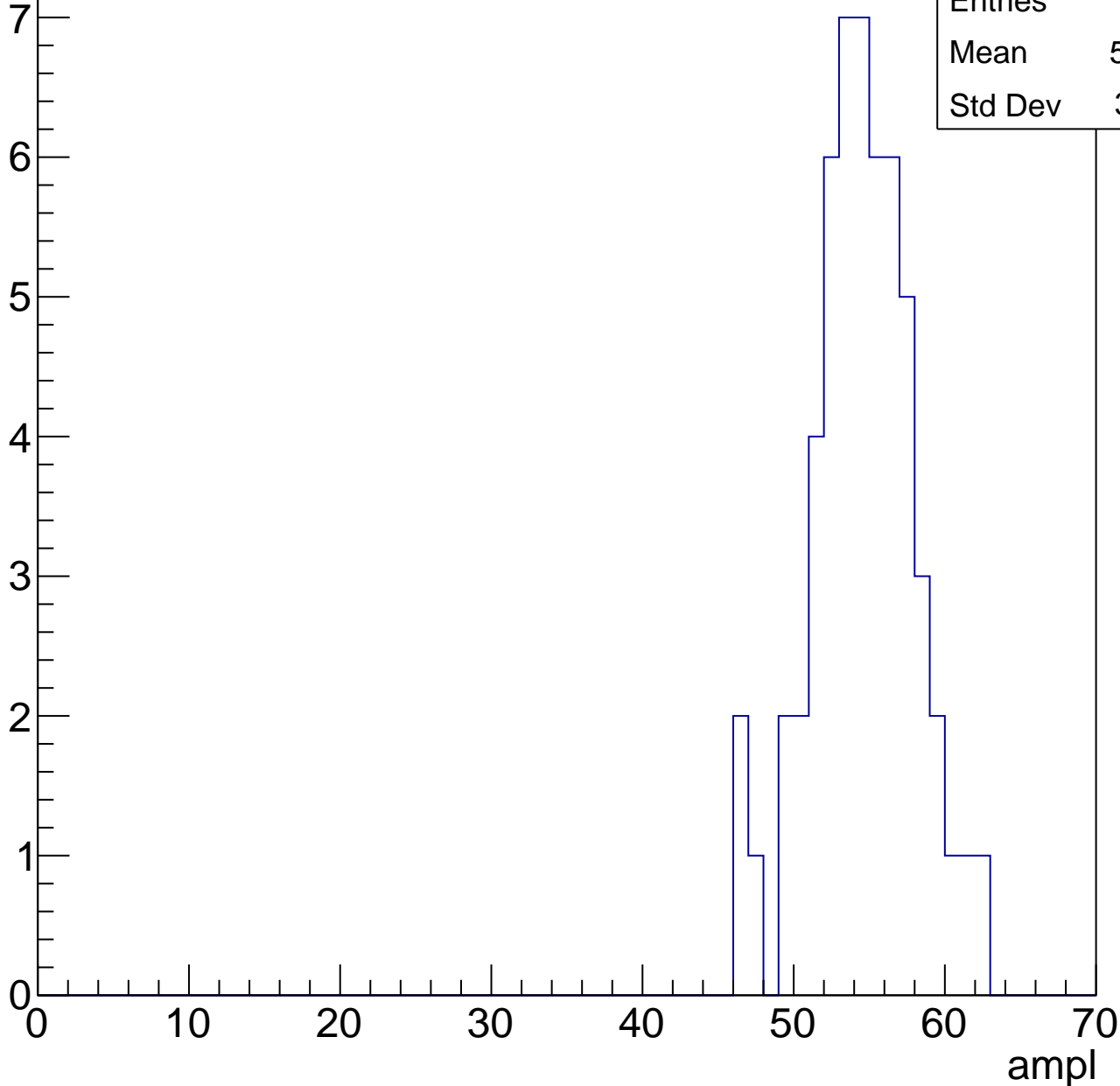


B1L003S, U16-ch38, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

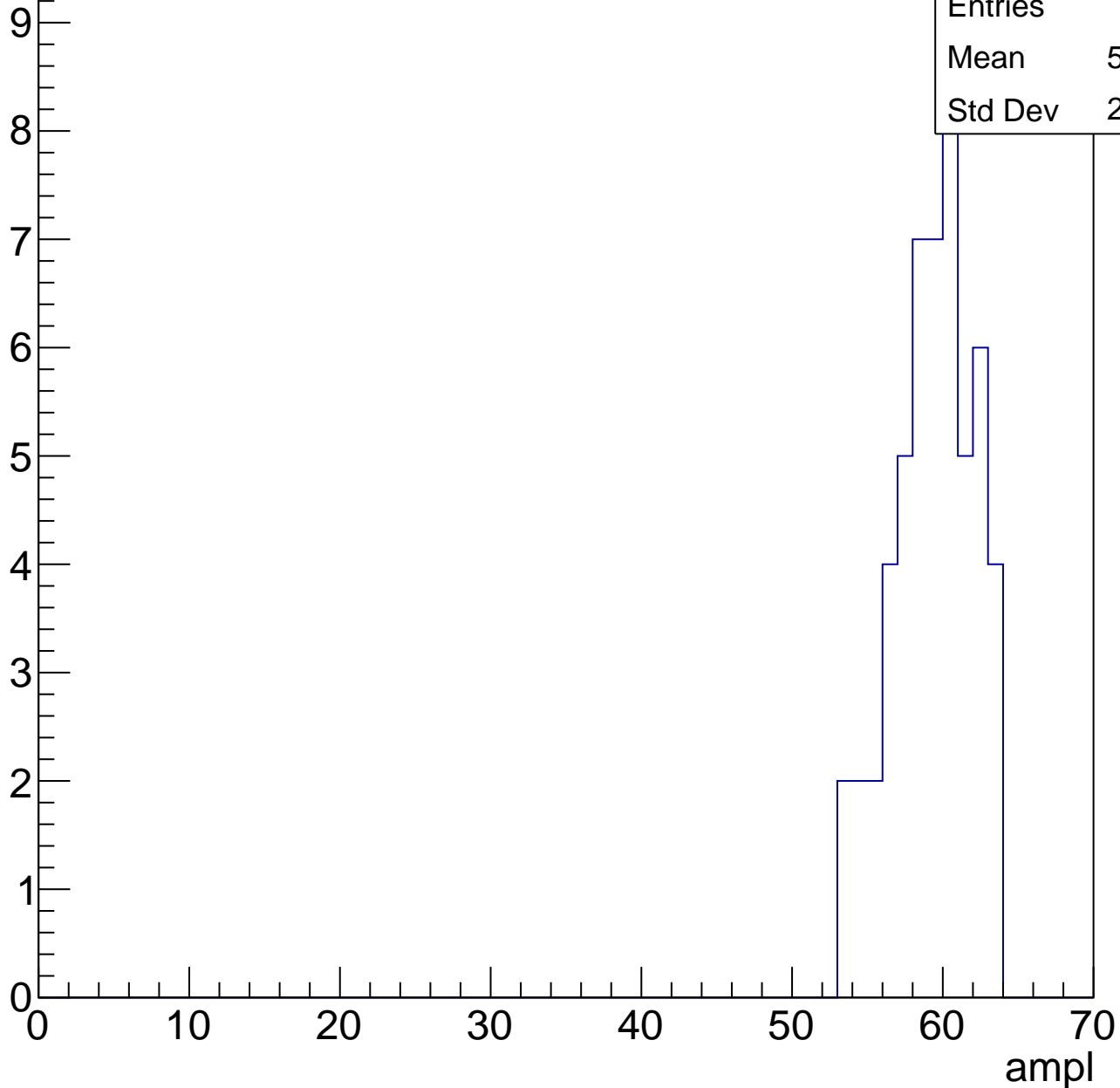
Entries	56
Mean	54.07
Std Dev	3.401



B1L003S, U16-ch38, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

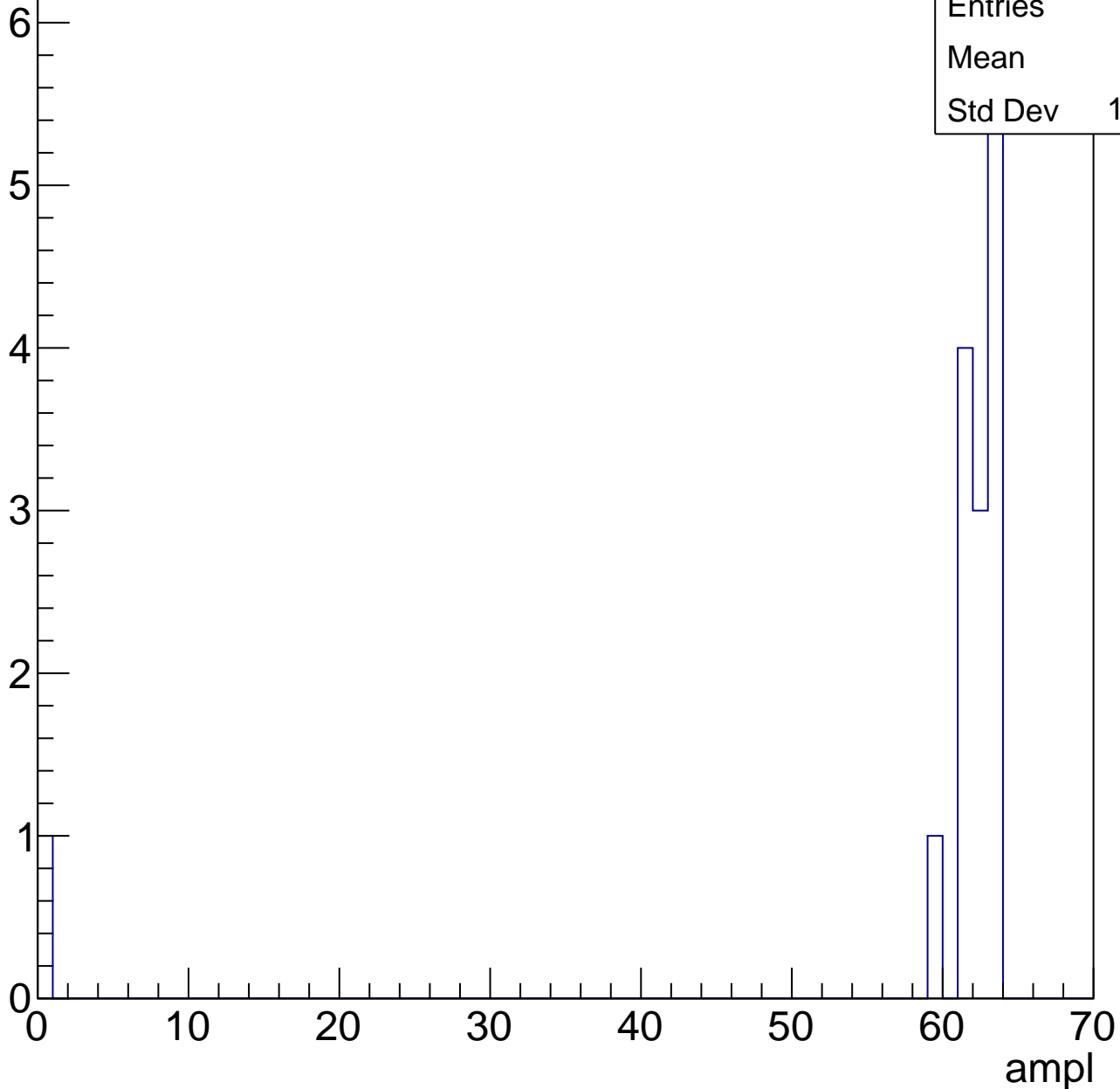


B1L003S, U16-ch38, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	57.8
Std Dev	15.49



B1L003S, U16-ch38, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch39, adc0

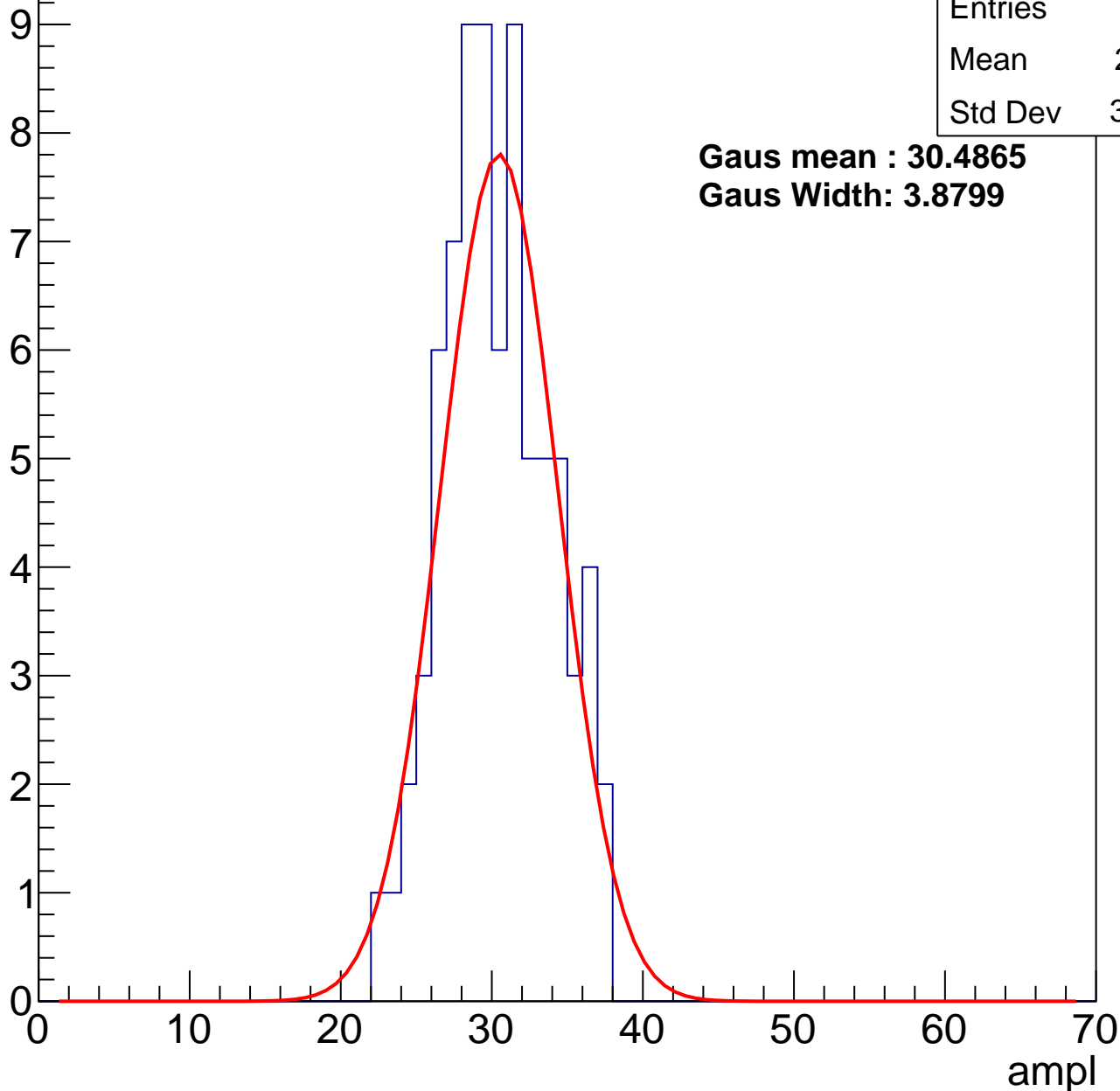
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	29.91
Std Dev	3.483

Gaus mean : 30.4865

Gaus Width: 3.8799



B1L003S, U16-ch39, adc1

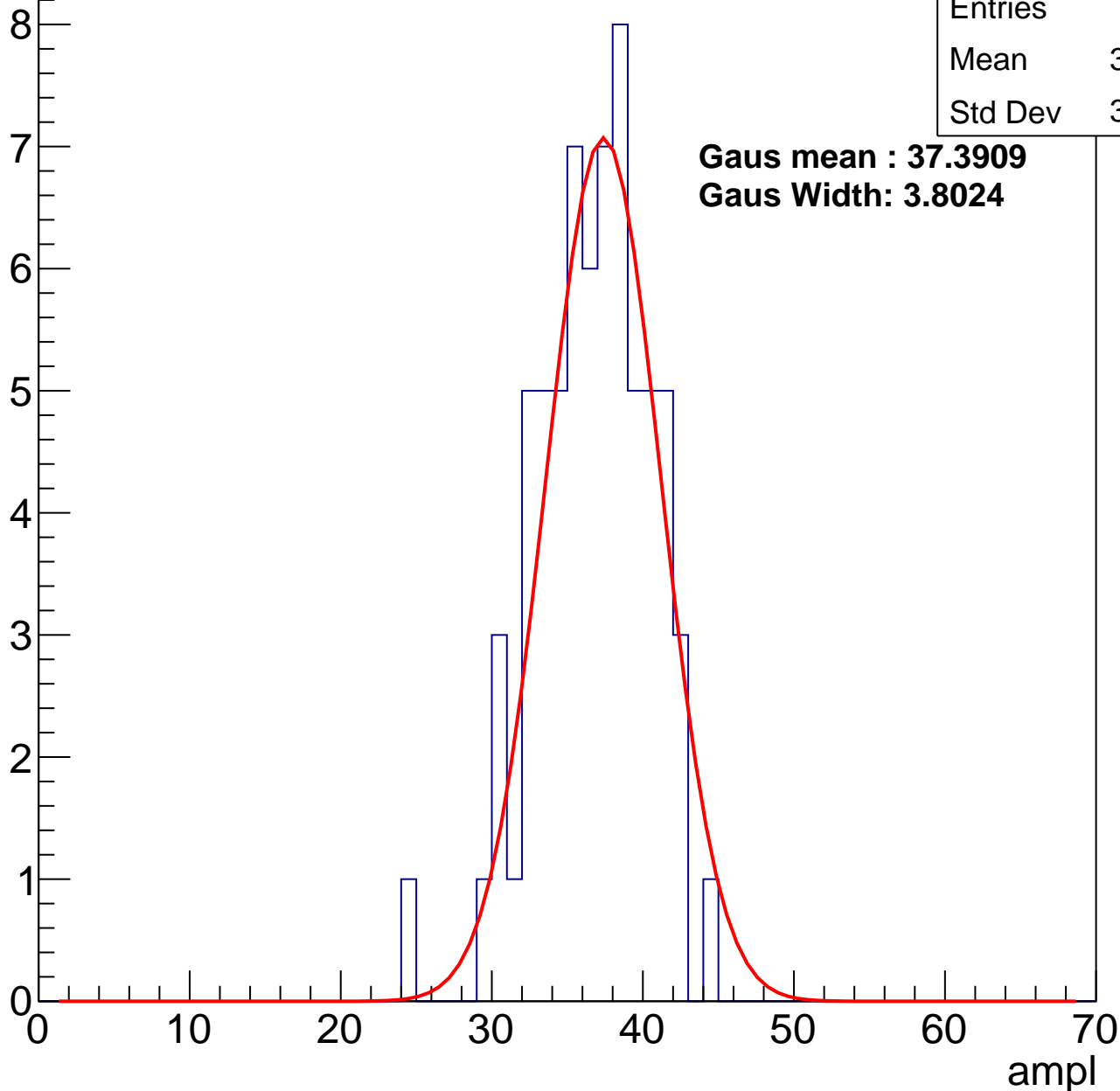
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.22
Std Dev	3.709

Gaus mean : 37.3909

Gaus Width: 3.8024



B1L003S, U16-ch39, adc2

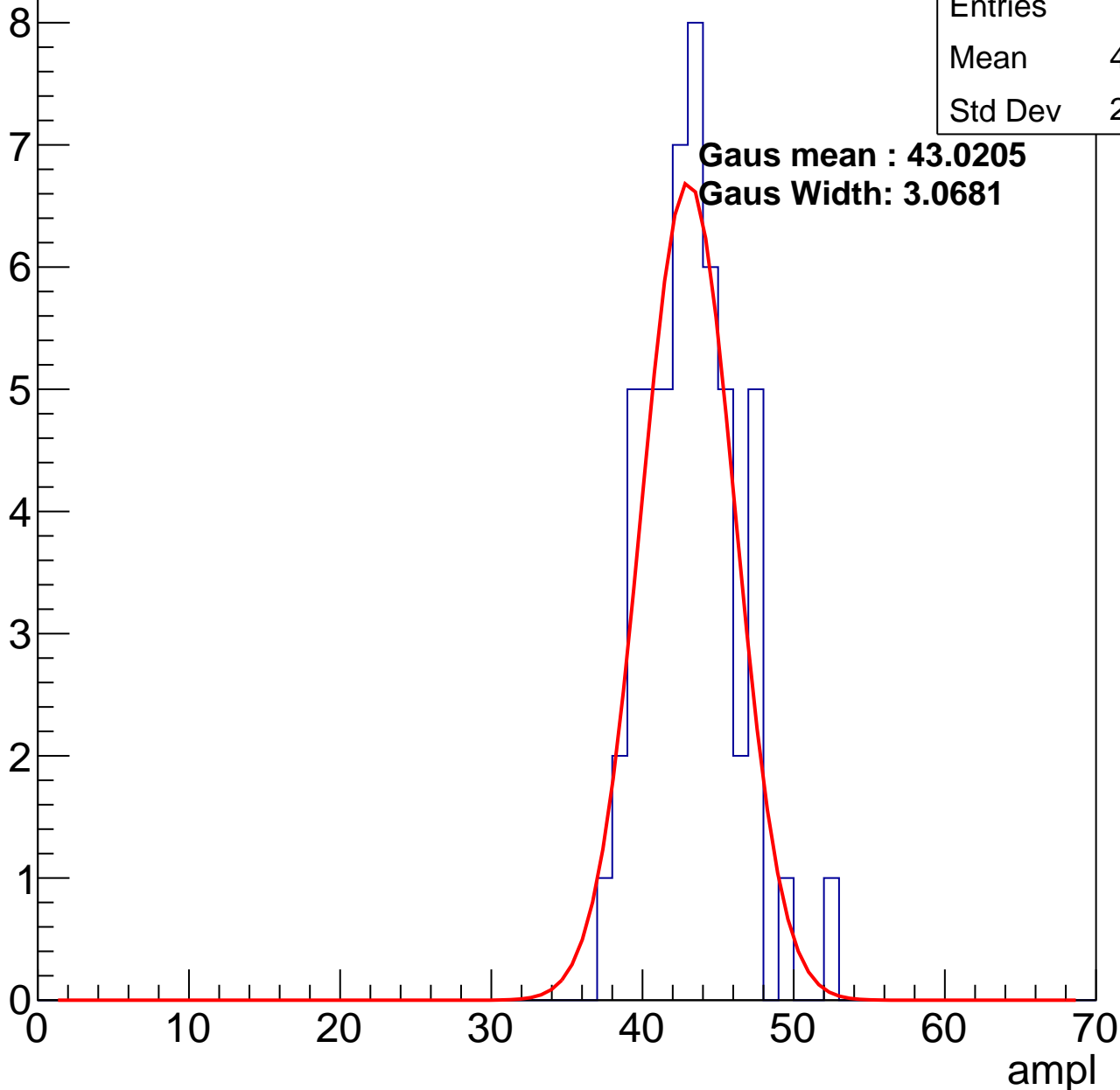
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.79
Std Dev	2.999

Gaus mean : 43.0205

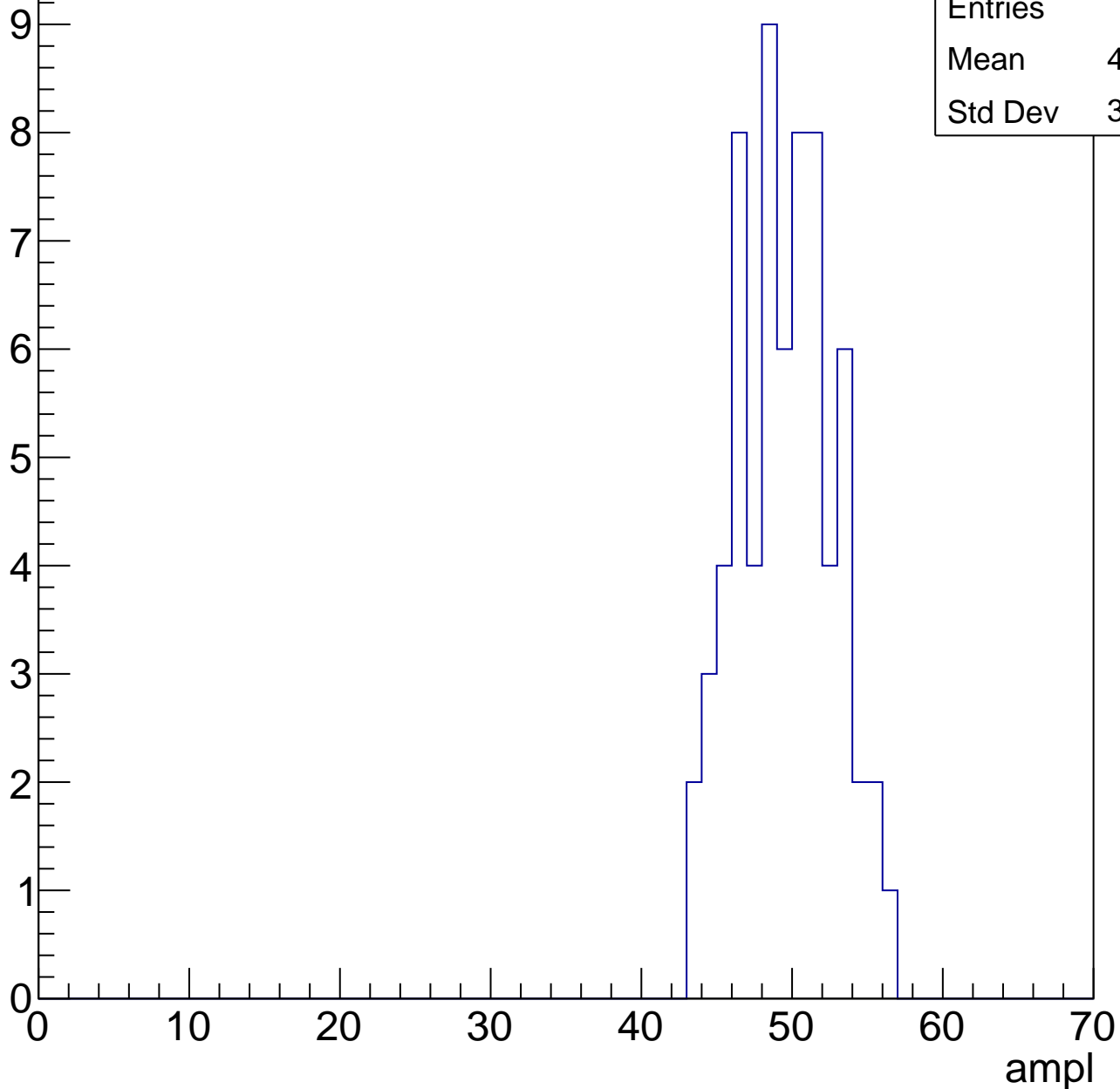
Gaus Width: 3.0681



B1L003S, U16-ch39, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

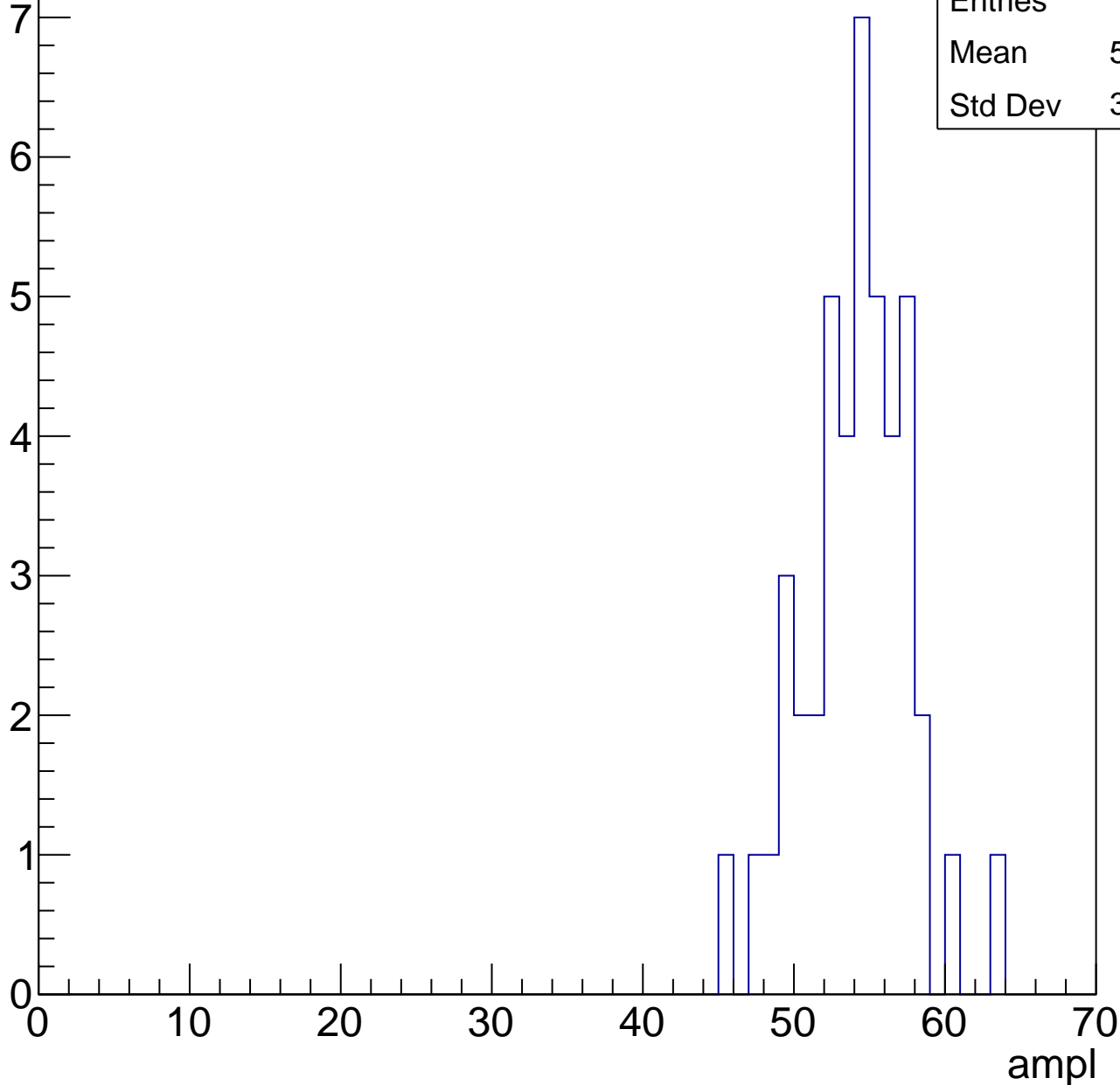


B1L003S, U16-ch39, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	53.68
Std Dev	3.456

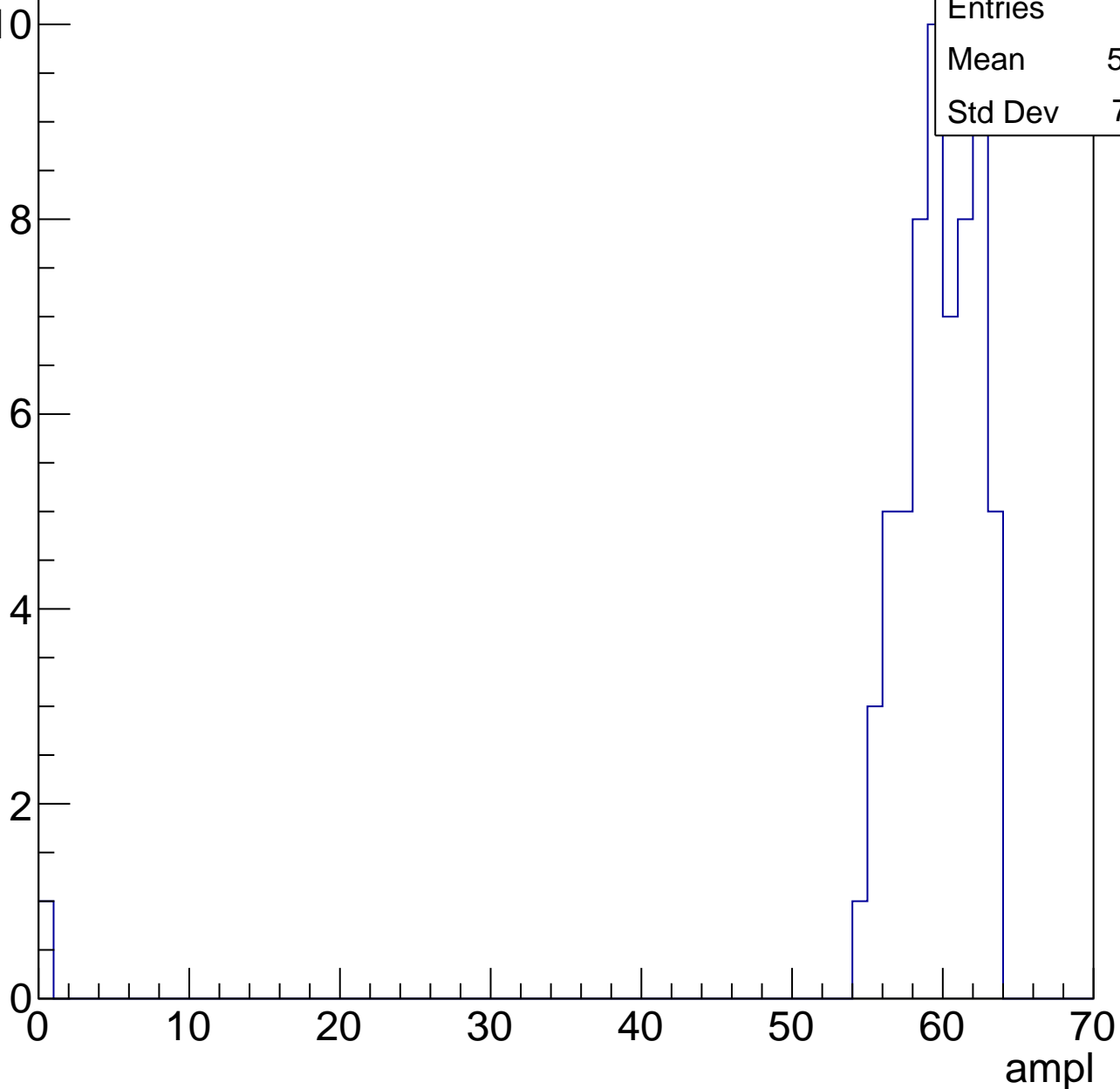


B1L003S, U16-ch39, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

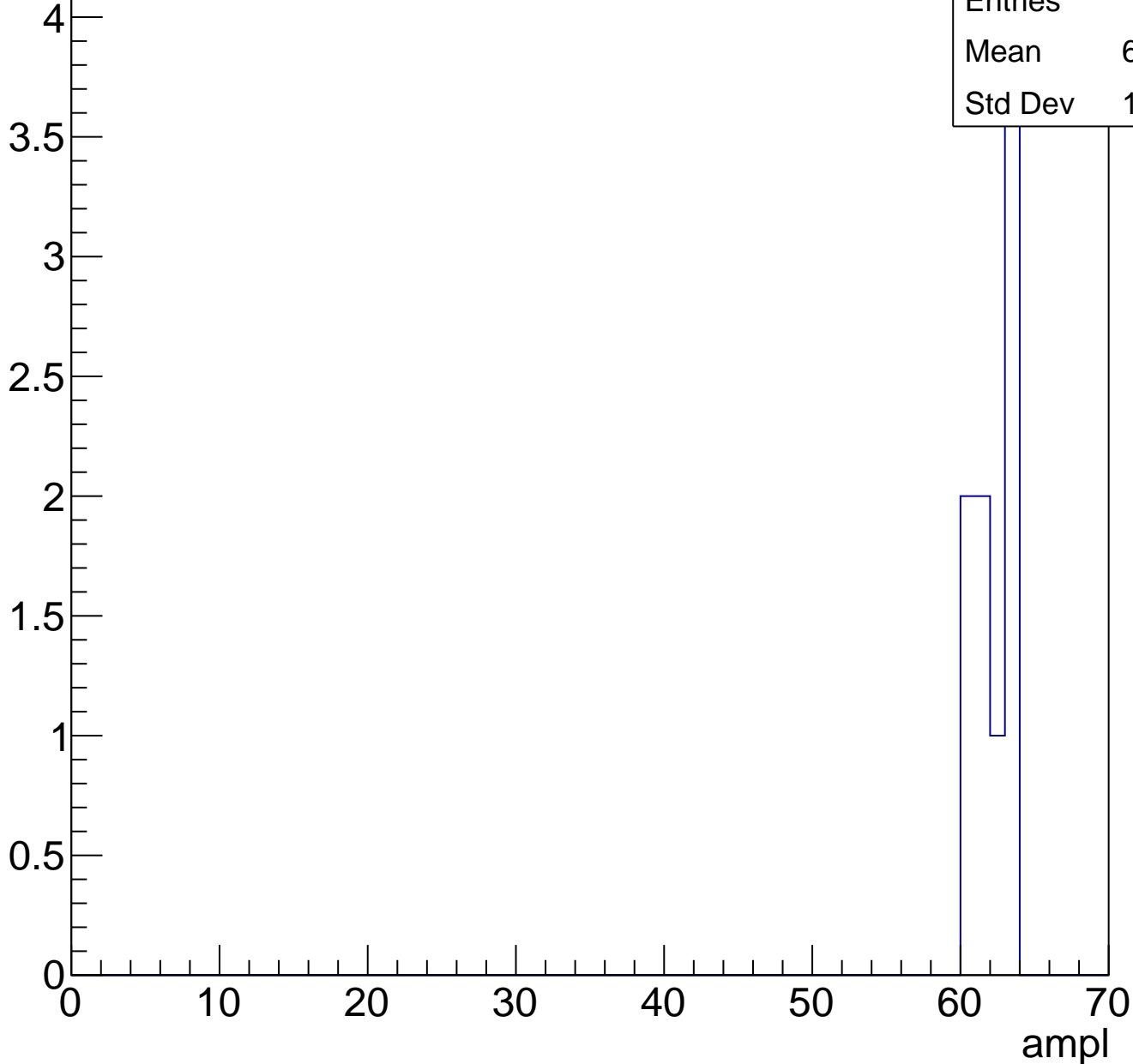
Entries	62
Mean	58.37
Std Dev	7.831



B1L003S, U16-ch39, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch39, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch40, adc0

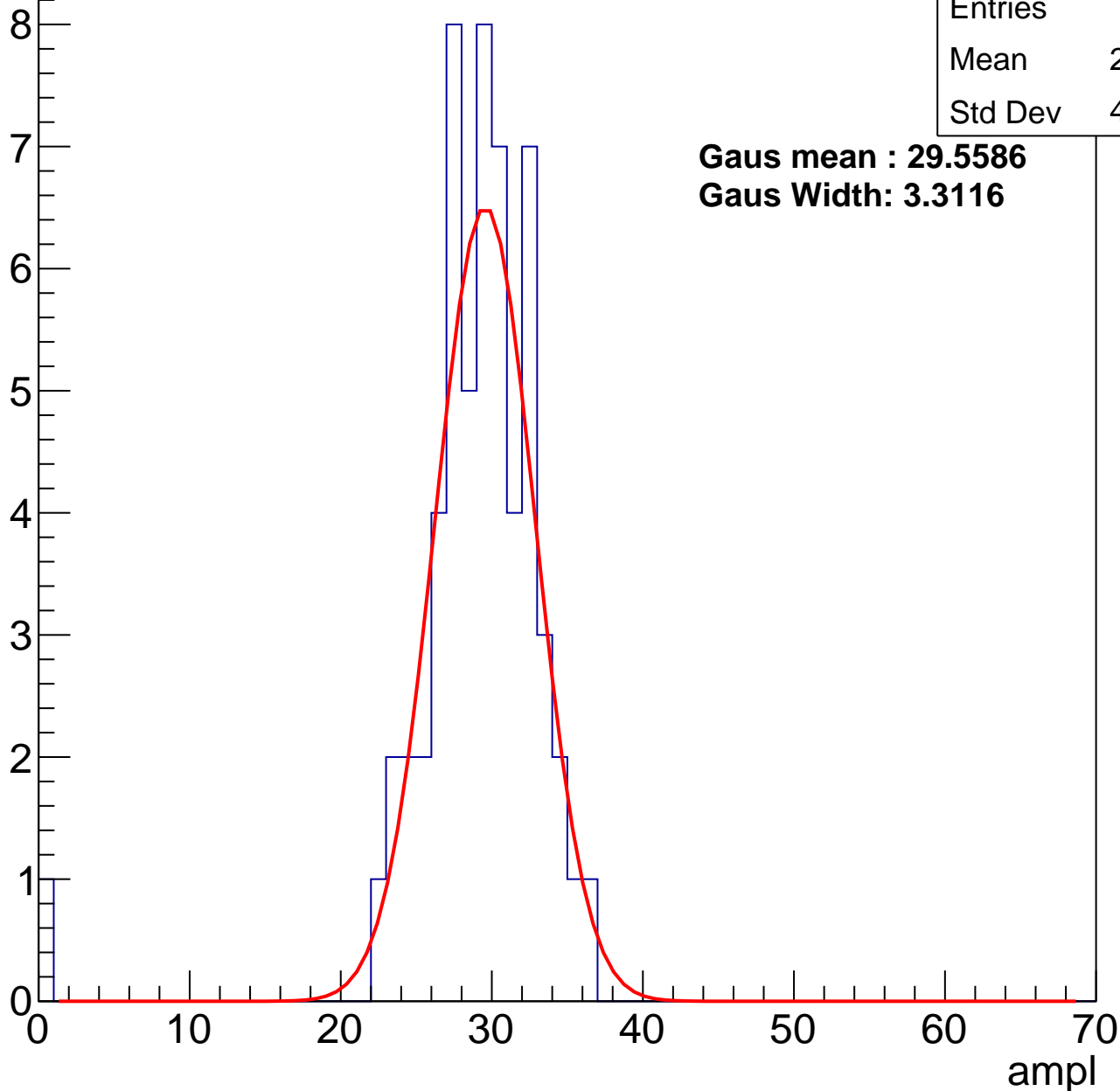
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.52
Std Dev	4.865

Gaus mean : 29.5586

Gaus Width: 3.3116



B1L003S, U16-ch40, adc1

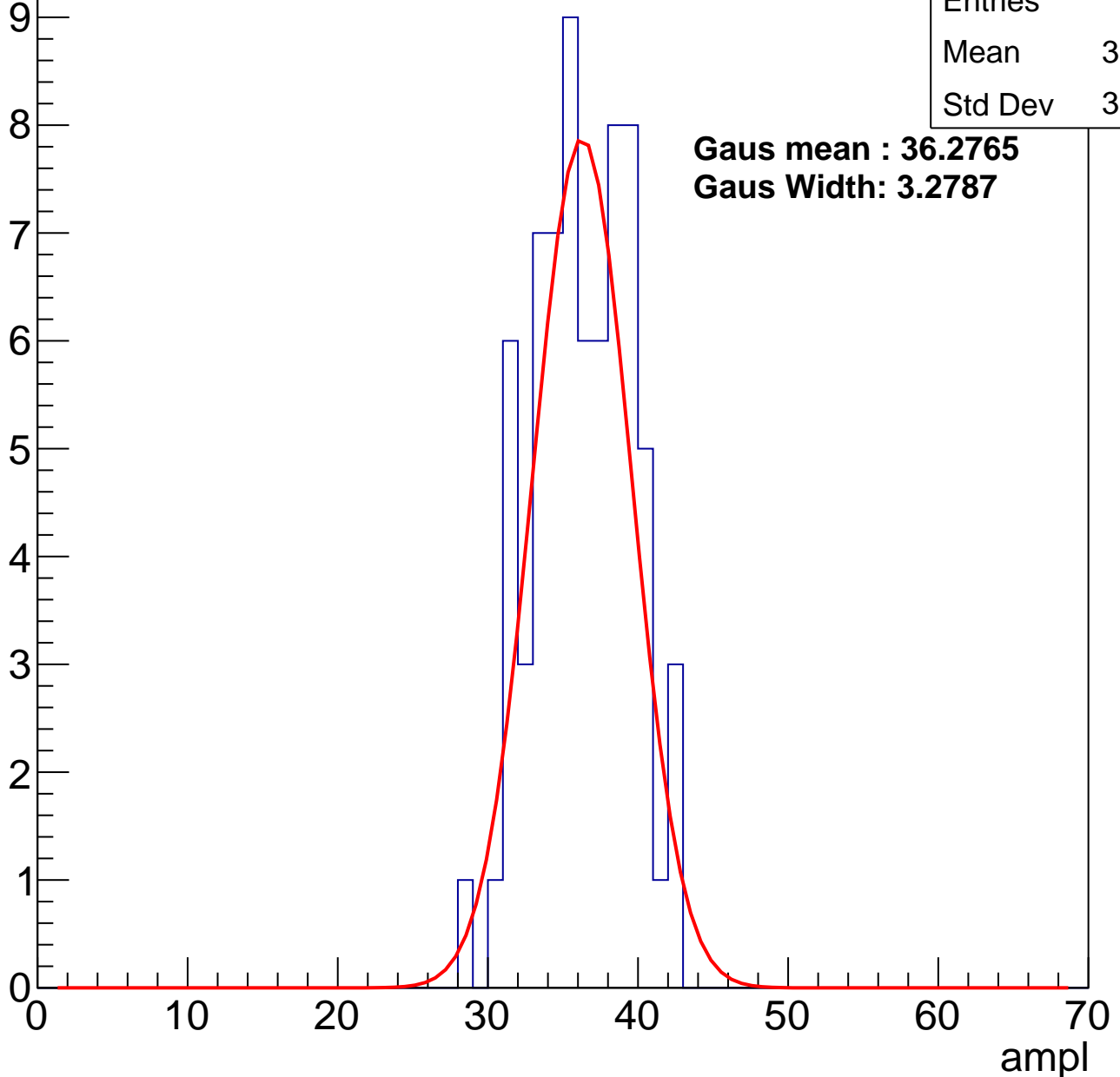
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.85
Std Dev	3.183

Gaus mean : 36.2765

Gaus Width: 3.2787



B1L003S, U16-ch40, adc2

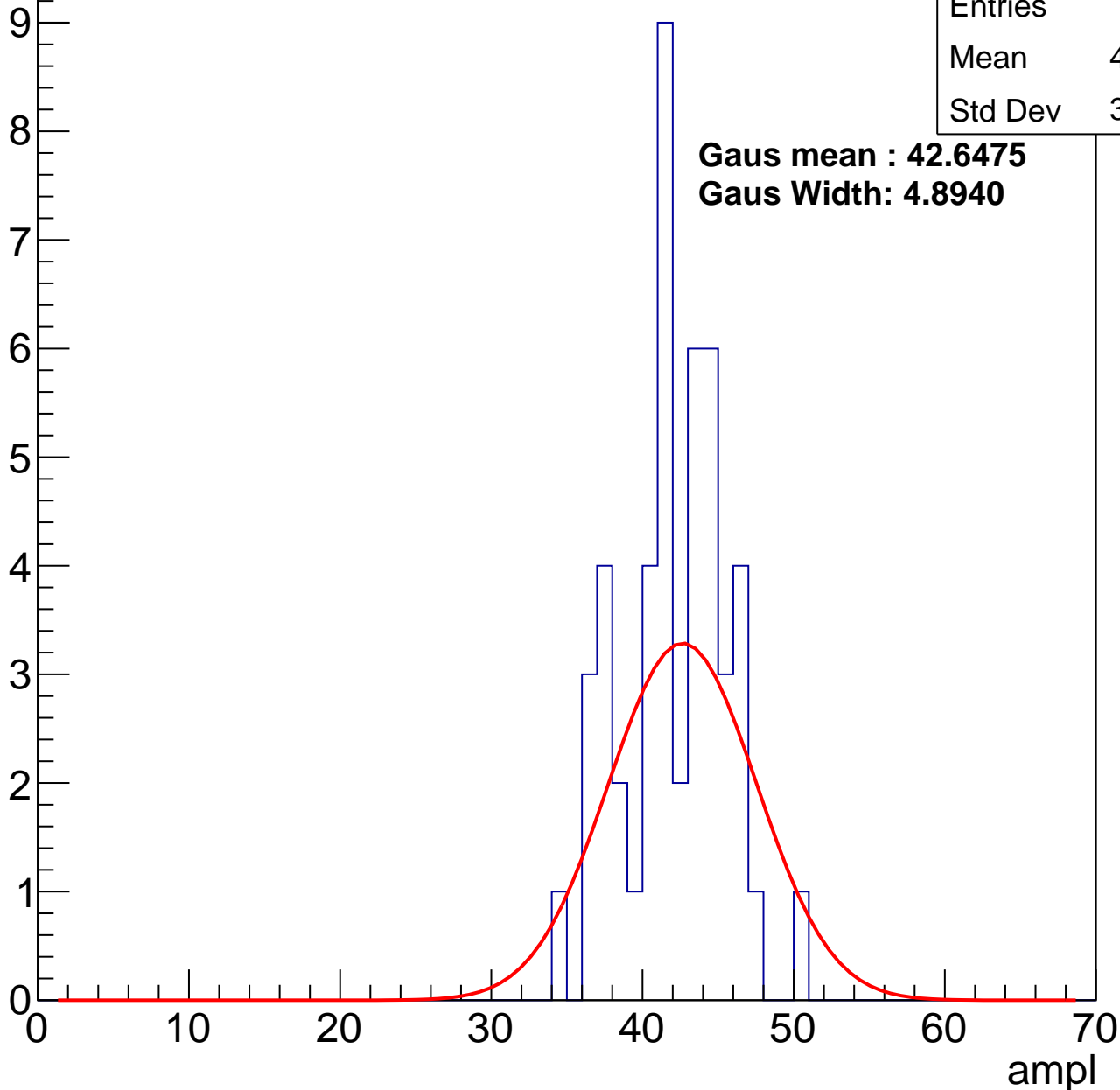
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	41.62
Std Dev	3.393

Gaus mean : 42.6475

Gaus Width: 4.8940

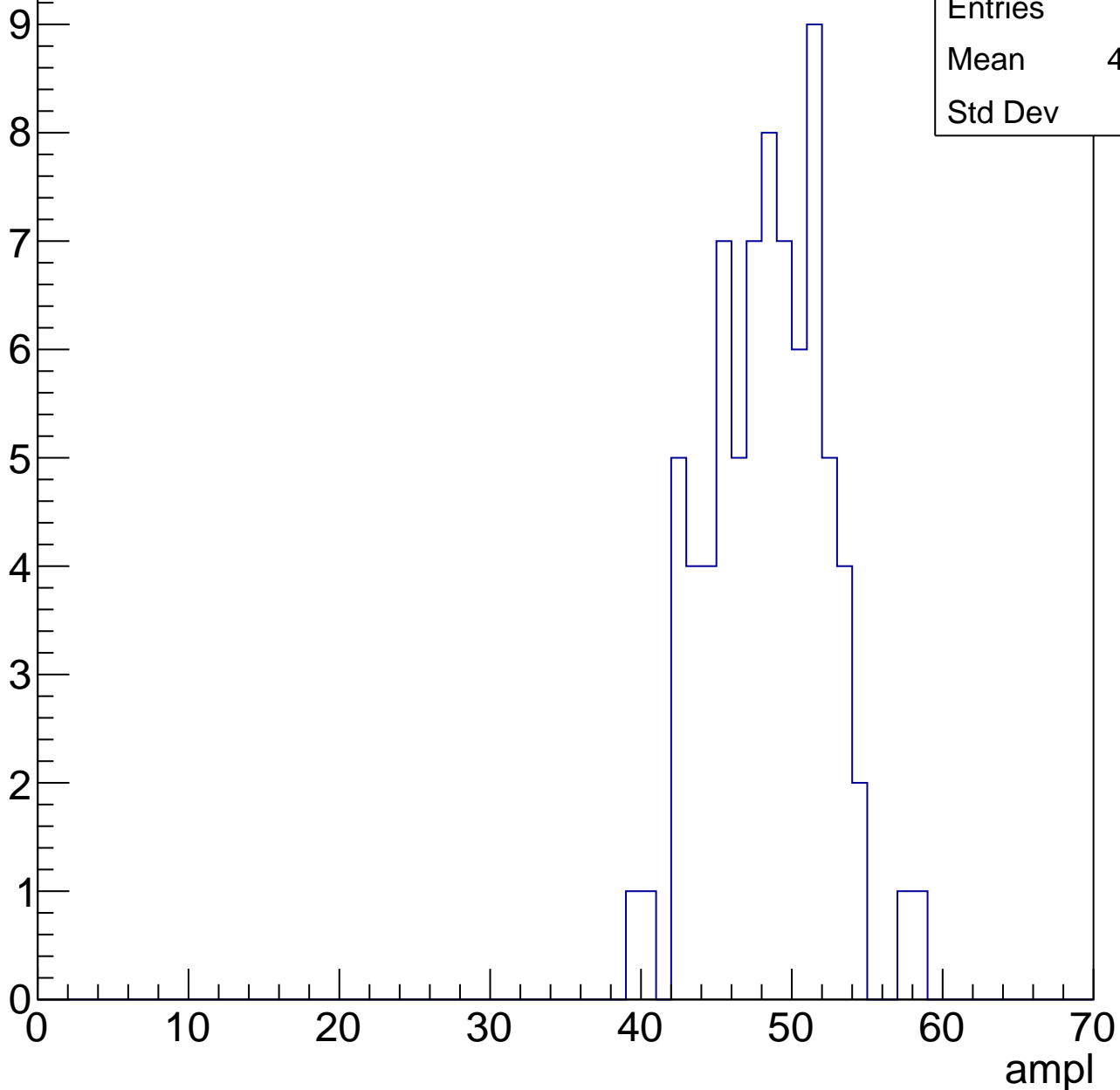


B1L003S, U16-ch40, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	47.95
Std Dev	3.82

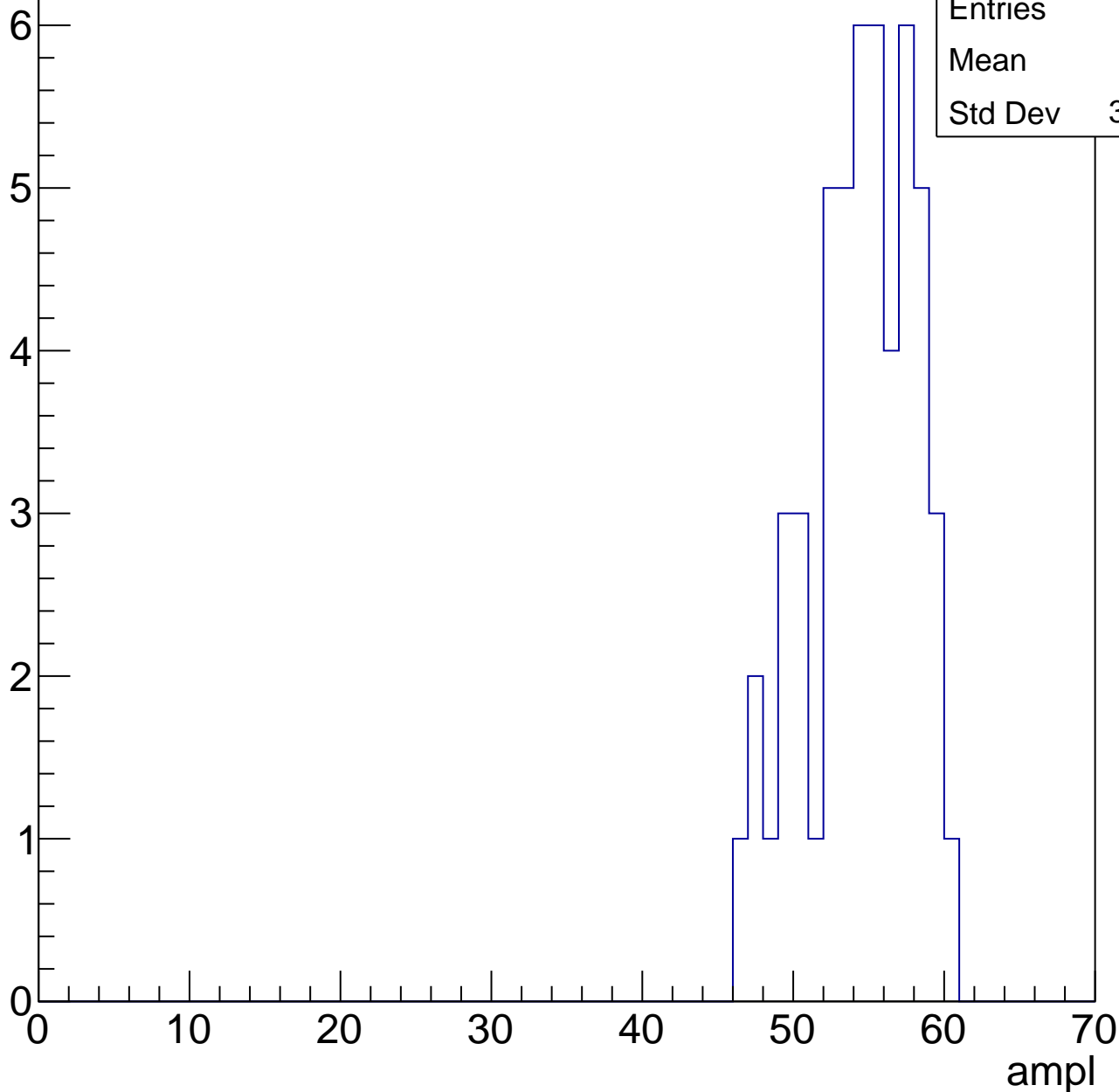


B1L003S, U16-ch40, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	54
Std Dev	3.459

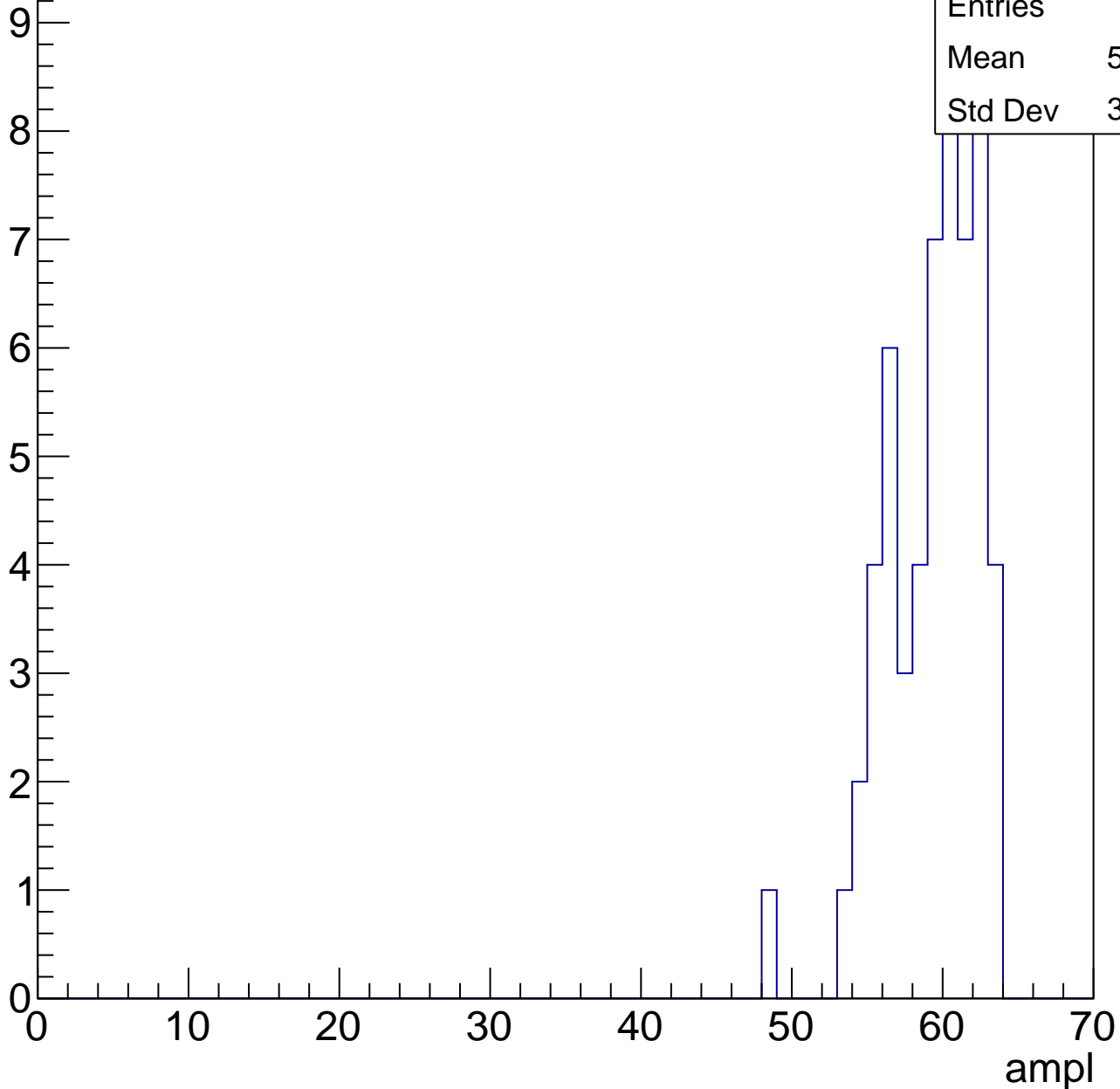


B1L003S, U16-ch40, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.86
Std Dev	3.026

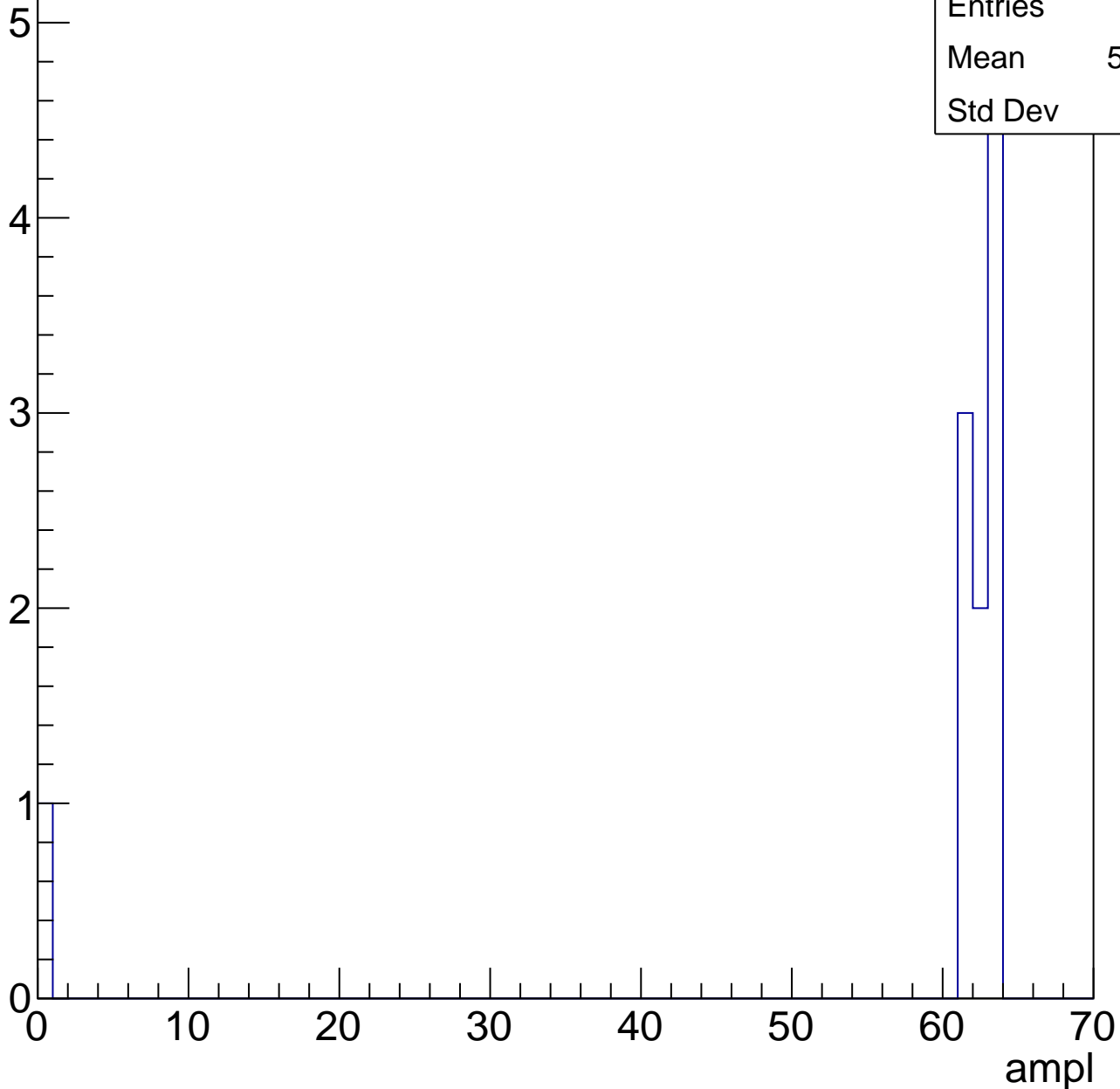


B1L003S, U16-ch40, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.55
Std Dev	17.9



B1L003S, U16-ch40, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch41, adc0

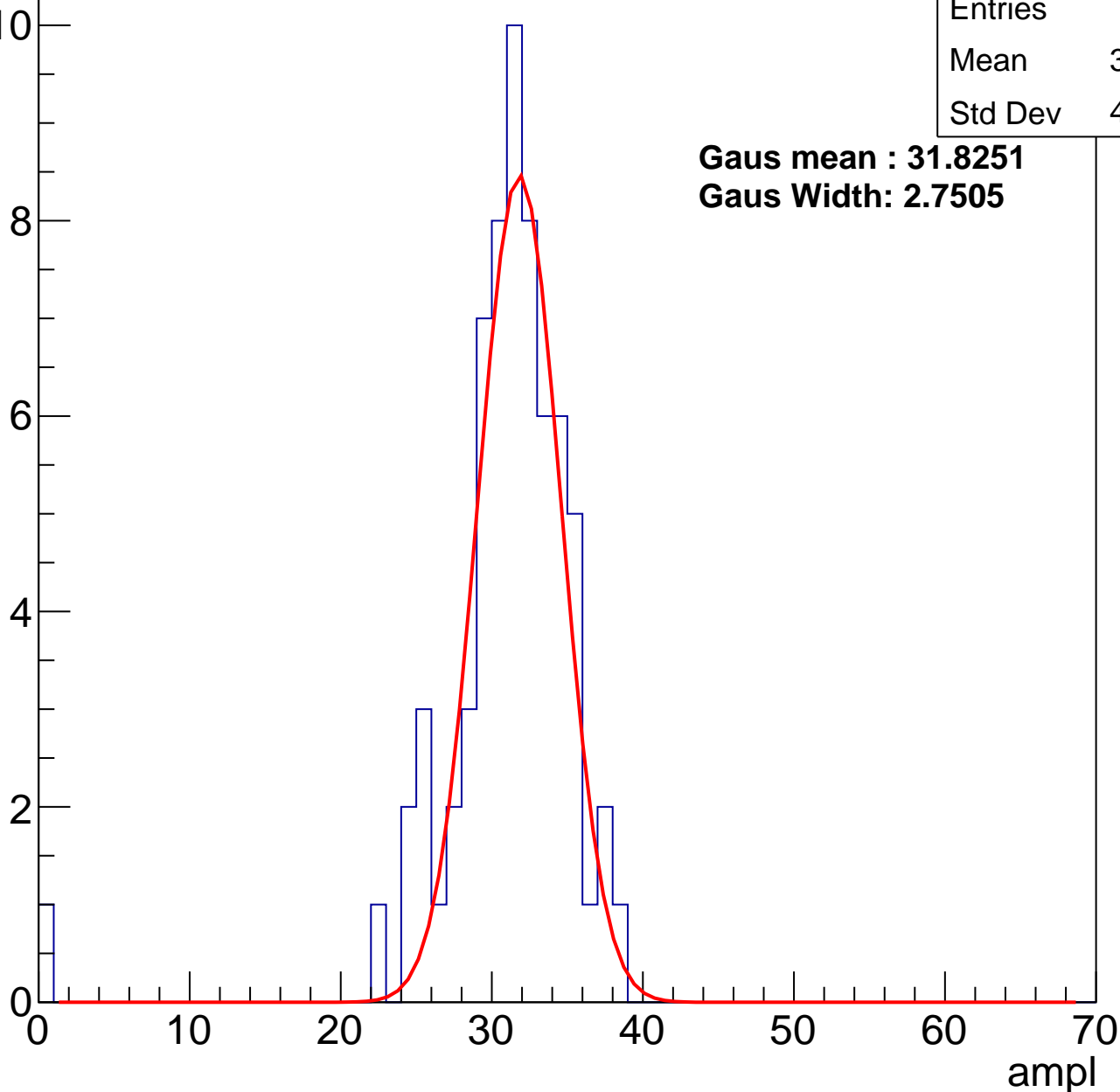
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	30.49
Std Dev	4.982

Gaus mean : 31.8251

Gaus Width: 2.7505



B1L003S, U16-ch41, adc1

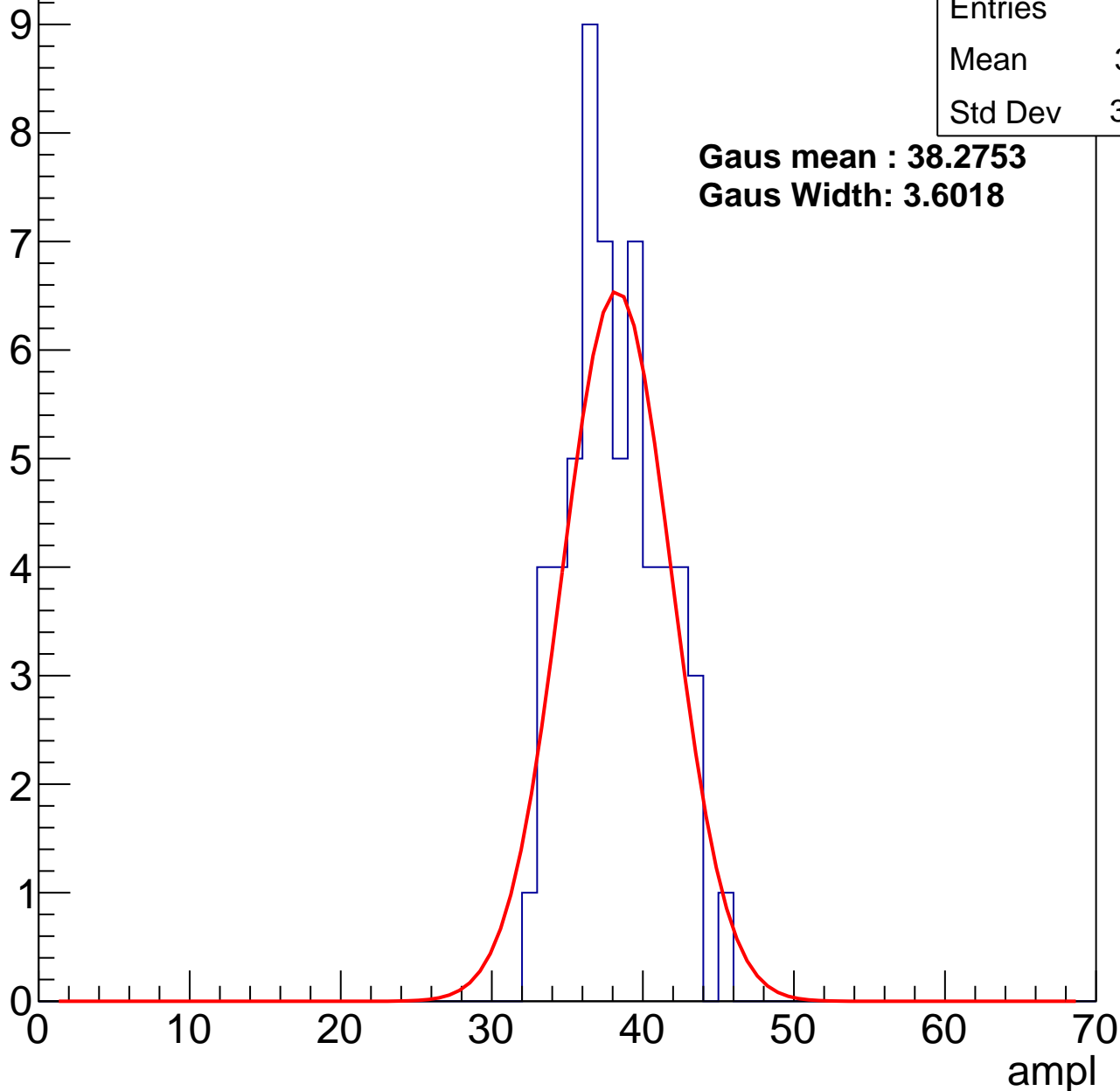
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	37.71
Std Dev	3.023

Gaus mean : 38.2753

Gaus Width: 3.6018



B1L003S, U16-ch41, adc2

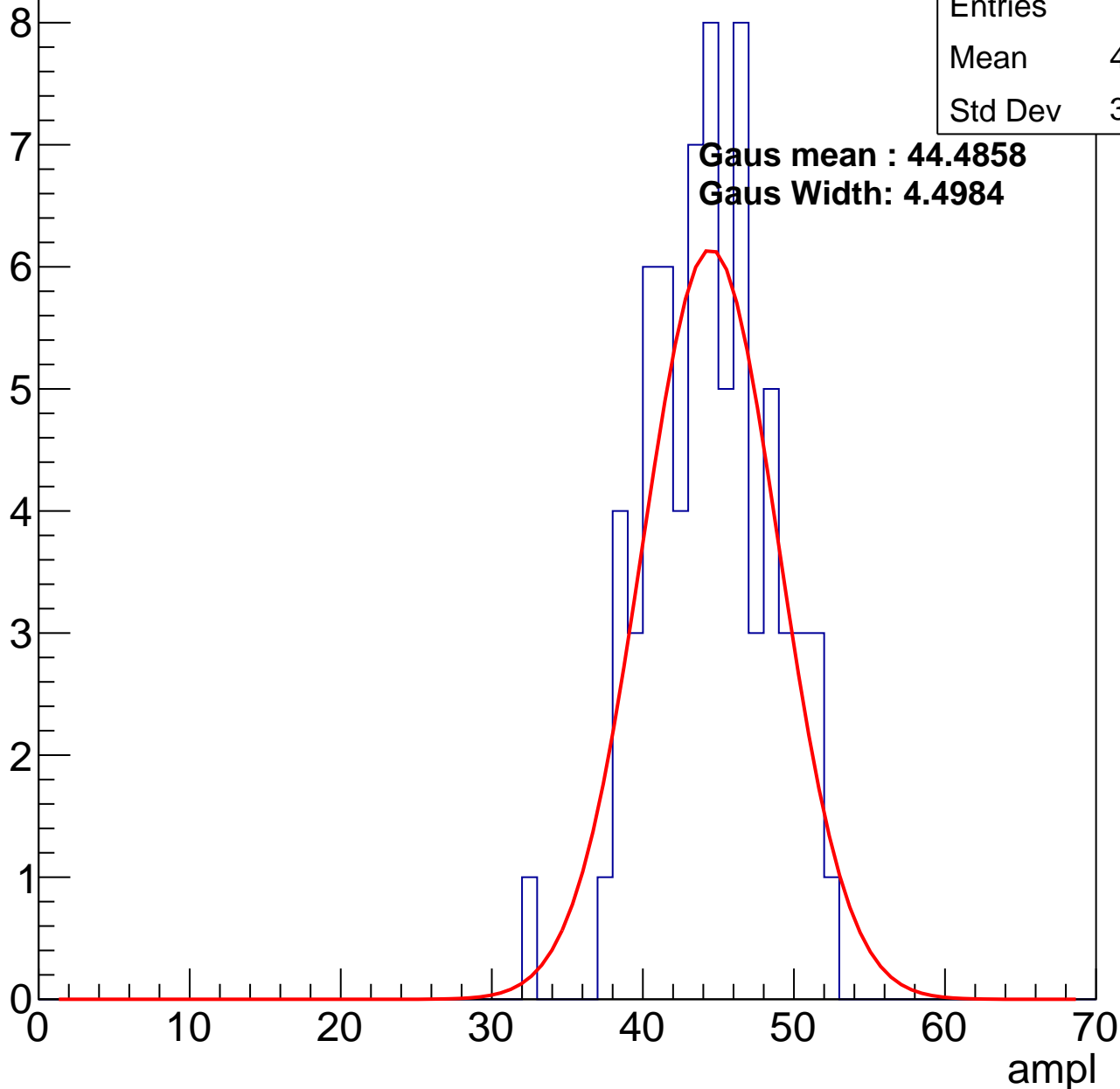
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.96
Std Dev	3.977

Gaus mean : 44.4858

Gaus Width: 4.4984

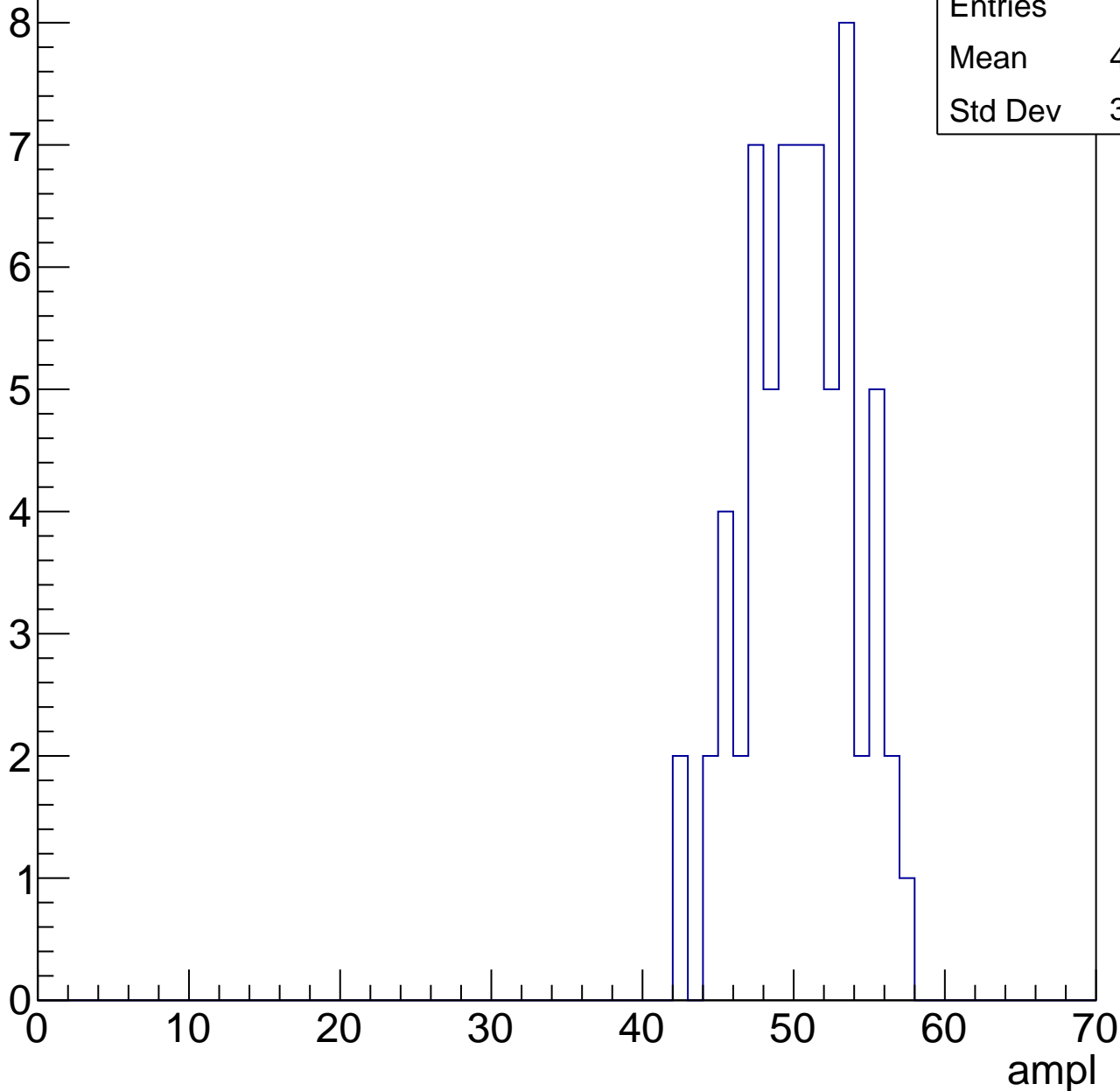


B1L003S, U16-ch41, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.98
Std Dev	3.479

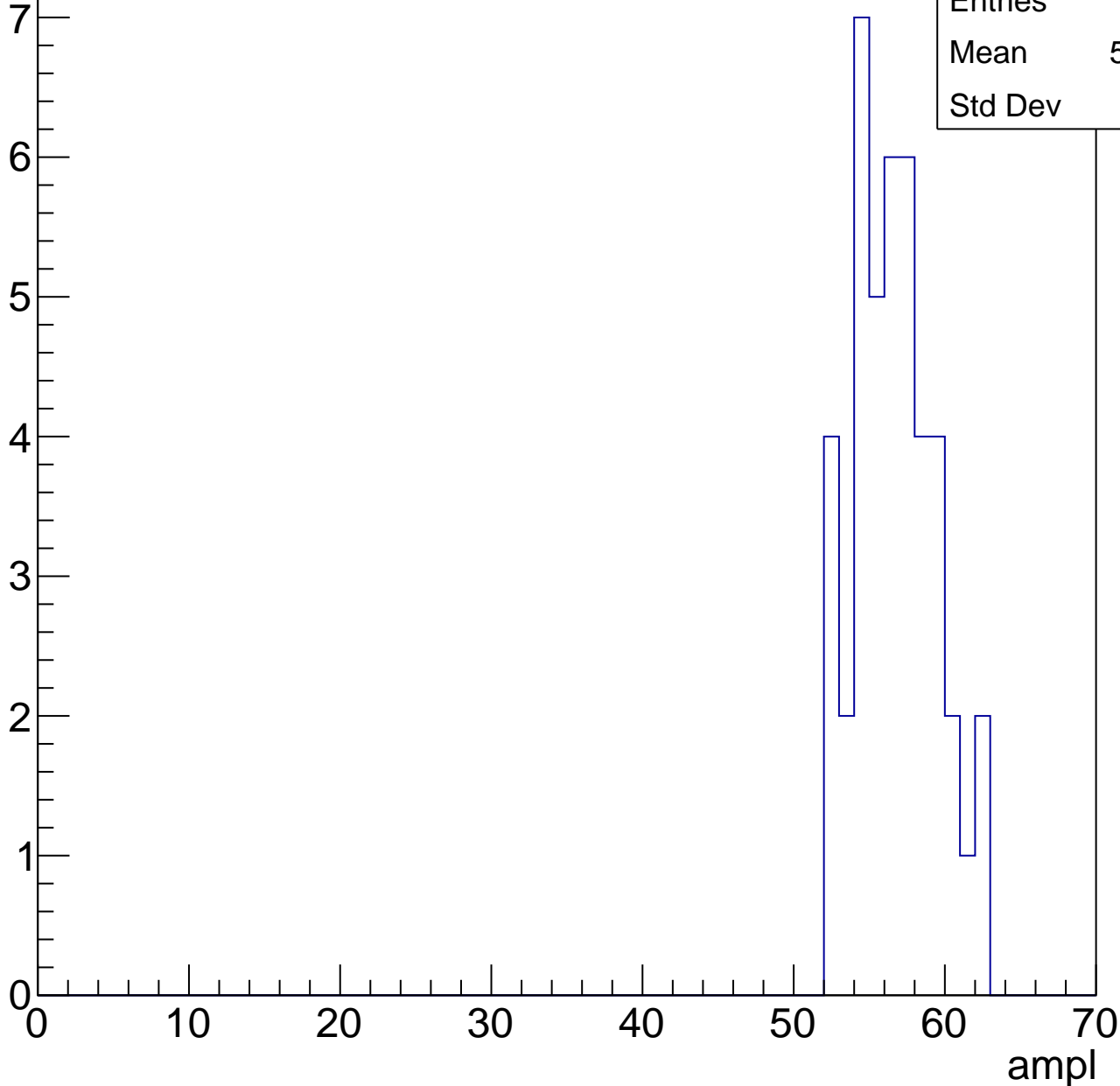


B1L003S, U16-ch41, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	56.23
Std Dev	2.64

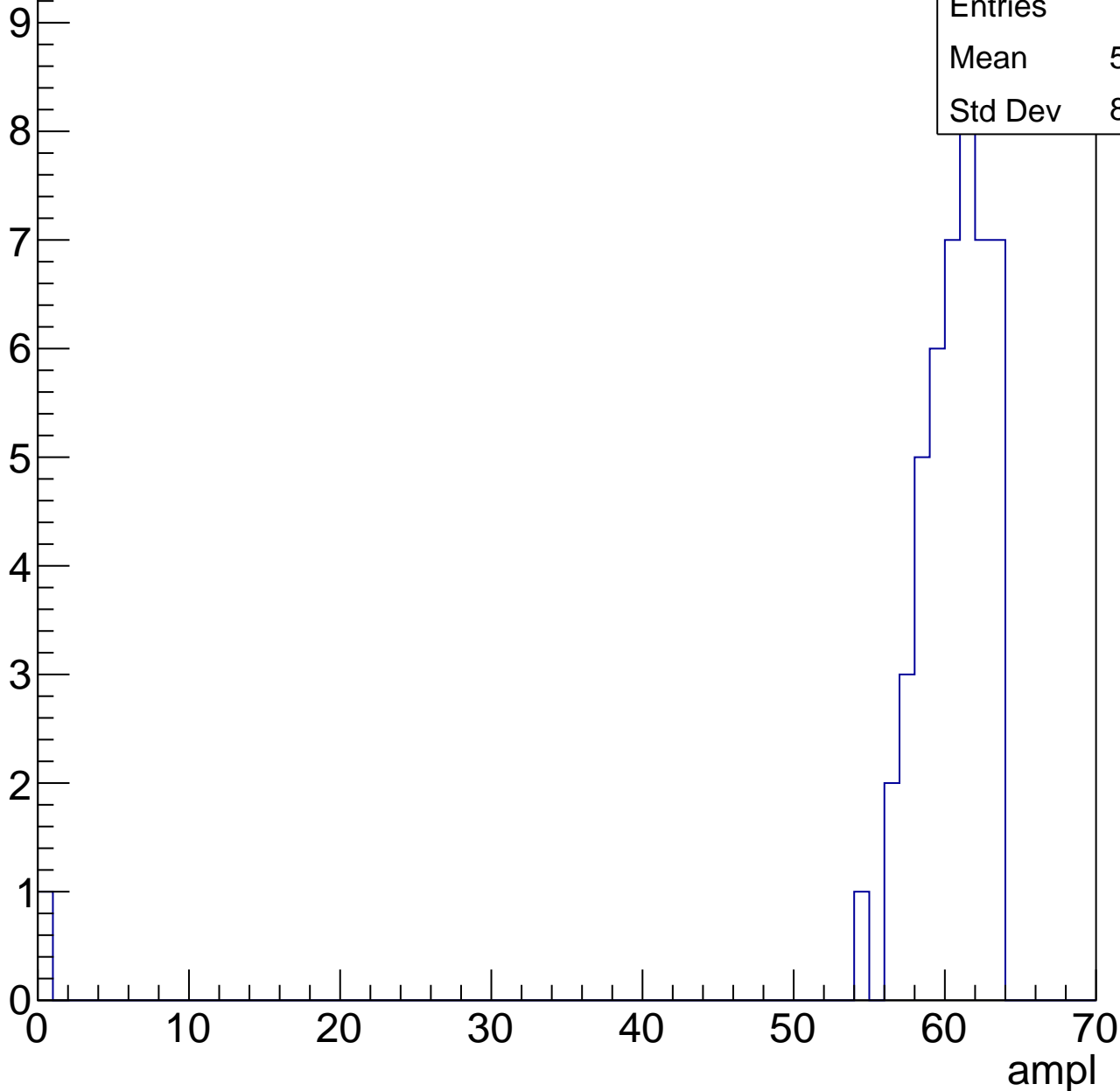


B1L003S, U16-ch41, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

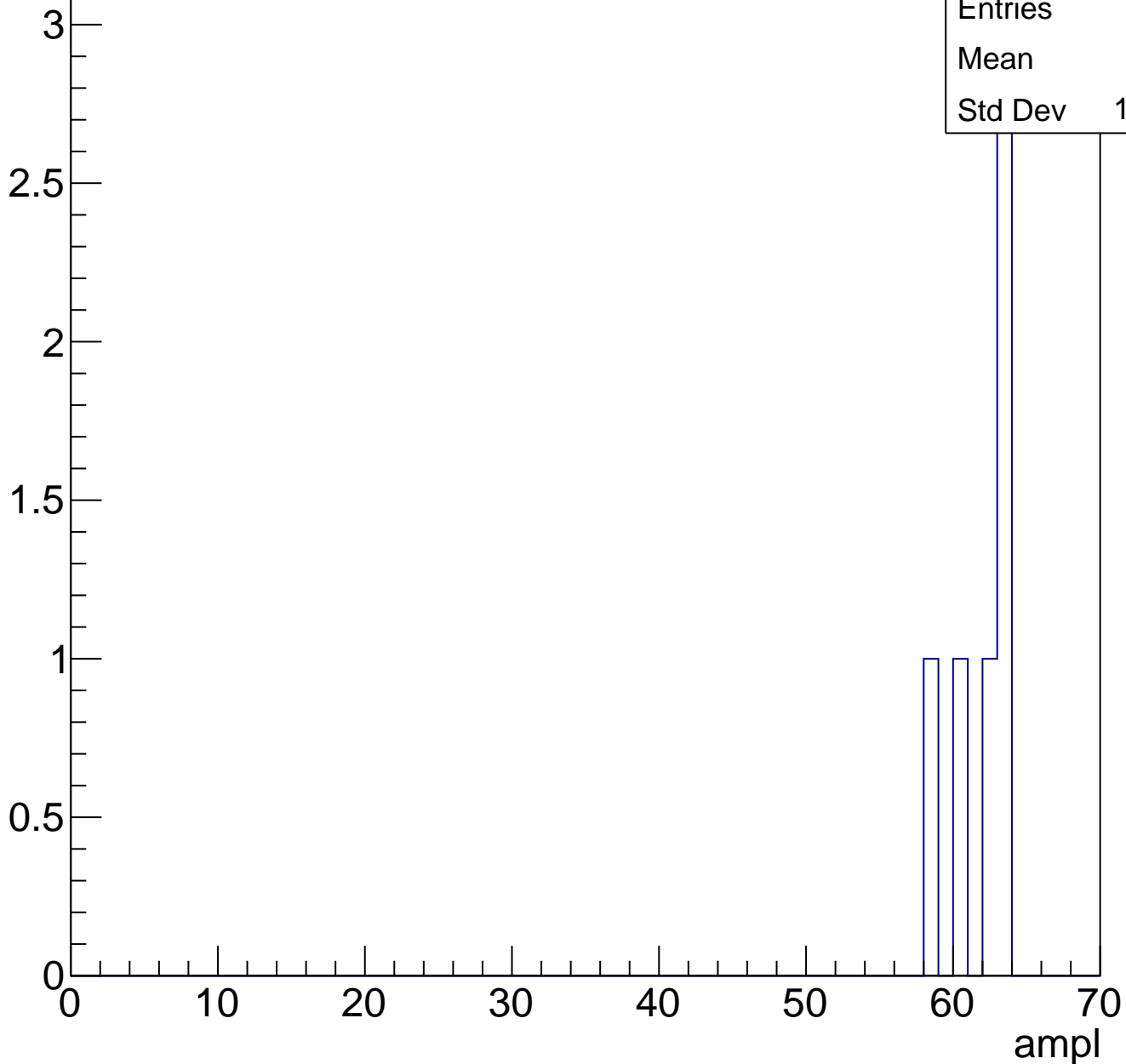
Entries	48
Mean	58.85
Std Dev	8.848



B1L003S, U16-ch41, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch41, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch42, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	28.67
Std Dev	3.555

Gaus mean : 29.1136

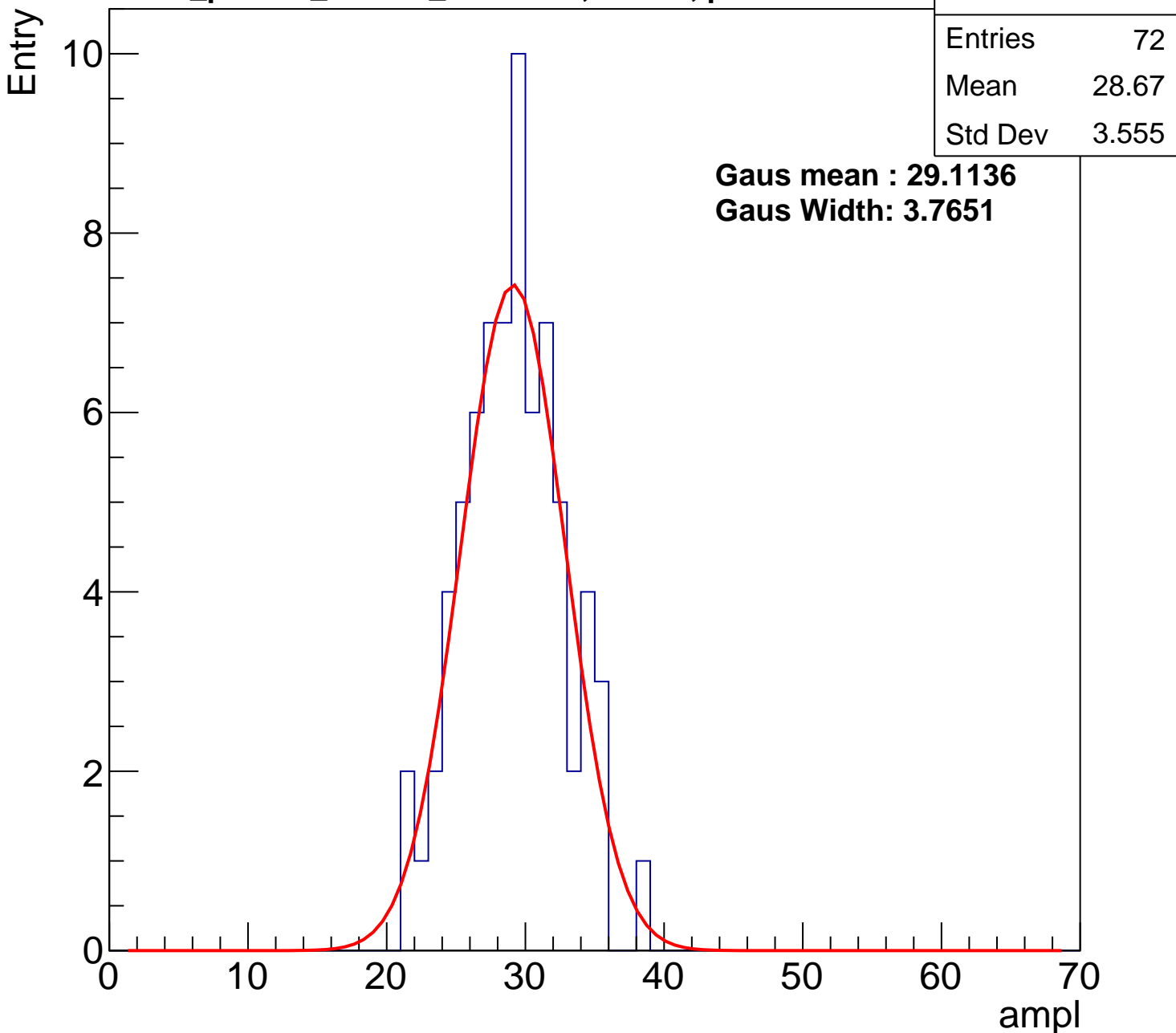
Gaus Width: 3.7651

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch42, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	34.77
Std Dev	3.431

Gaus mean : 34.8998

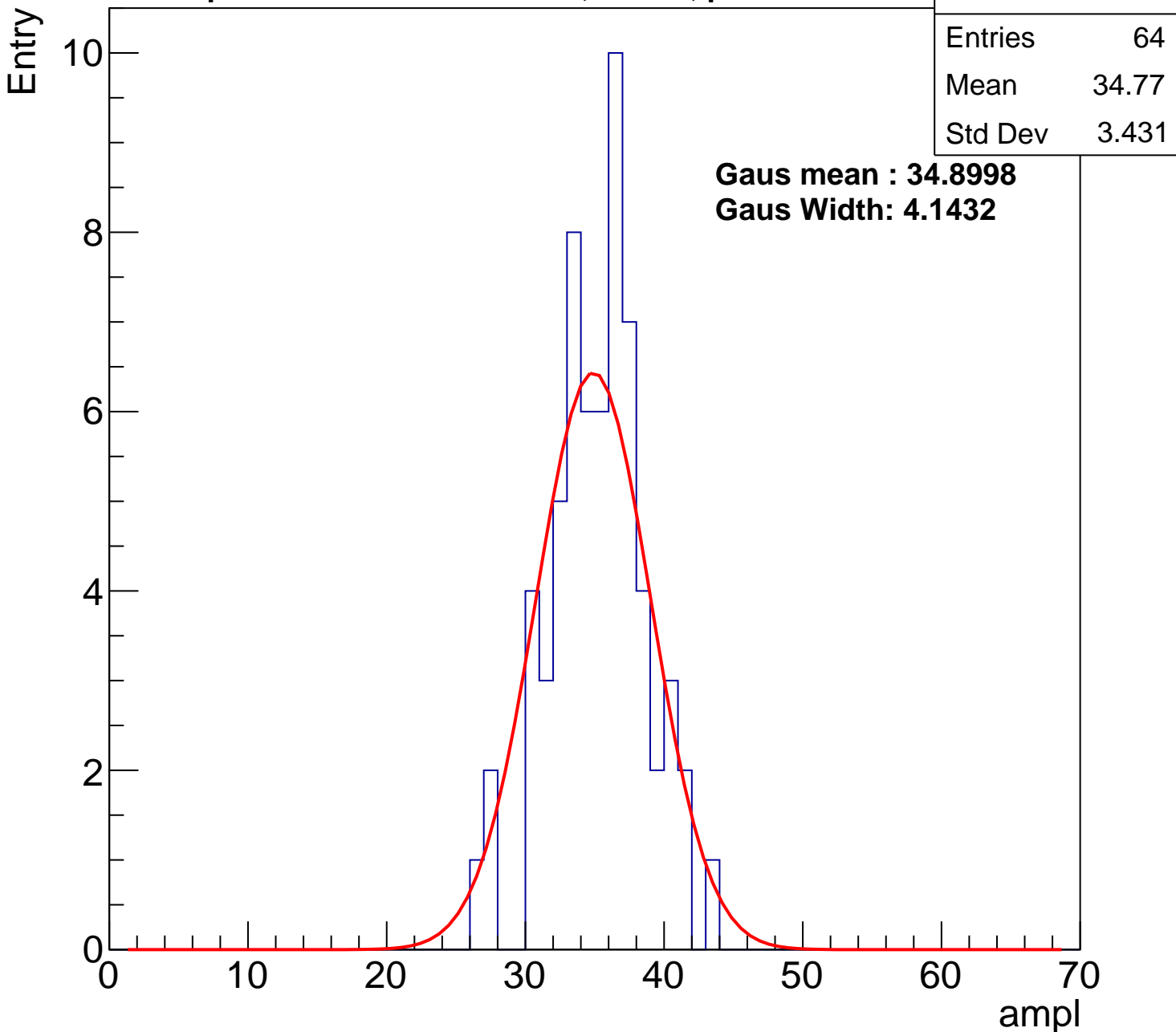
Gaus Width: 4.1432

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch42, adc2

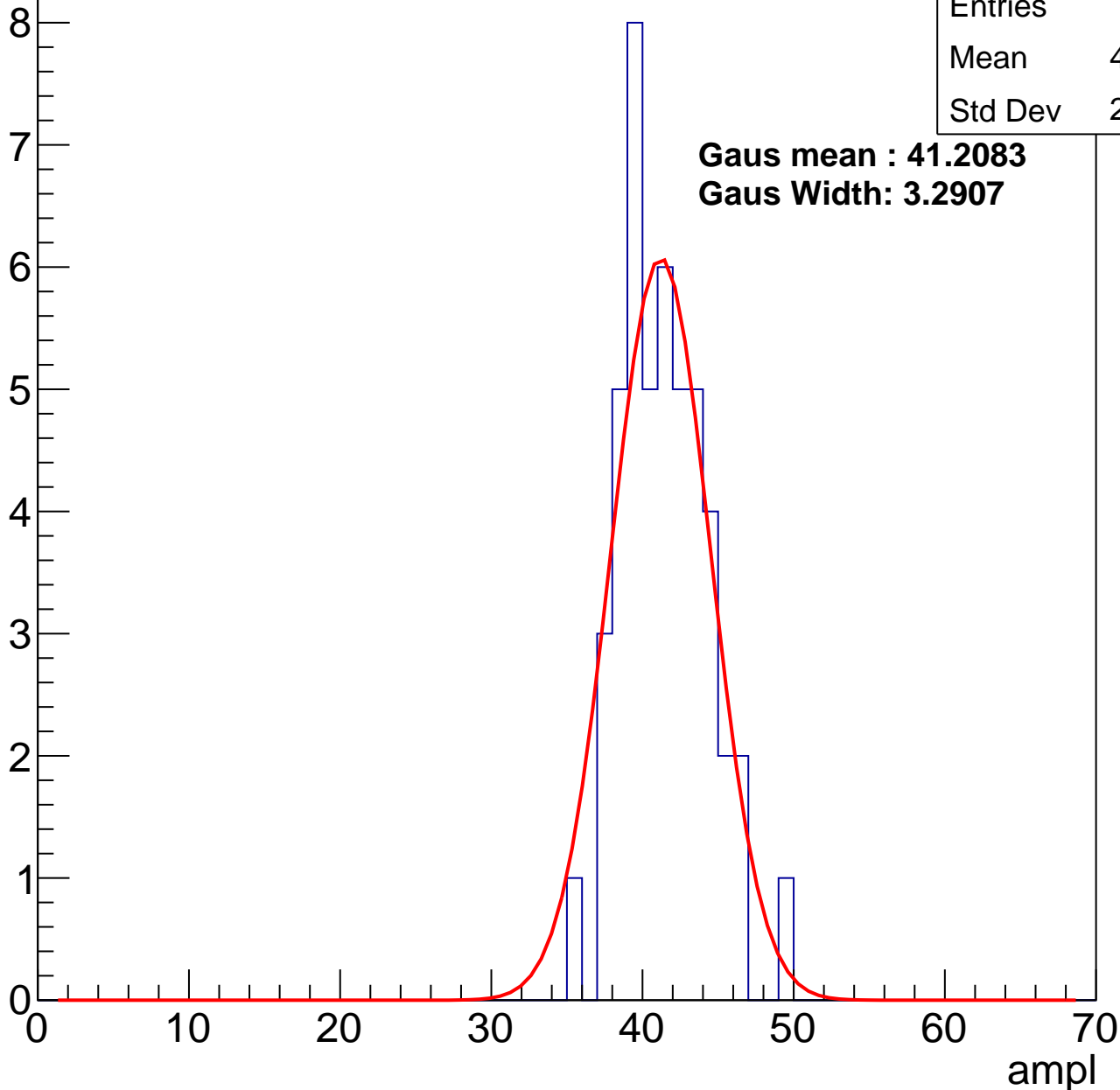
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	40.98
Std Dev	2.817

Gaus mean : 41.2083

Gaus Width: 3.2907

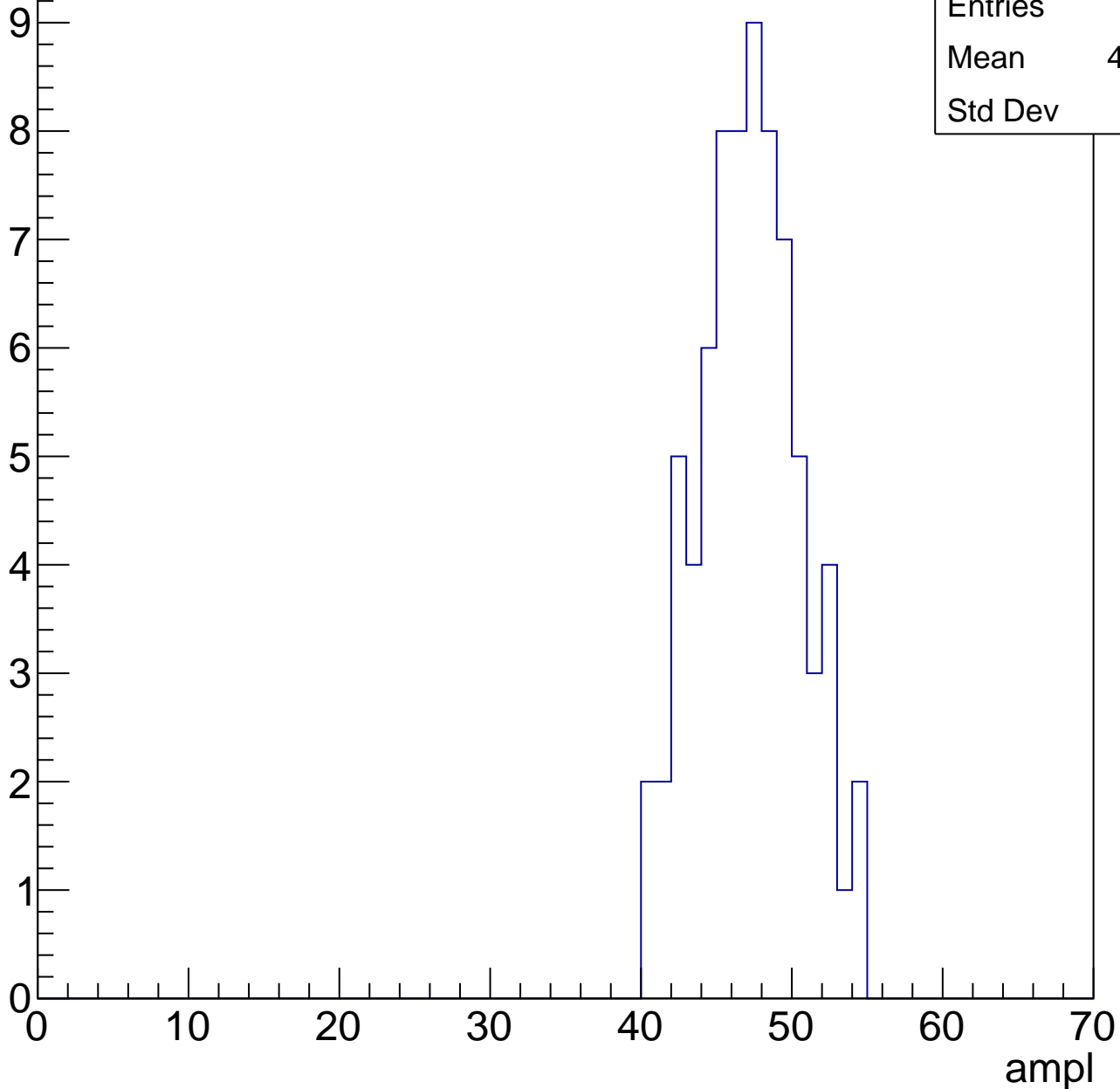


B1L003S, U16-ch42, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

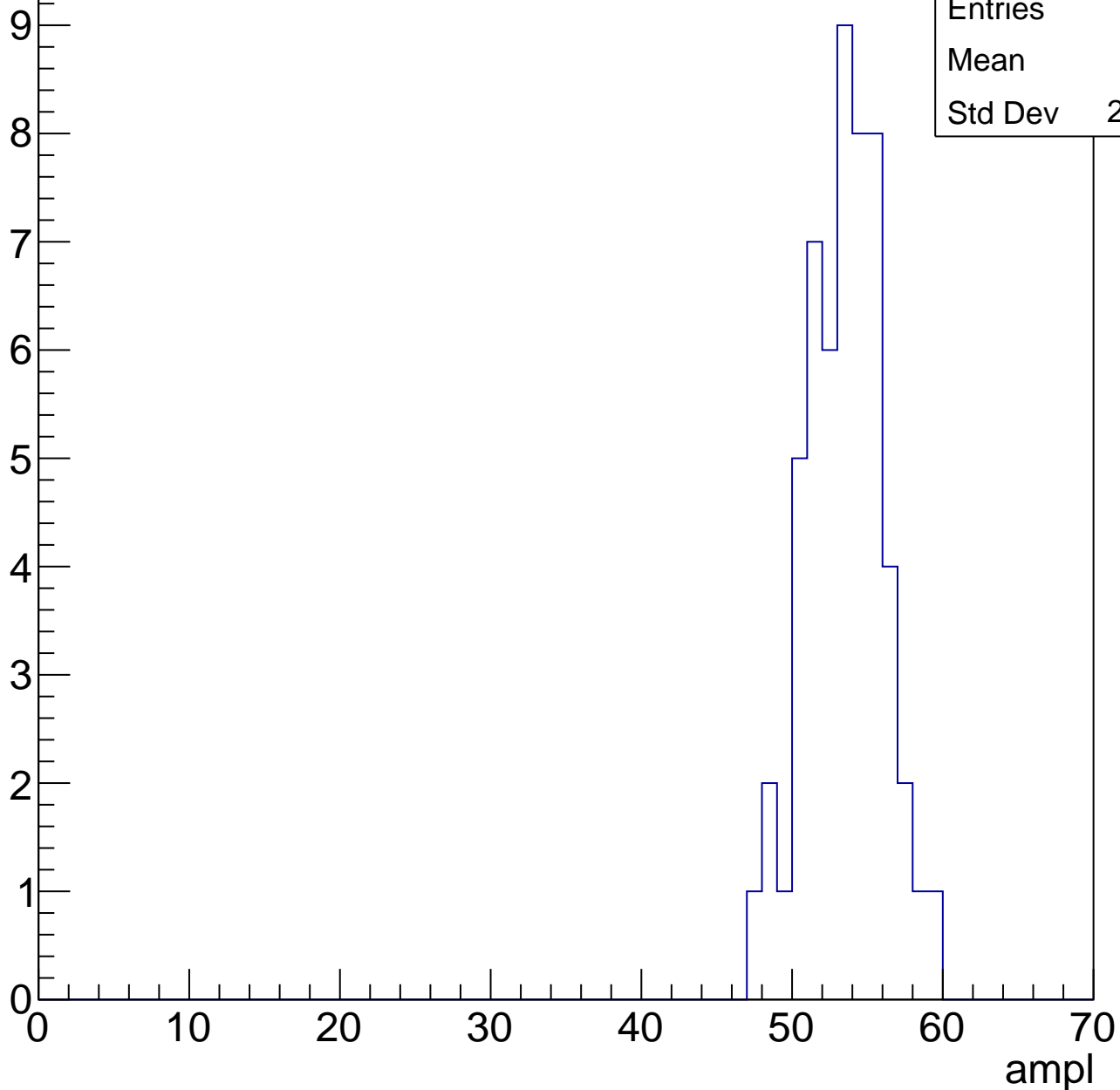
Entries	74
Mean	46.73
Std Dev	3.31



B1L003S, U16-ch42, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

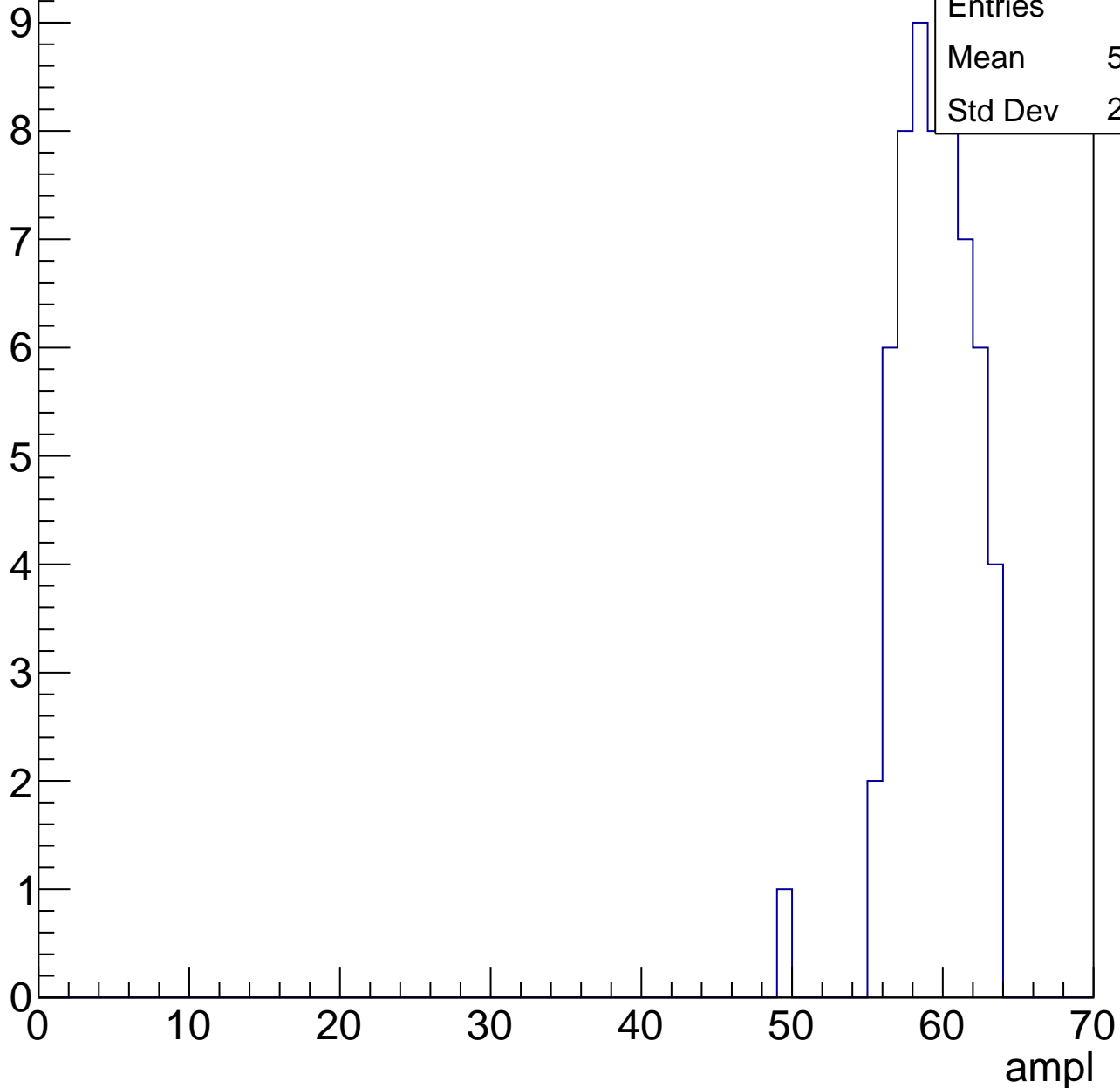


Entries	55
Mean	53
Std Dev	2.523

B1L003S, U16-ch42, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

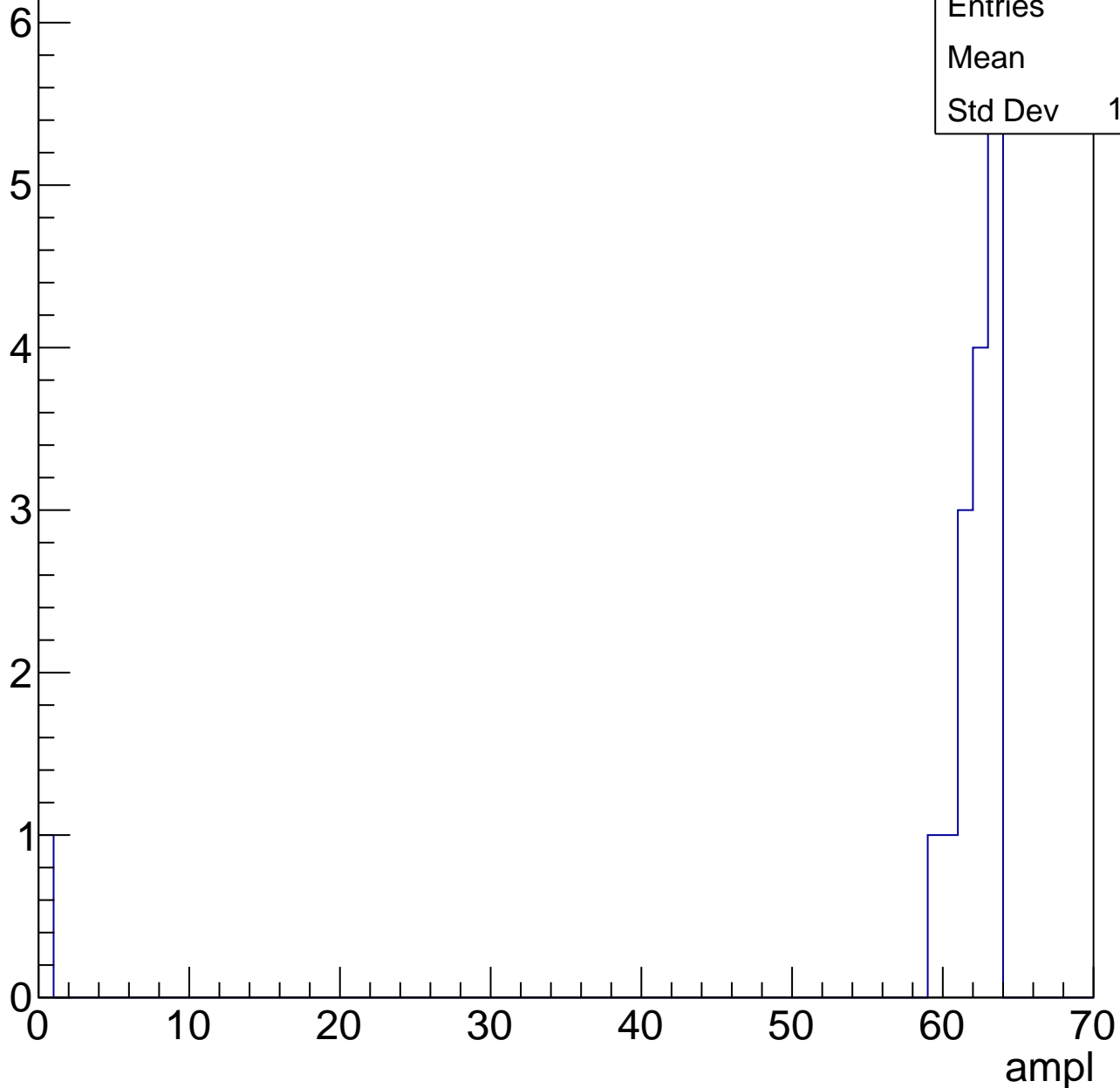


B1L003S, U16-ch42, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

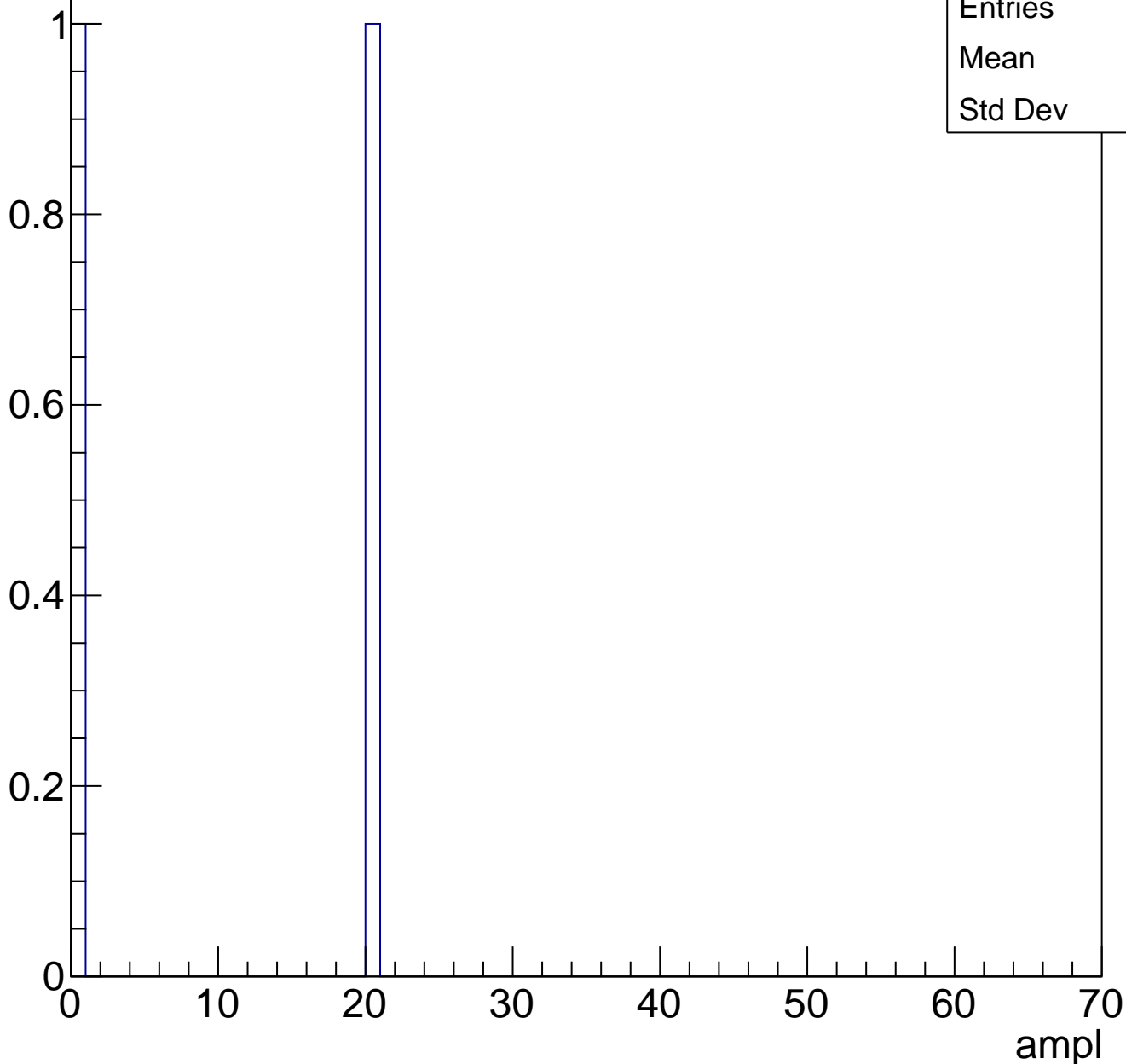
Entries	16
Mean	58
Std Dev	15.02



B1L003S, U16-ch42, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10
Std Dev	10

B1L003S, U16-ch43, adc0

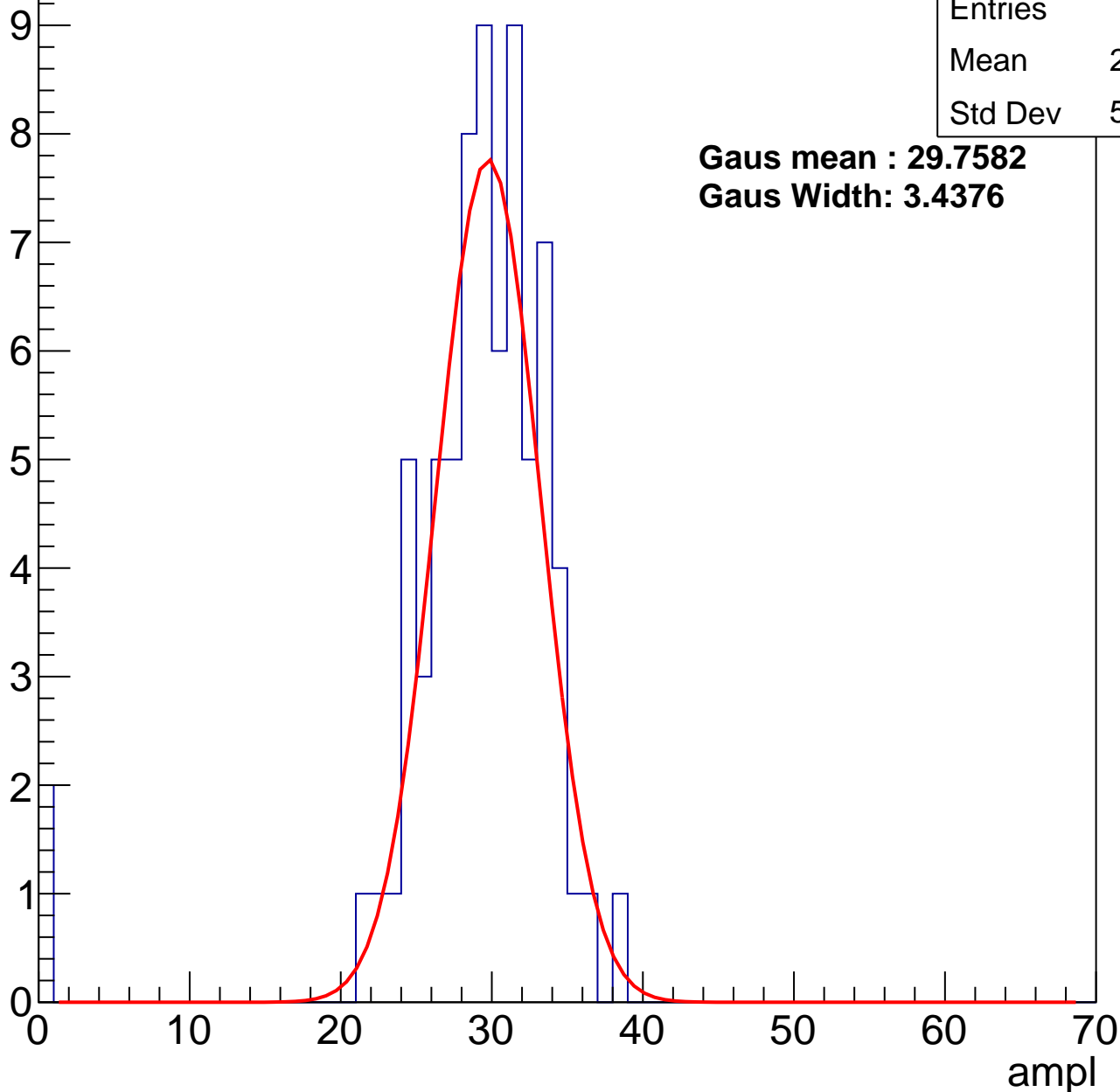
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	28.46
Std Dev	5.836

Gaus mean : 29.7582

Gaus Width: 3.4376



B1L003S, U16-ch43, adc1

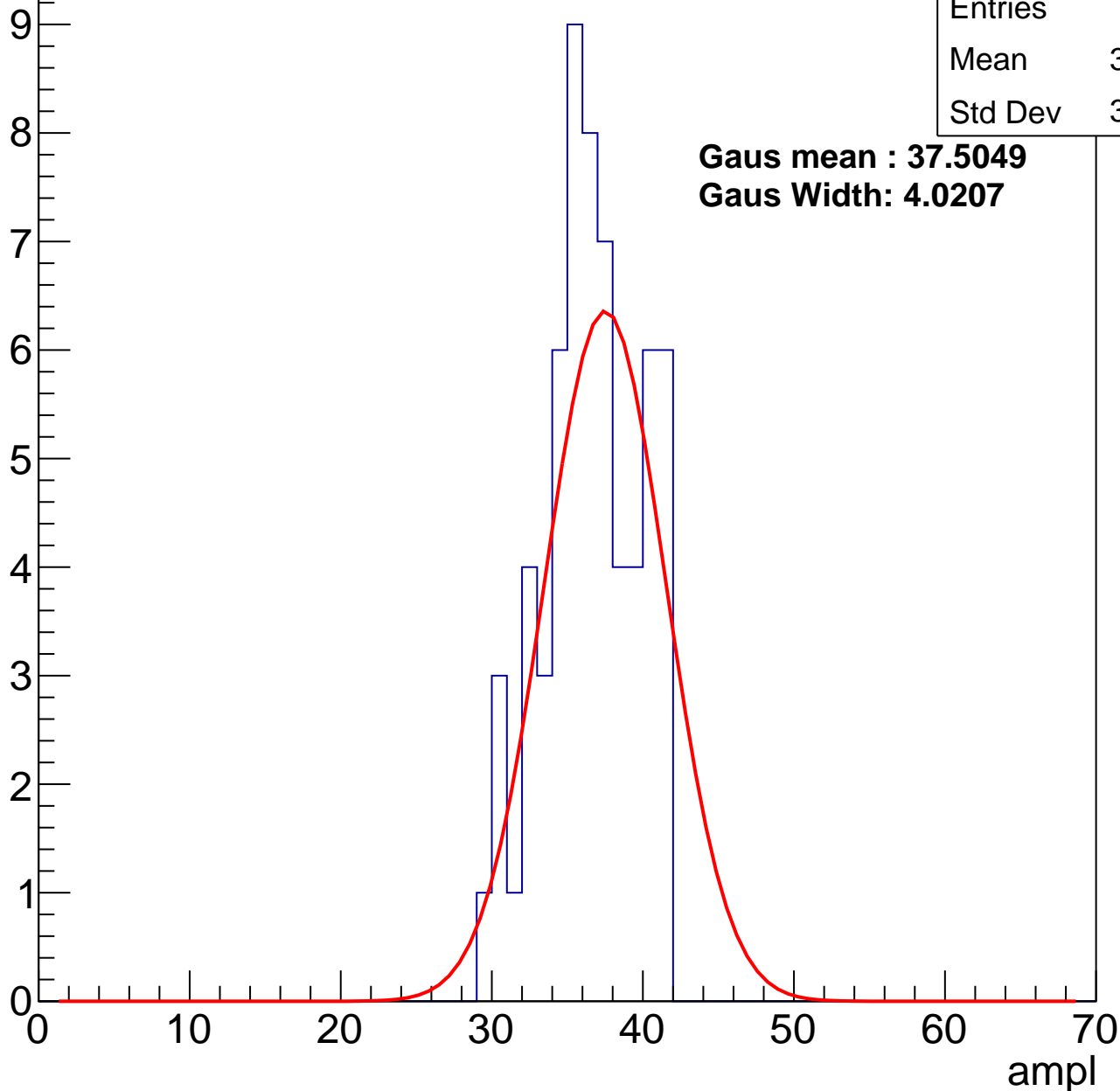
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.08
Std Dev	3.138

Gaus mean : 37.5049

Gaus Width: 4.0207



B1L003S, U16-ch43, adc2

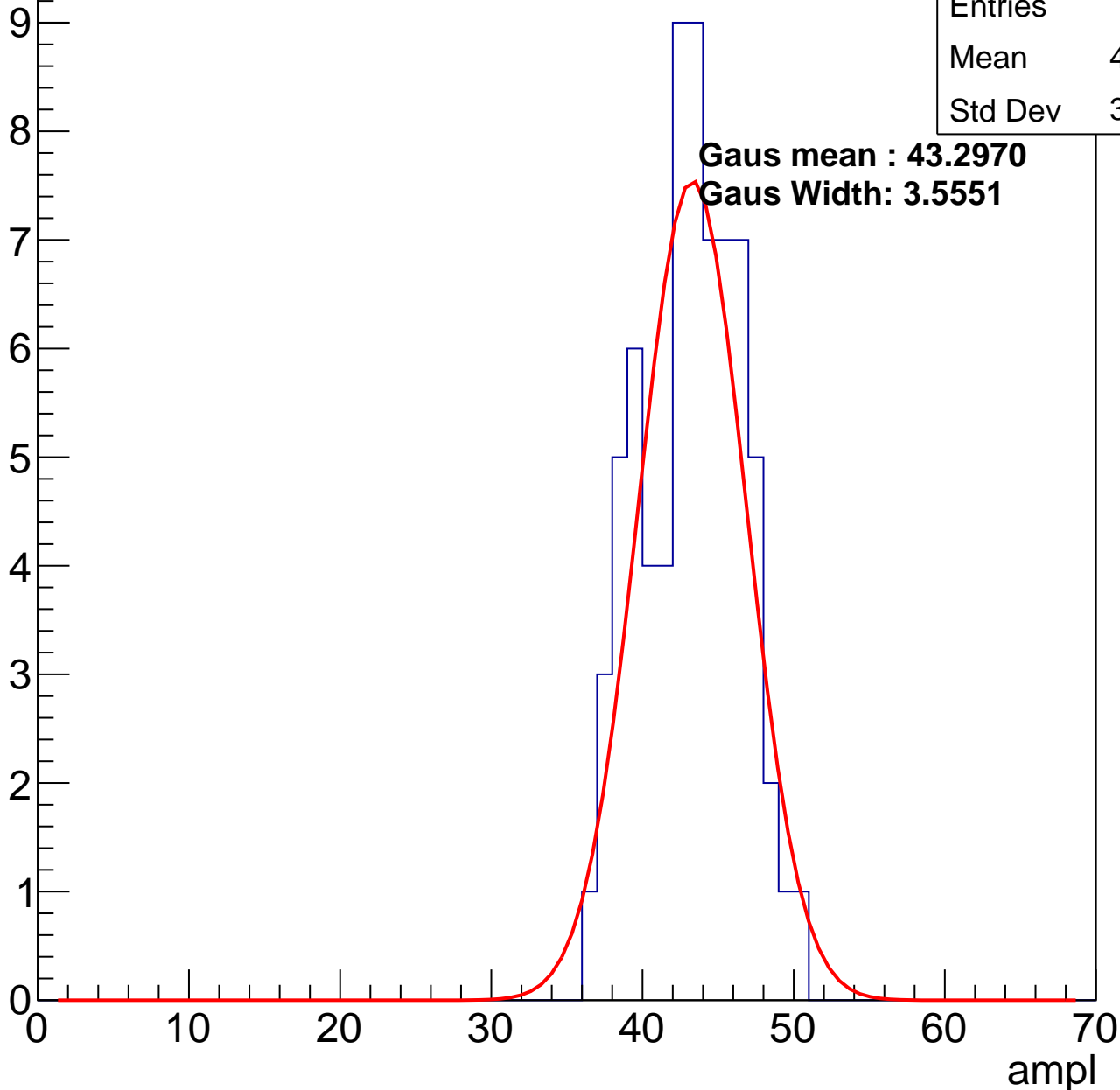
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42.75
Std Dev	3.245

Gaus mean : 43.2970

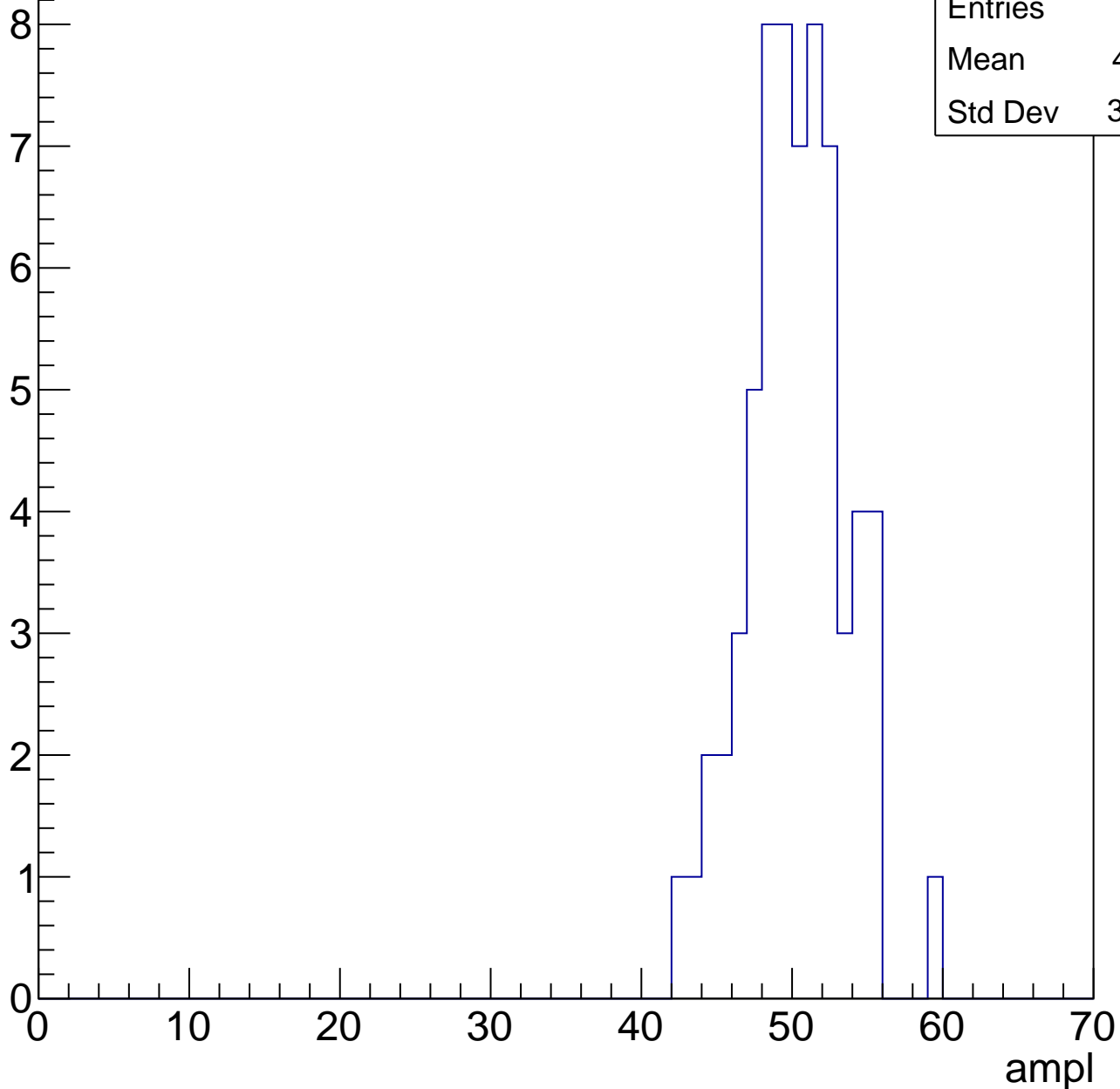
Gaus Width: 3.5551



B1L003S, U16-ch43, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

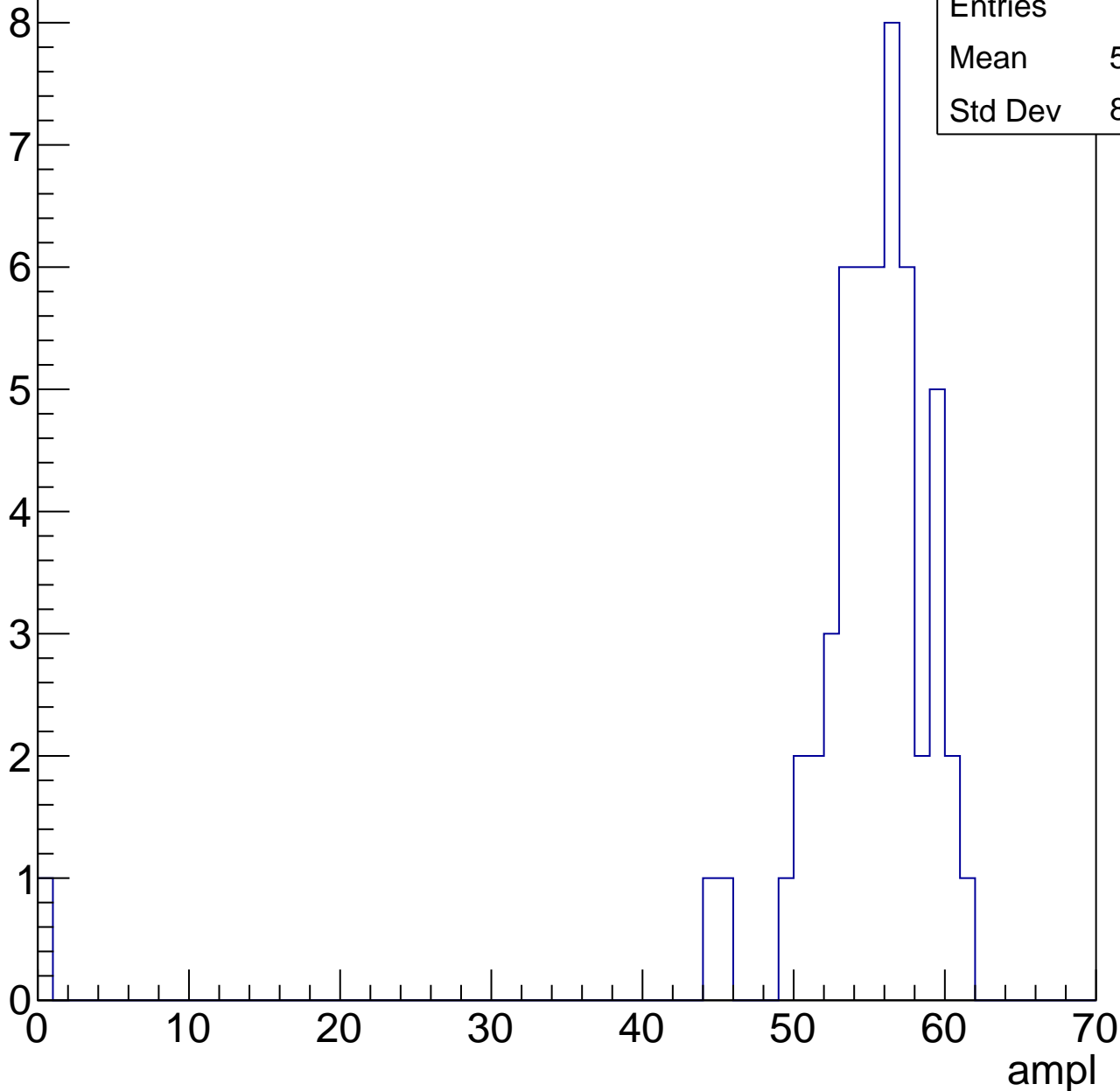


B1L003S, U16-ch43, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

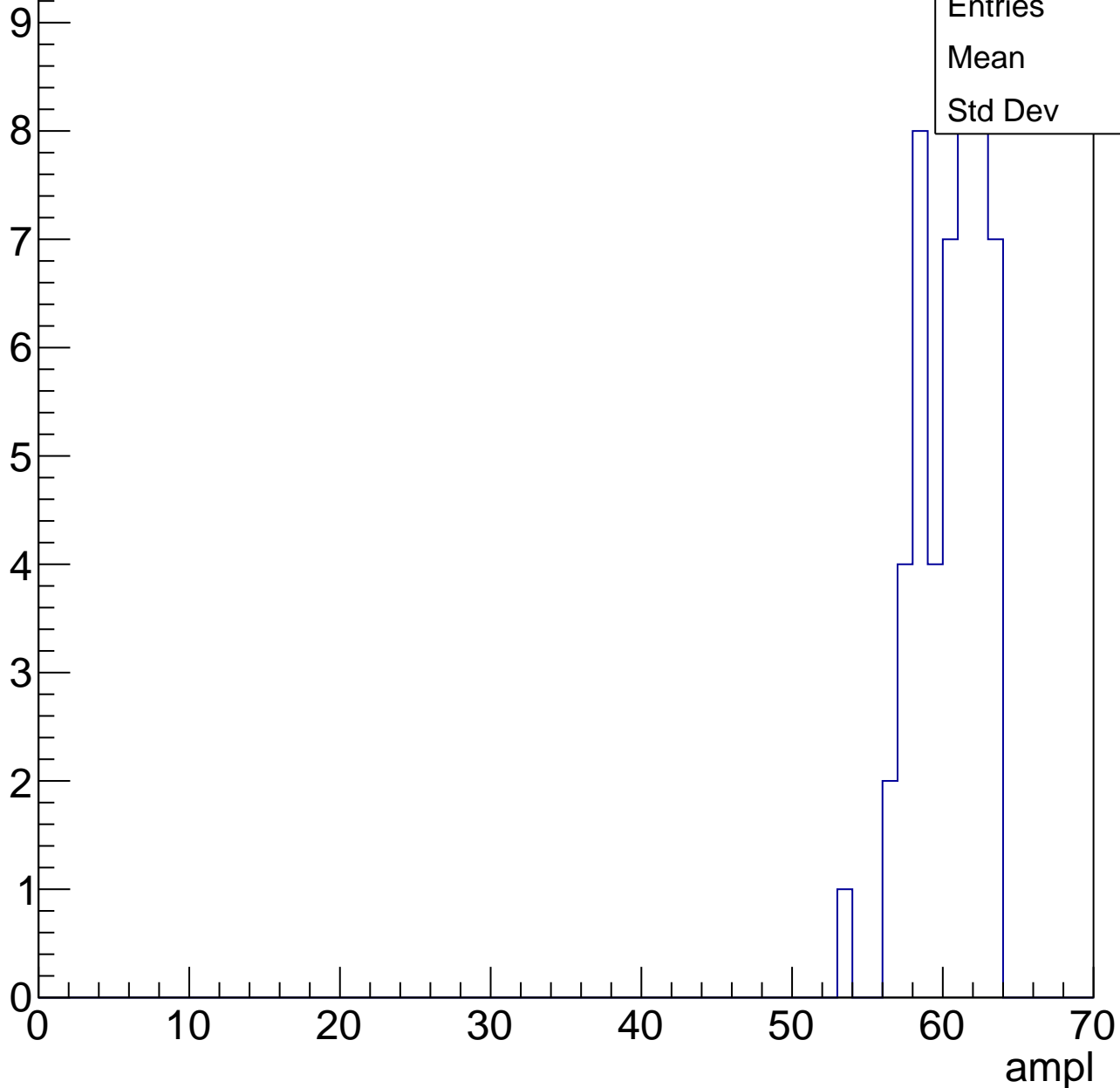
Entries	53
Mean	53.77
Std Dev	8.193



B1L003S, U16-ch43, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch43, adc6

calib_packv5_042523_0143.root, FC#13, port D2

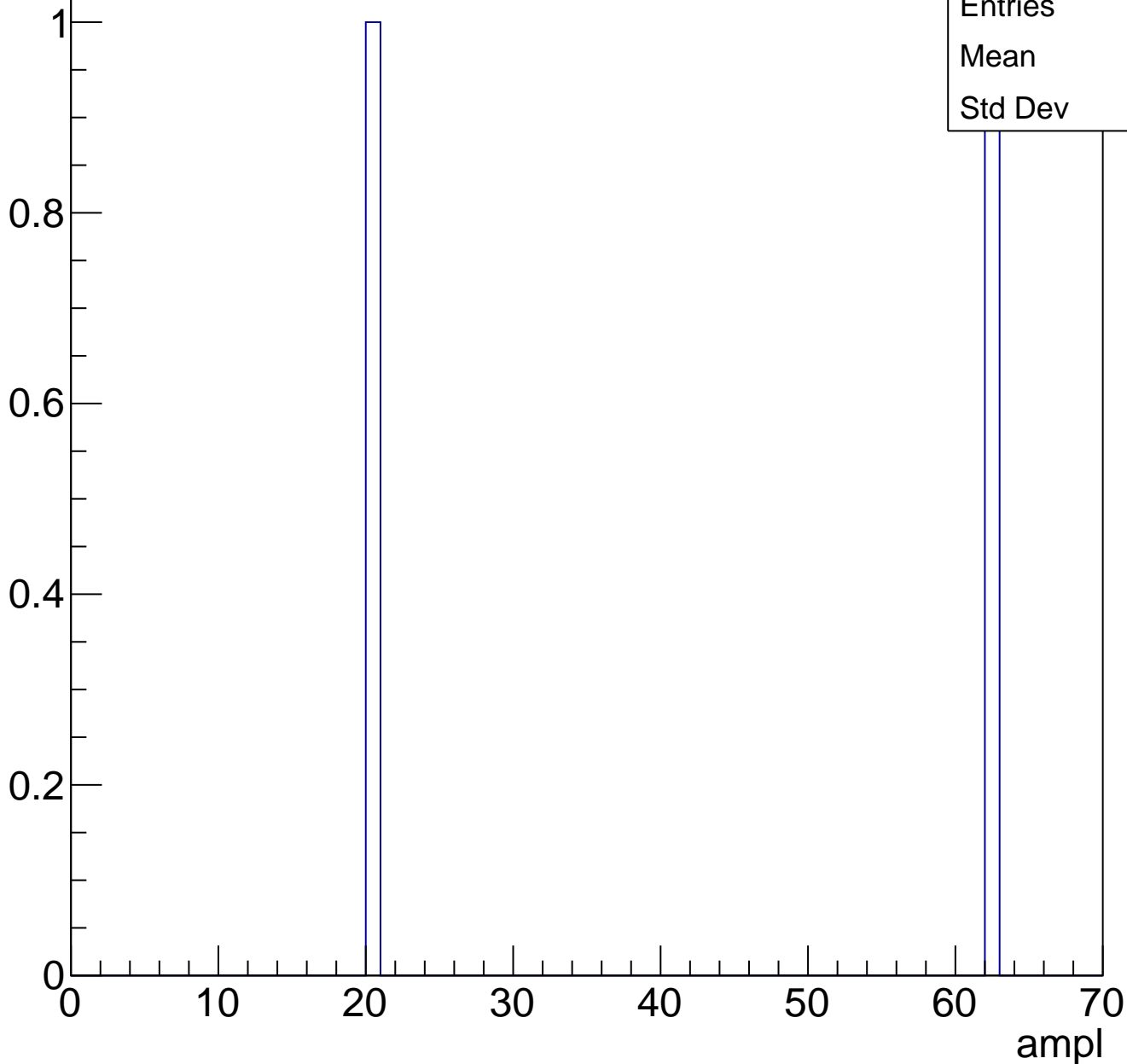
Entry



B1L003S, U16-ch43, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch44, adc0

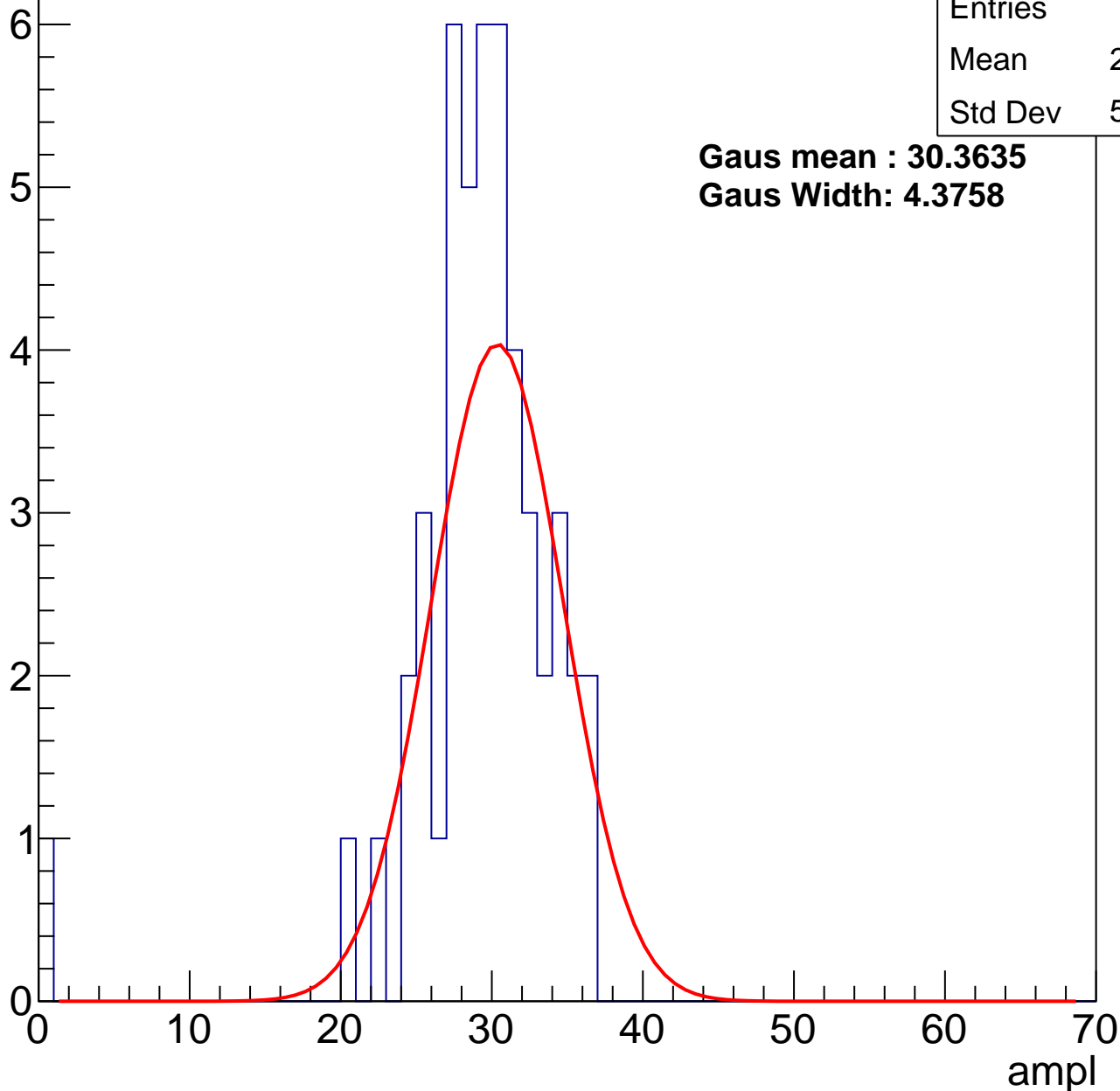
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	28.69
Std Dev	5.455

Gaus mean : 30.3635

Gaus Width: 4.3758



B1L003S, U16-ch44, adc1

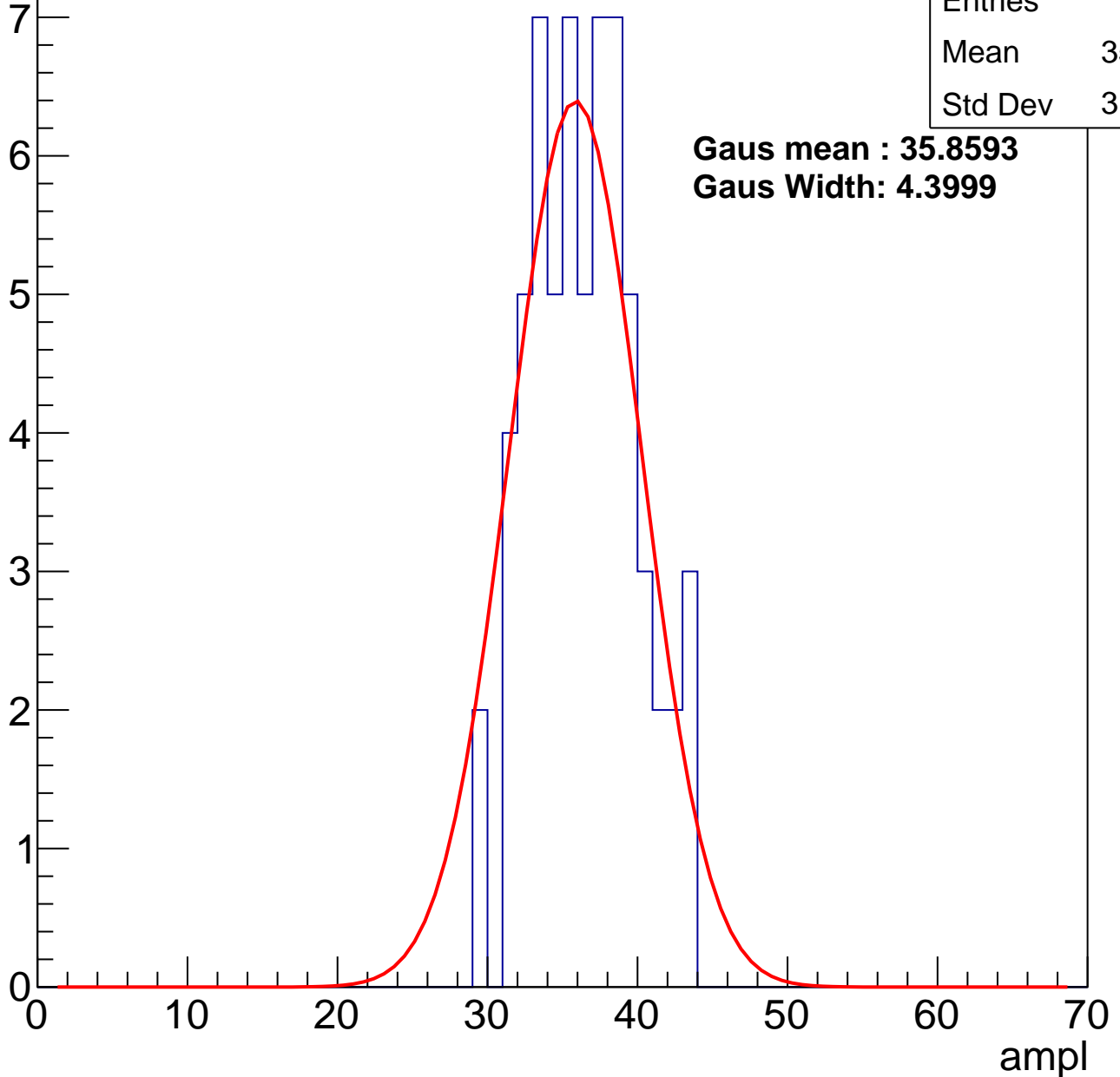
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.98
Std Dev	3.457

Gaus mean : 35.8593

Gaus Width: 4.3999



B1L003S, U16-ch44, adc2

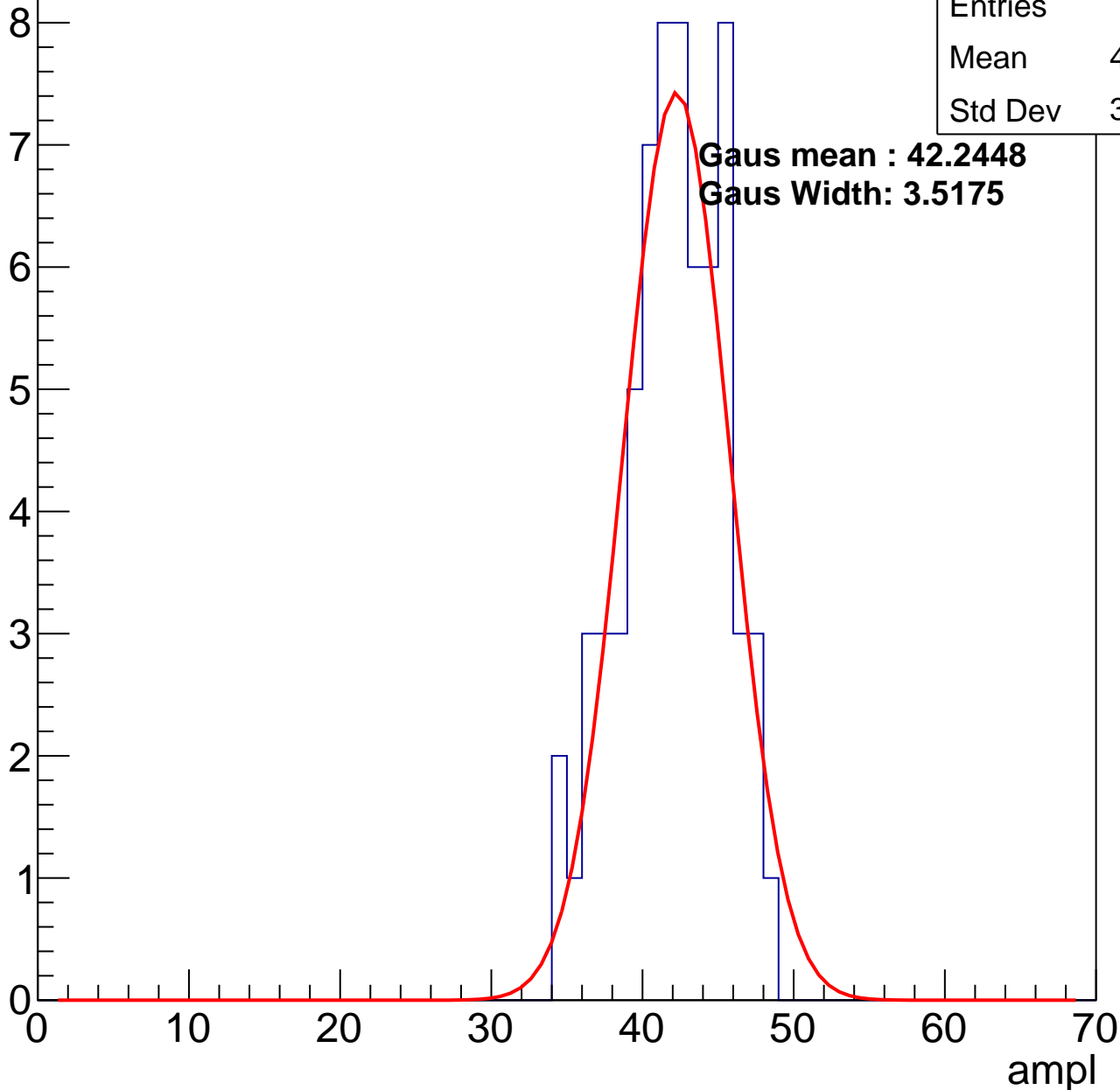
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.55
Std Dev	3.316

Gaus mean : 42.2448

Gaus Width: 3.5175

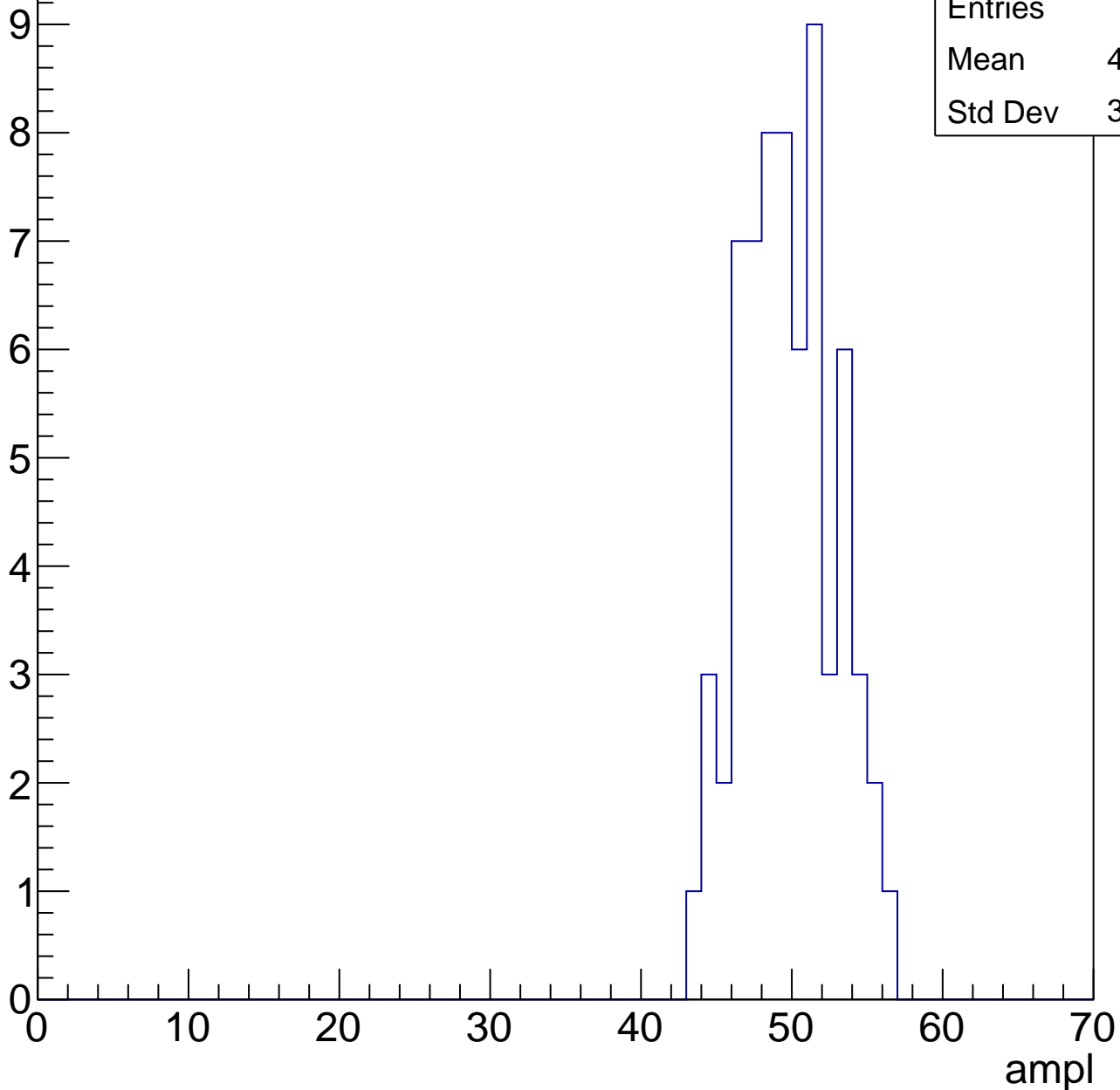


B1L003S, U16-ch44, adc3

calib_packv5_042523_0143.root, FC#13, port D2

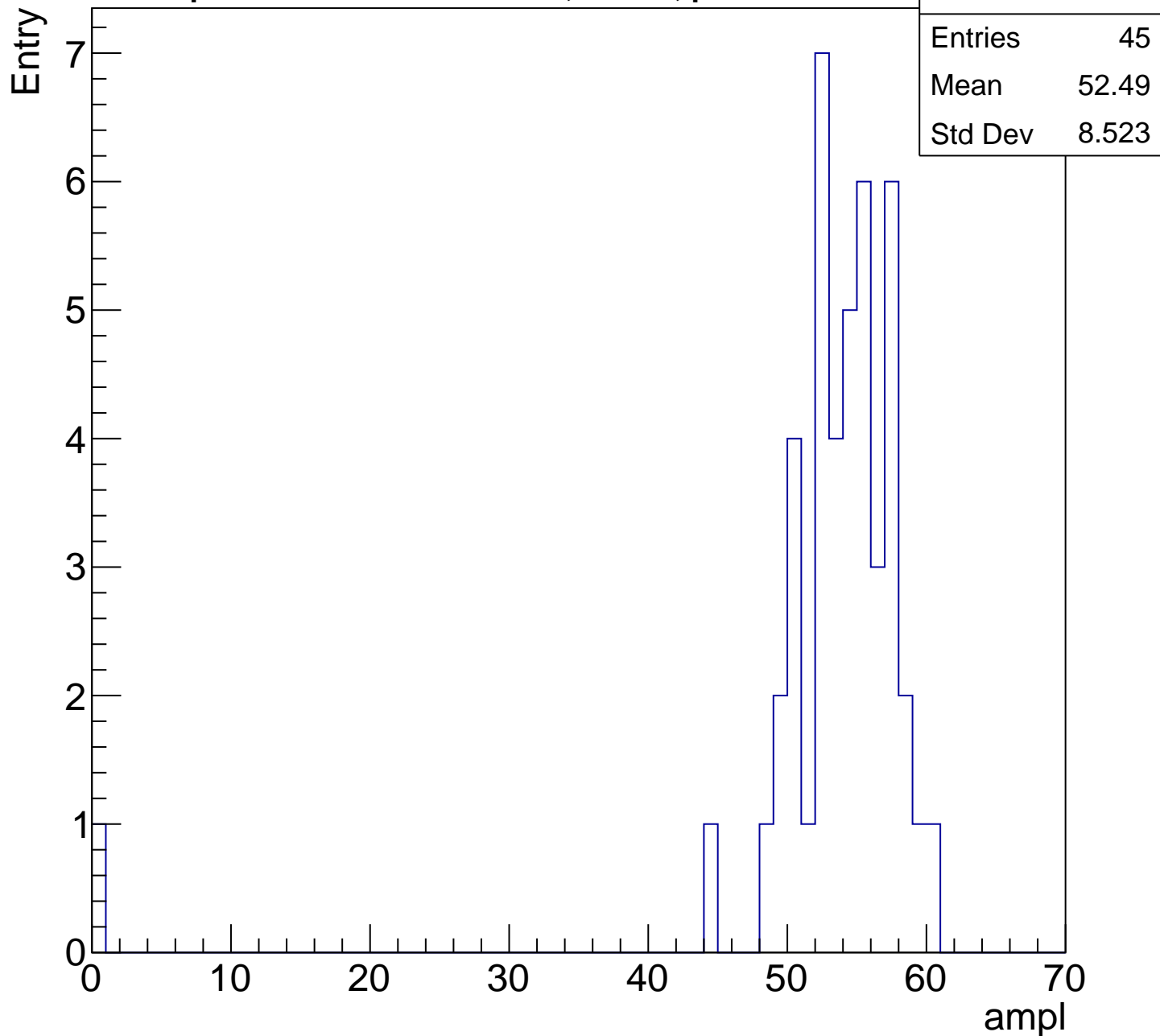
Entry

Entries	66
Mean	49.29
Std Dev	3.009



B1L003S, U16-ch44, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch44, adc5

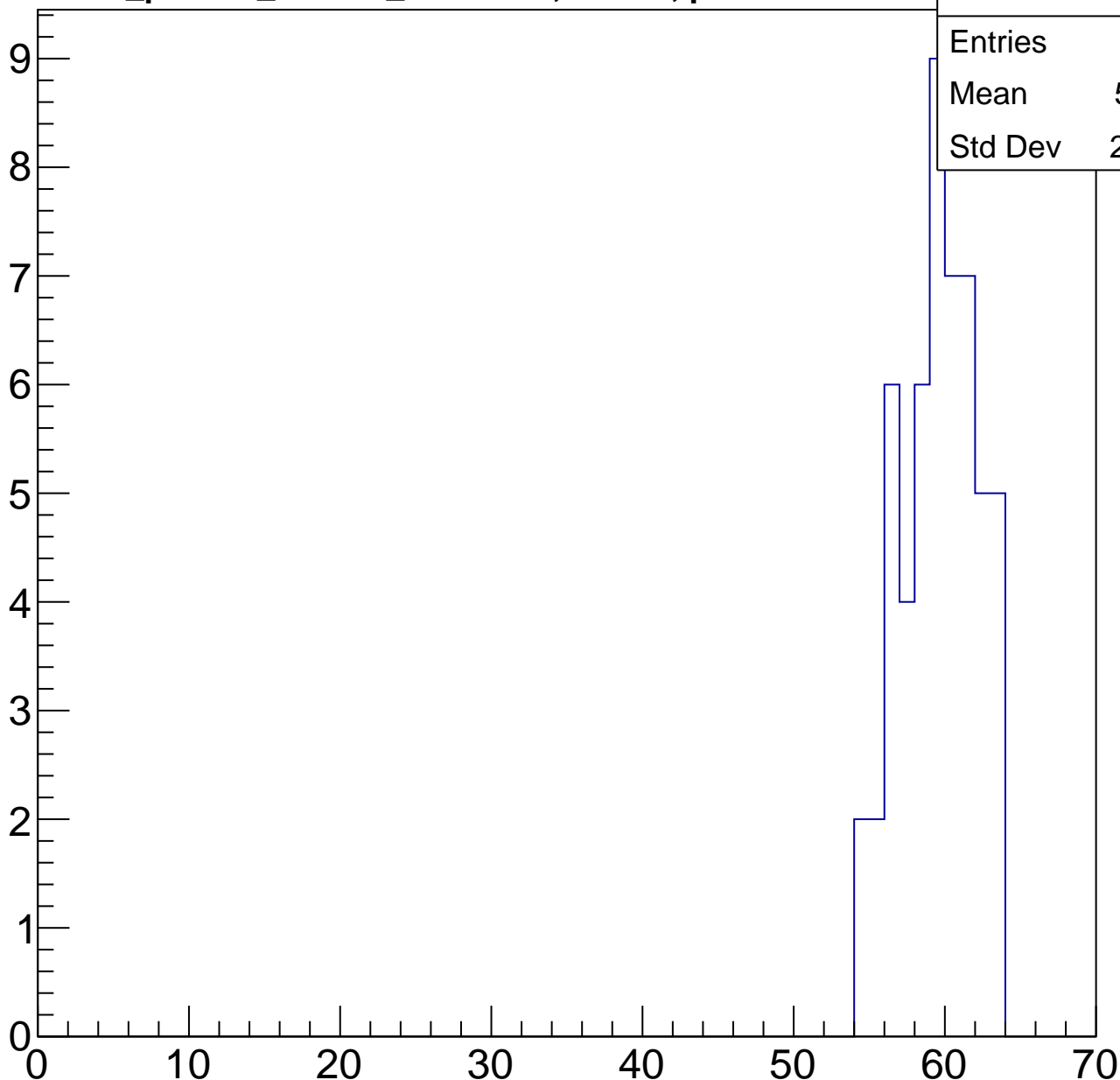
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.11
Std Dev	2.447

ampl

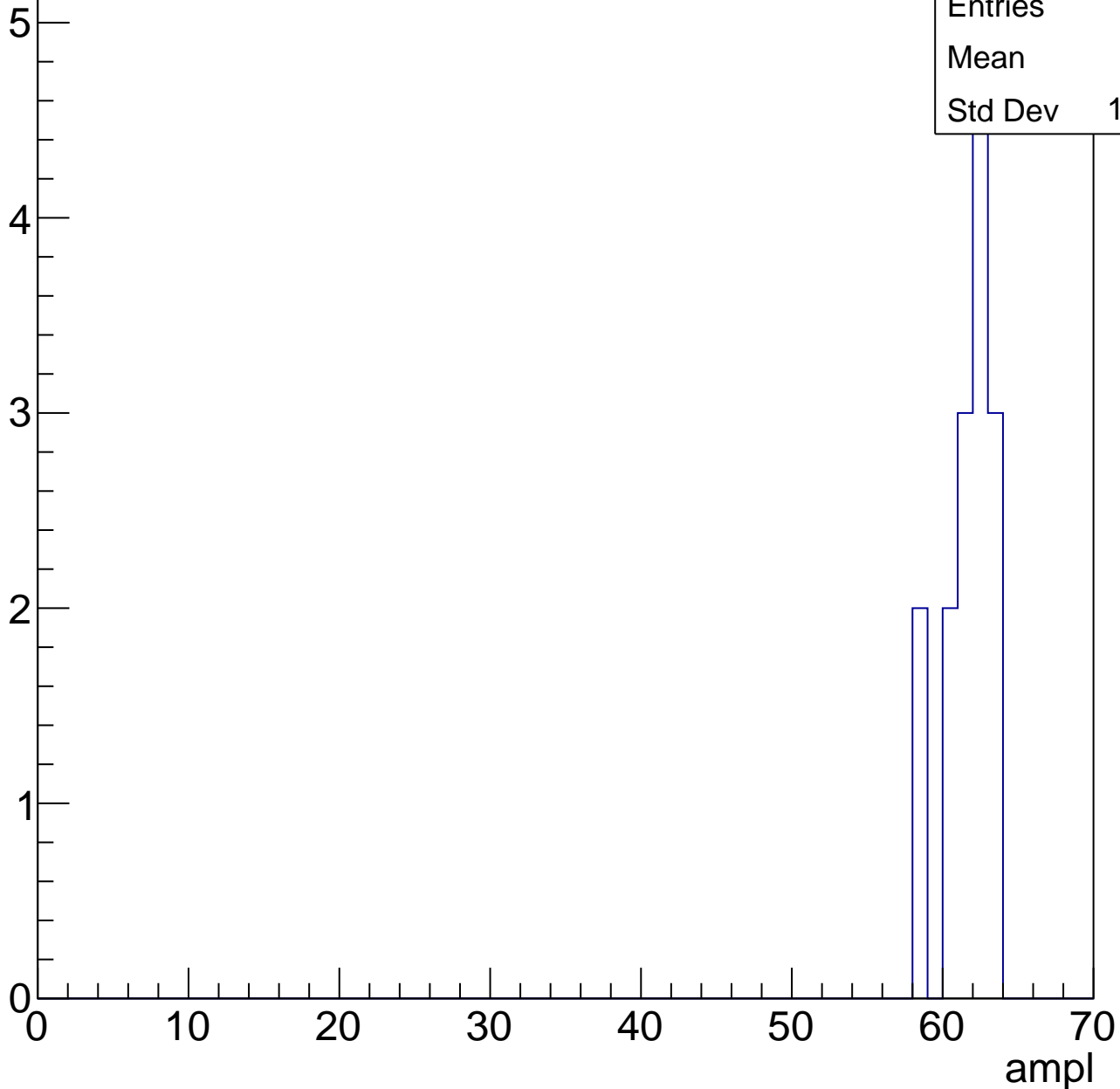


B1L003S, U16-ch44, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.2
Std Dev	1.558



B1L003S, U16-ch44, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch45, adc0

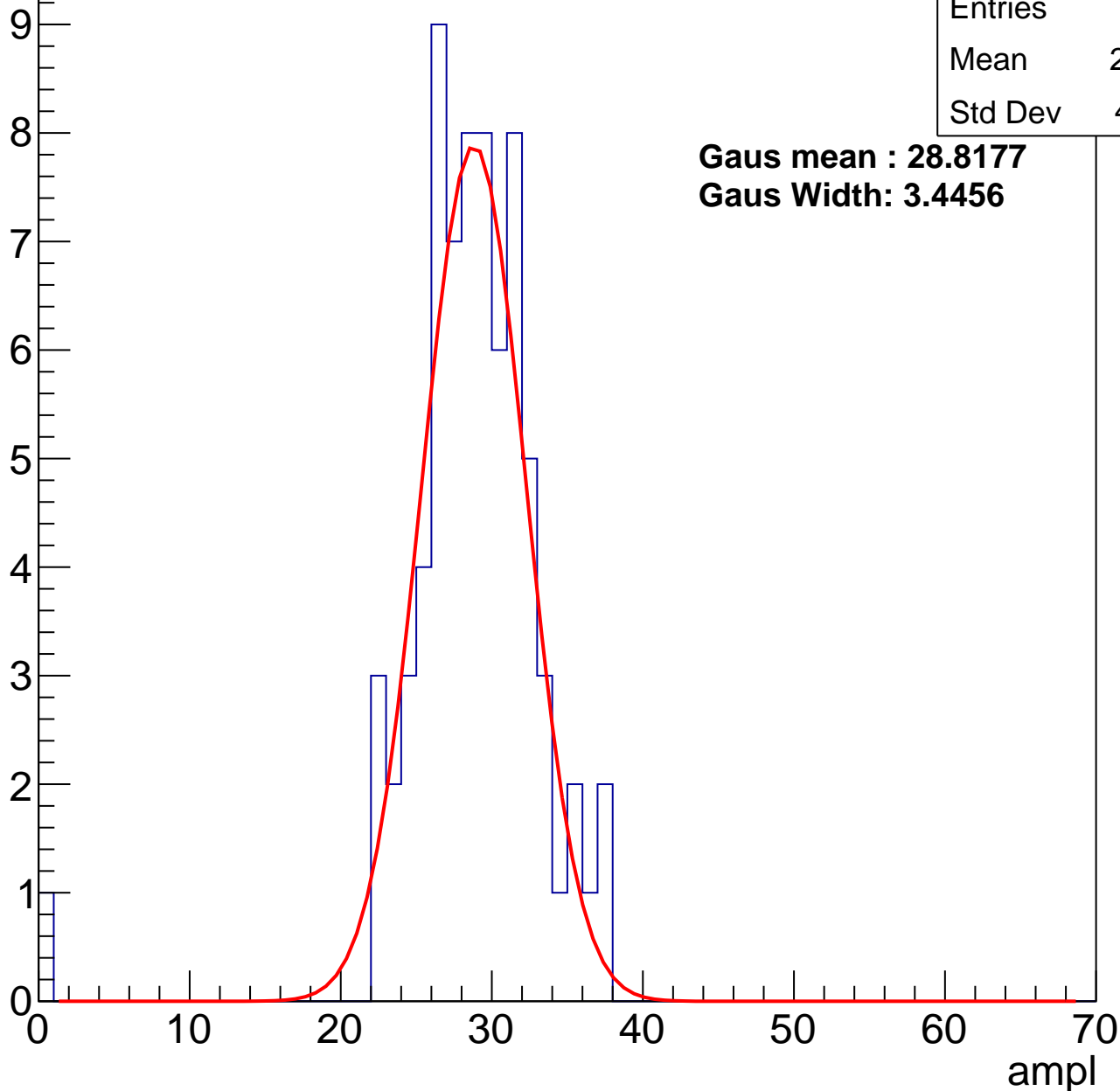
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	28.27
Std Dev	4.801

Gaus mean : 28.8177

Gaus Width: 3.4456



B1L003S, U16-ch45, adc1

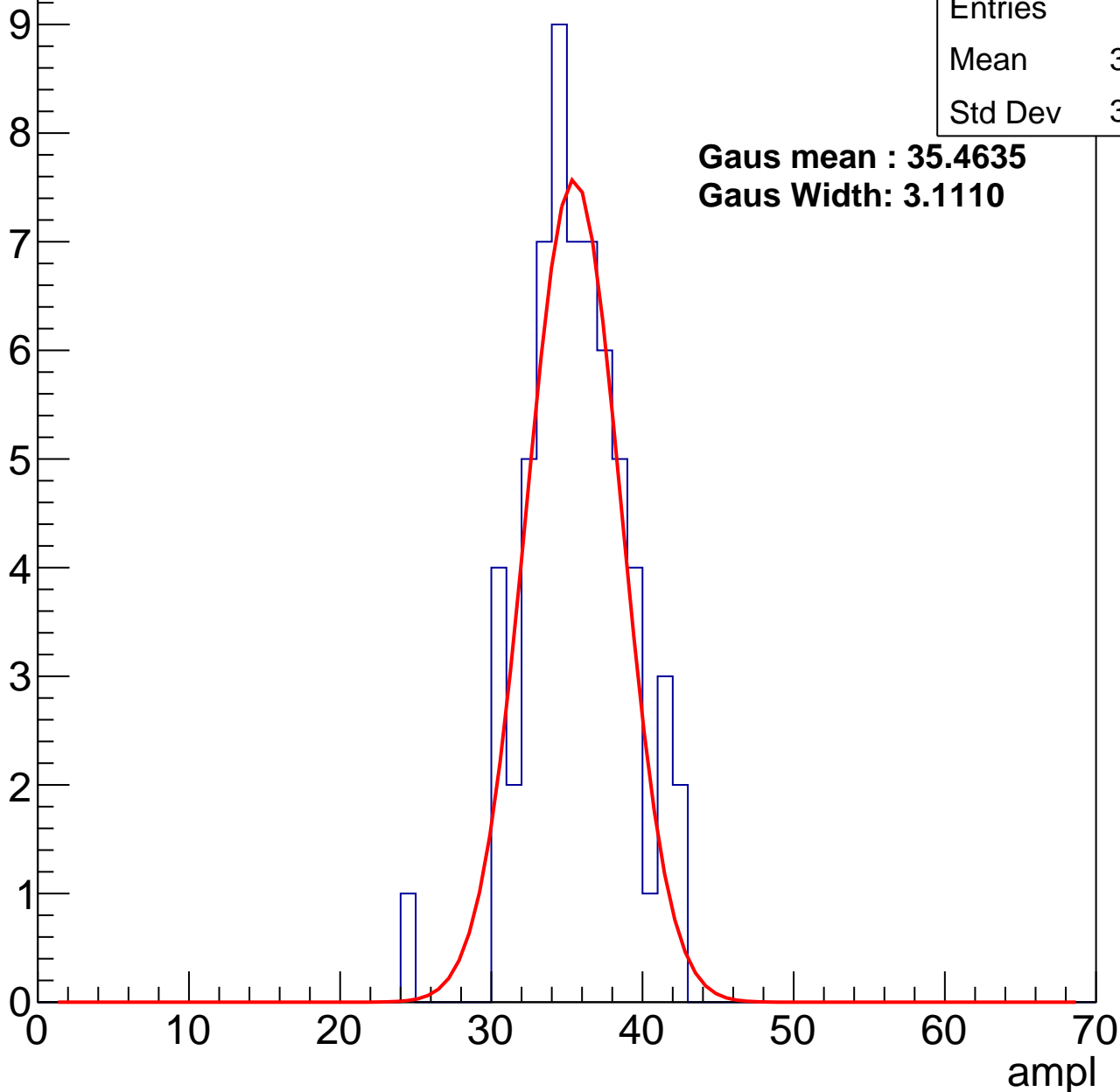
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.16
Std Dev	3.344

Gaus mean : 35.4635

Gaus Width: 3.1110



B1L003S, U16-ch45, adc2

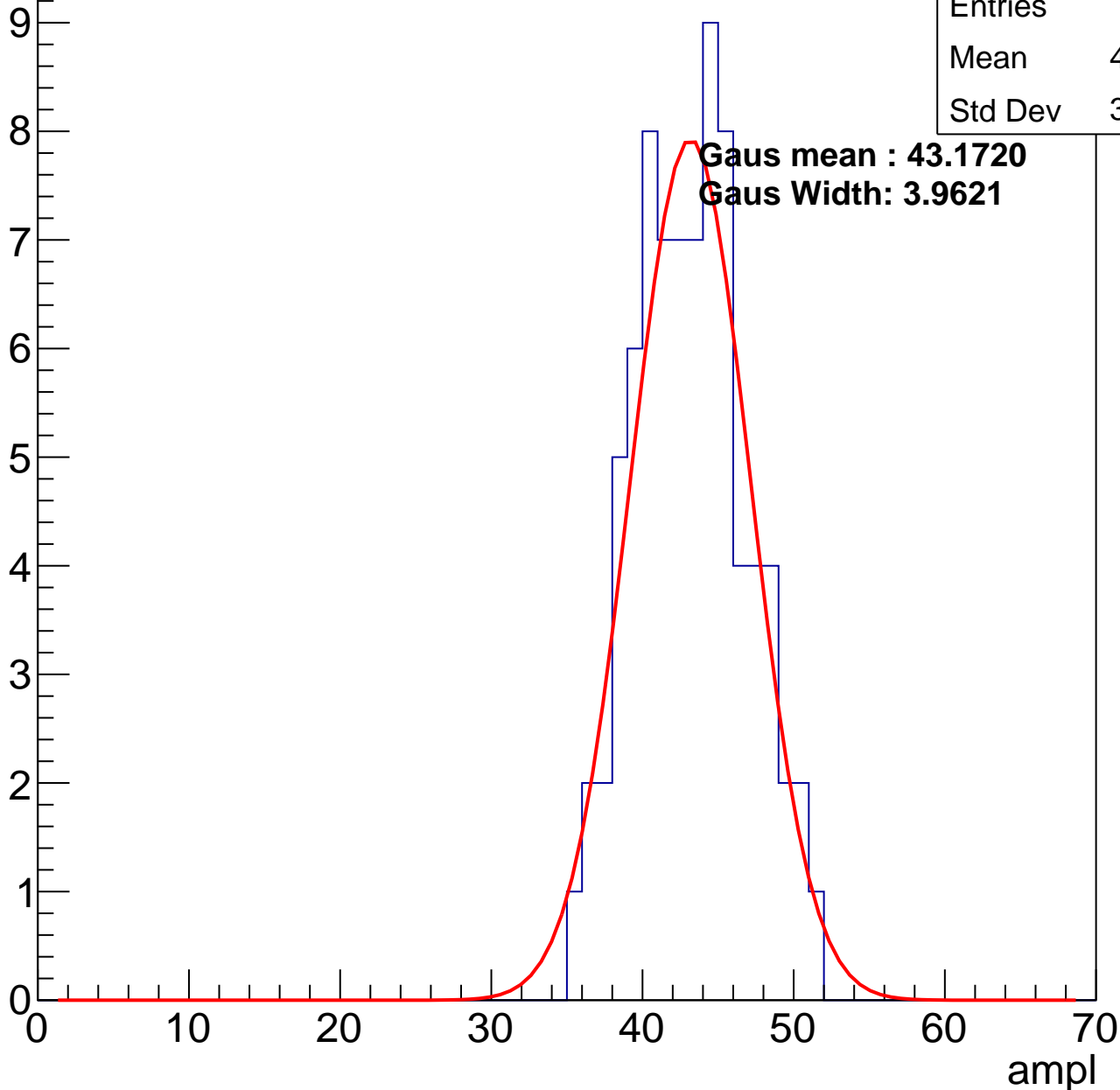
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	42.73
Std Dev	3.613

Gaus mean : 43.1720

Gaus Width: 3.9621

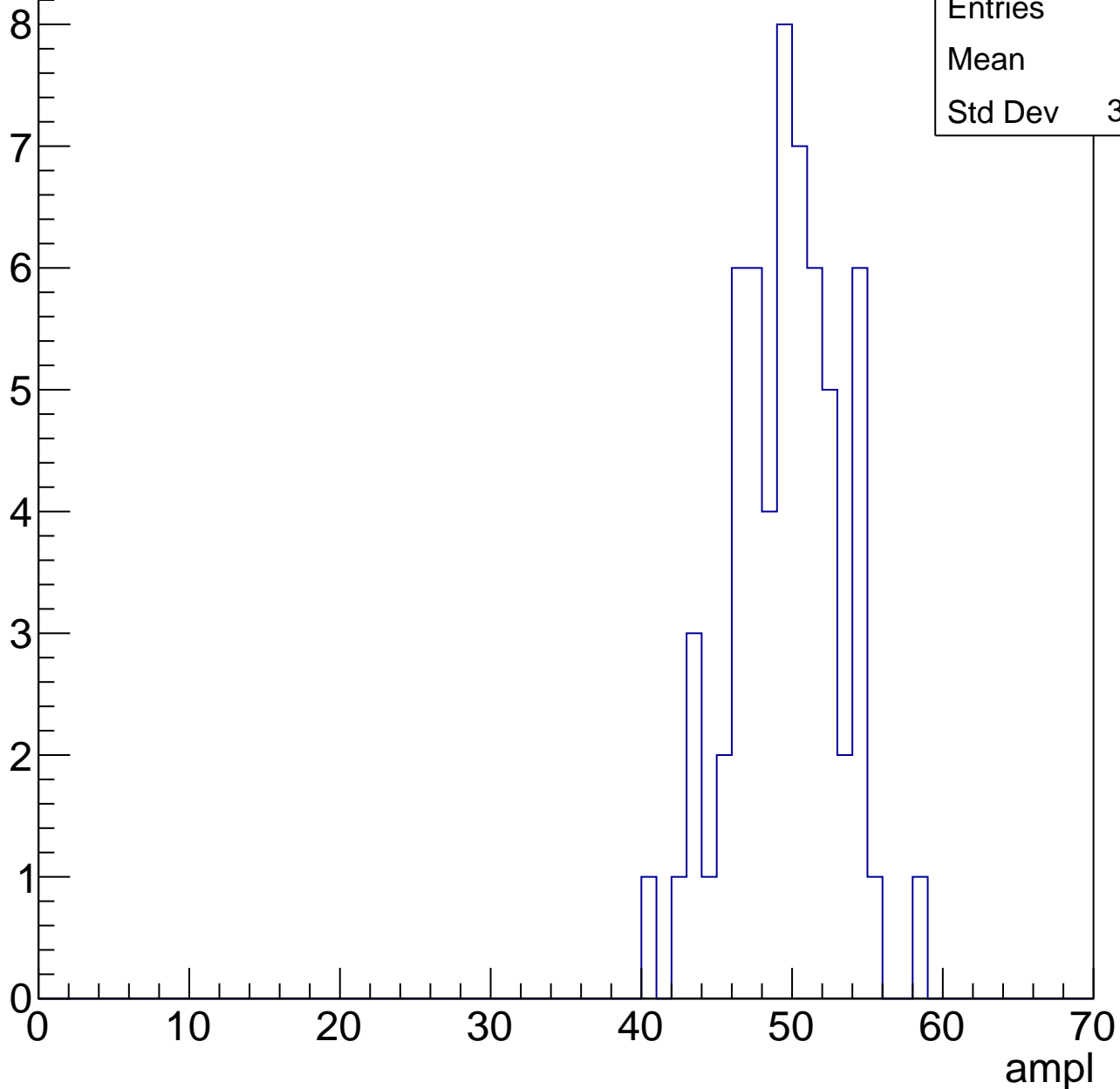


B1L003S, U16-ch45, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.1
Std Dev	3.539

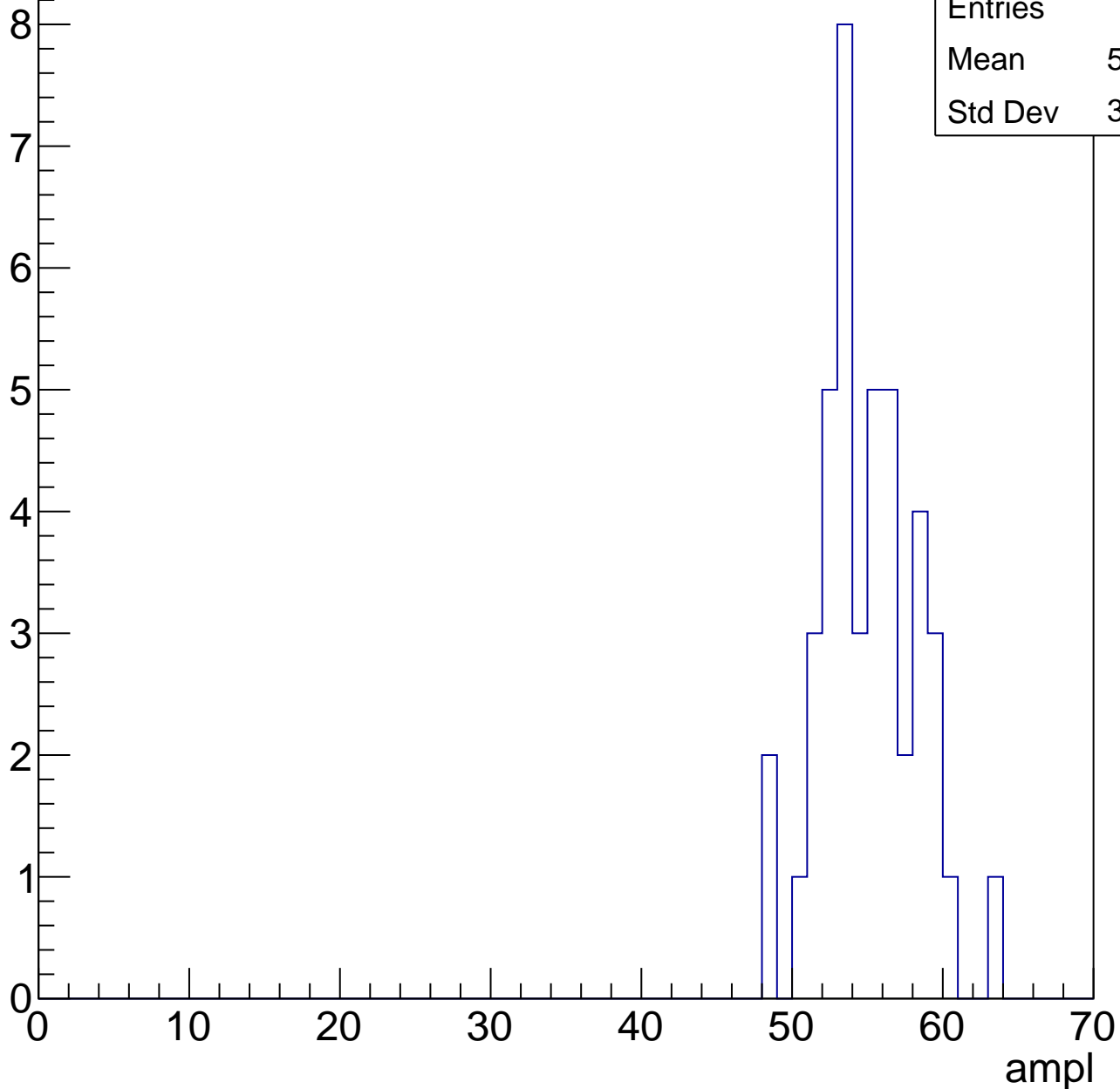


B1L003S, U16-ch45, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	54.56
Std Dev	3.157



B1L003S, U16-ch45, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	58.04
Std Dev	8.279

Entry

10

8

6

4

2

0

0

10

20

30

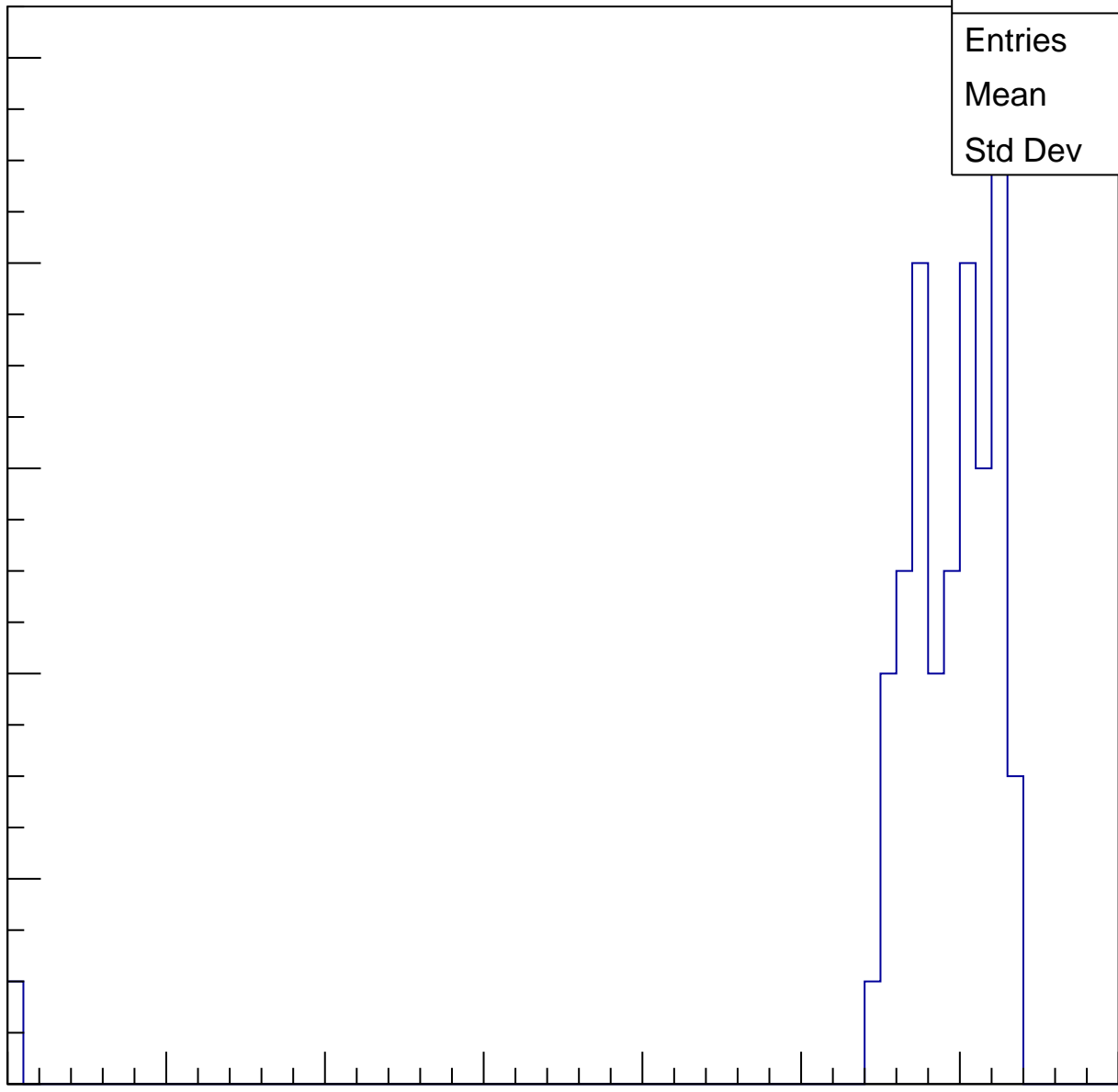
40

50

60

70

ampl

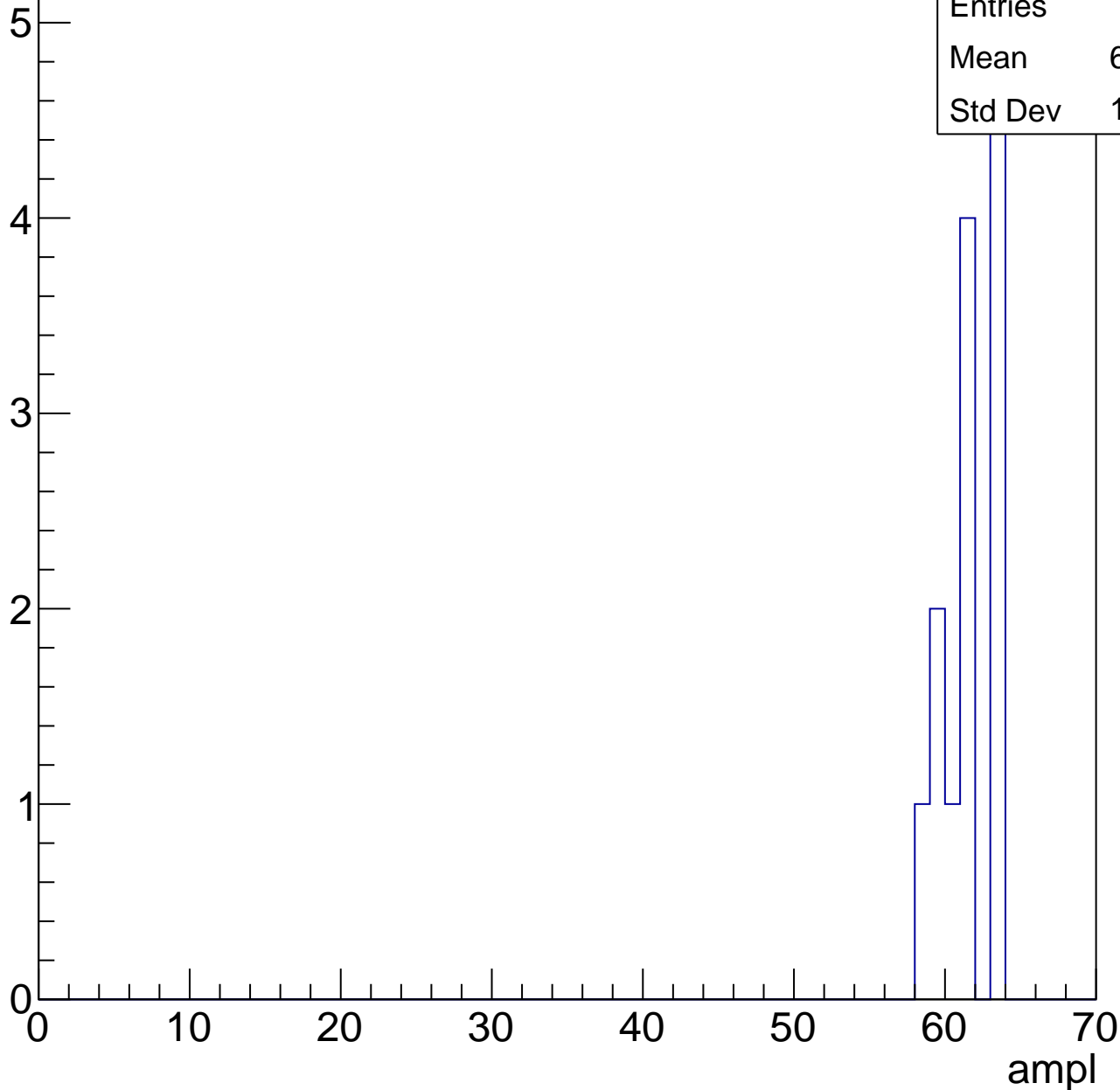


B1L003S, U16-ch45, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

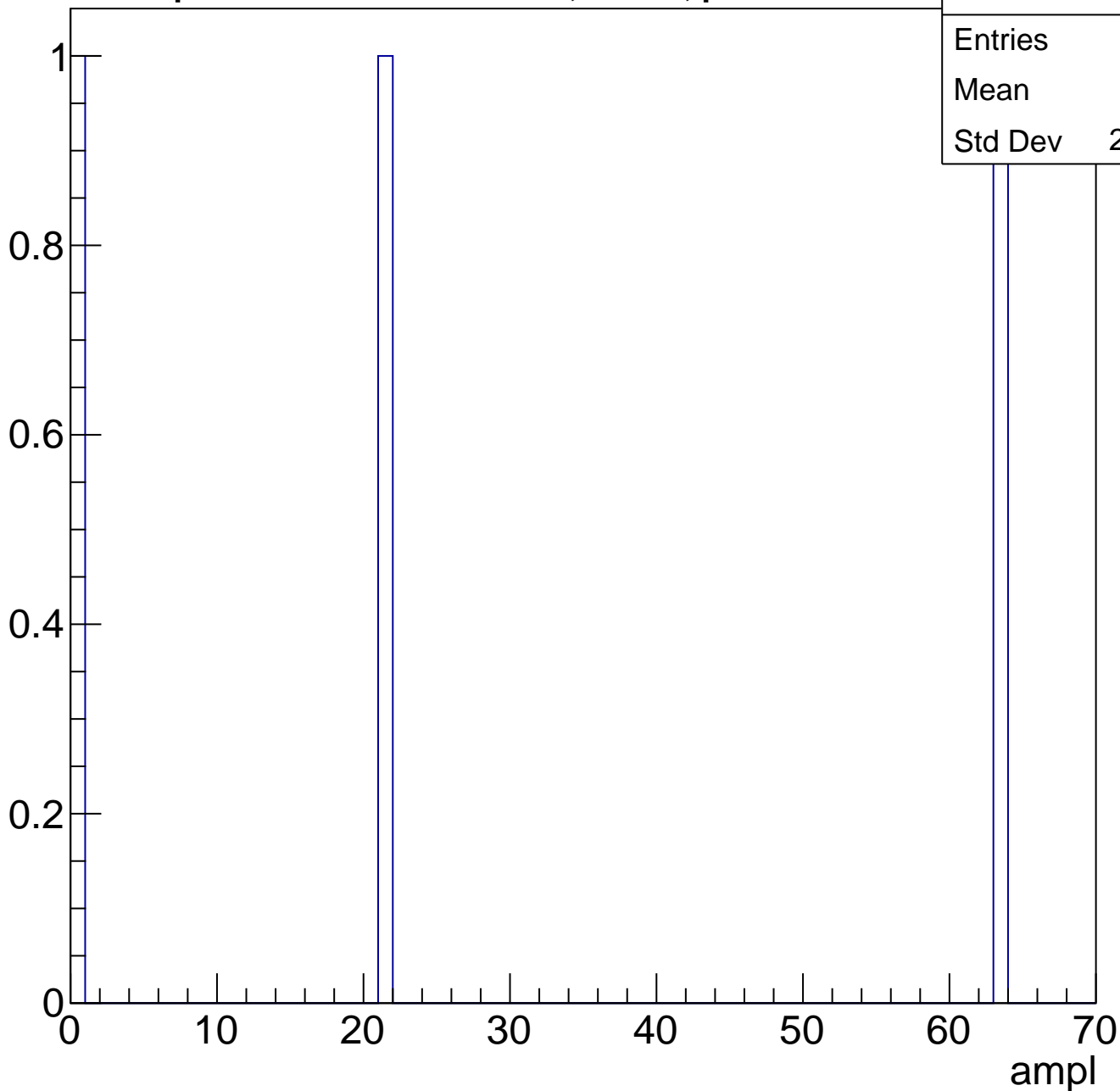
Entries	13
Mean	61.15
Std Dev	1.703



B1L003S, U16-ch45, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L003S, U16-ch46, adc0

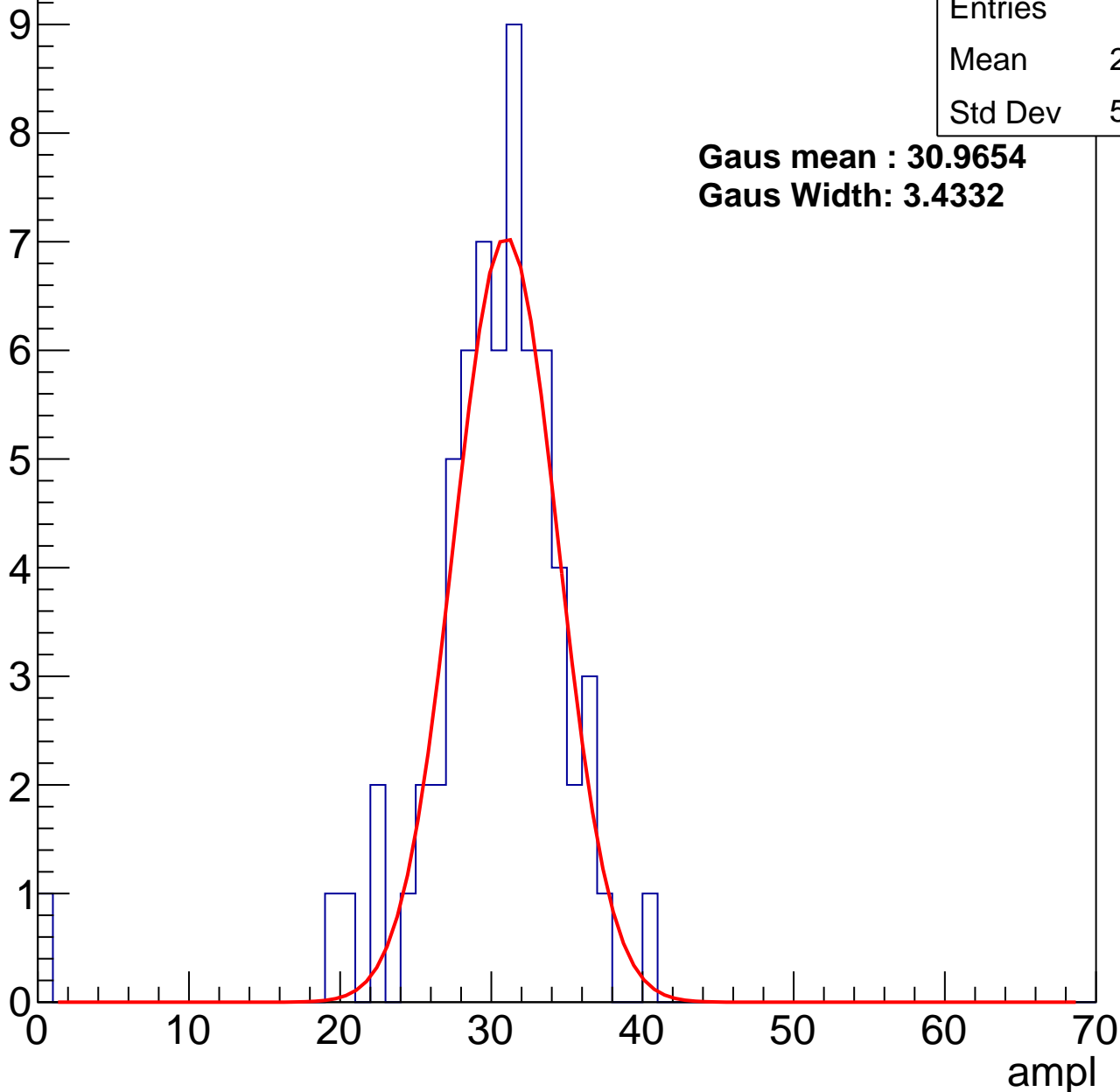
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.62
Std Dev	5.353

Gaus mean : 30.9654

Gaus Width: 3.4332



B1L003S, U16-ch46, adc1

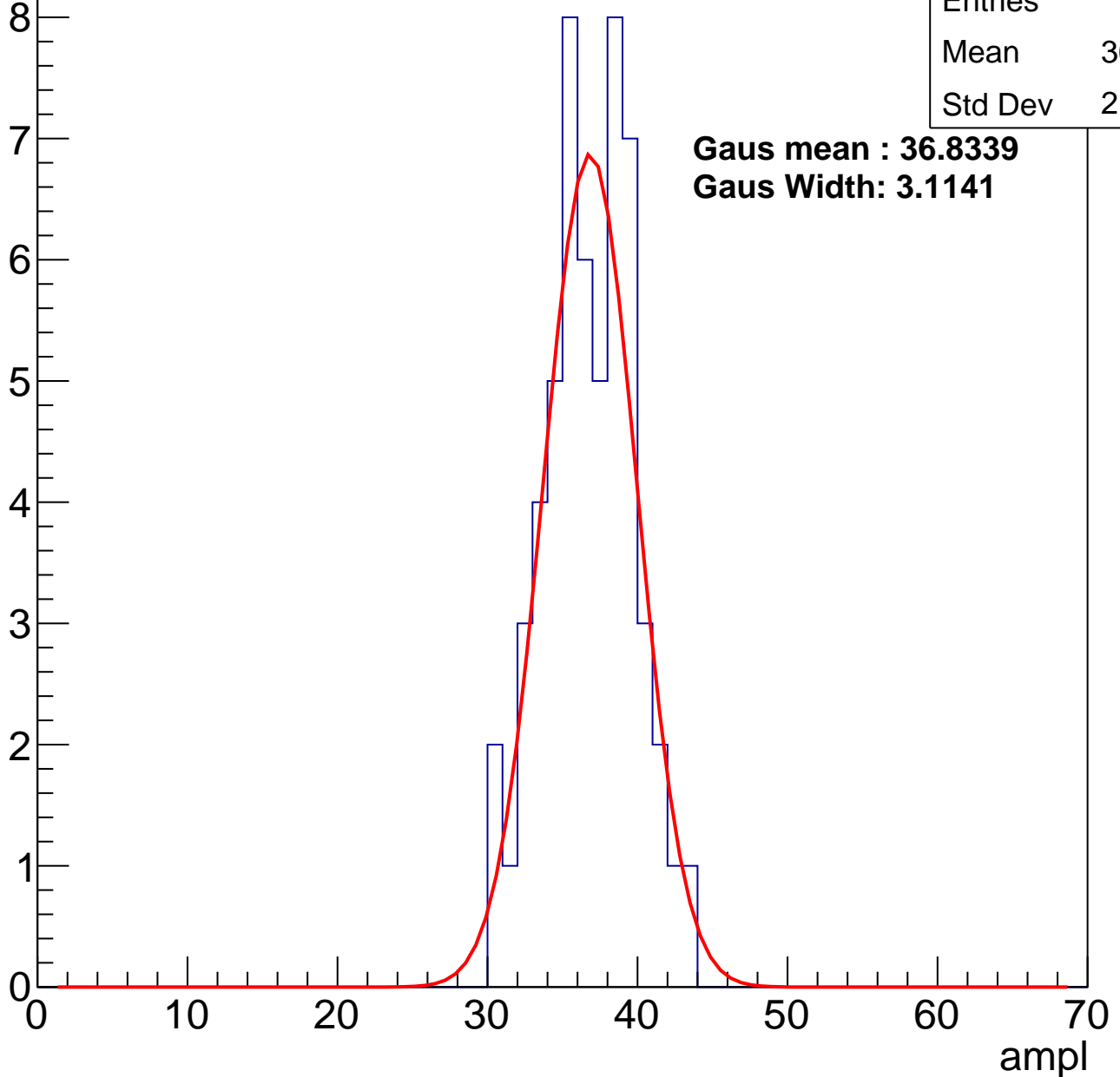
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.32
Std Dev	2.947

Gaus mean : 36.8339

Gaus Width: 3.1141



B1L003S, U16-ch46, adc2

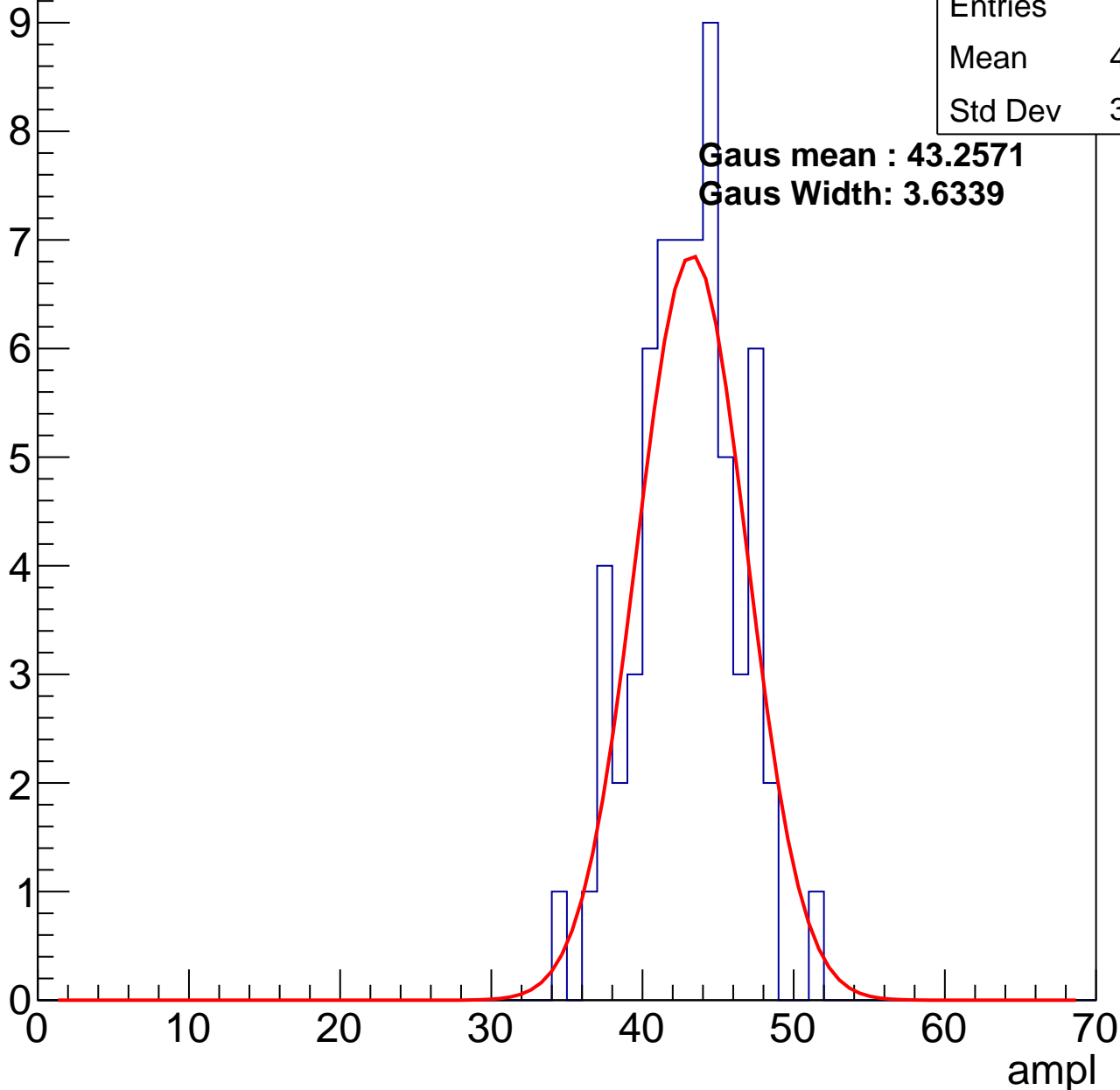
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.52
Std Dev	3.335

Gaus mean : 43.2571

Gaus Width: 3.6339

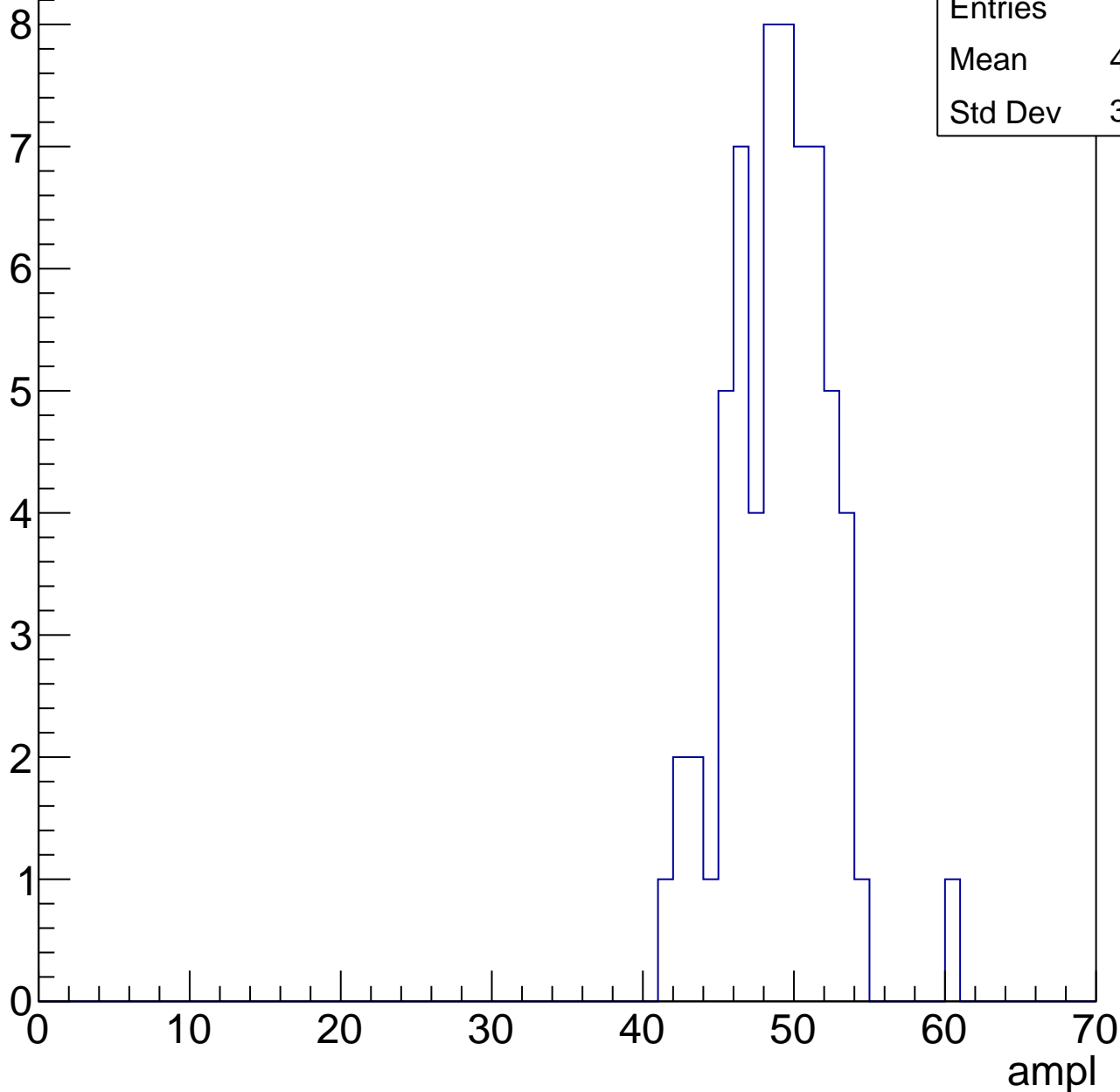


B1L003S, U16-ch46, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

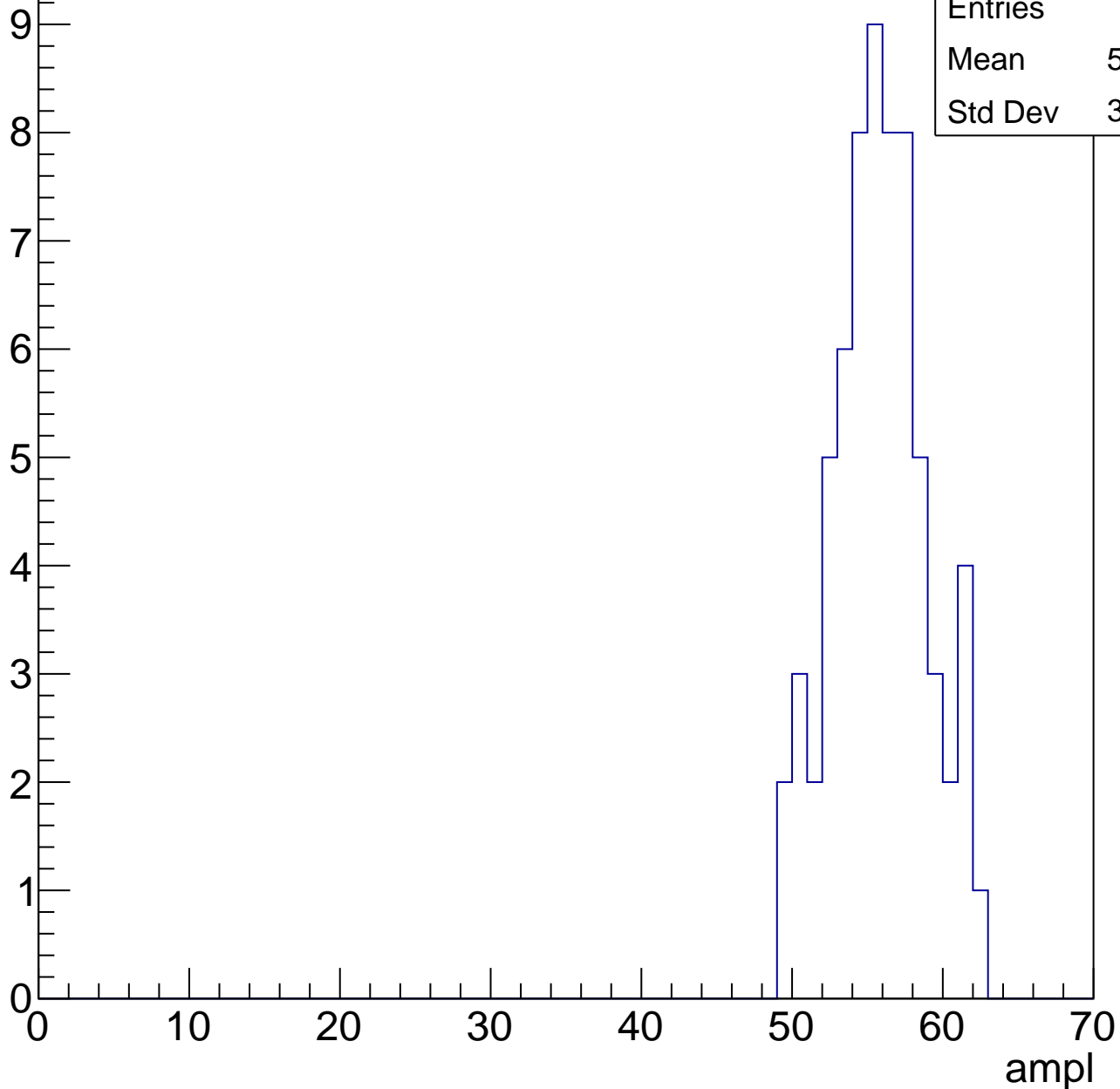
Entries	63
Mean	48.56
Std Dev	3.342



B1L003S, U16-ch46, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	66
Mean	55.33
Std Dev	3.076

B1L003S, U16-ch46, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	59.85
Std Dev	2.333

ampl

0

10

20

30

40

50

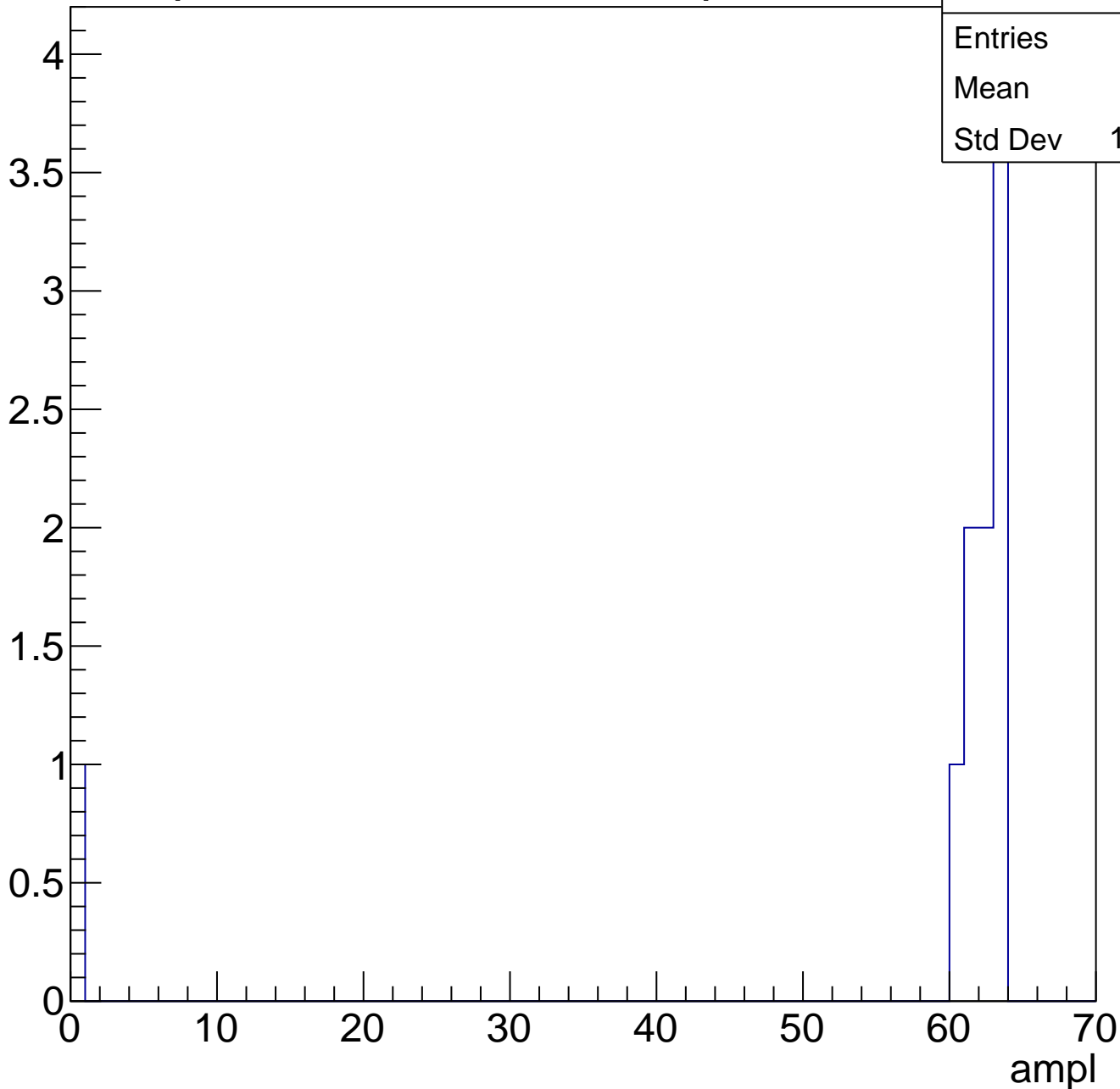
60

70

B1L003S, U16-ch46, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch46, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch47, adc0

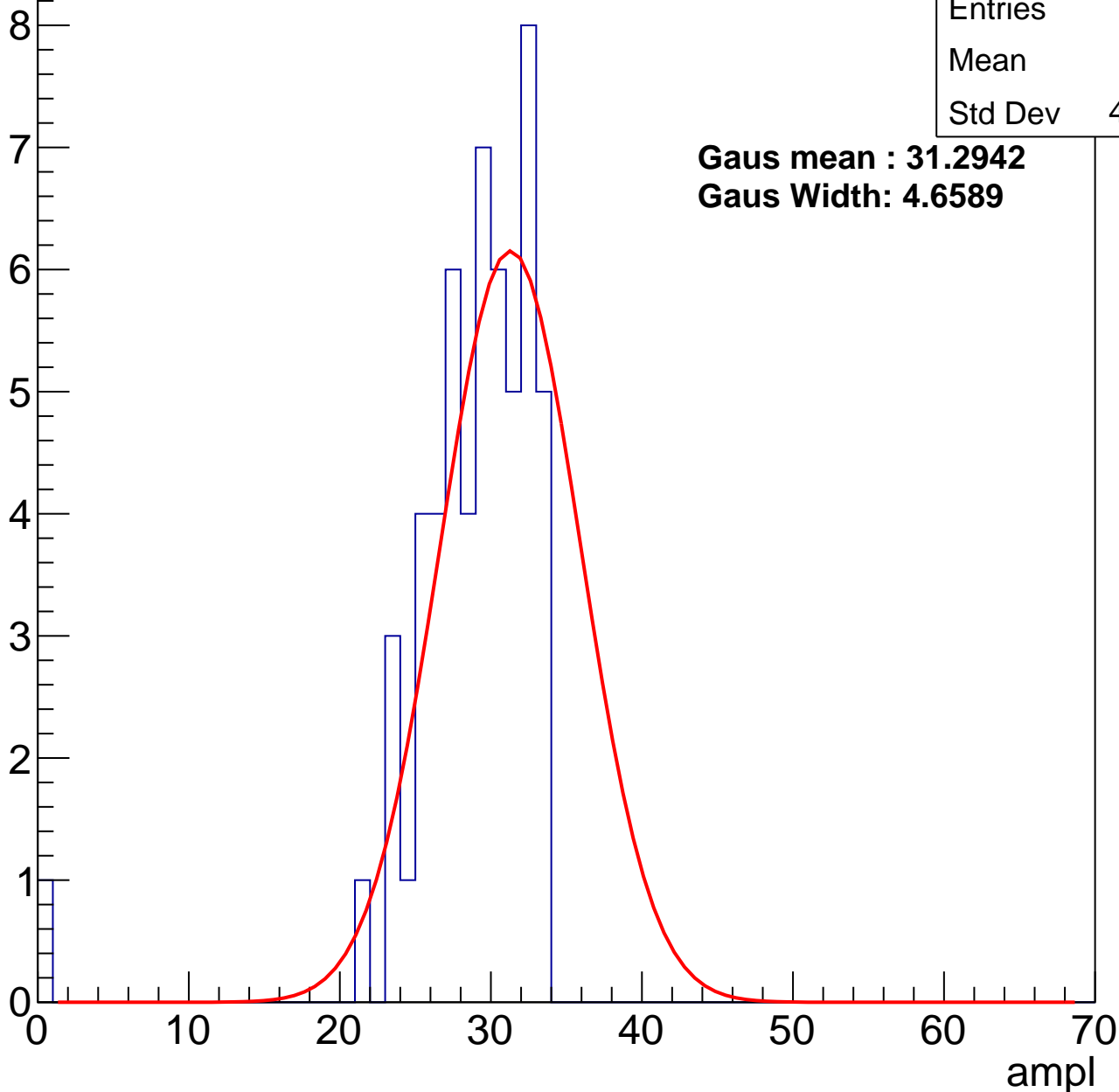
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.2
Std Dev	4.878

Gaus mean : 31.2942

Gaus Width: 4.6589



B1L003S, U16-ch47, adc1

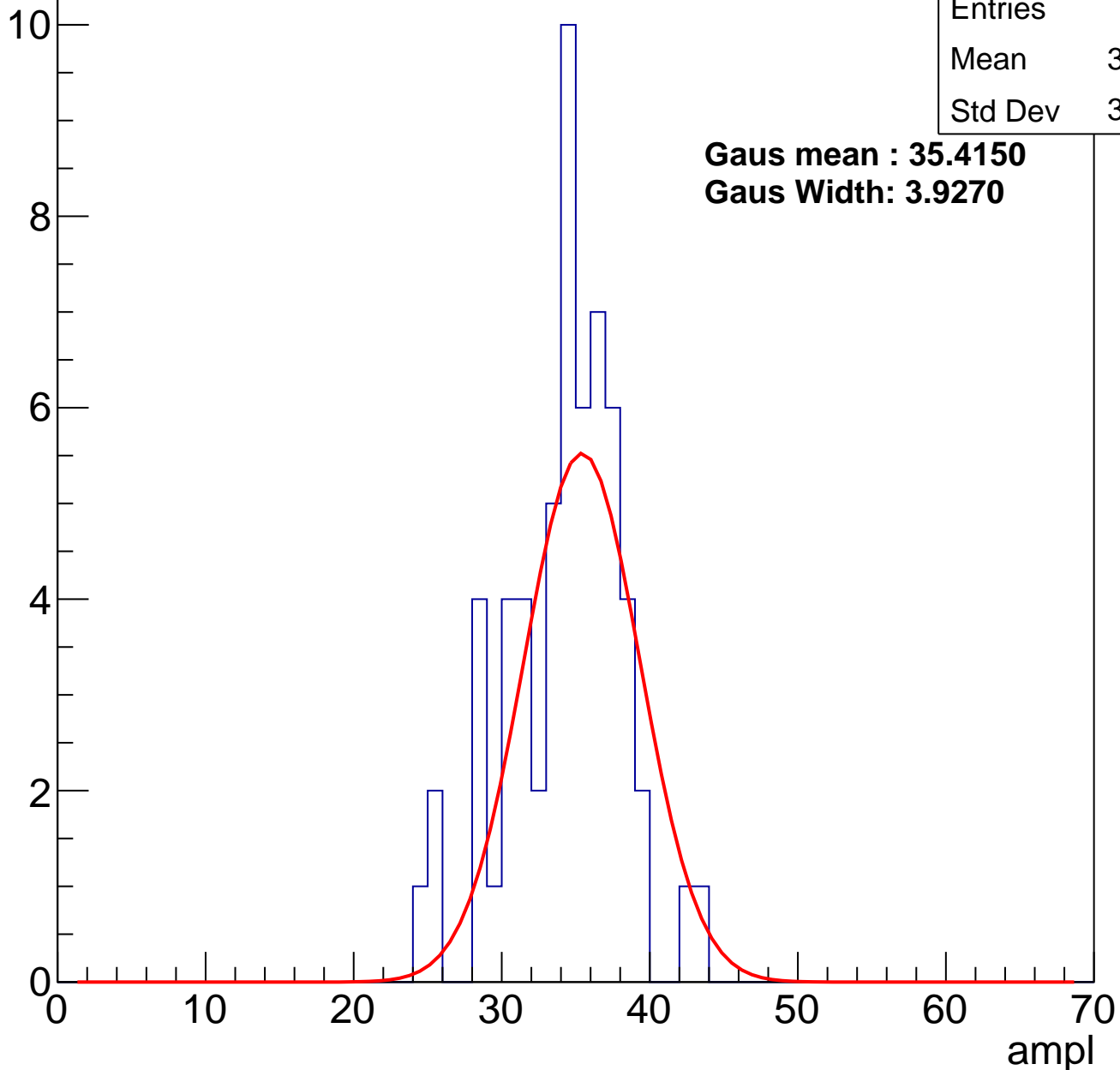
calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	33.78
Std Dev	3.847

Gaus mean : 35.4150

Gaus Width: 3.9270

Entry



B1L003S, U16-ch47, adc2

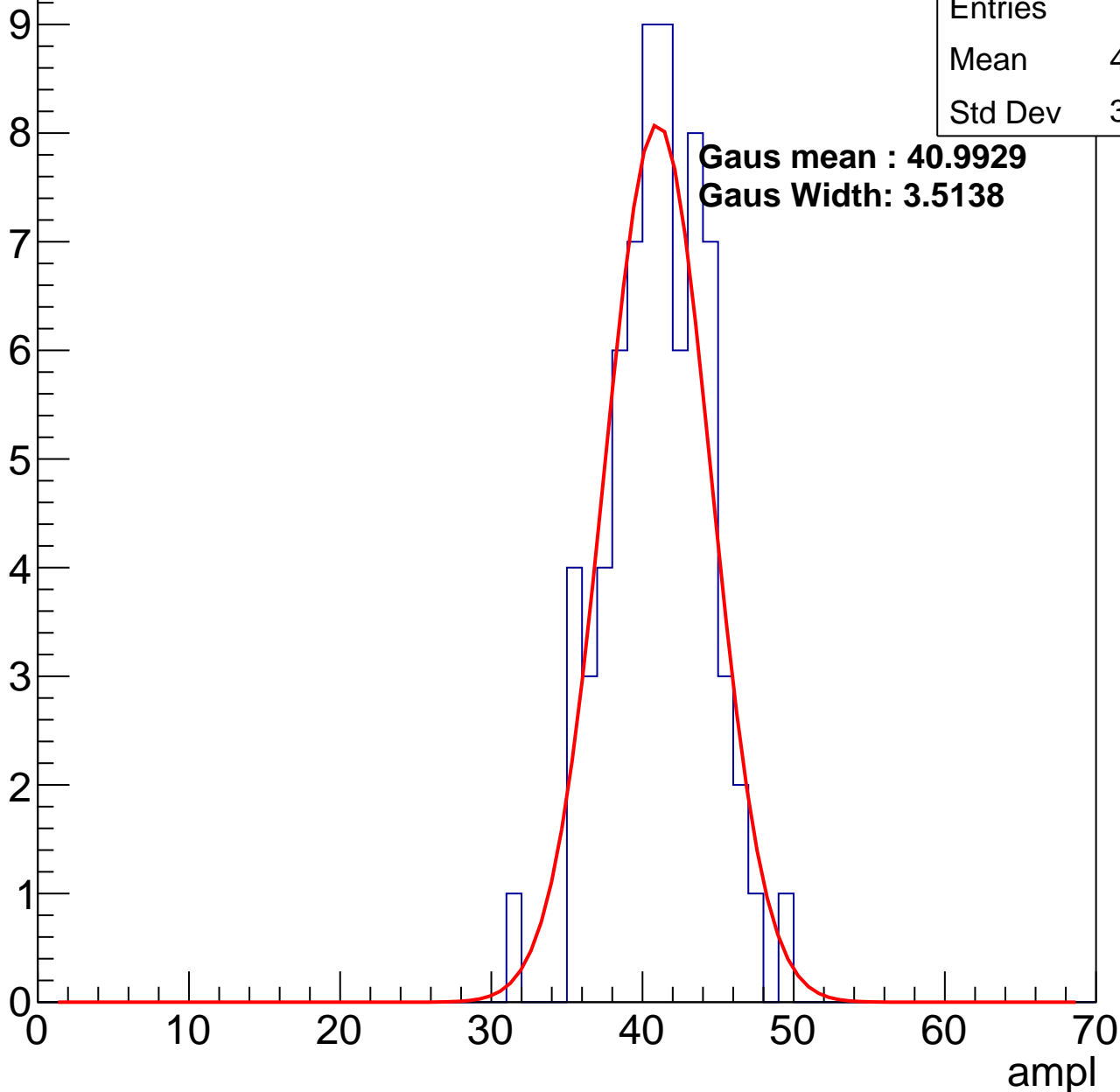
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	40.62
Std Dev	3.278

Gaus mean : 40.9929

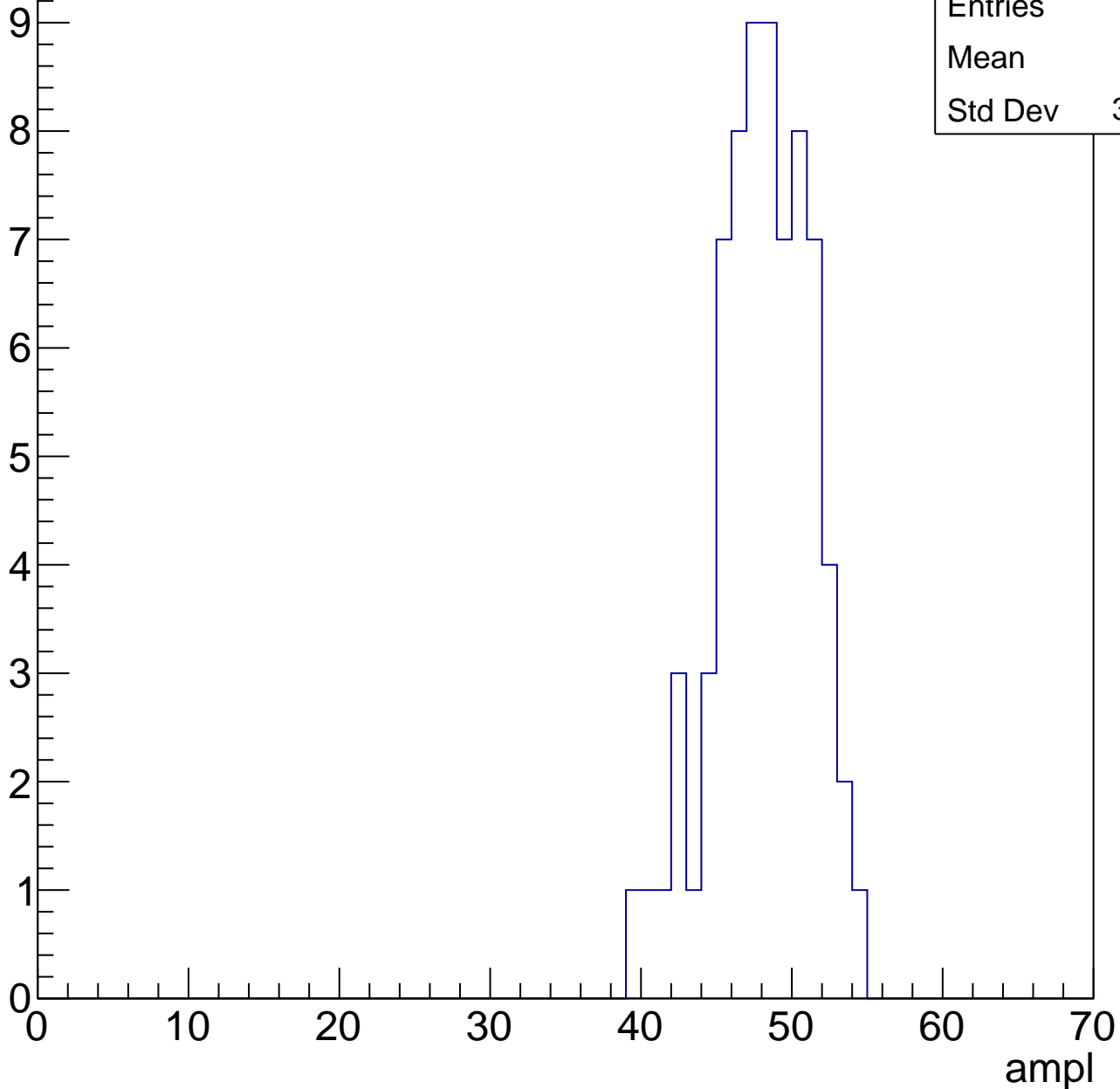
Gaus Width: 3.5138



B1L003S, U16-ch47, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

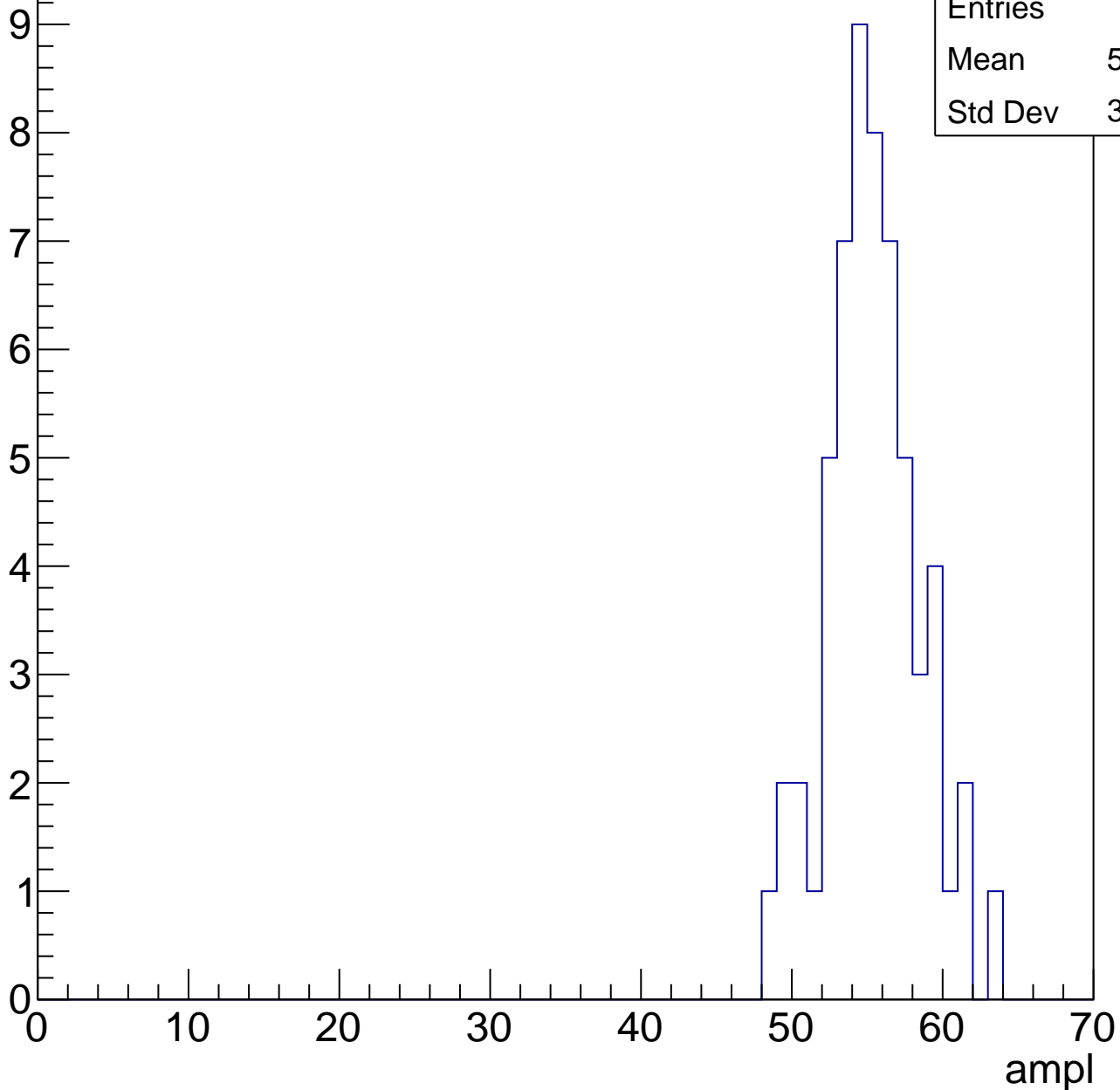


B1L003S, U16-ch47, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.93
Std Dev	3.073

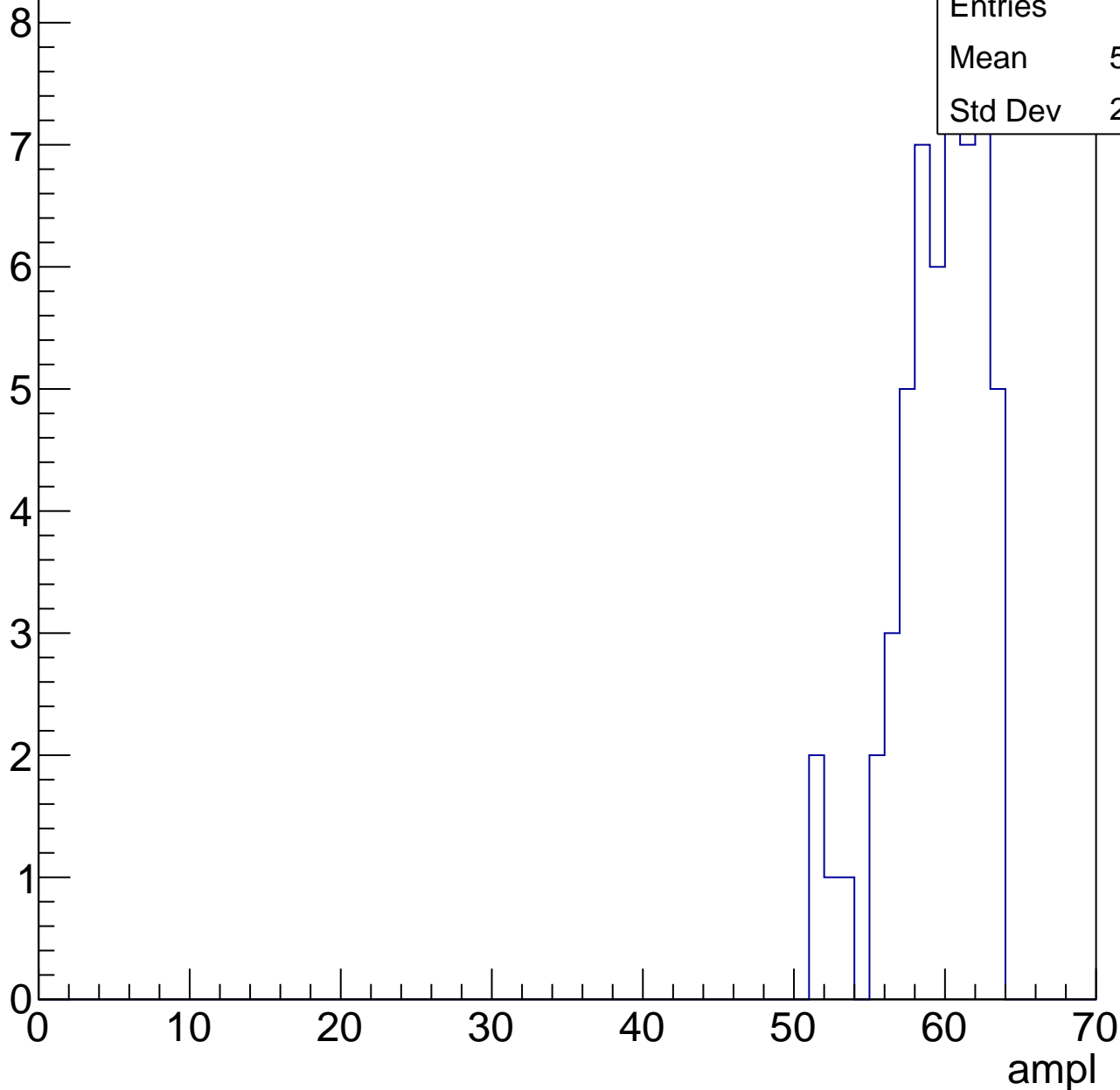


B1L003S, U16-ch47, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

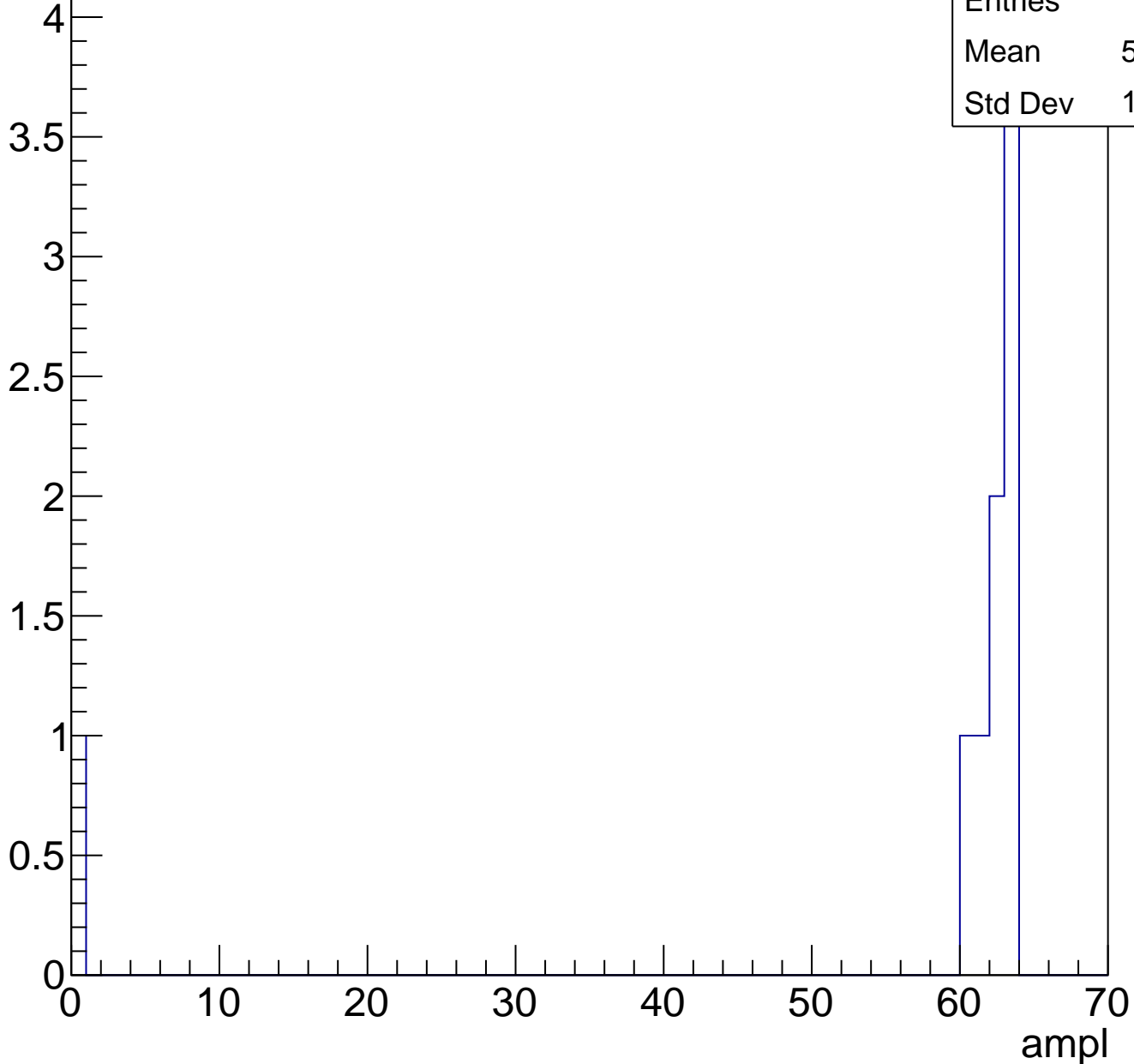
Entries	55
Mean	59.05
Std Dev	2.975



B1L003S, U16-ch47, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch47, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch48, adc0

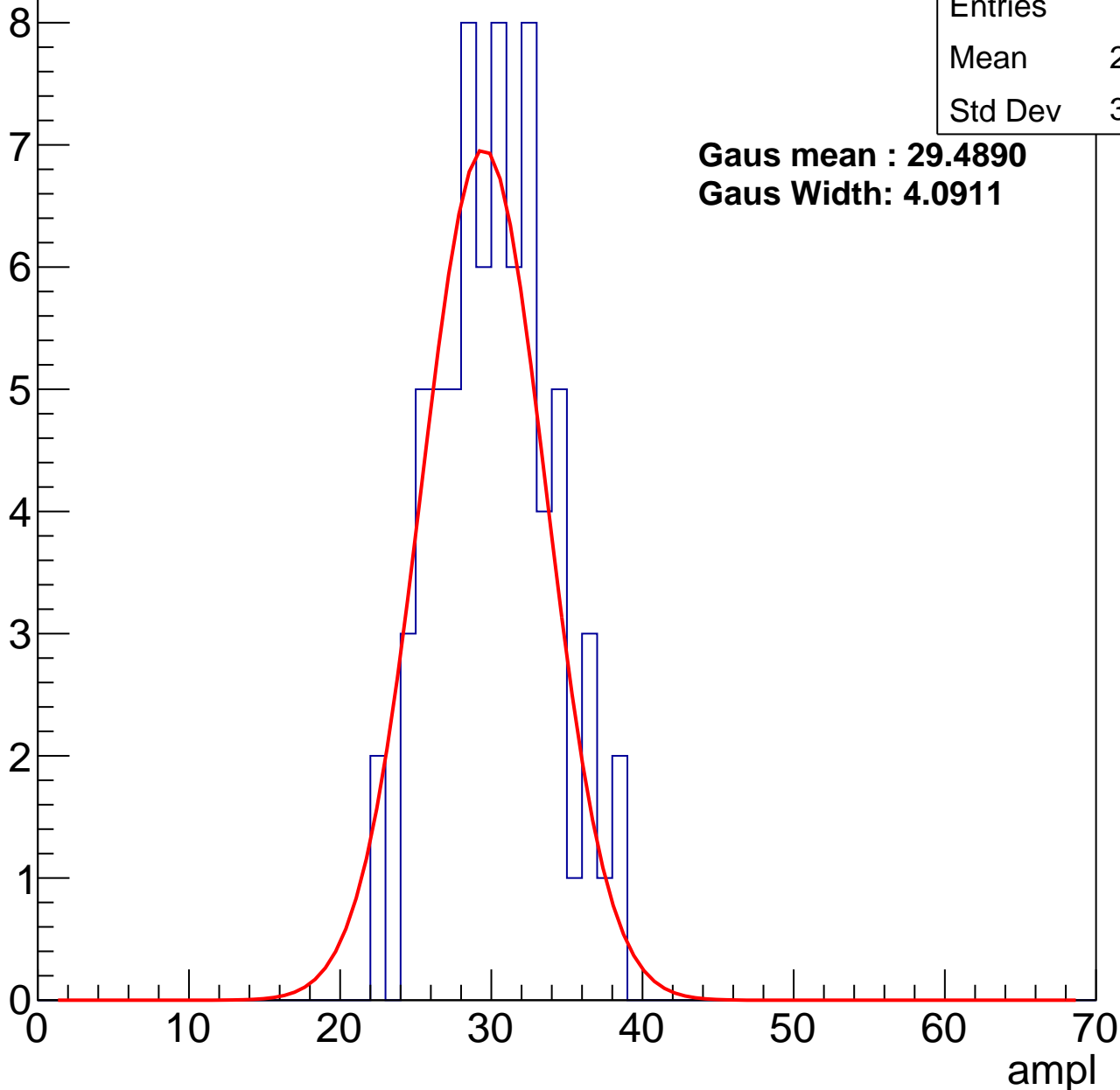
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.78
Std Dev	3.698

Gaus mean : 29.4890

Gaus Width: 4.0911



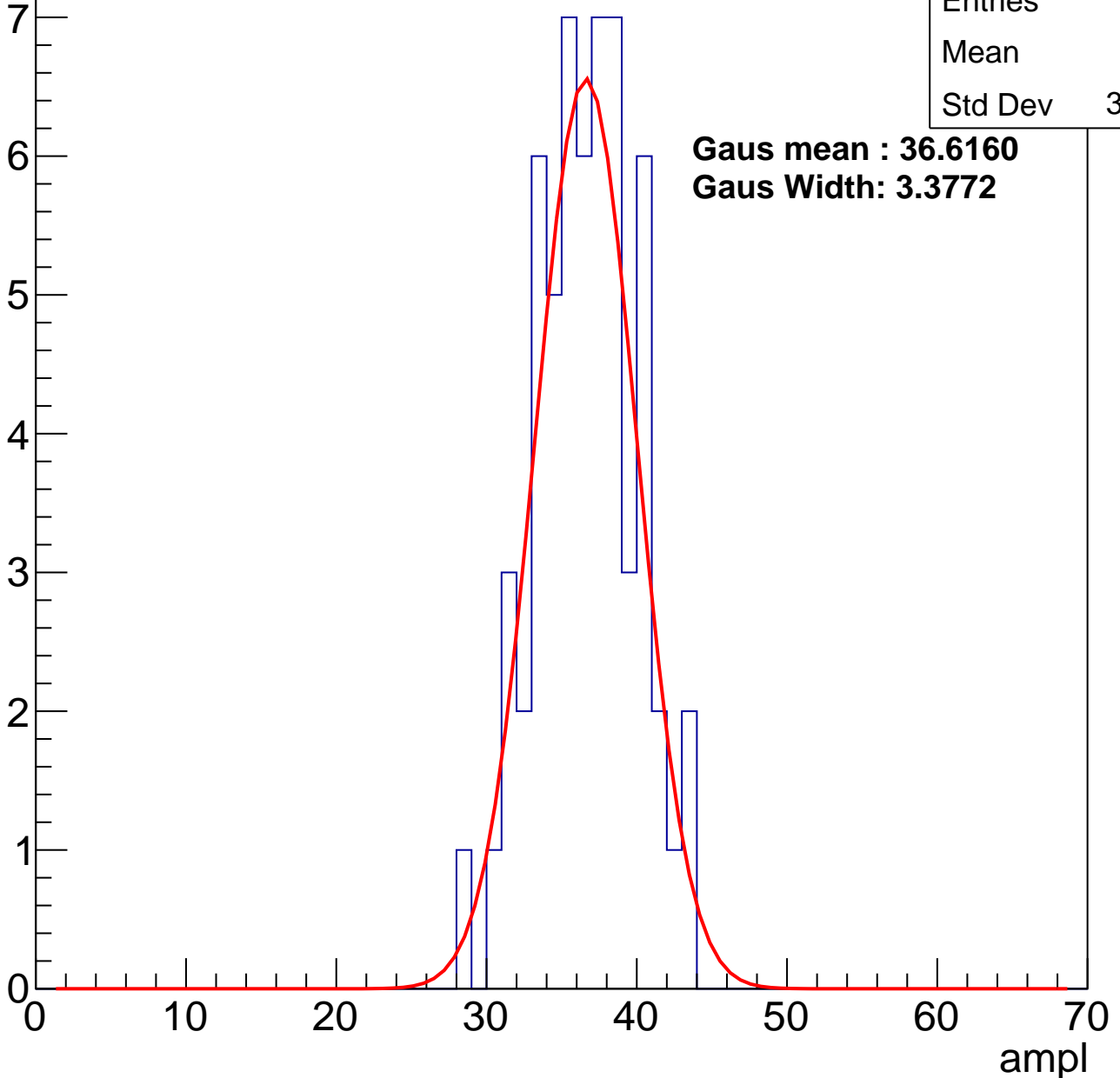
B1L003S, U16-ch48, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.2
Std Dev	3.261

Gaus mean : 36.6160
Gaus Width: 3.3772



B1L003S, U16-ch48, adc2

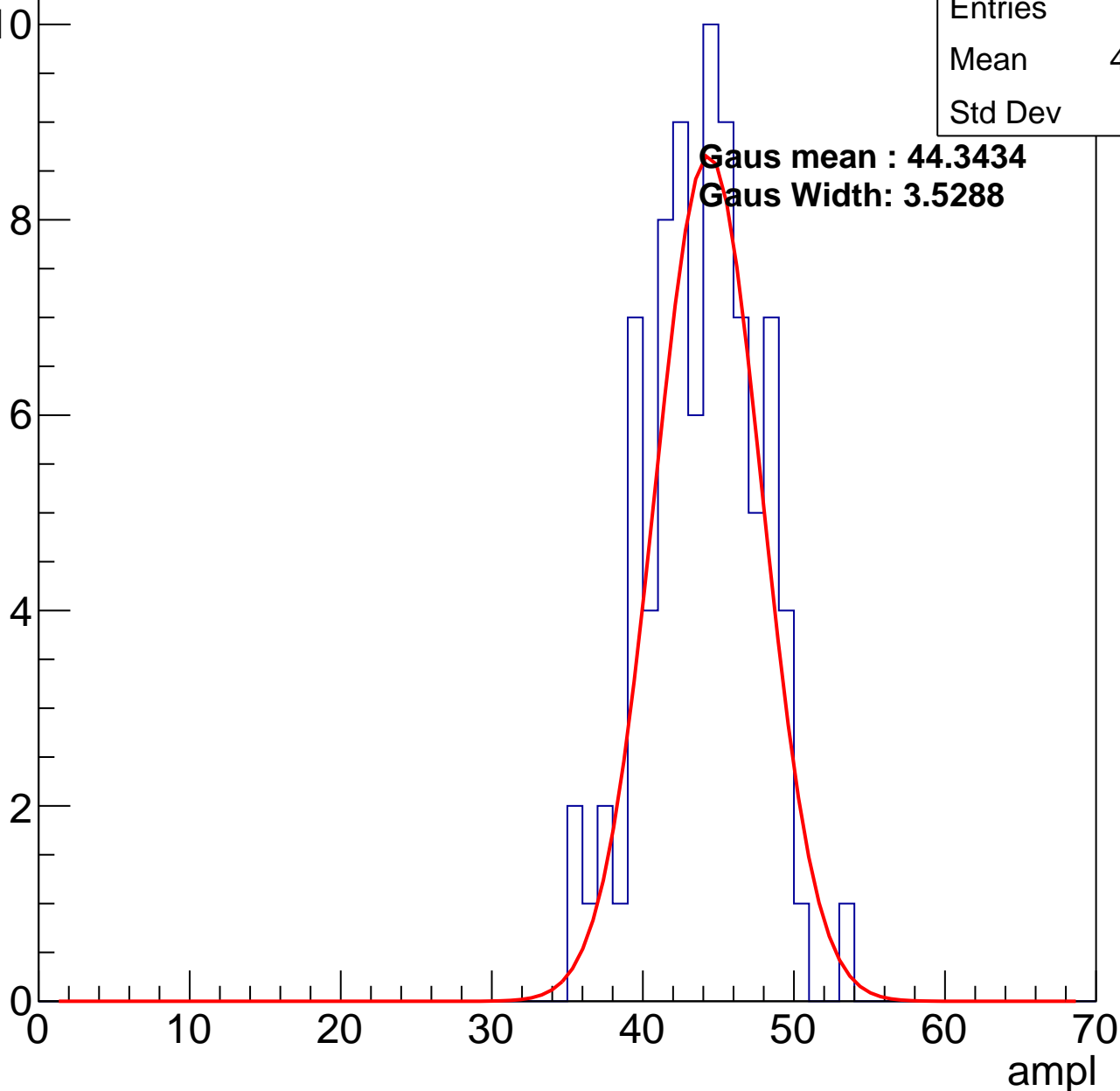
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	43.48
Std Dev	3.62

Gaus mean : 44.3434

Gaus Width: 3.5288

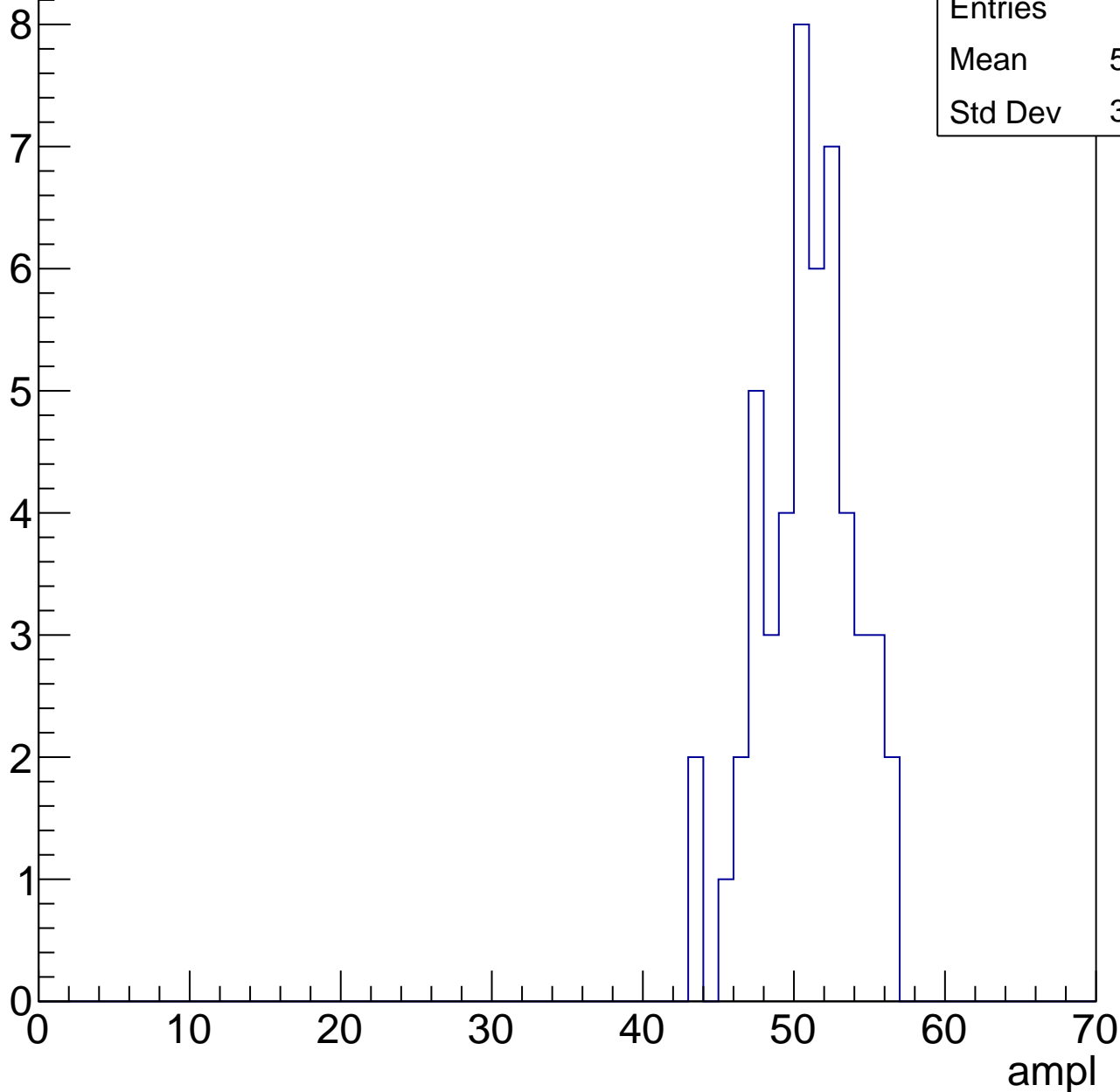


B1L003S, U16-ch48, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	50.38
Std Dev	3.078

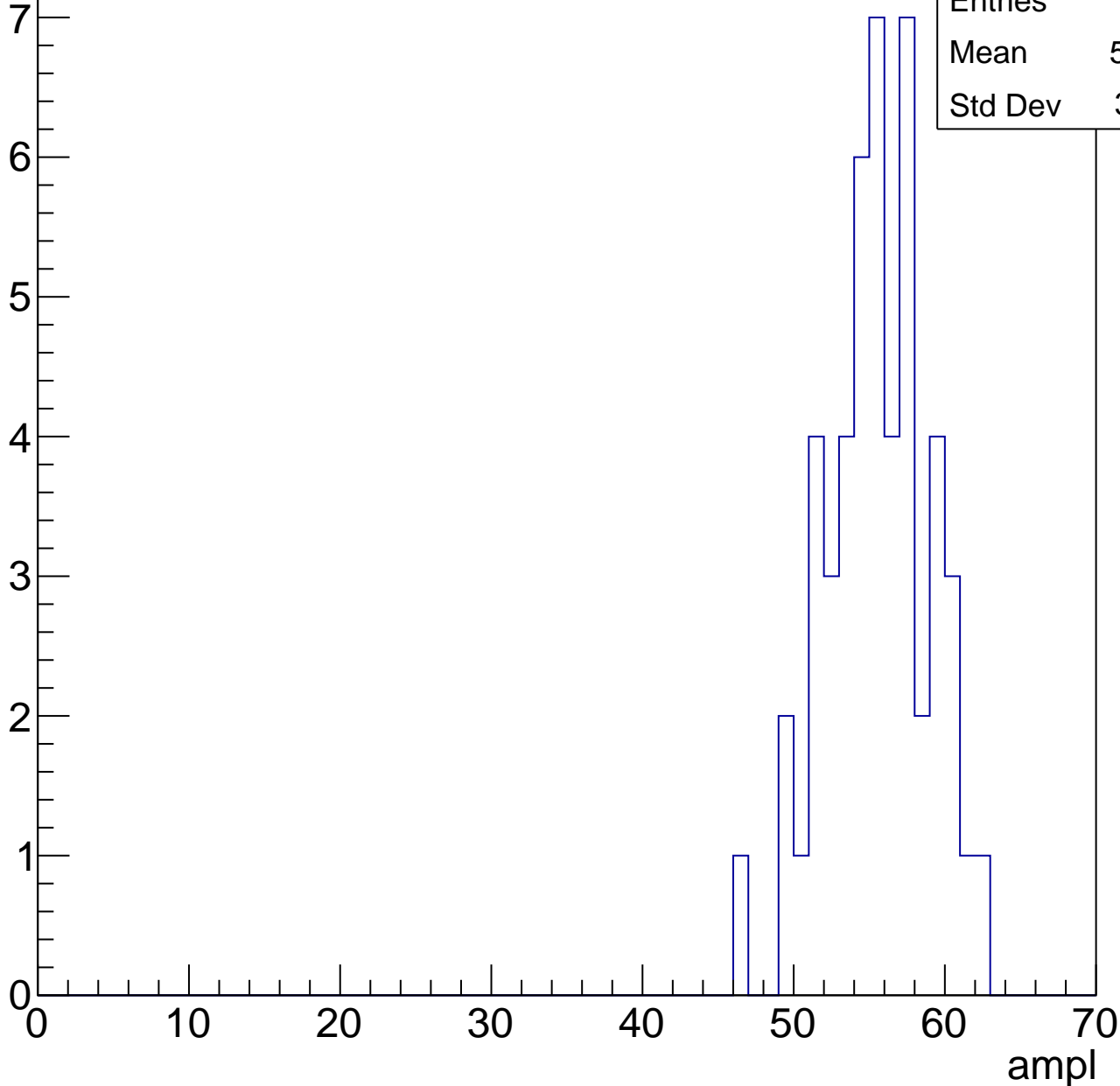


B1L003S, U16-ch48, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.06
Std Dev	3.361

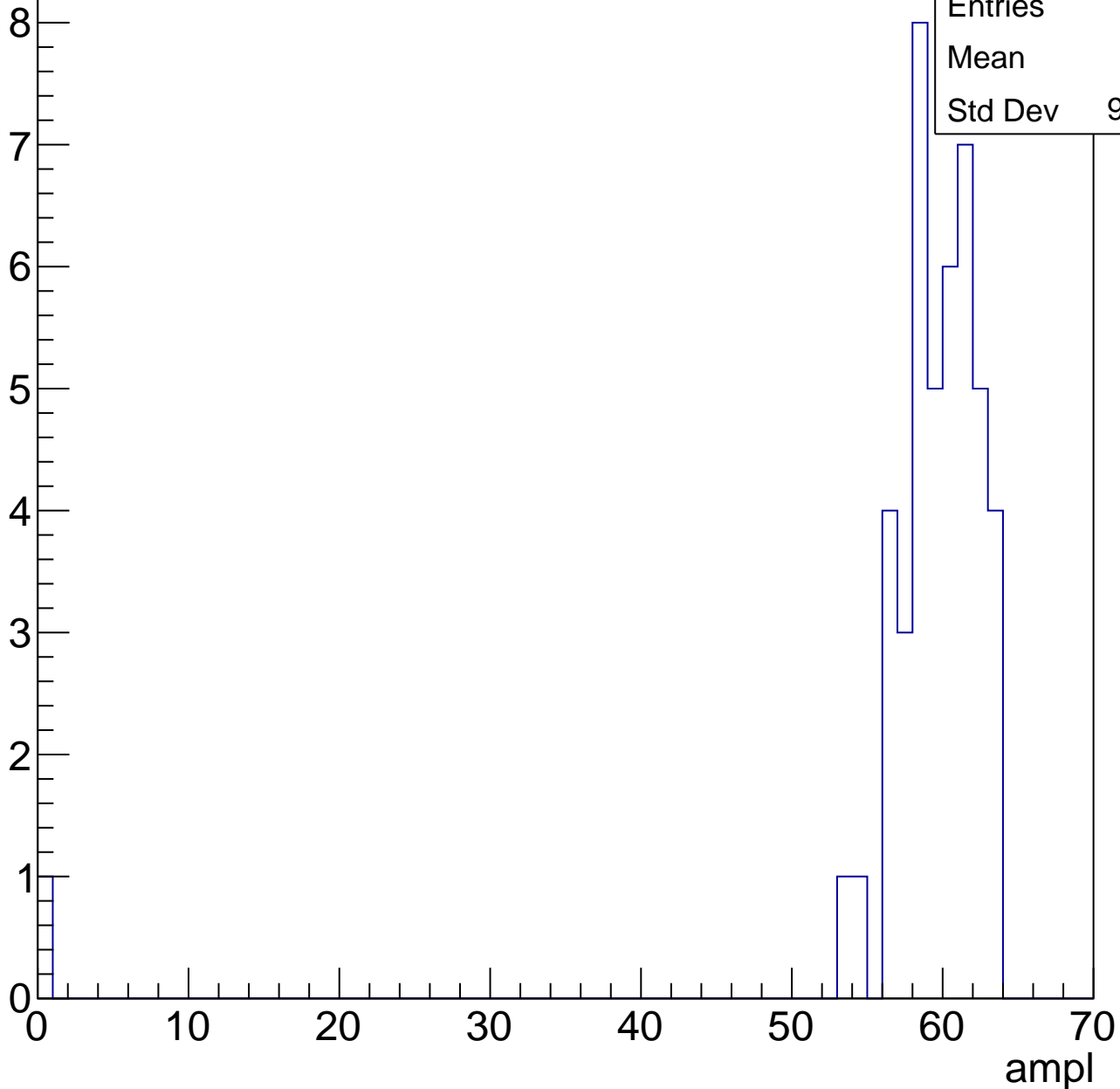


B1L003S, U16-ch48, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58
Std Dev	9.063

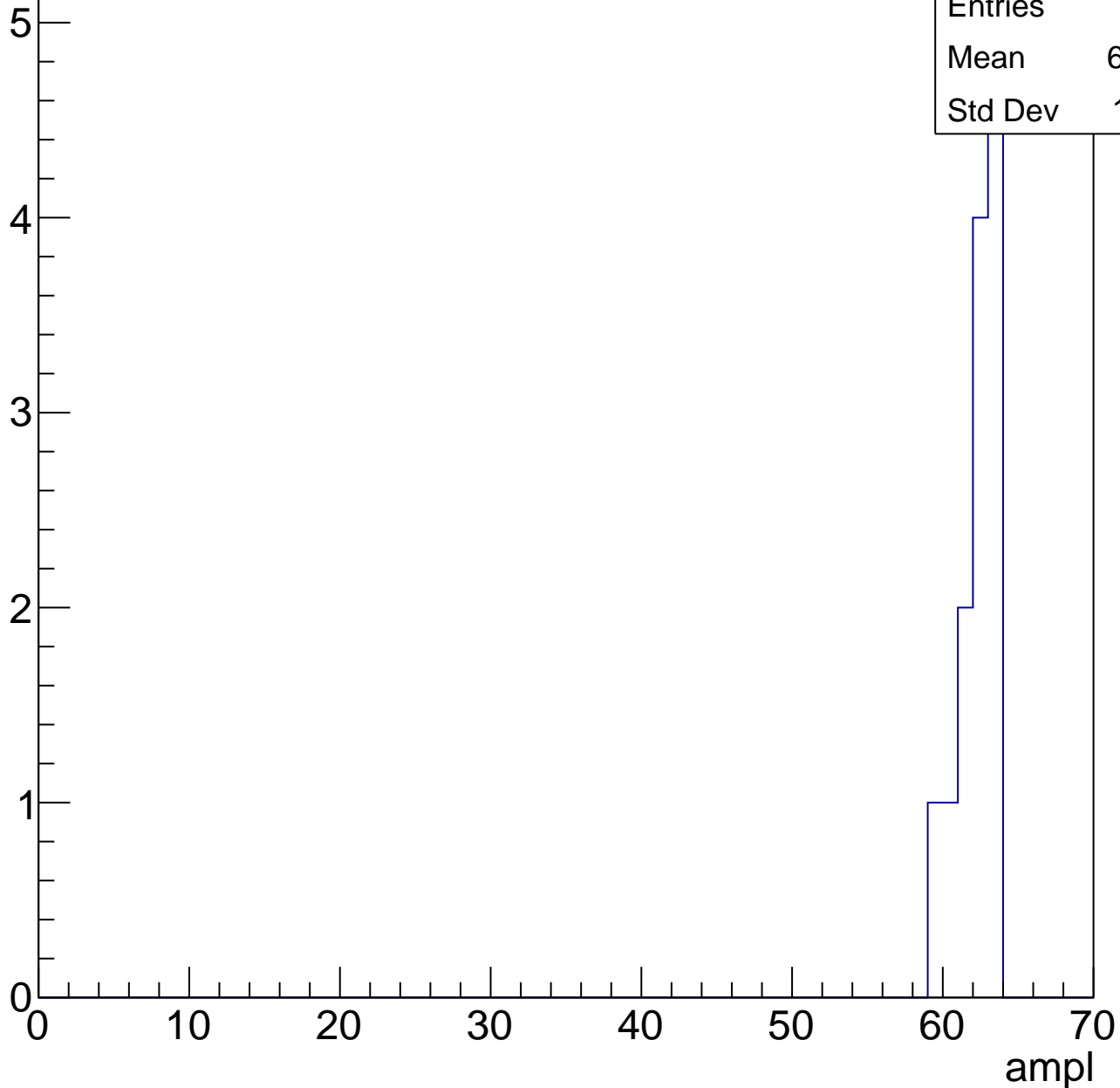


B1L003S, U16-ch48, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.85
Std Dev	1.231



B1L003S, U16-ch48, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U16-ch49, adc0

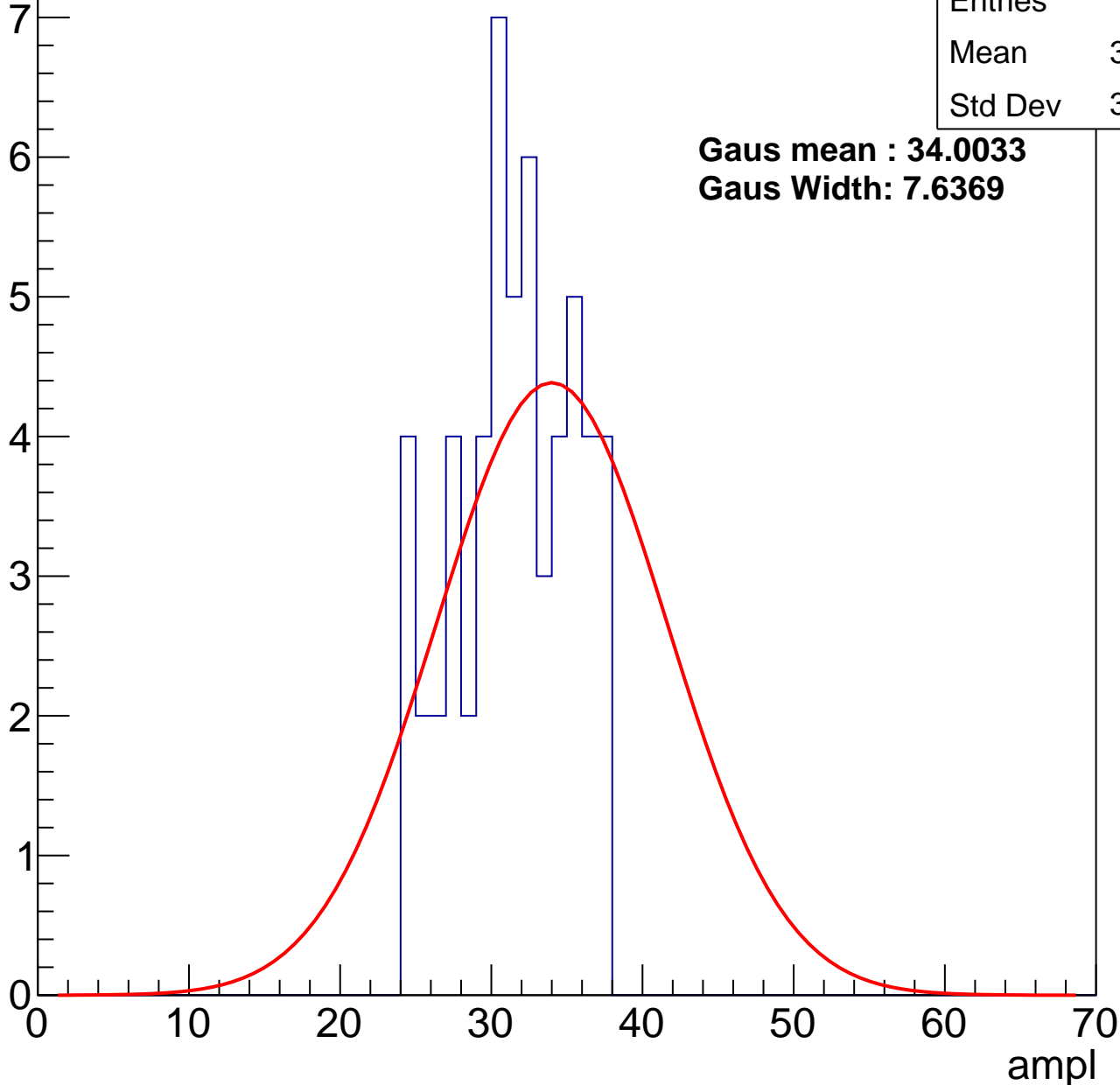
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	31.02
Std Dev	3.782

Gaus mean : 34.0033

Gaus Width: 7.6369



B1L003S, U16-ch49, adc1

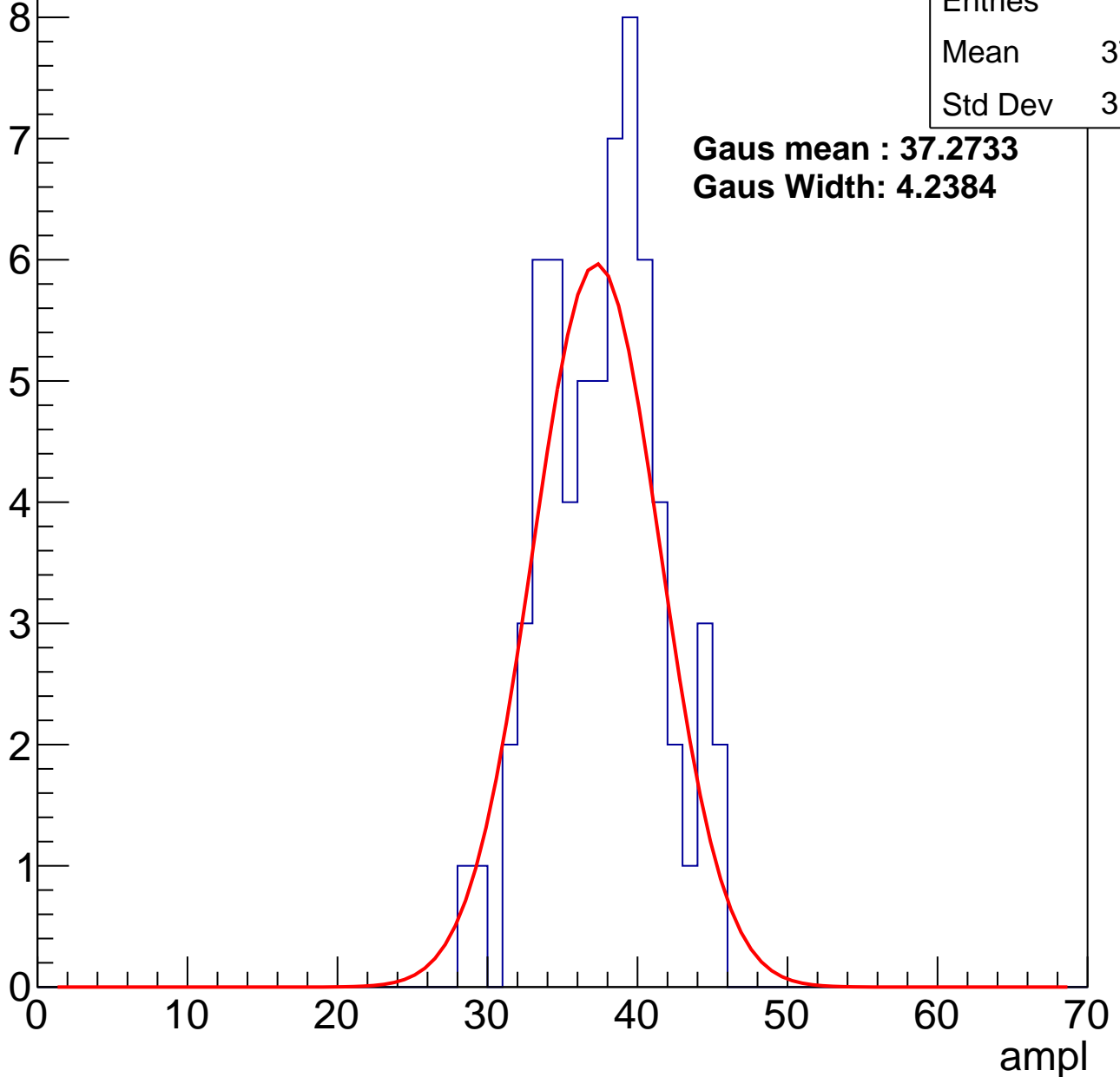
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	37.17
Std Dev	3.852

Gaus mean : 37.2733

Gaus Width: 4.2384



B1L003S, U16-ch49, adc2

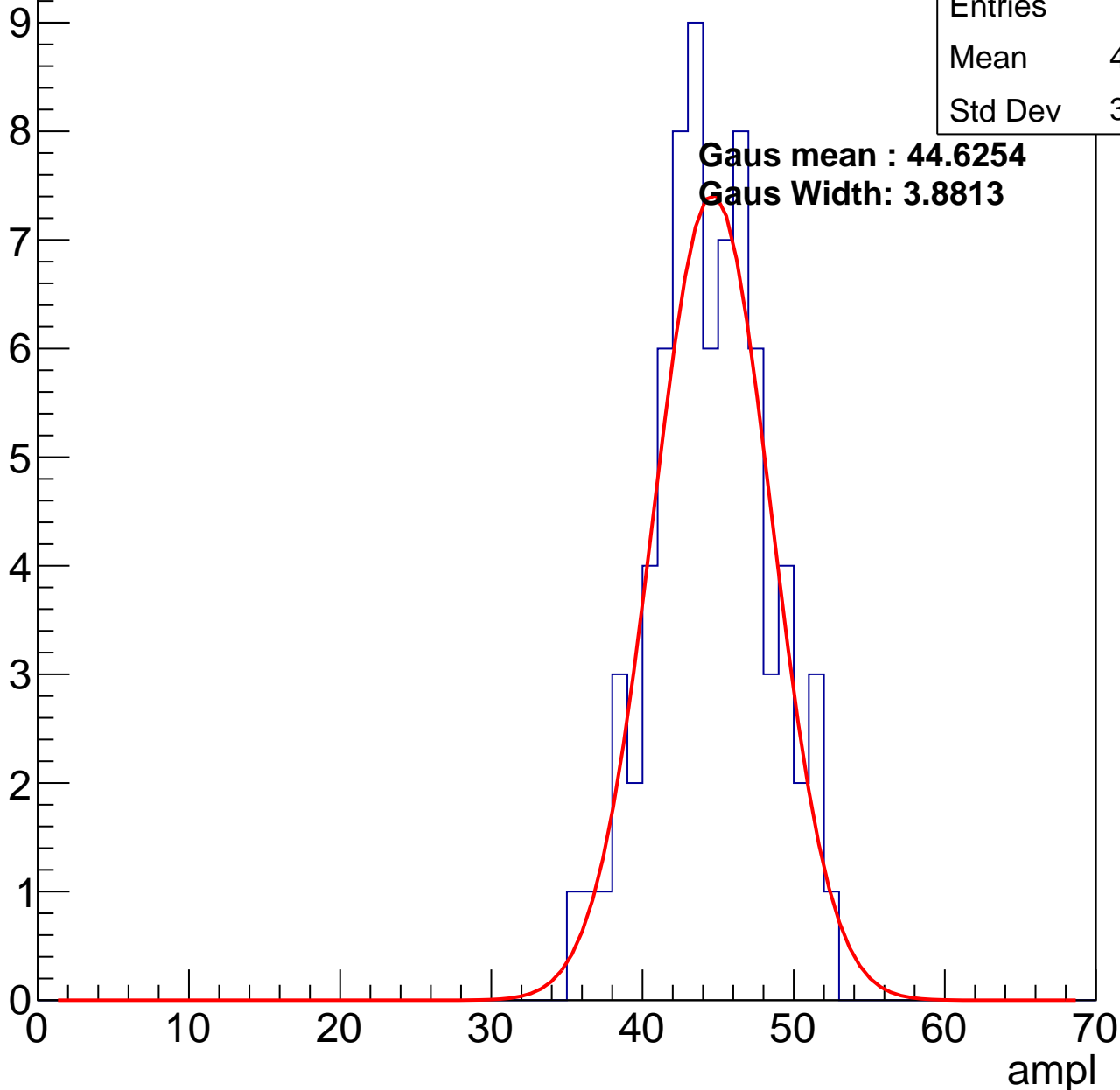
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	44.04
Std Dev	3.715

Gaus mean : 44.6254

Gaus Width: 3.8813

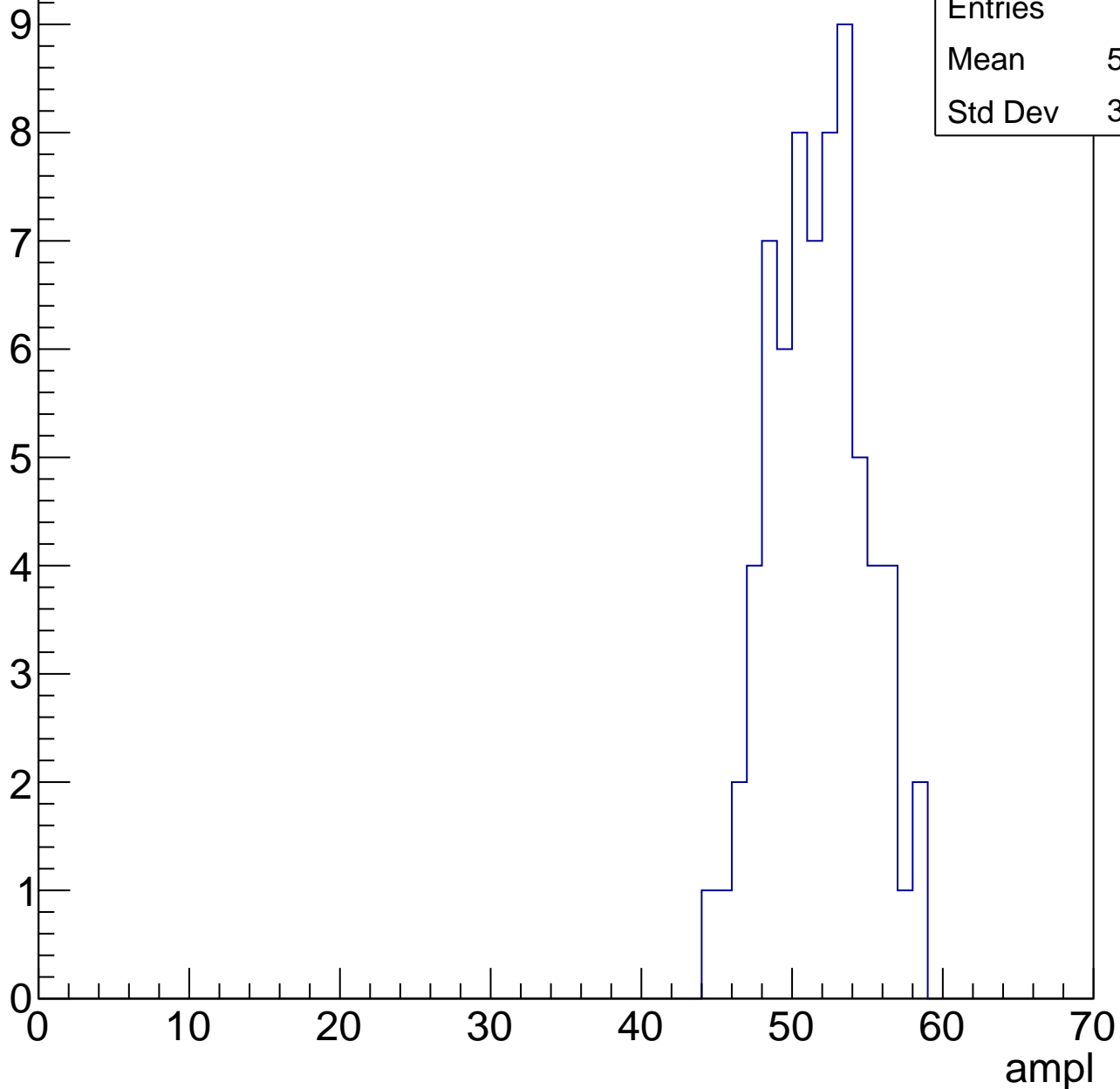


B1L003S, U16-ch49, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	51.25
Std Dev	3.132

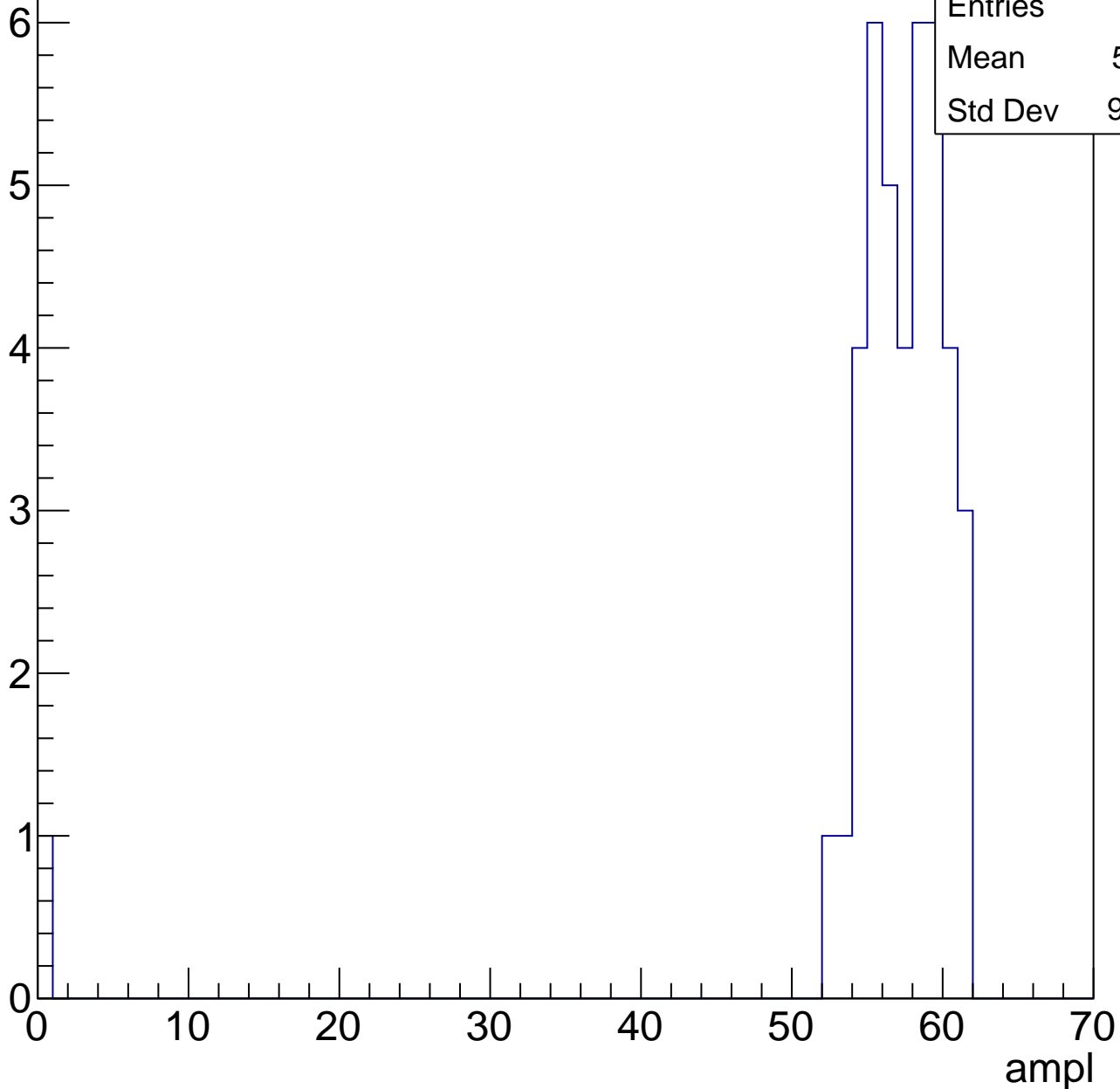


B1L003S, U16-ch49, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	55.71
Std Dev	9.107

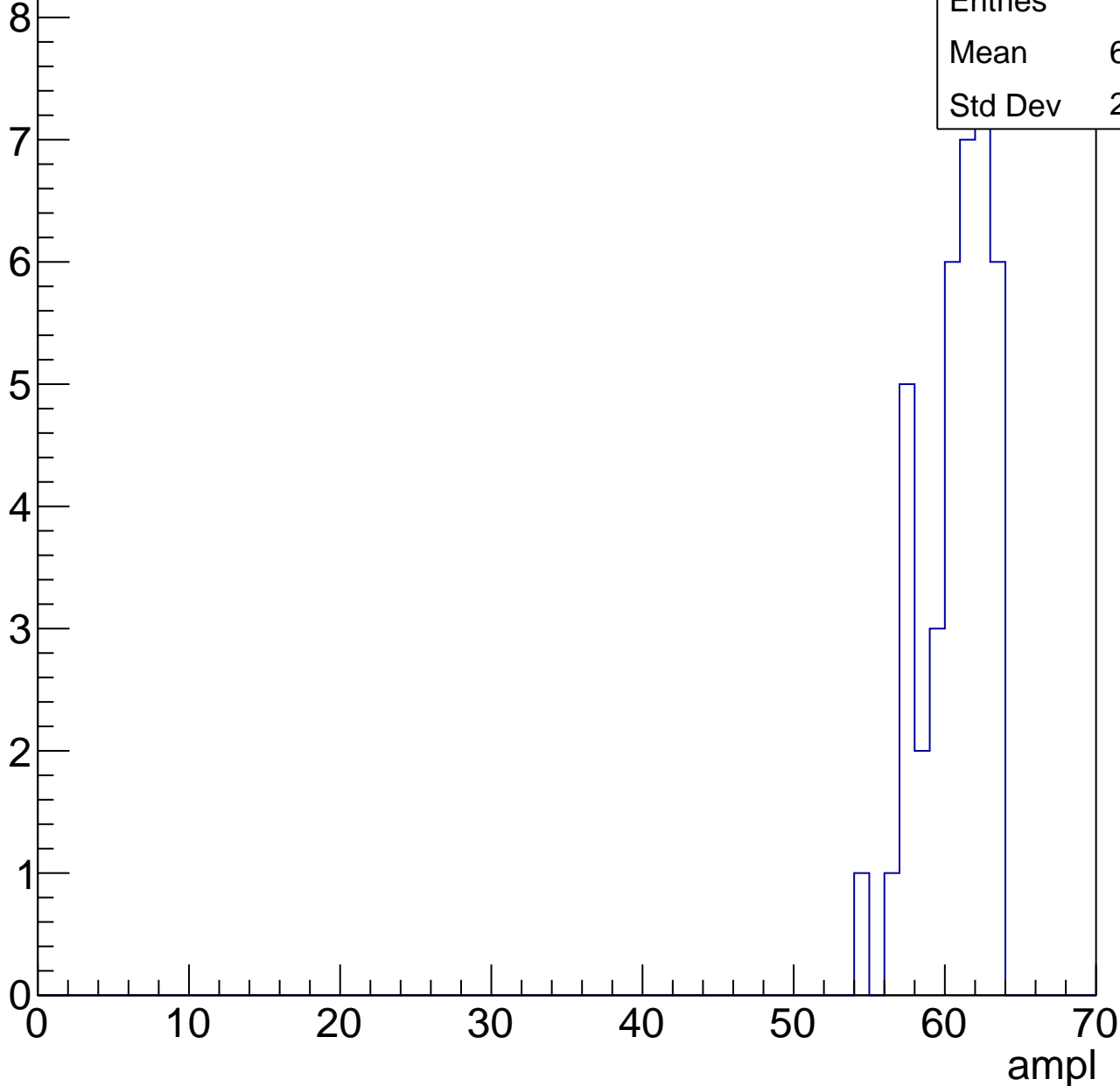


B1L003S, U16-ch49, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

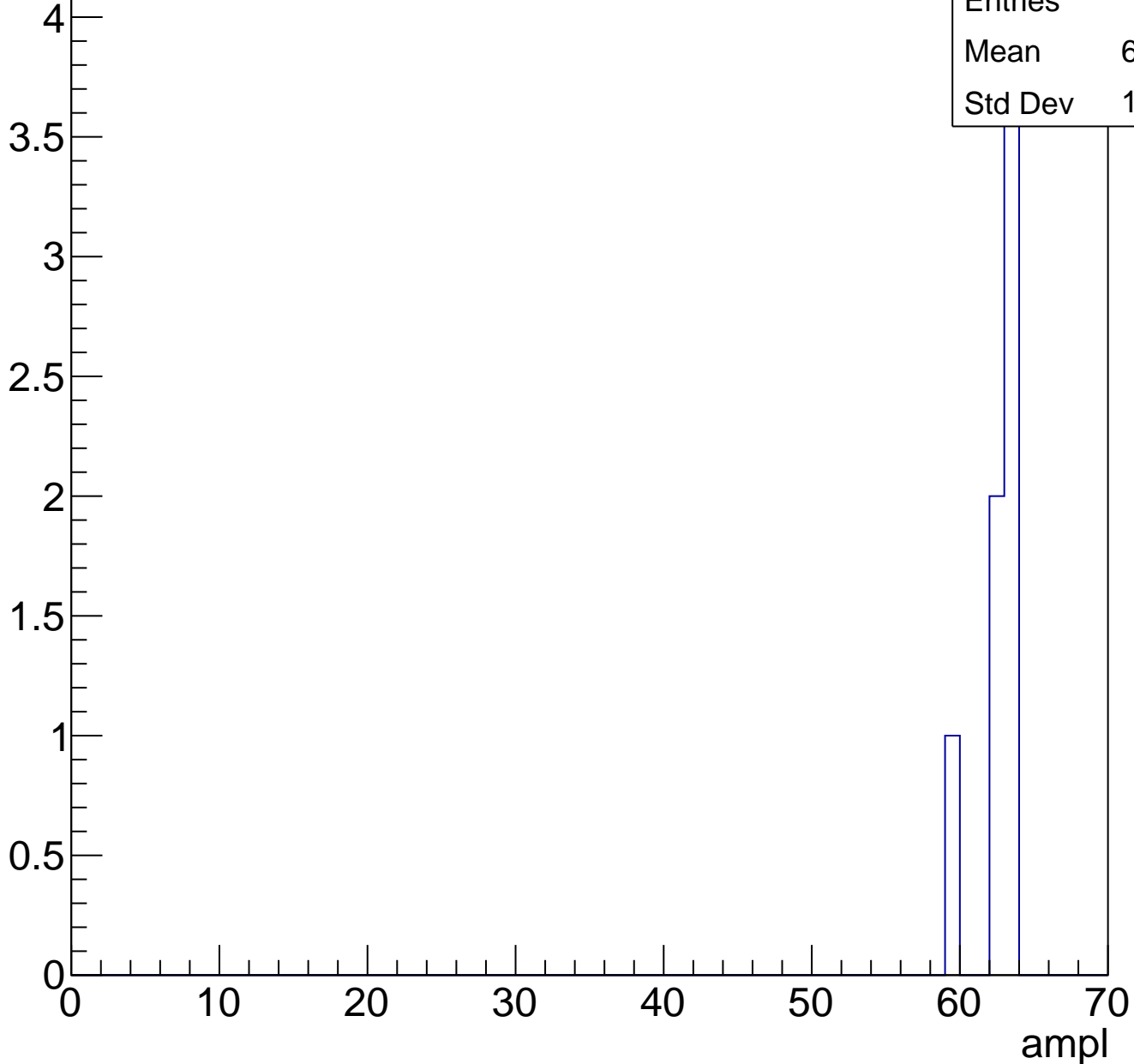
Entries	39
Mean	60.23
Std Dev	2.258



B1L003S, U16-ch49, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch49, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch50, adc0

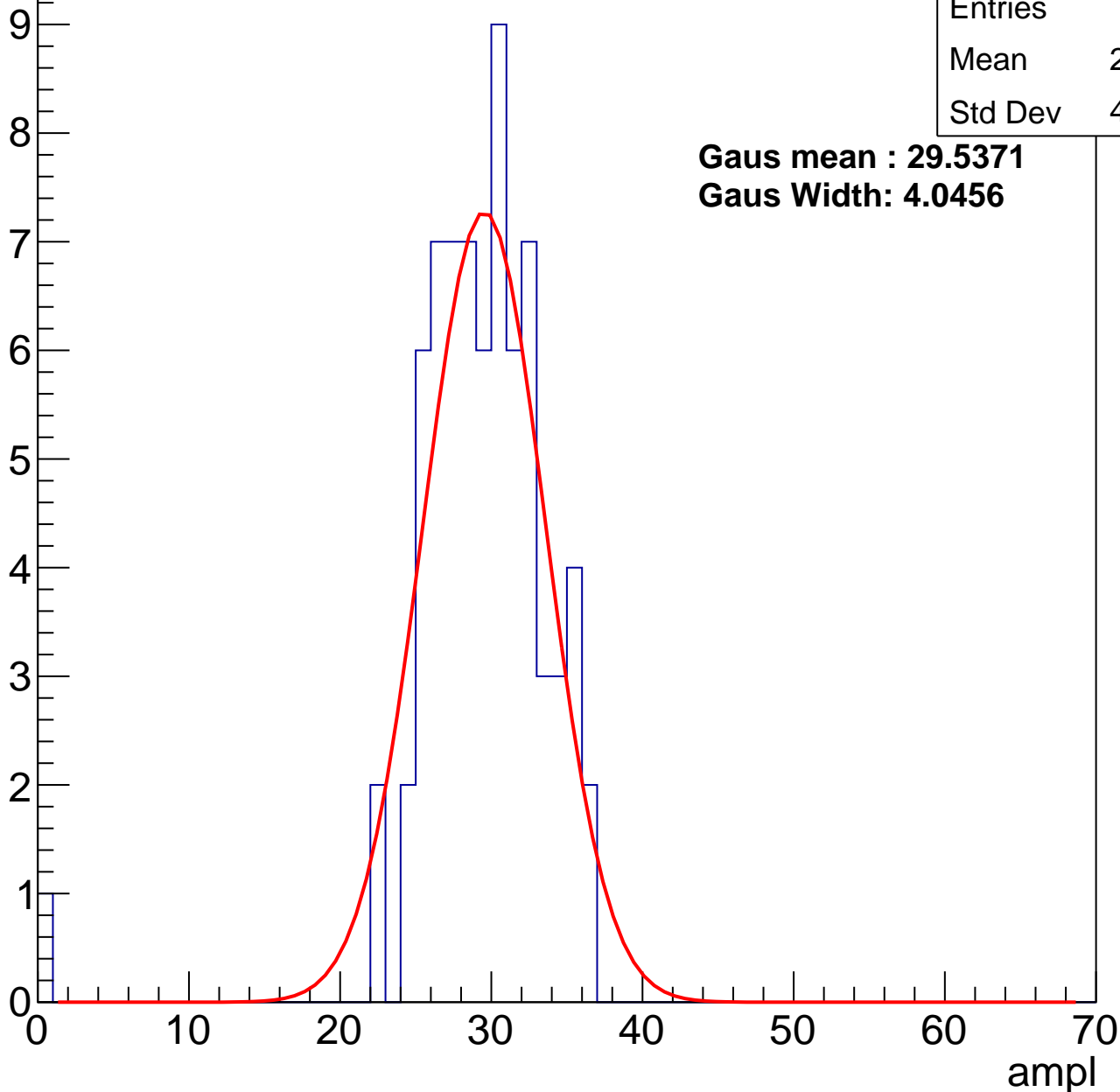
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	28.83
Std Dev	4.776

Gaus mean : 29.5371

Gaus Width: 4.0456



B1L003S, U16-ch50, adc1

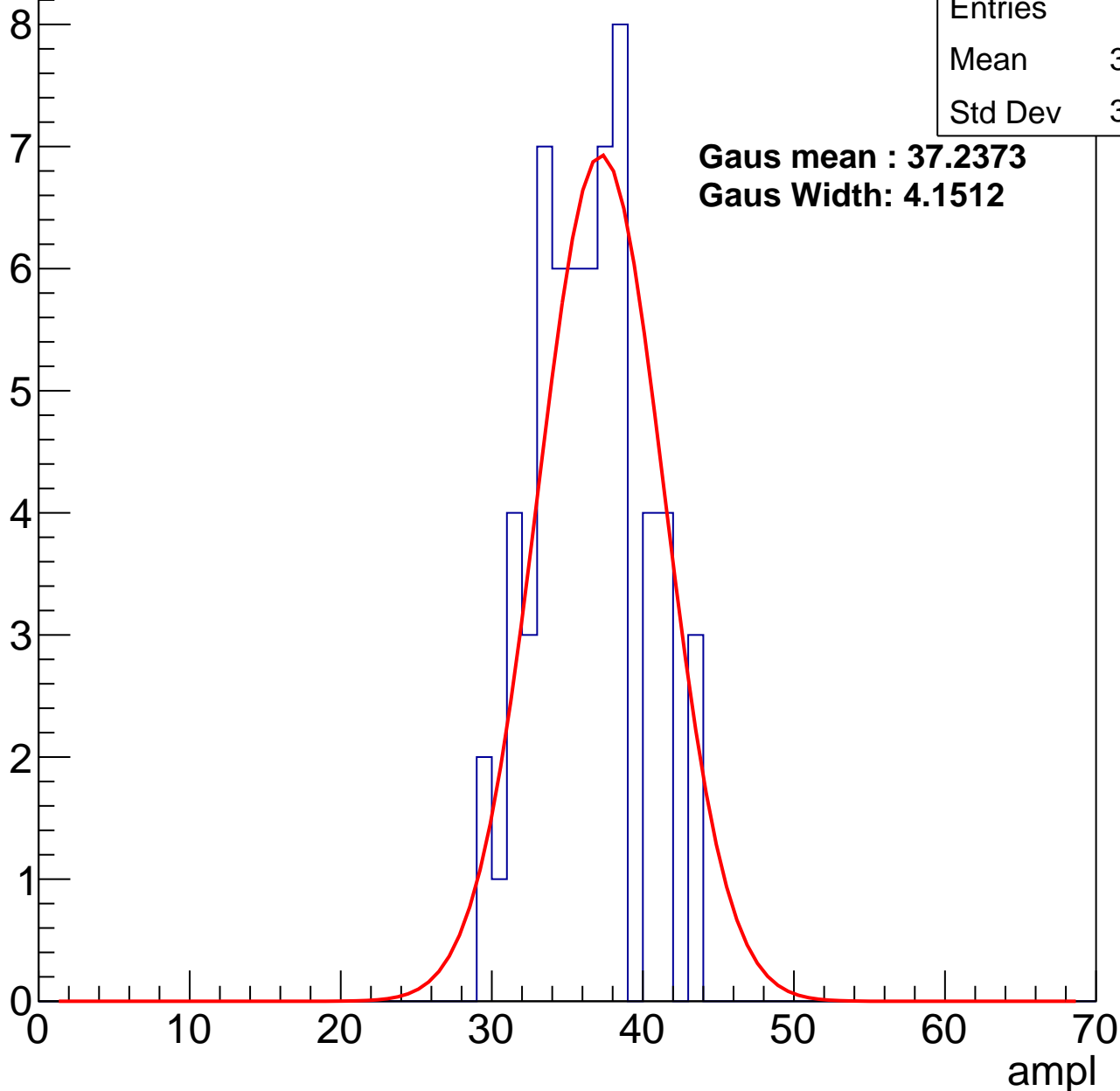
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.82
Std Dev	3.443

Gaus mean : 37.2373

Gaus Width: 4.1512



B1L003S, U16-ch50, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	41.24
Std Dev	3.027

Gaus mean : 41.5796

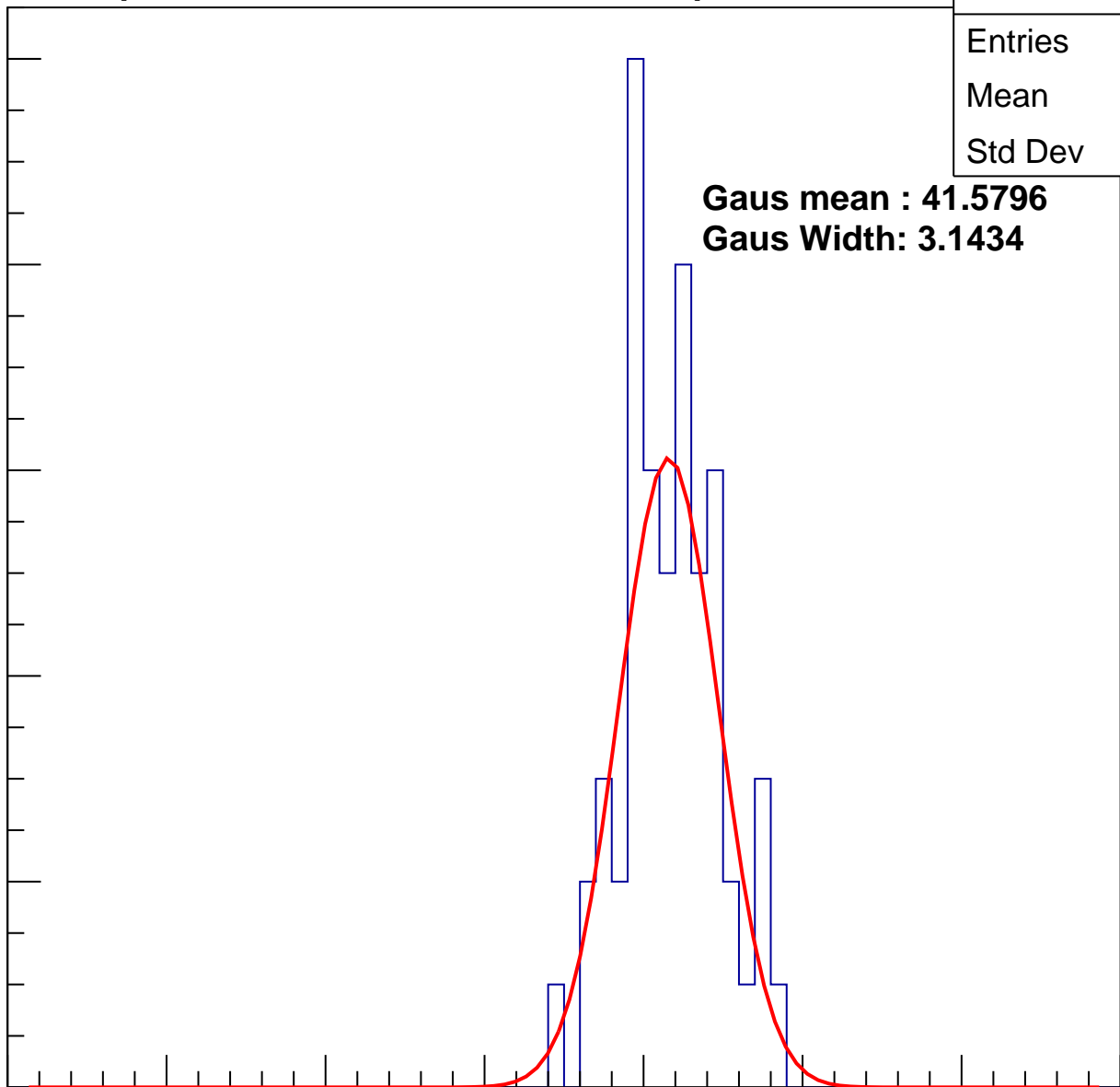
Gaus Width: 3.1434

Entry

10
8
6
4
2
0

ampl

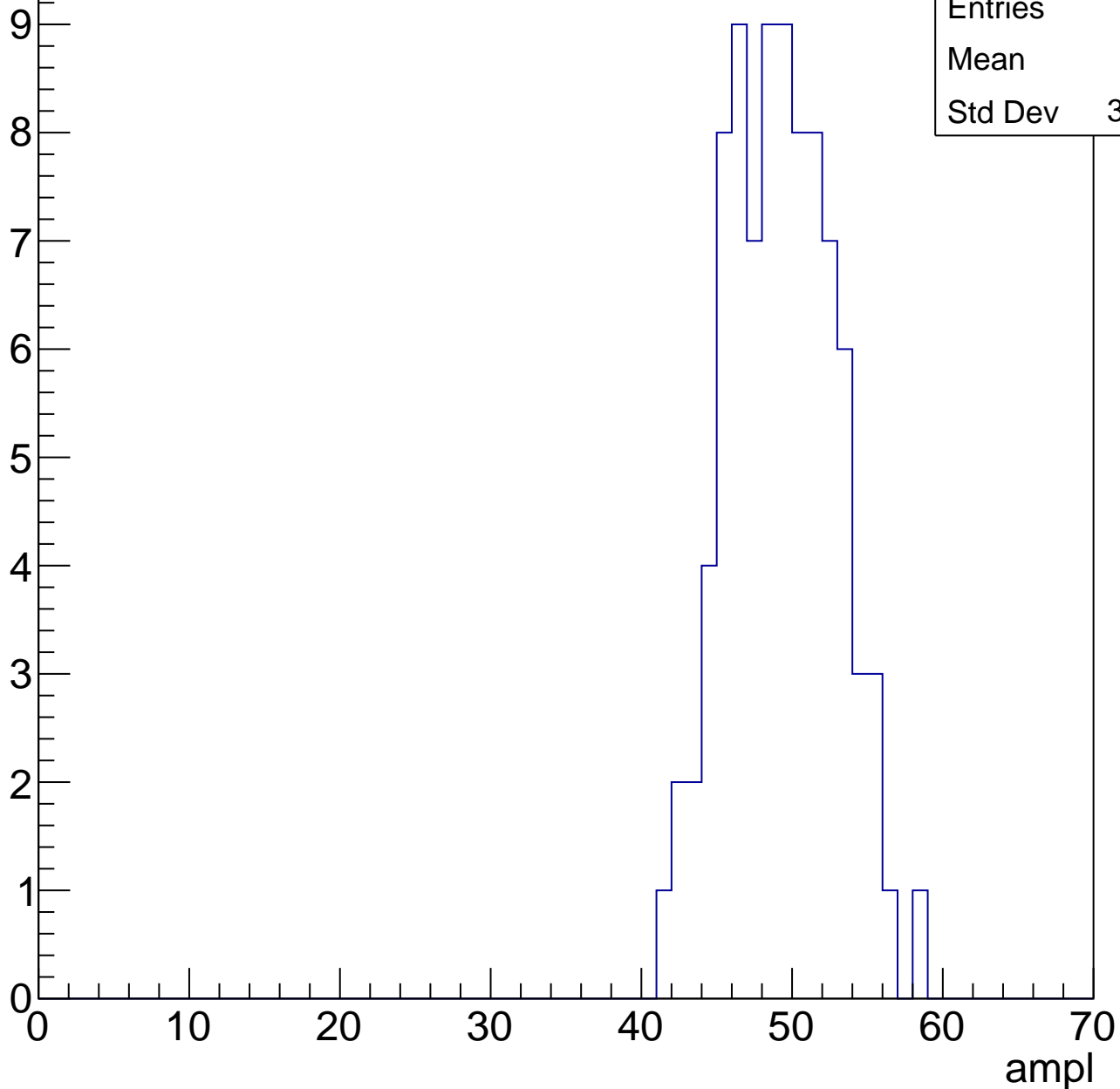
0 10 20 30 40 50 60 70



B1L003S, U16-ch50, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



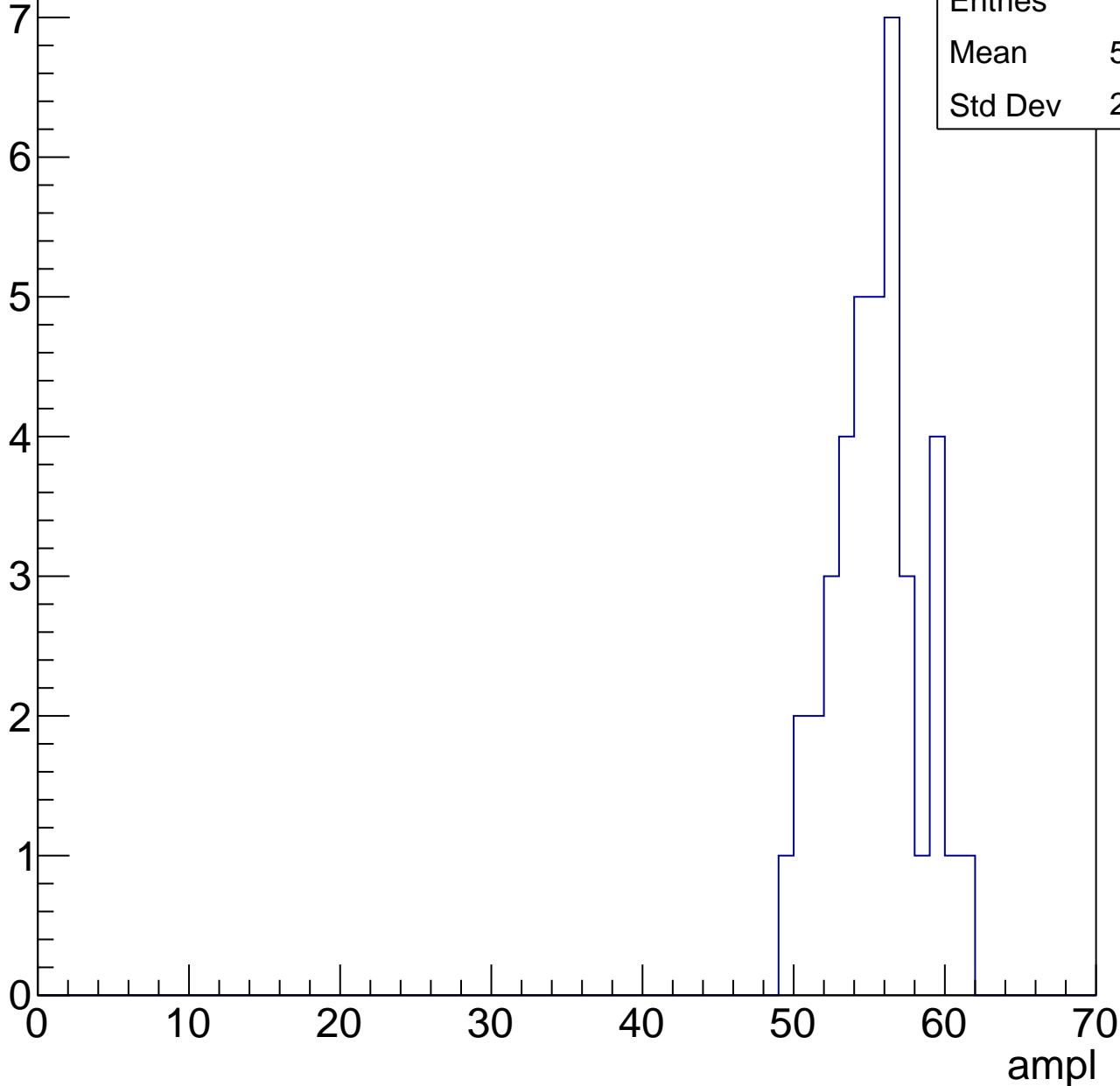
Entries	88
Mean	48.8
Std Dev	3.517

B1L003S, U16-ch50, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

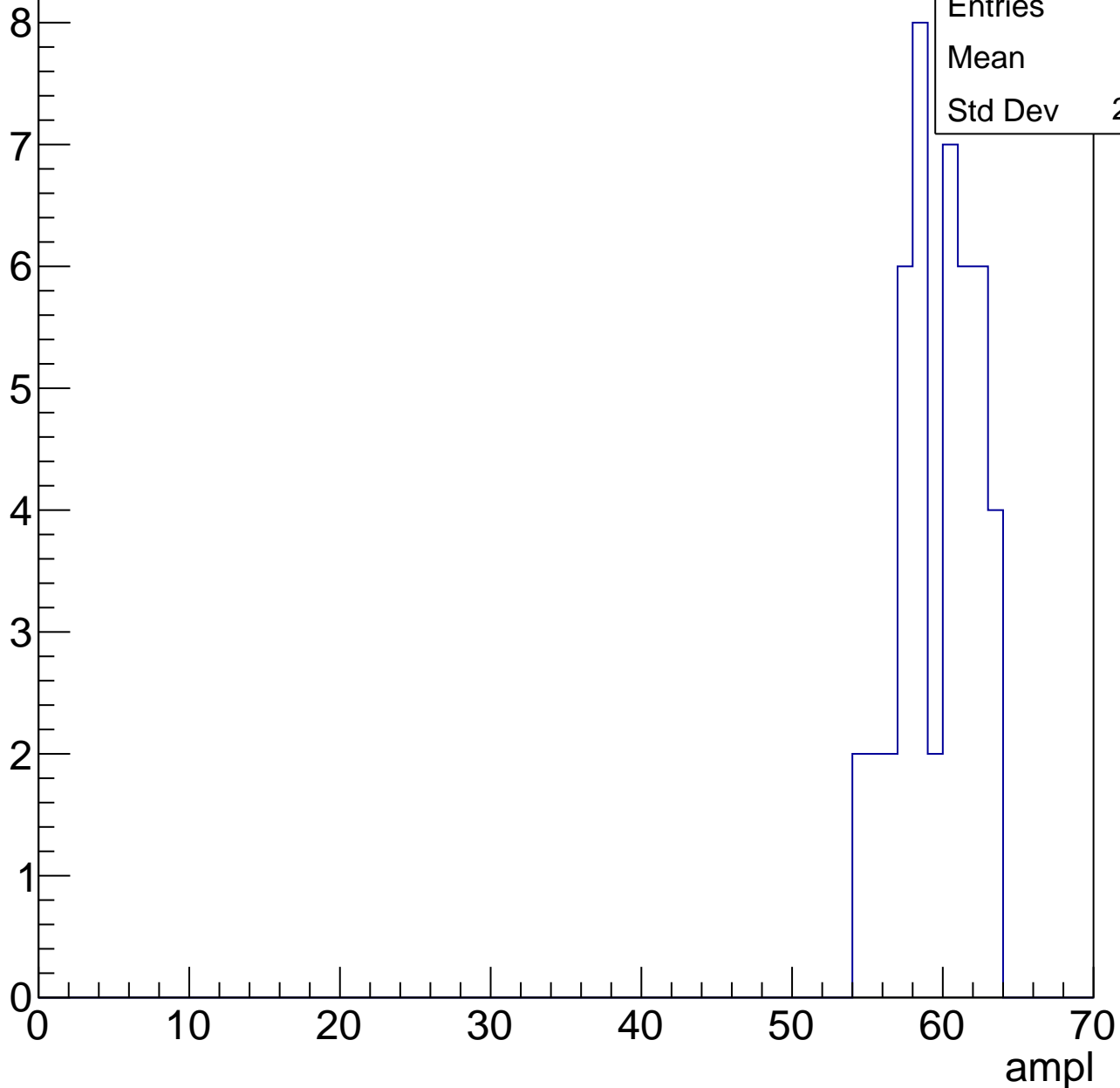
Entries	39
Mean	54.92
Std Dev	2.859



B1L003S, U16-ch50, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

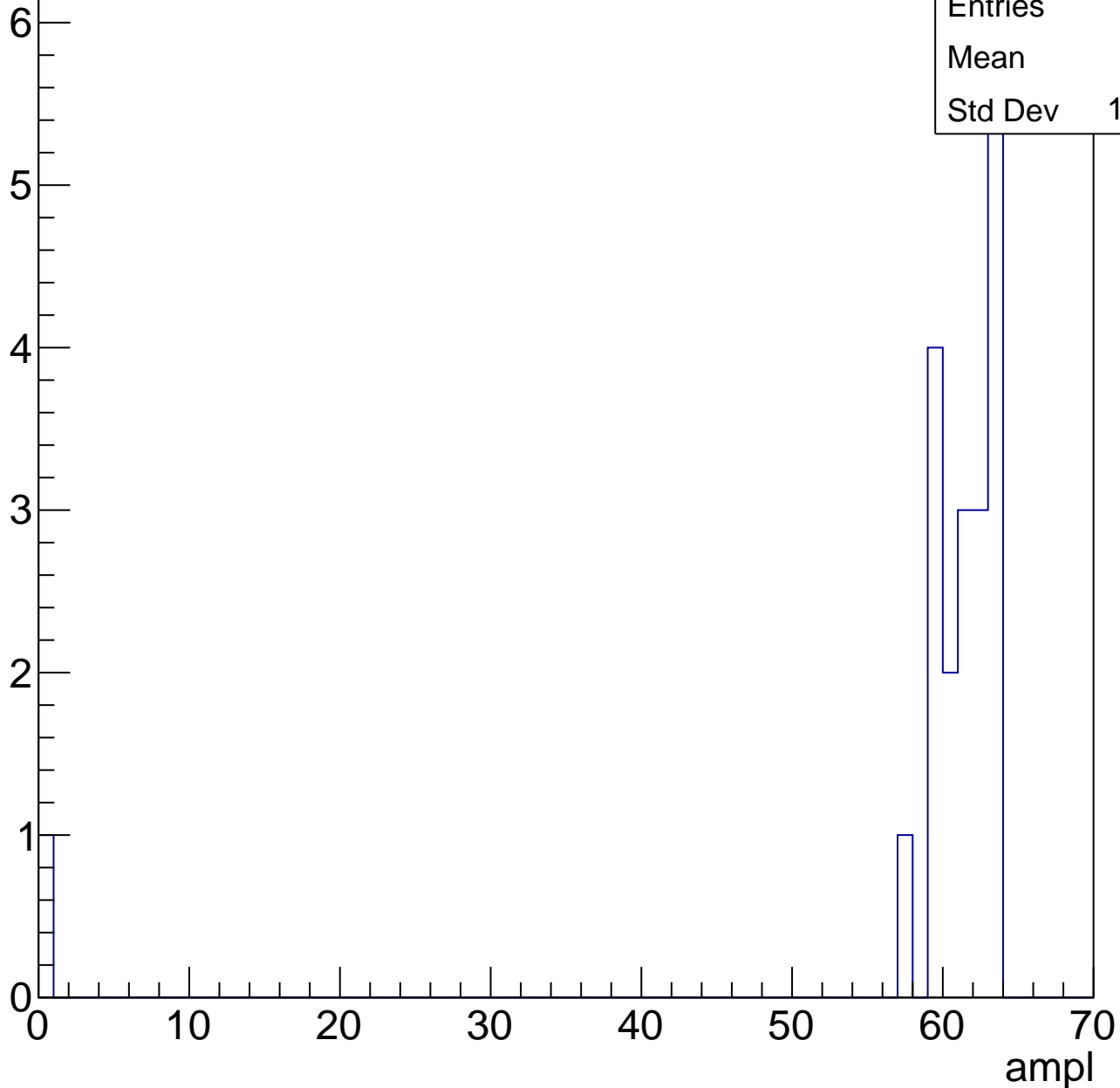


B1L003S, U16-ch50, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58
Std Dev	13.42



B1L003S, U16-ch50, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch51, adc0

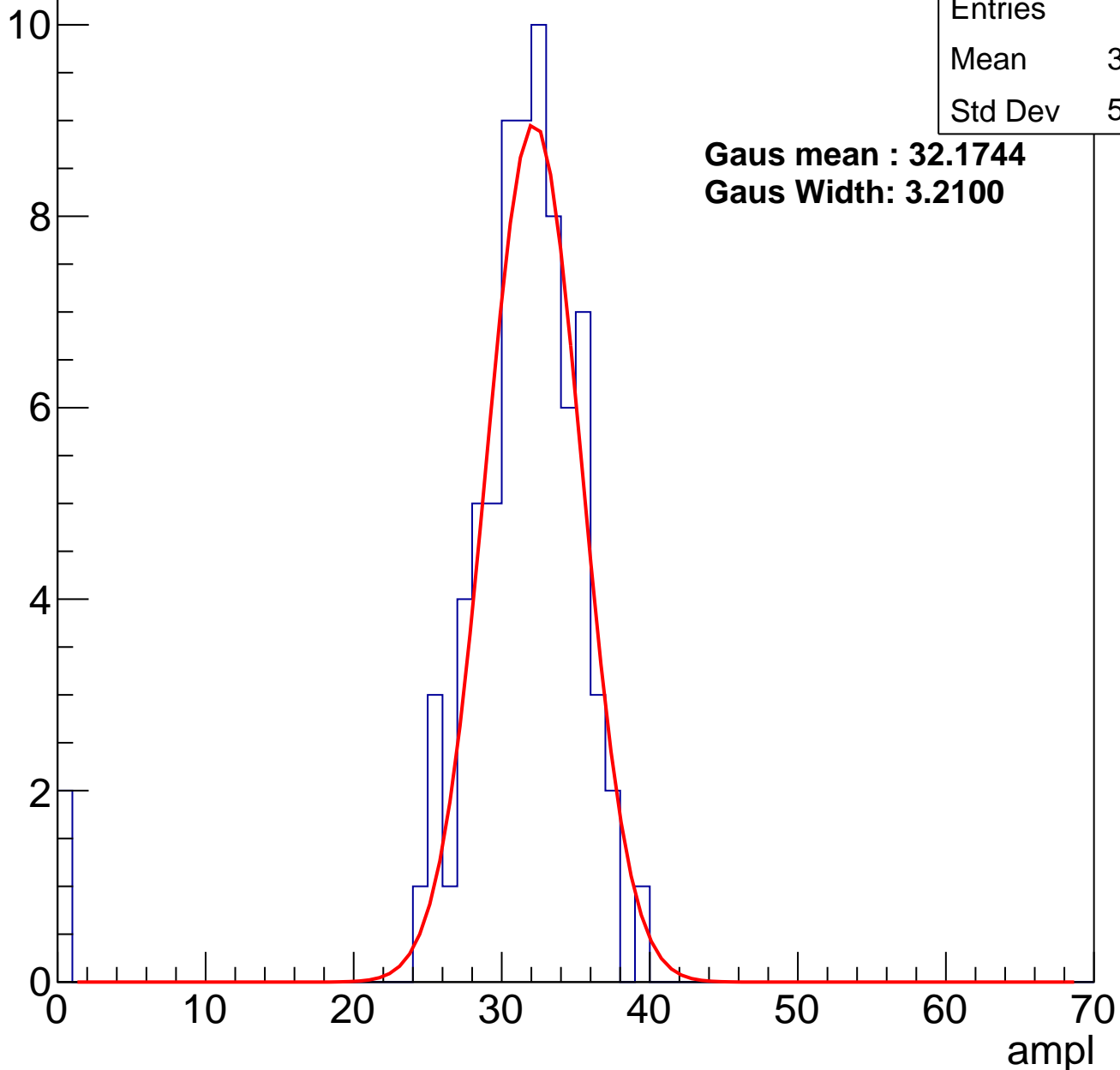
calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	30.54
Std Dev	5.899

Gaus mean : 32.1744

Gaus Width: 3.2100

Entry



B1L003S, U16-ch51, adc1

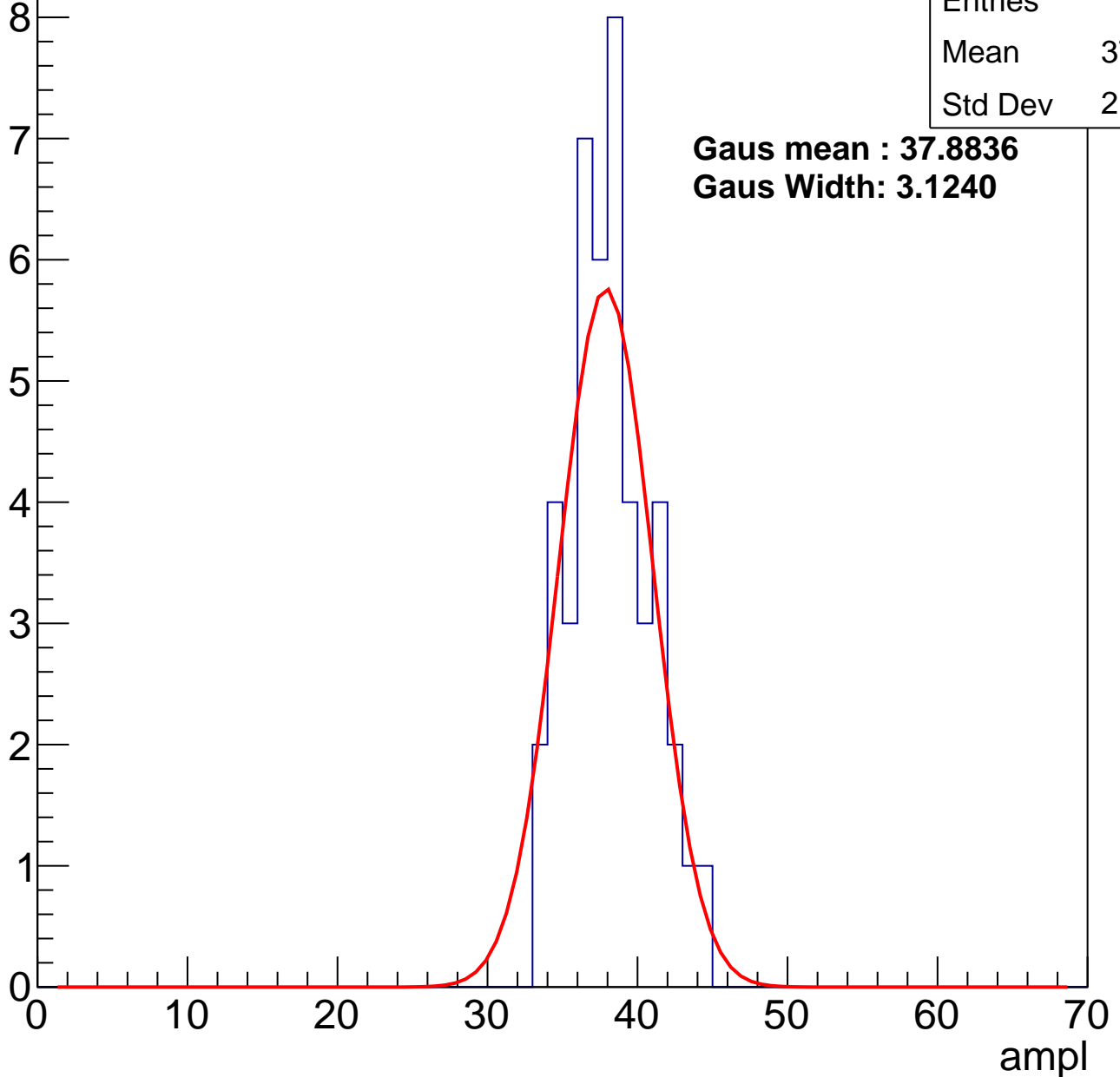
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	37.69
Std Dev	2.648

Gaus mean : 37.8836

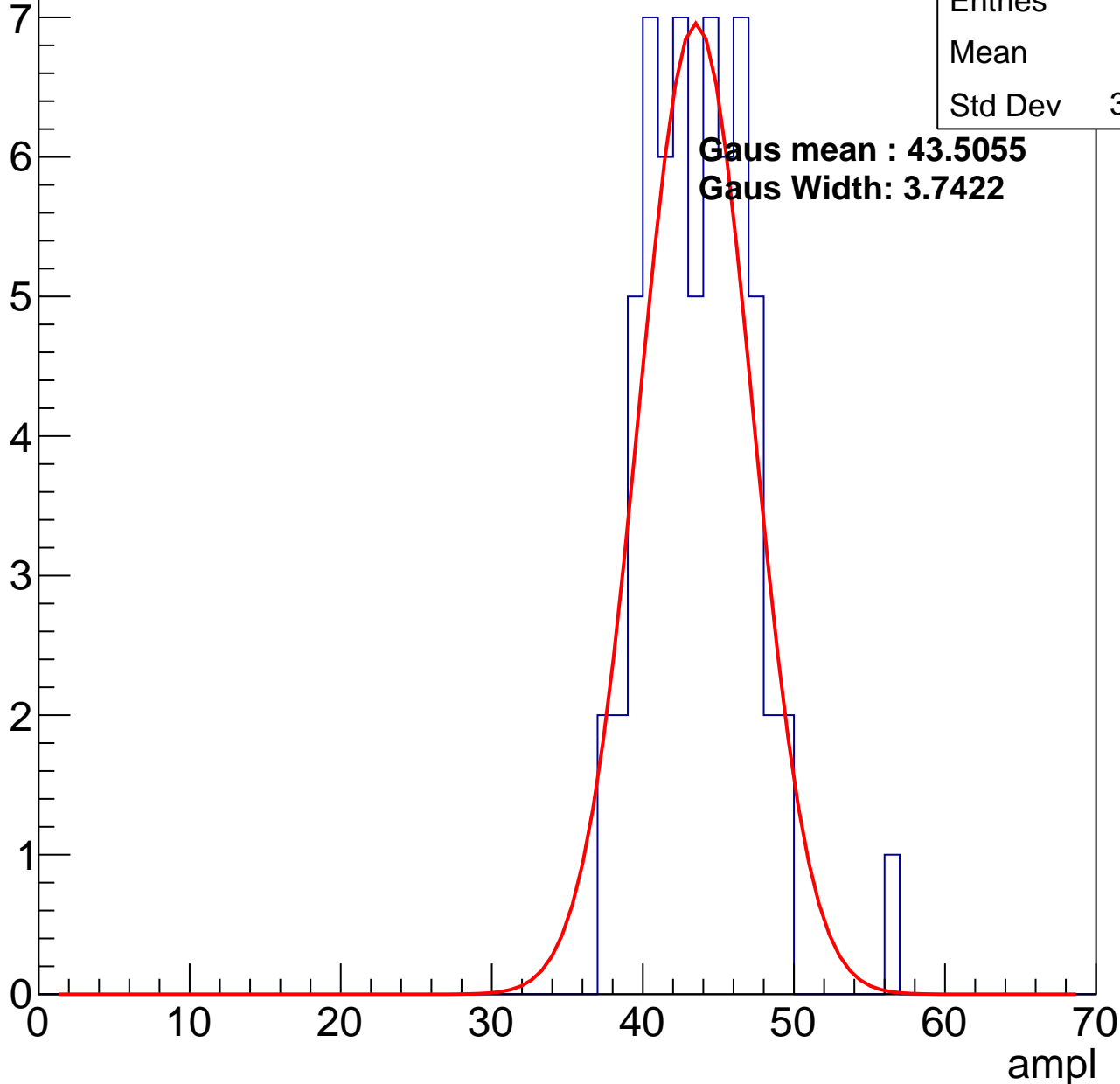
Gaus Width: 3.1240



B1L003S, U16-ch51, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

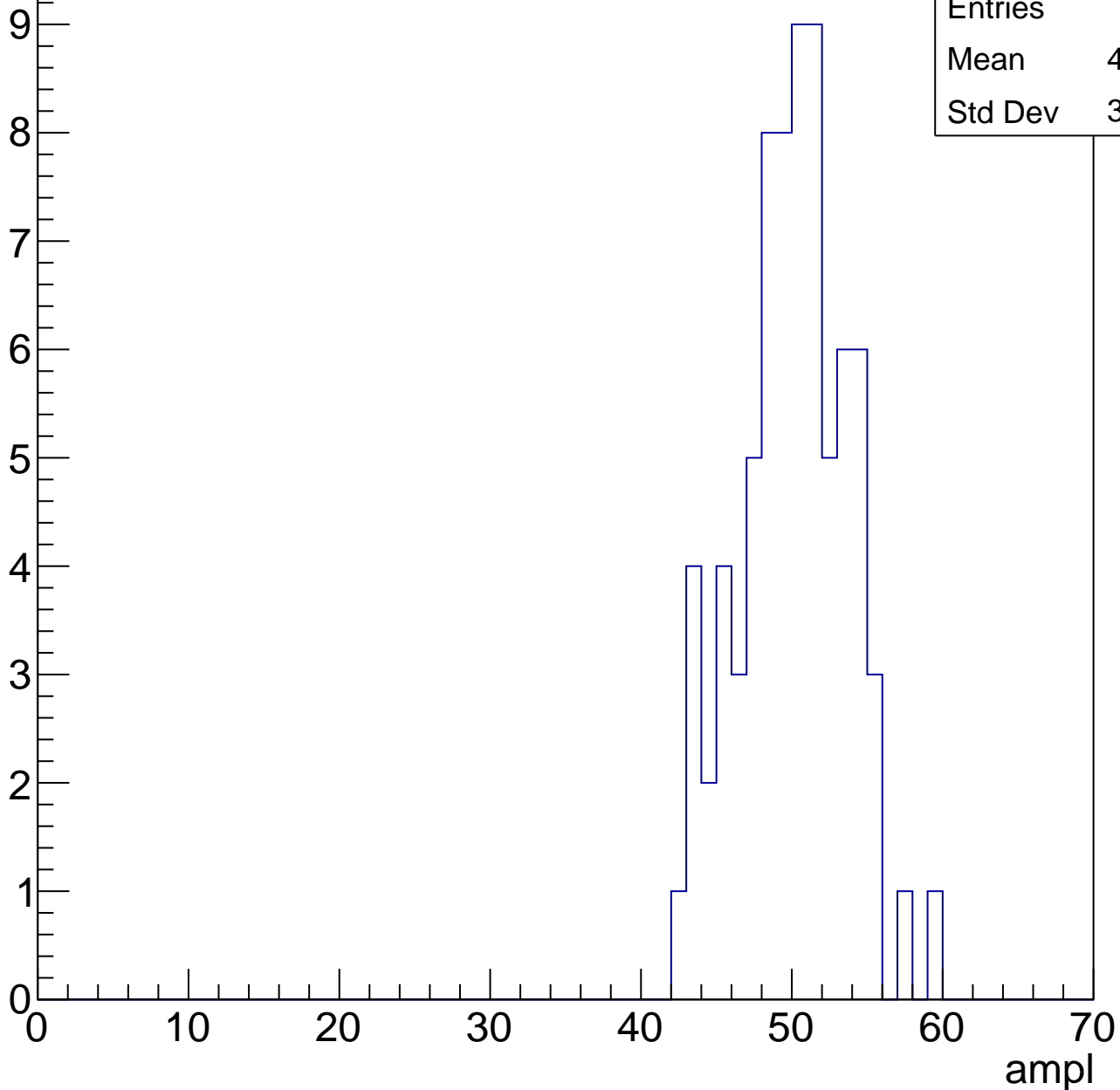


B1L003S, U16-ch51, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	49.64
Std Dev	3.539

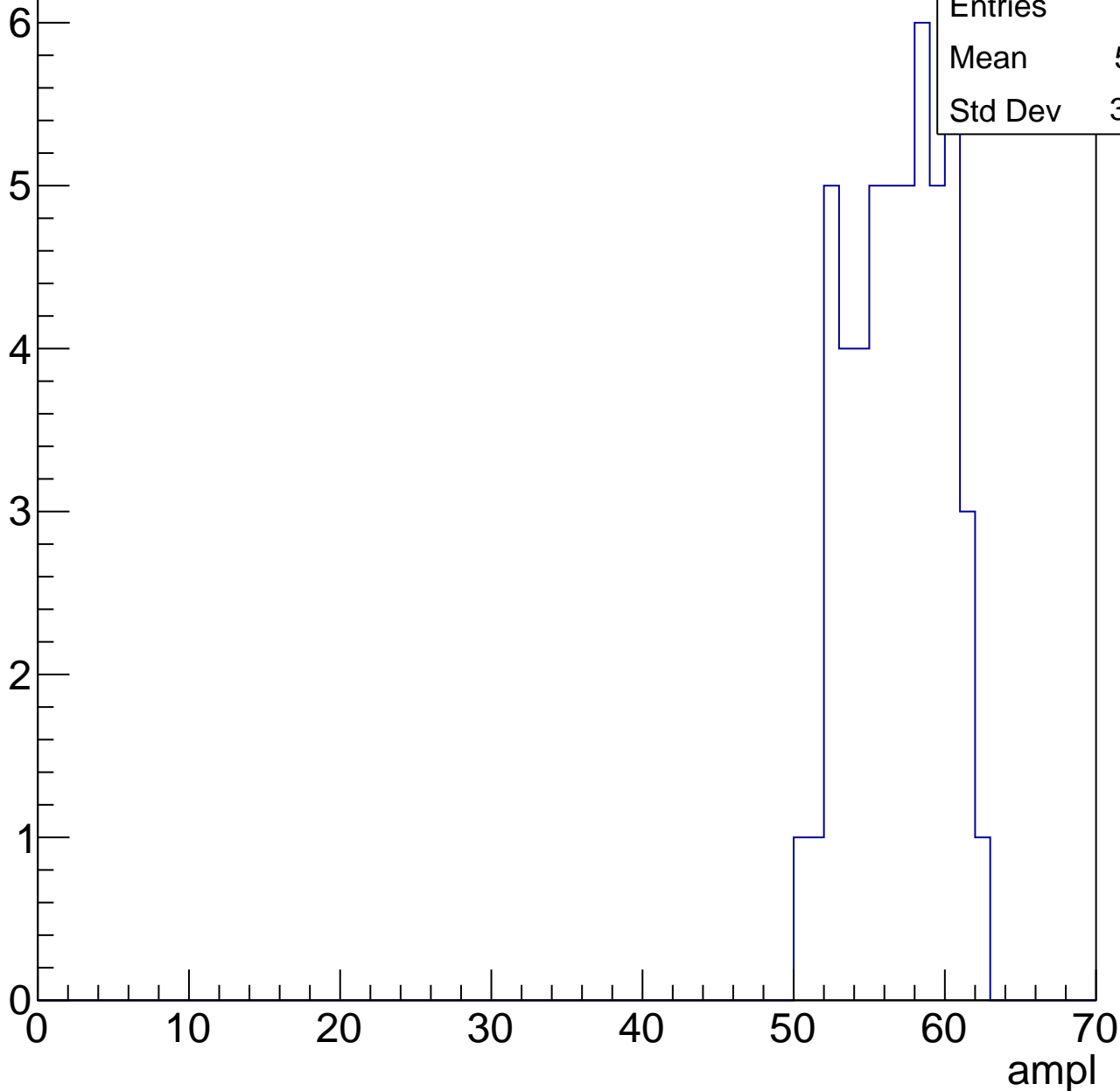


B1L003S, U16-ch51, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.41
Std Dev	3.037



B1L003S, U16-ch51, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	58.36
Std Dev	9.454

ampl

B1L003S, U16-ch51, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch51, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch52, adc0

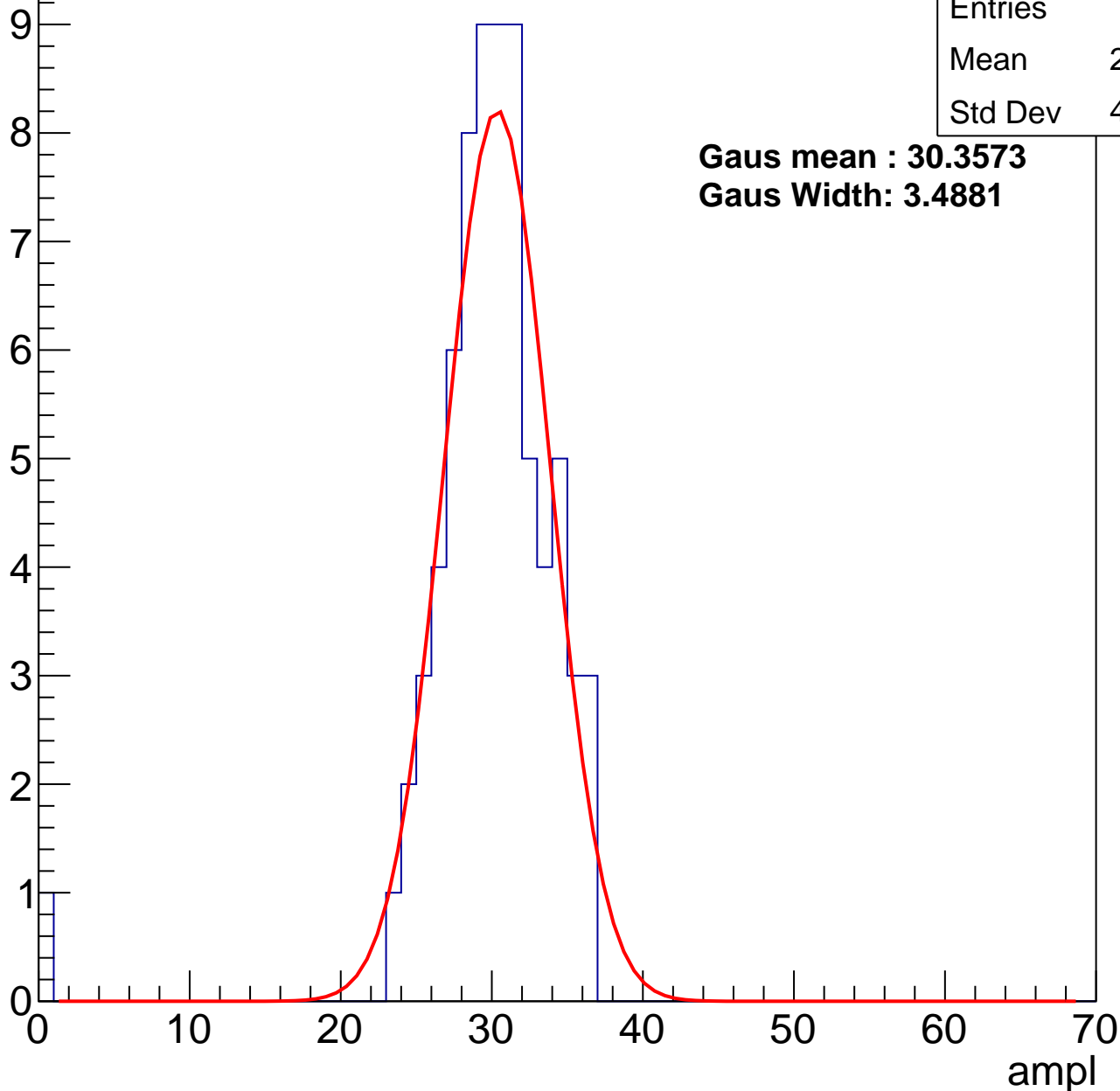
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.46
Std Dev	4.658

Gaus mean : 30.3573

Gaus Width: 3.4881



B1L003S, U16-ch52, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	37.32
Std Dev	3.76

Gaus mean : 37.8664

Gaus Width: 3.6200

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L003S, U16-ch52, adc2

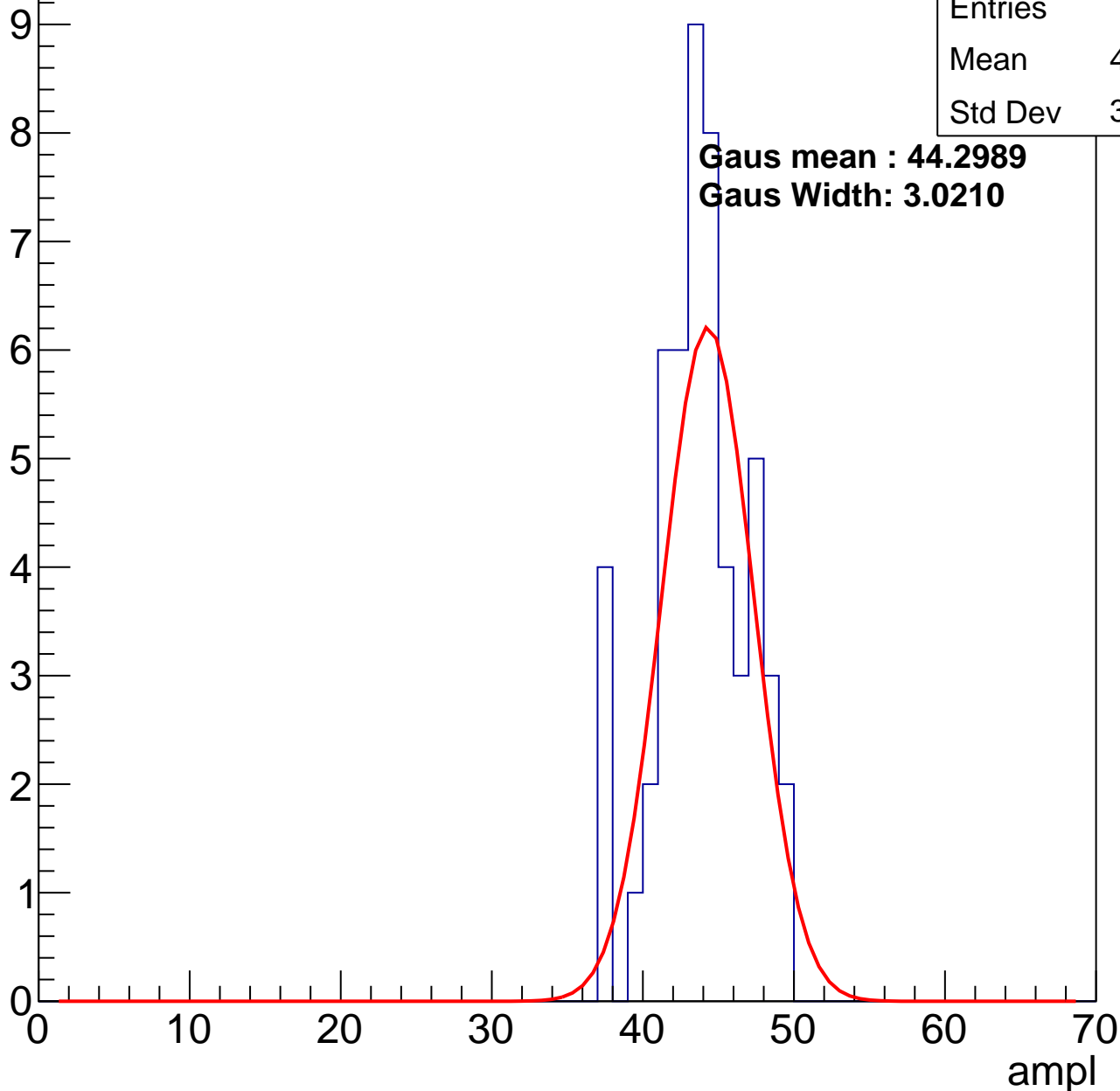
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.38
Std Dev	3.005

Gaus mean : 44.2989

Gaus Width: 3.0210

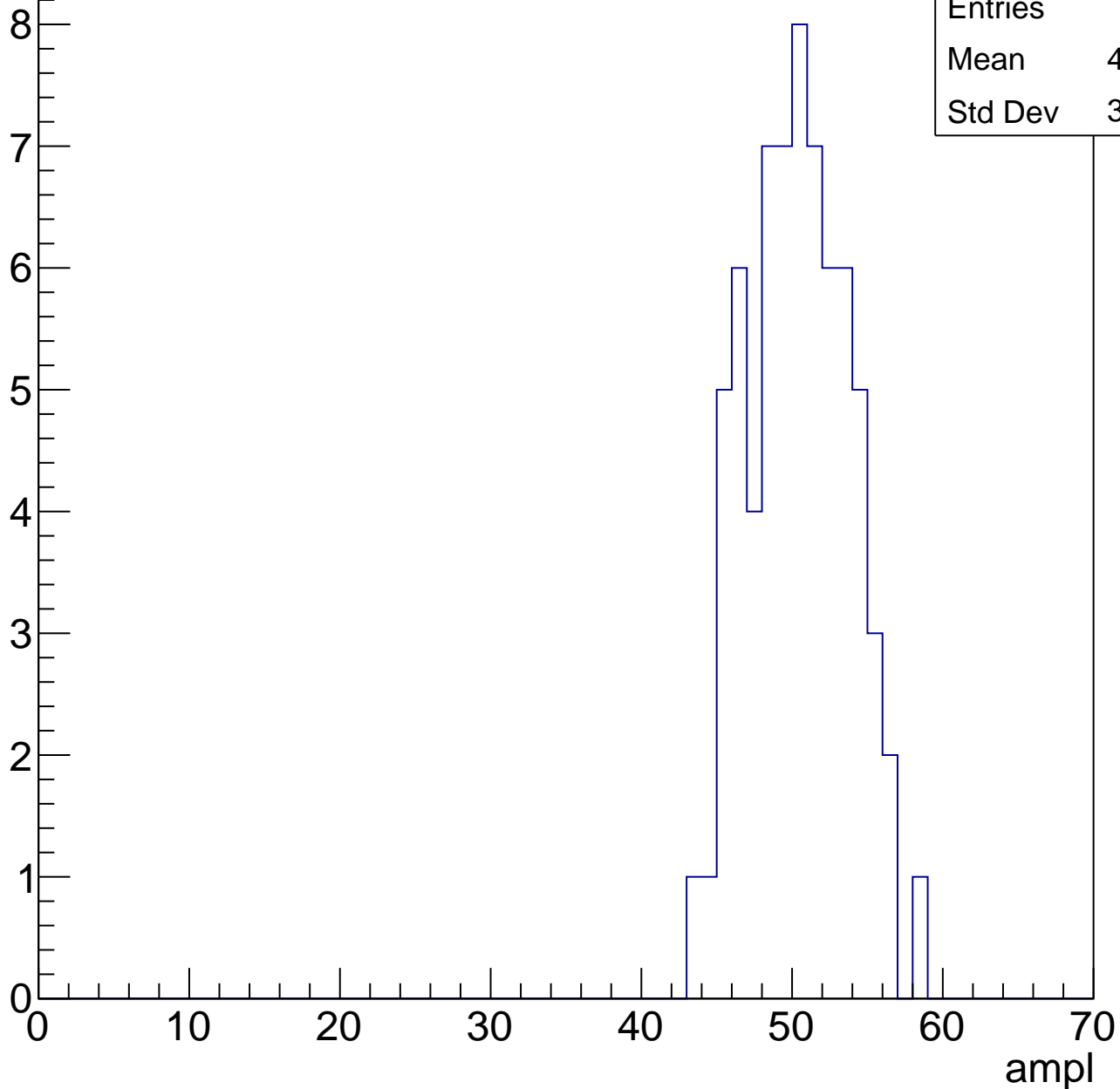


B1L003S, U16-ch52, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.96
Std Dev	3.303

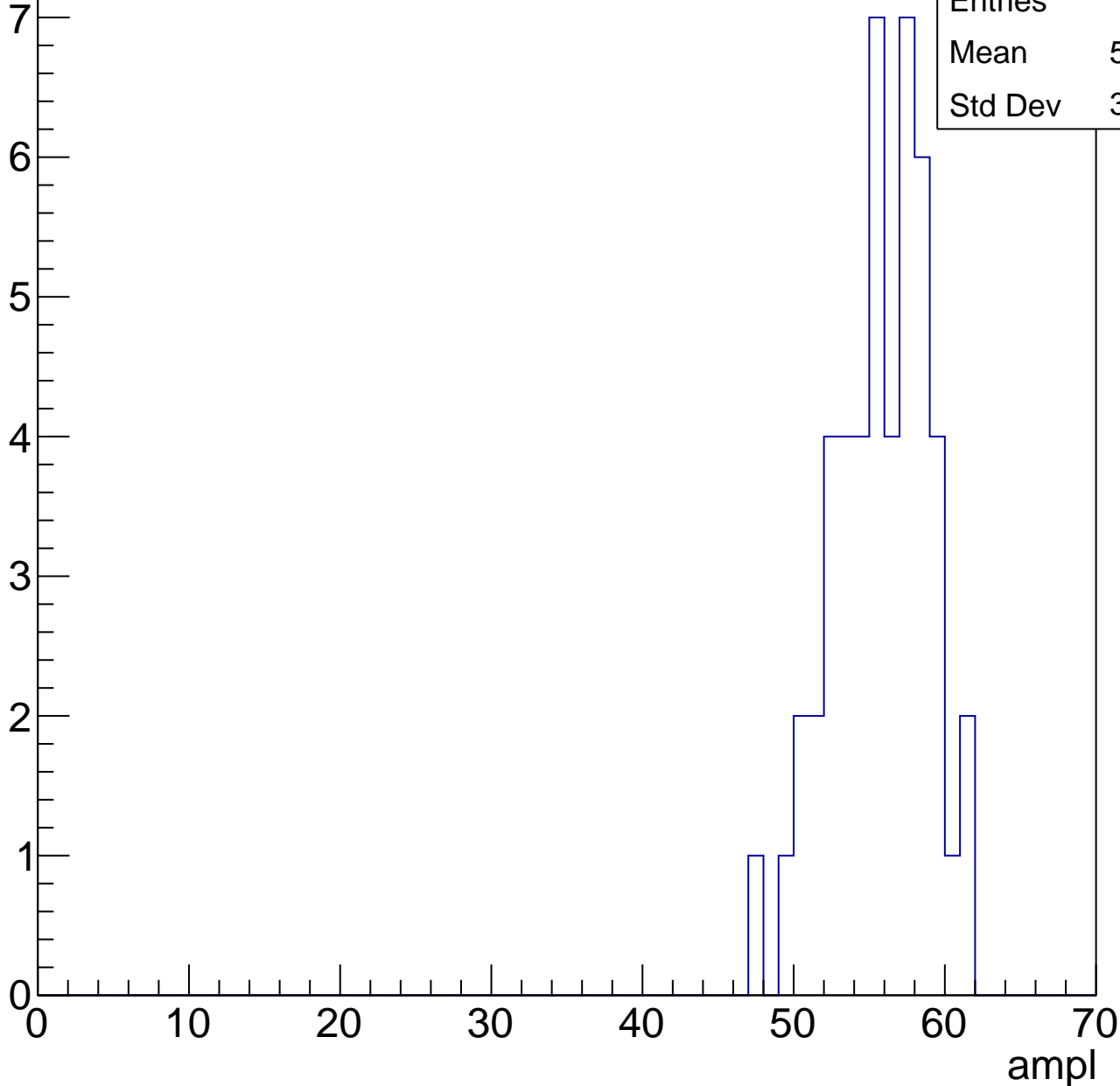


B1L003S, U16-ch52, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.27
Std Dev	3.135

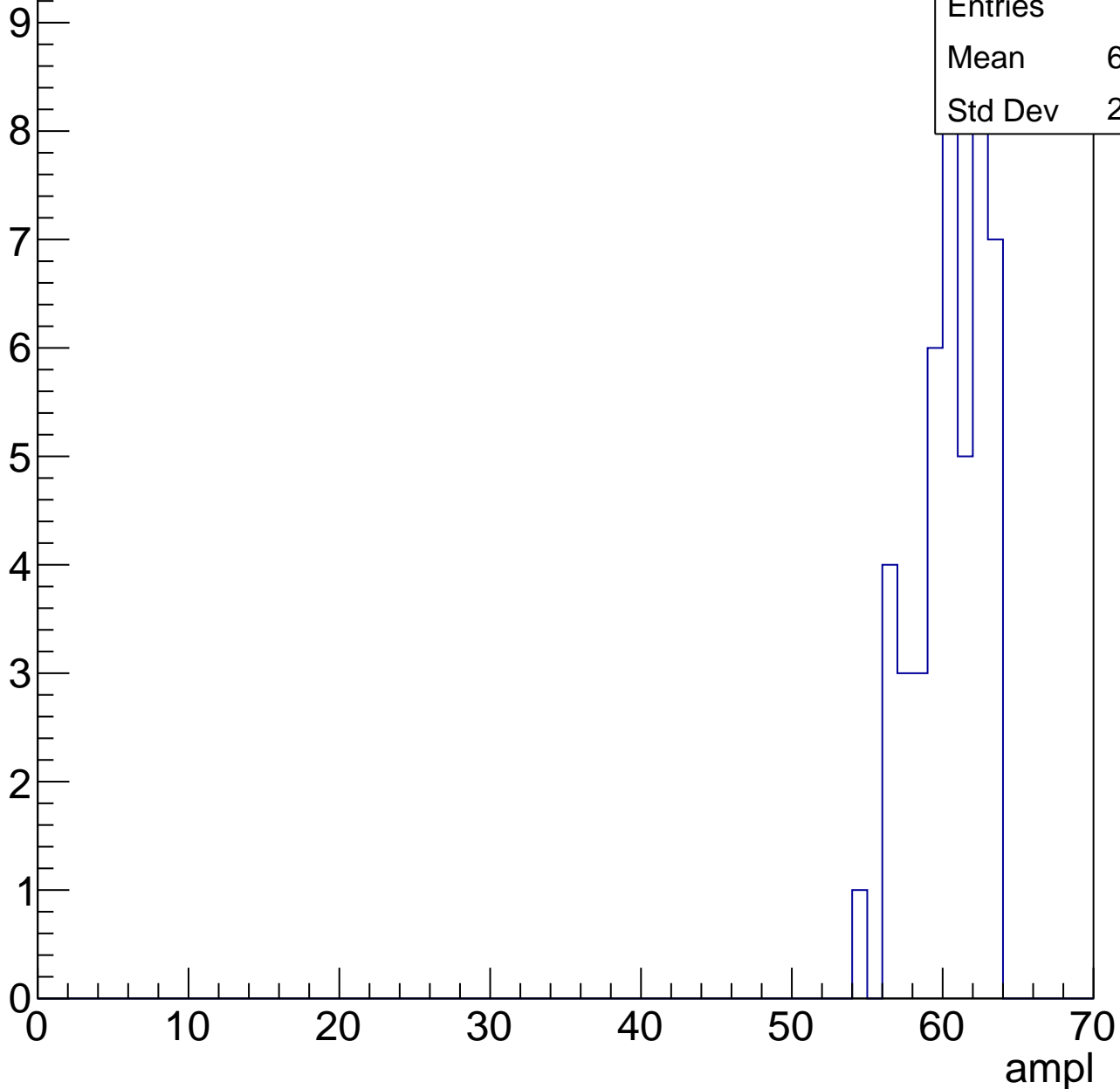


B1L003S, U16-ch52, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

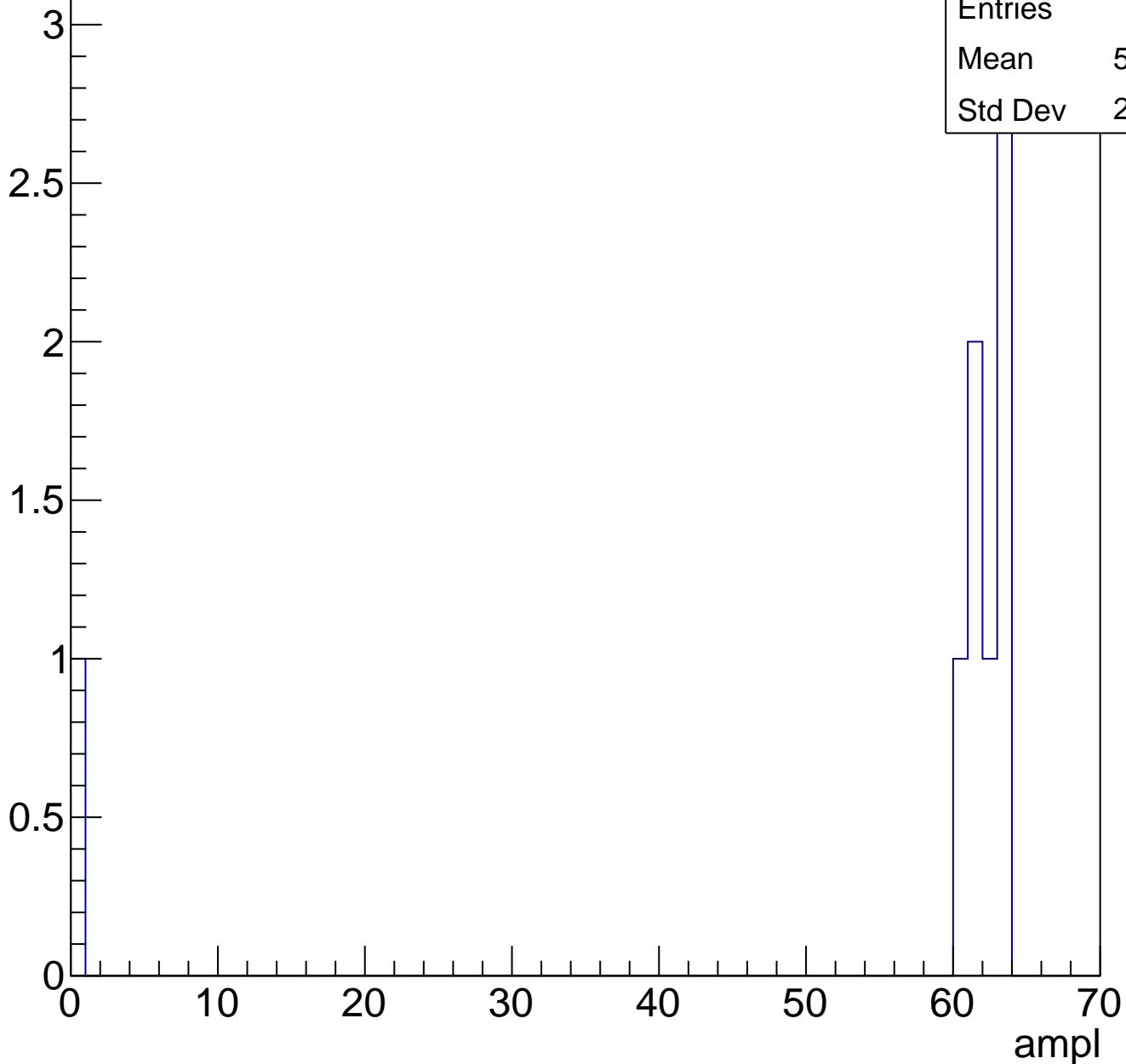
Entries	46
Mean	60.02
Std Dev	2.326



B1L003S, U16-ch52, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch52, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch53, adc0

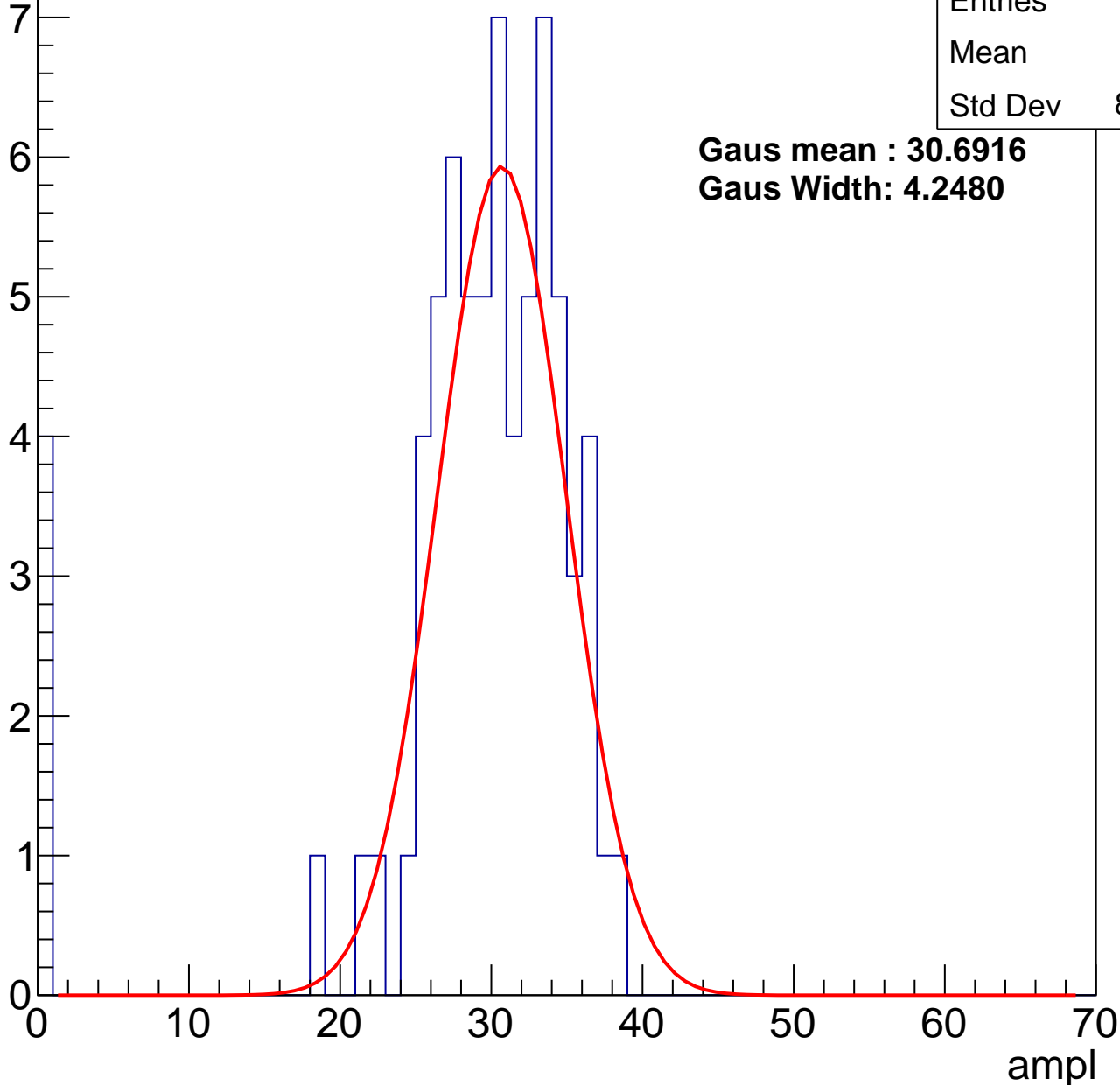
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.3
Std Dev	8.001

Gaus mean : 30.6916

Gaus Width: 4.2480



B1L003S, U16-ch53, adc1

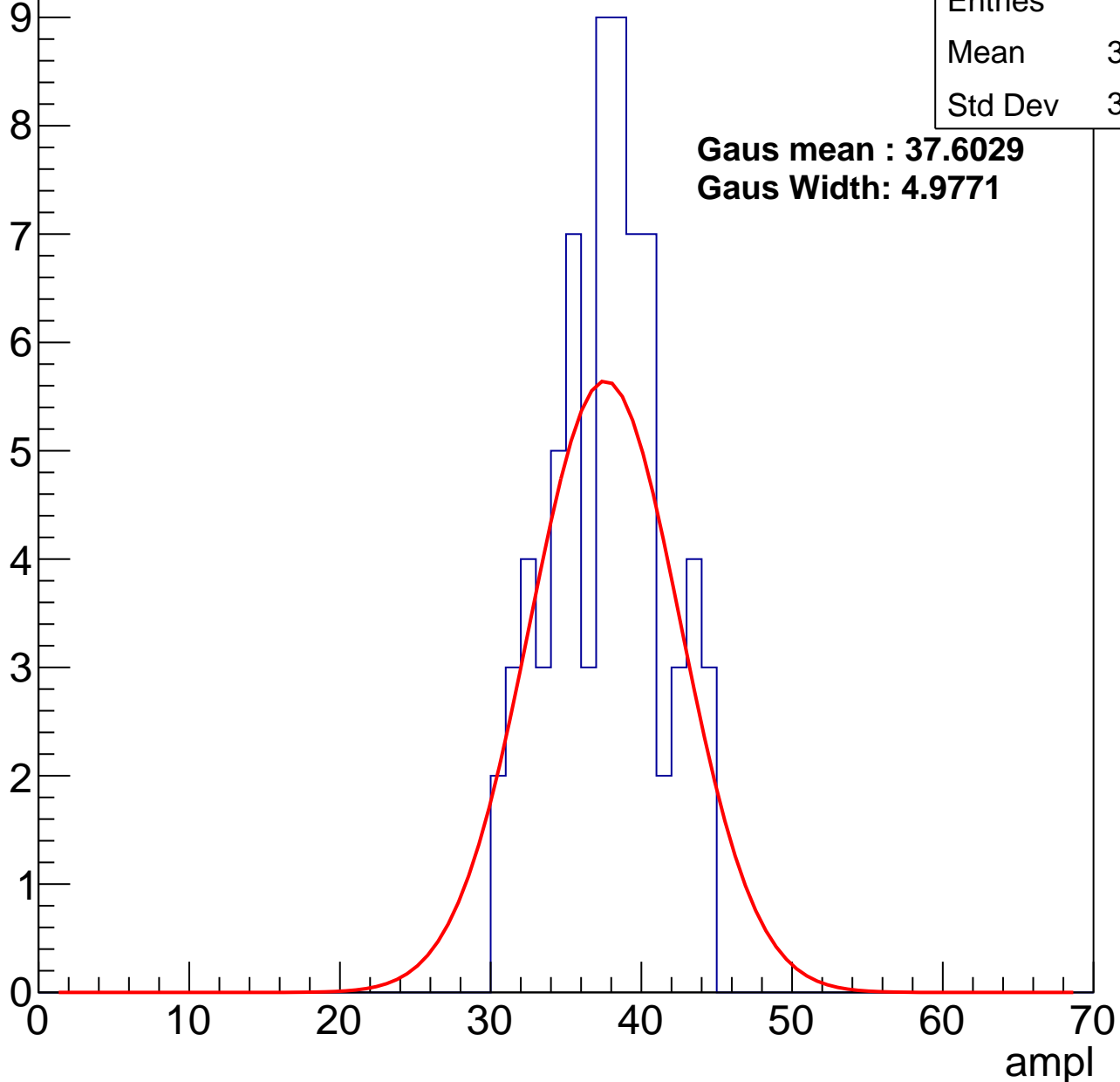
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	37.23
Std Dev	3.608

Gaus mean : 37.6029

Gaus Width: 4.9771



B1L003S, U16-ch53, adc2

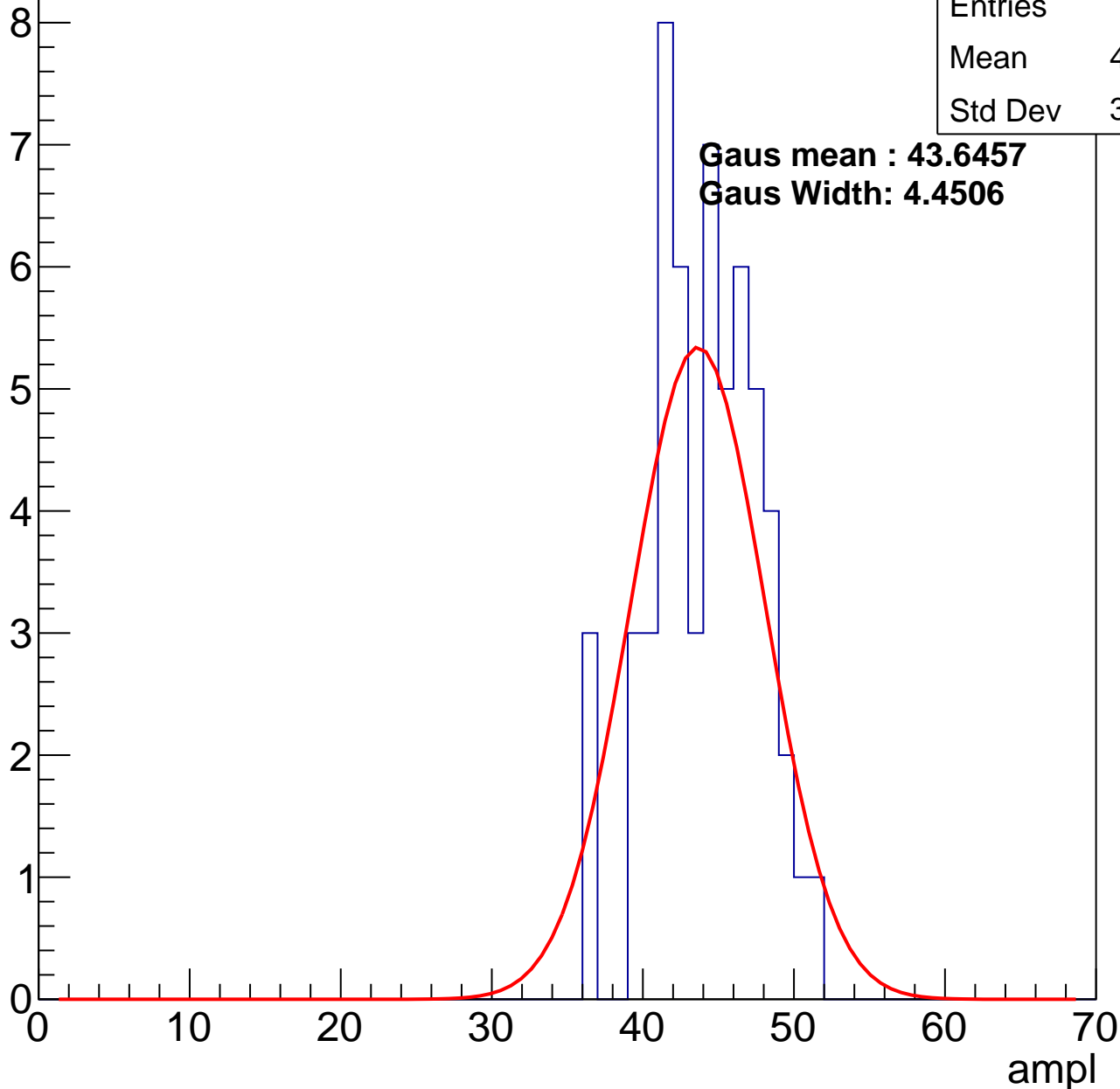
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.67
Std Dev	3.456

Gaus mean : 43.6457

Gaus Width: 4.4506

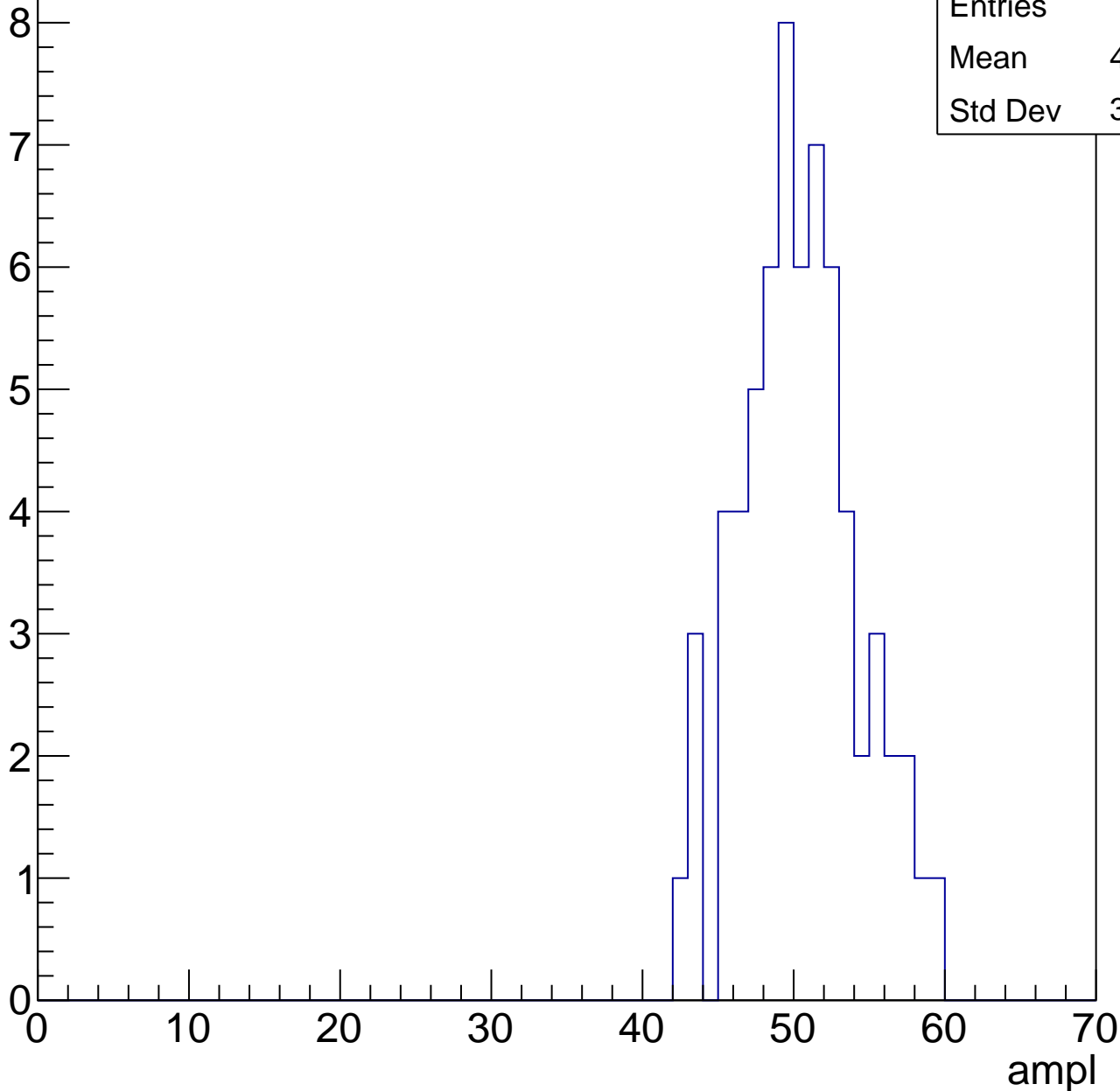


B1L003S, U16-ch53, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.95
Std Dev	3.805

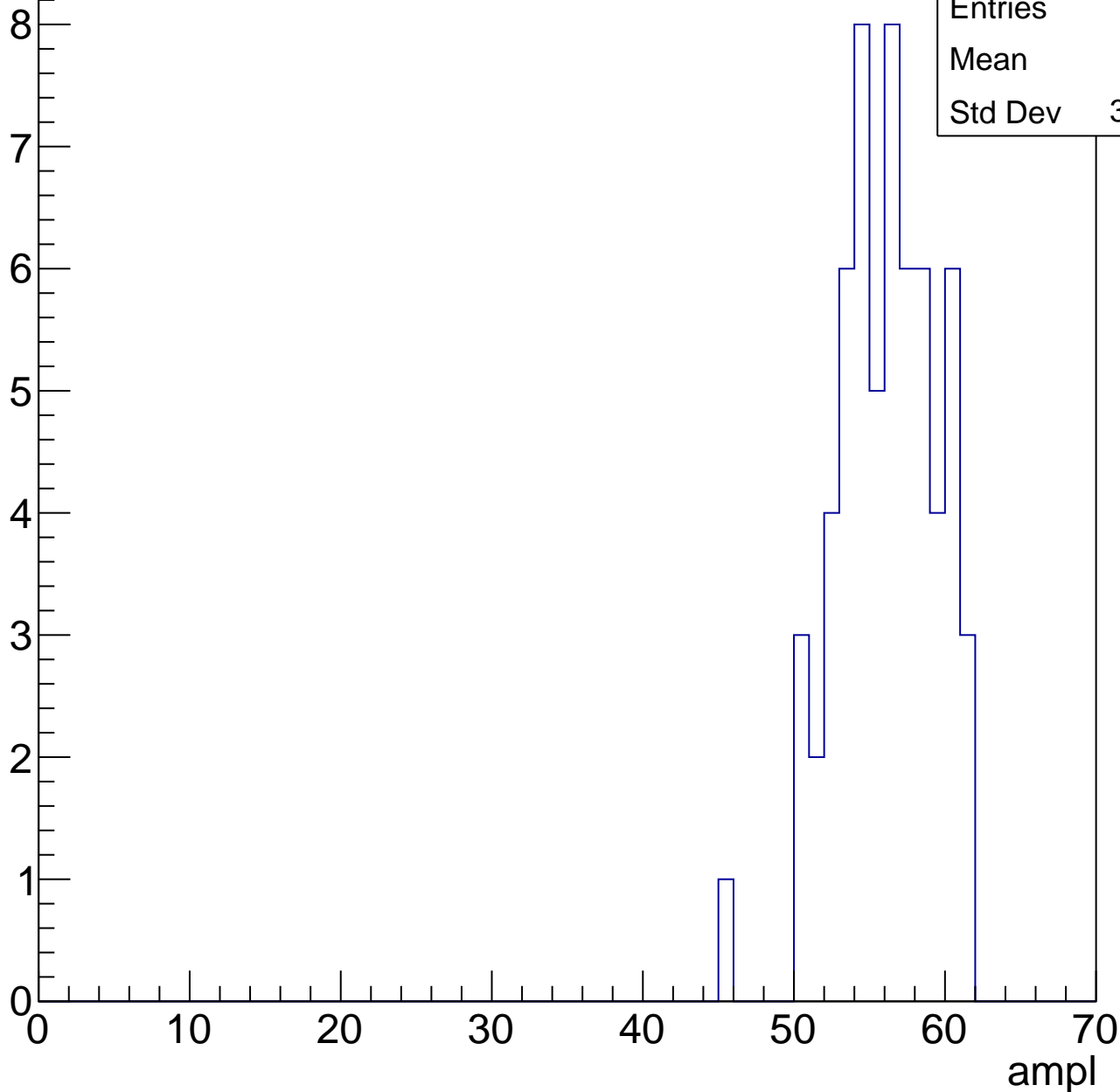


B1L003S, U16-ch53, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	55.6
Std Dev	3.265

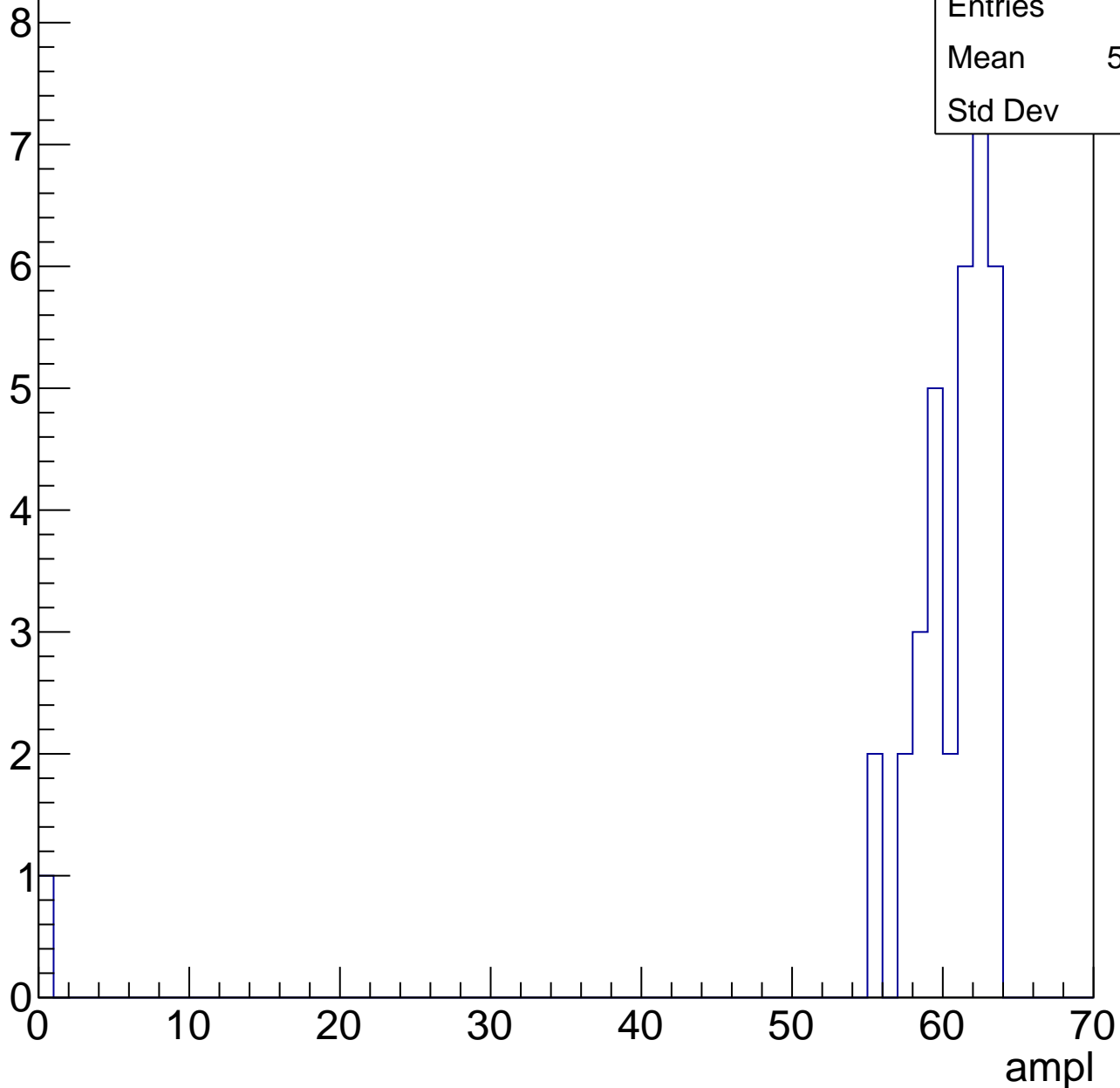


B1L003S, U16-ch53, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

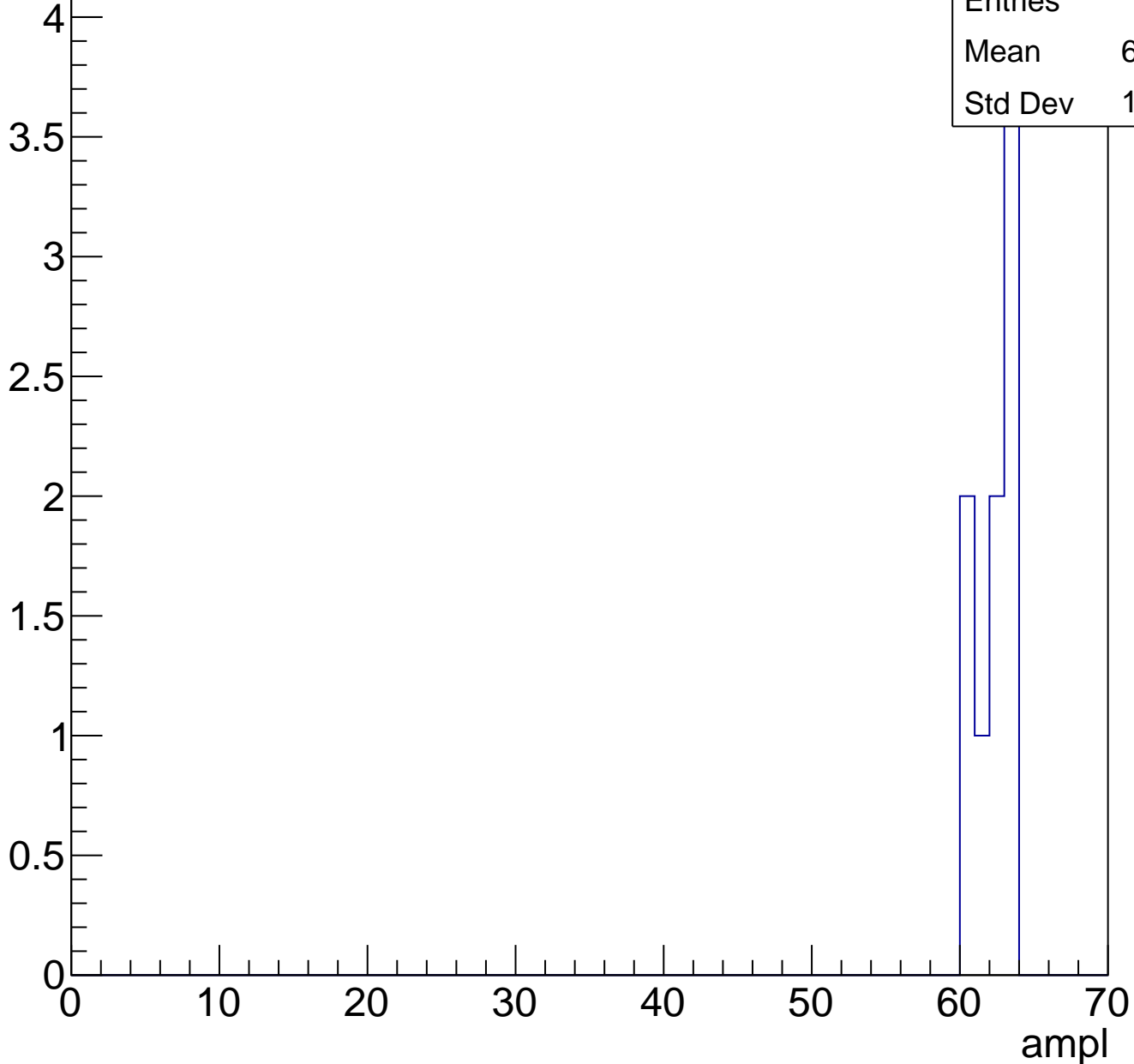
Entries	35
Mean	58.66
Std Dev	10.3



B1L003S, U16-ch53, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch53, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch54, adc0

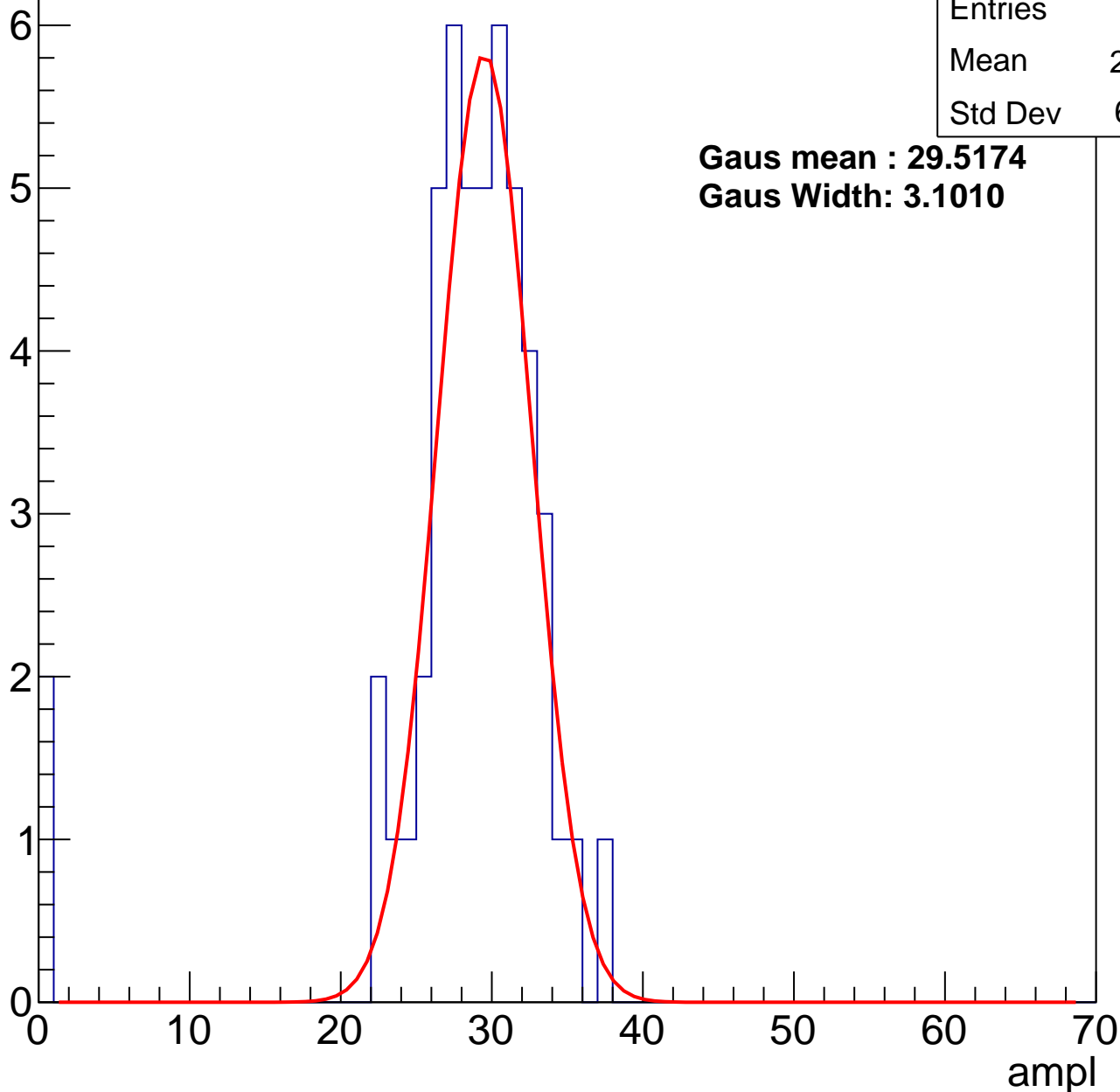
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	27.72
Std Dev	6.481

Gaus mean : 29.5174

Gaus Width: 3.1010



B1L003S, U16-ch54, adc1

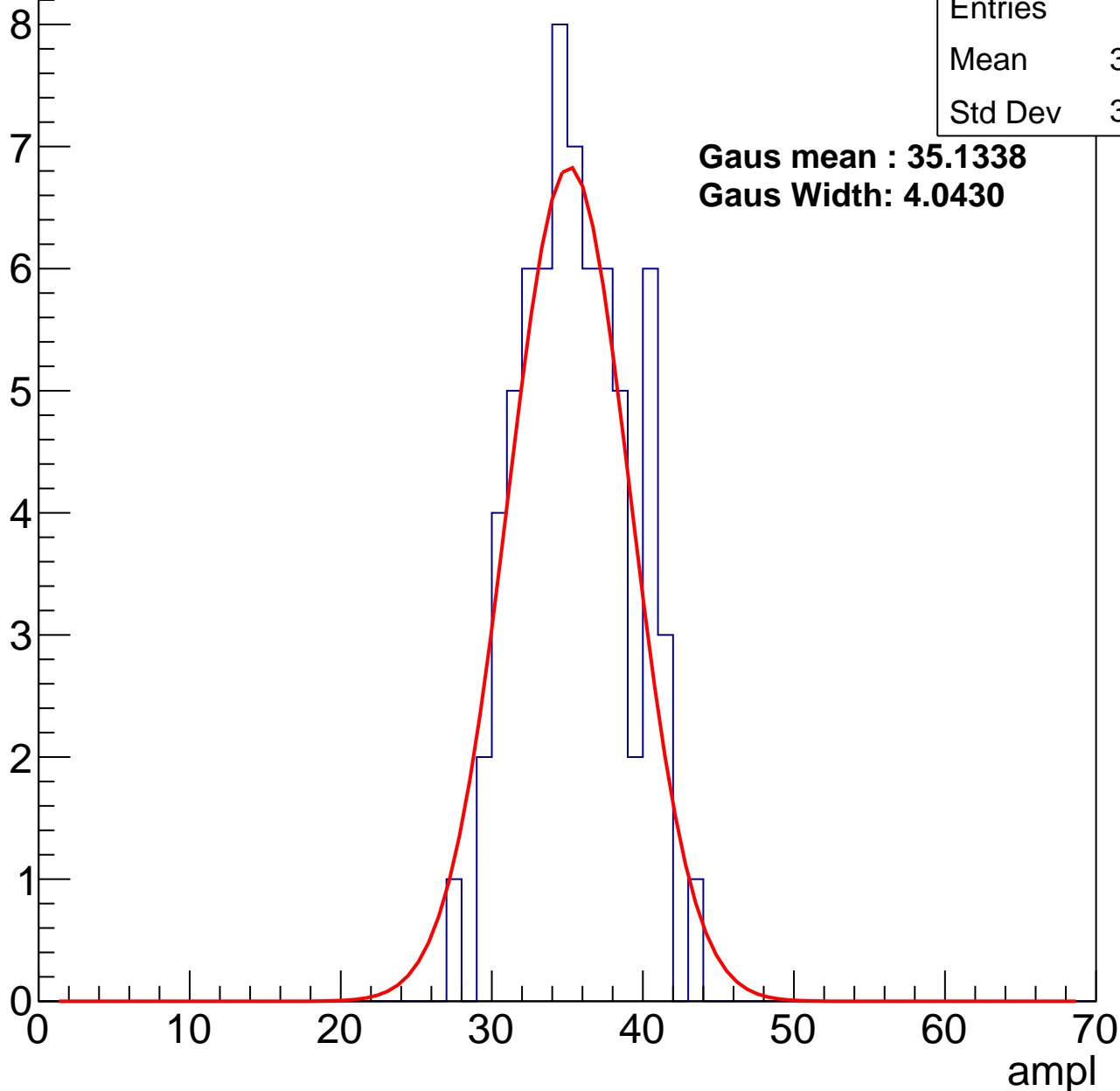
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	34.99
Std Dev	3.496

Gaus mean : 35.1338

Gaus Width: 4.0430



B1L003S, U16-ch54, adc2

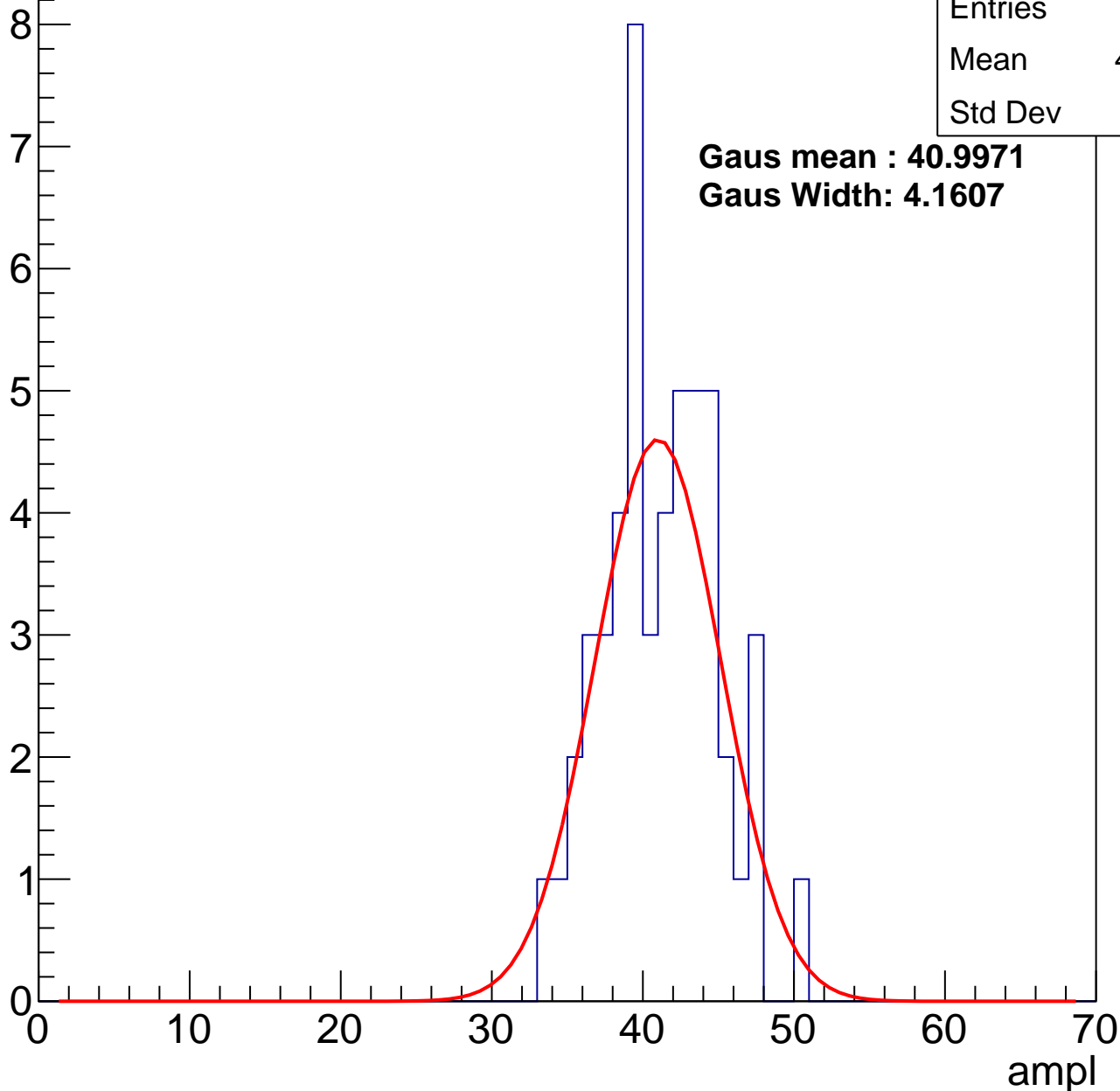
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	40.71
Std Dev	3.68

Gaus mean : 40.9971

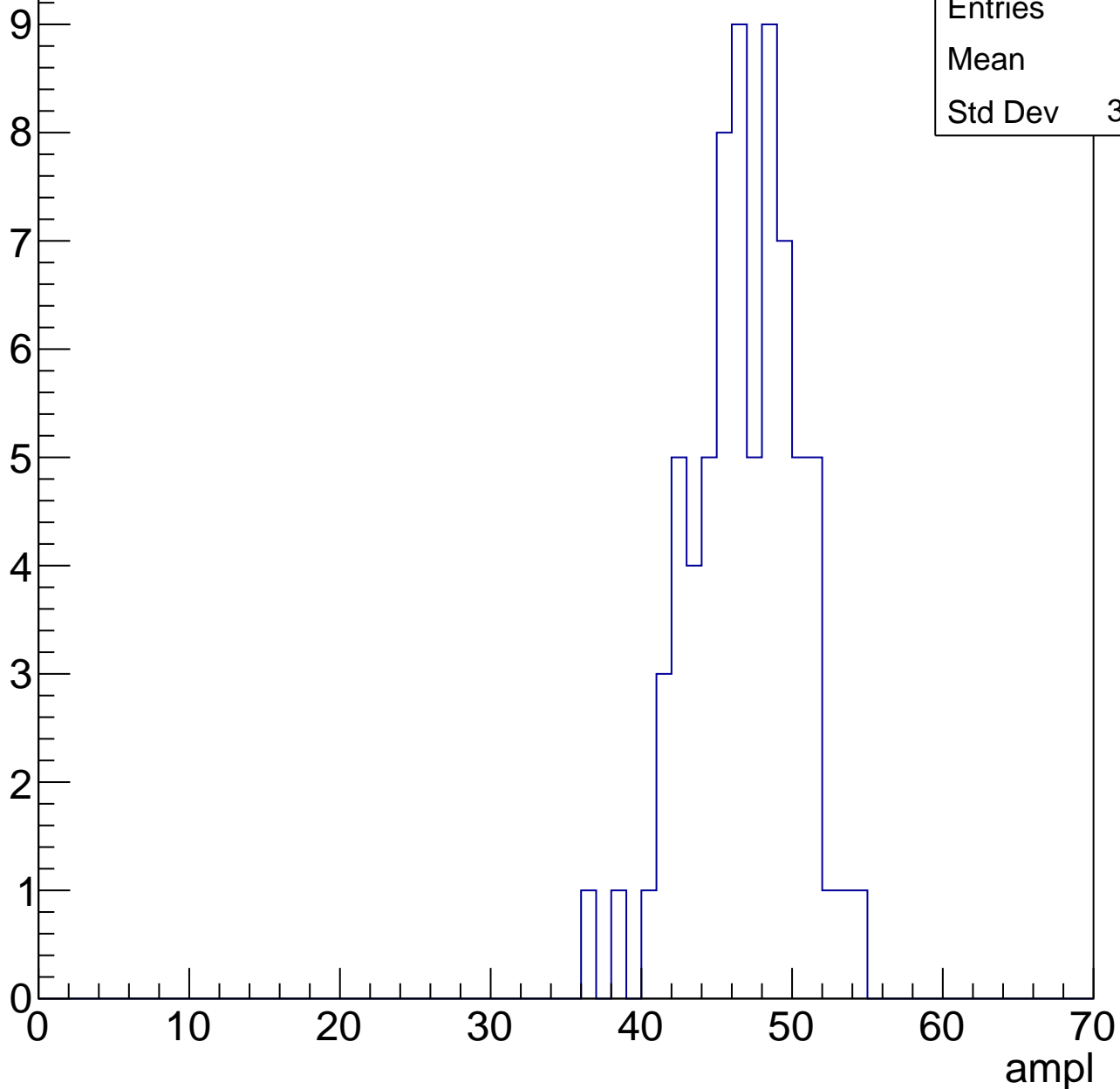
Gaus Width: 4.1607



B1L003S, U16-ch54, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

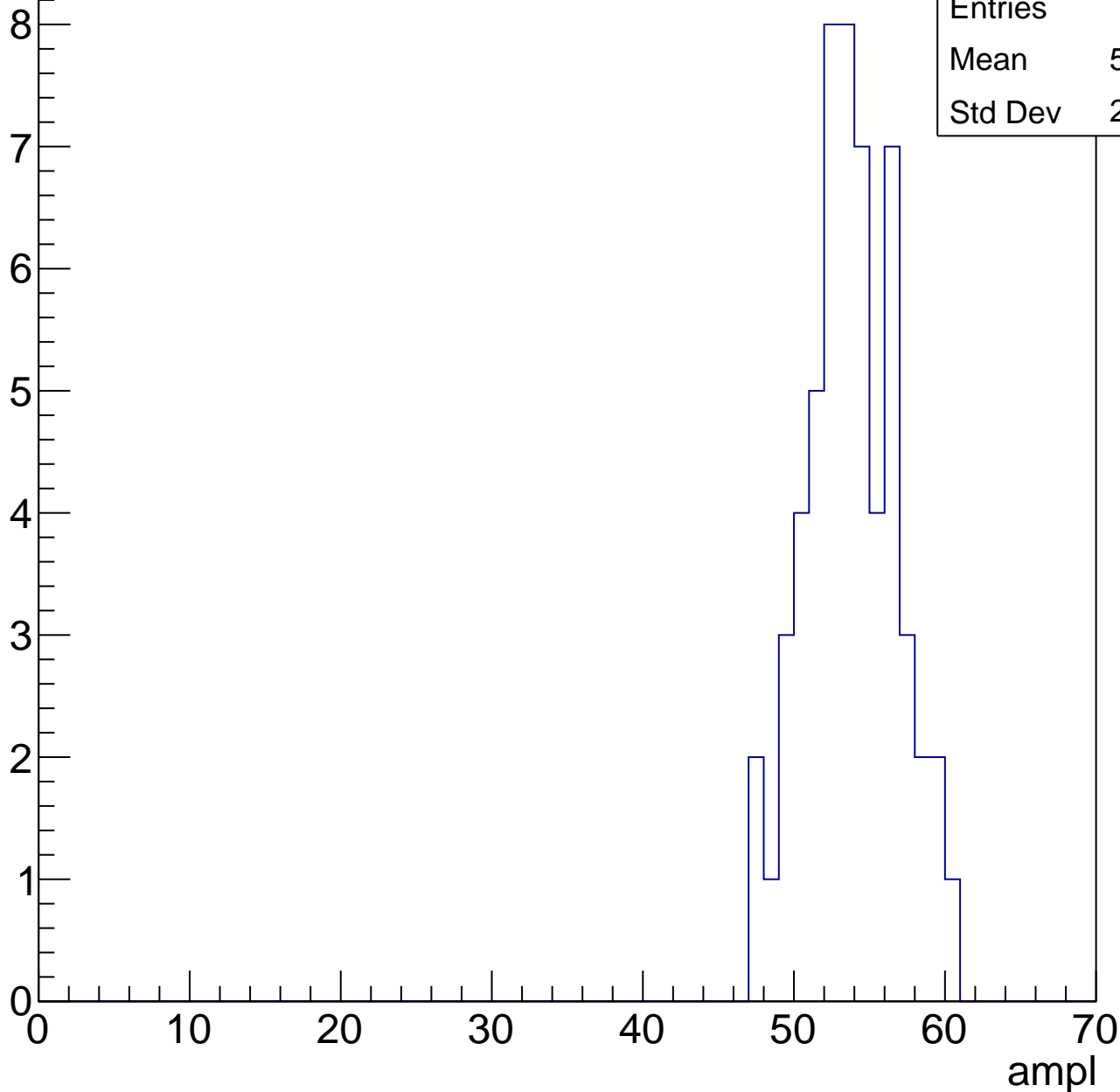


B1L003S, U16-ch54, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.32
Std Dev	2.986

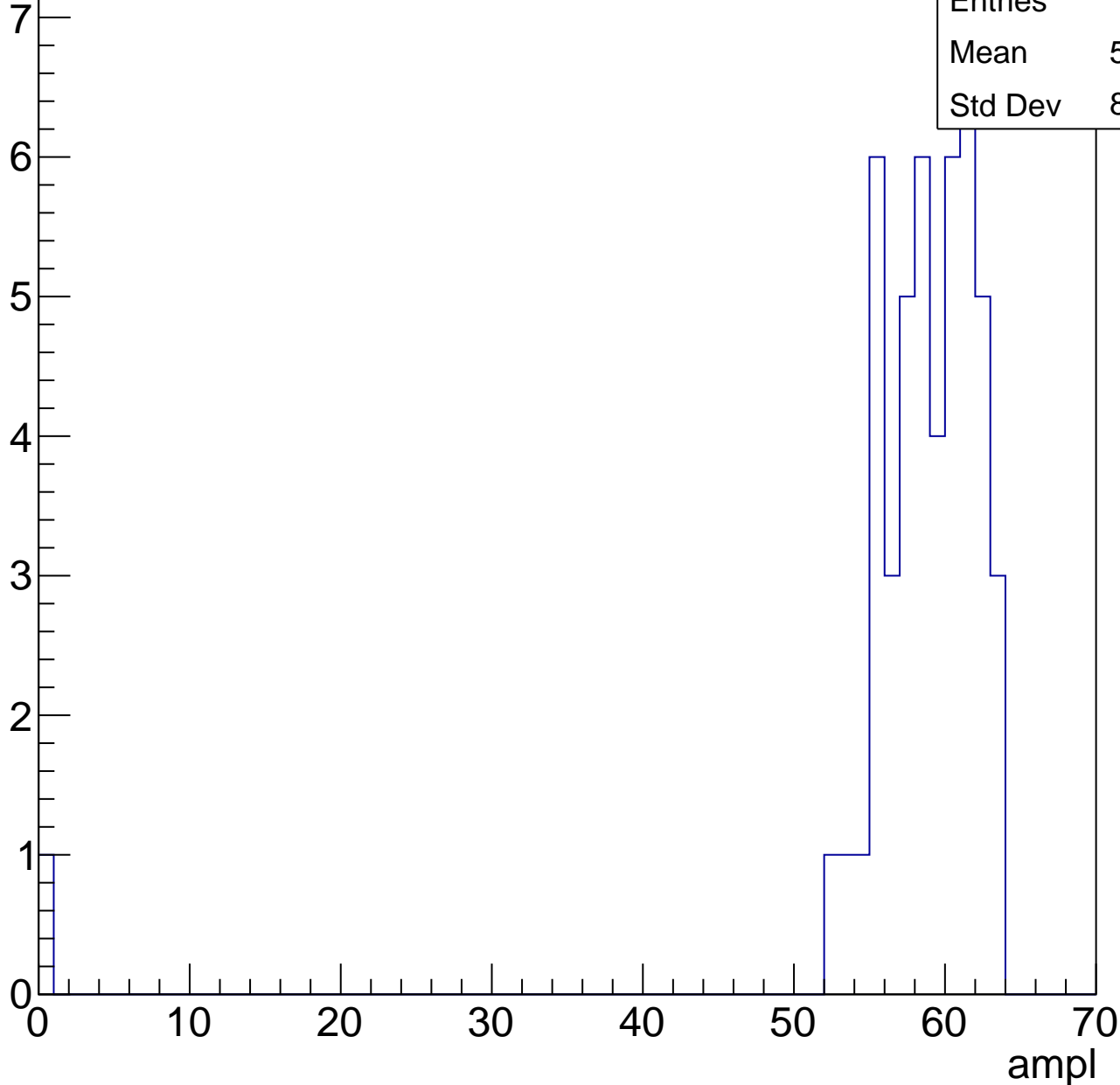


B1L003S, U16-ch54, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	57.39
Std Dev	8.736

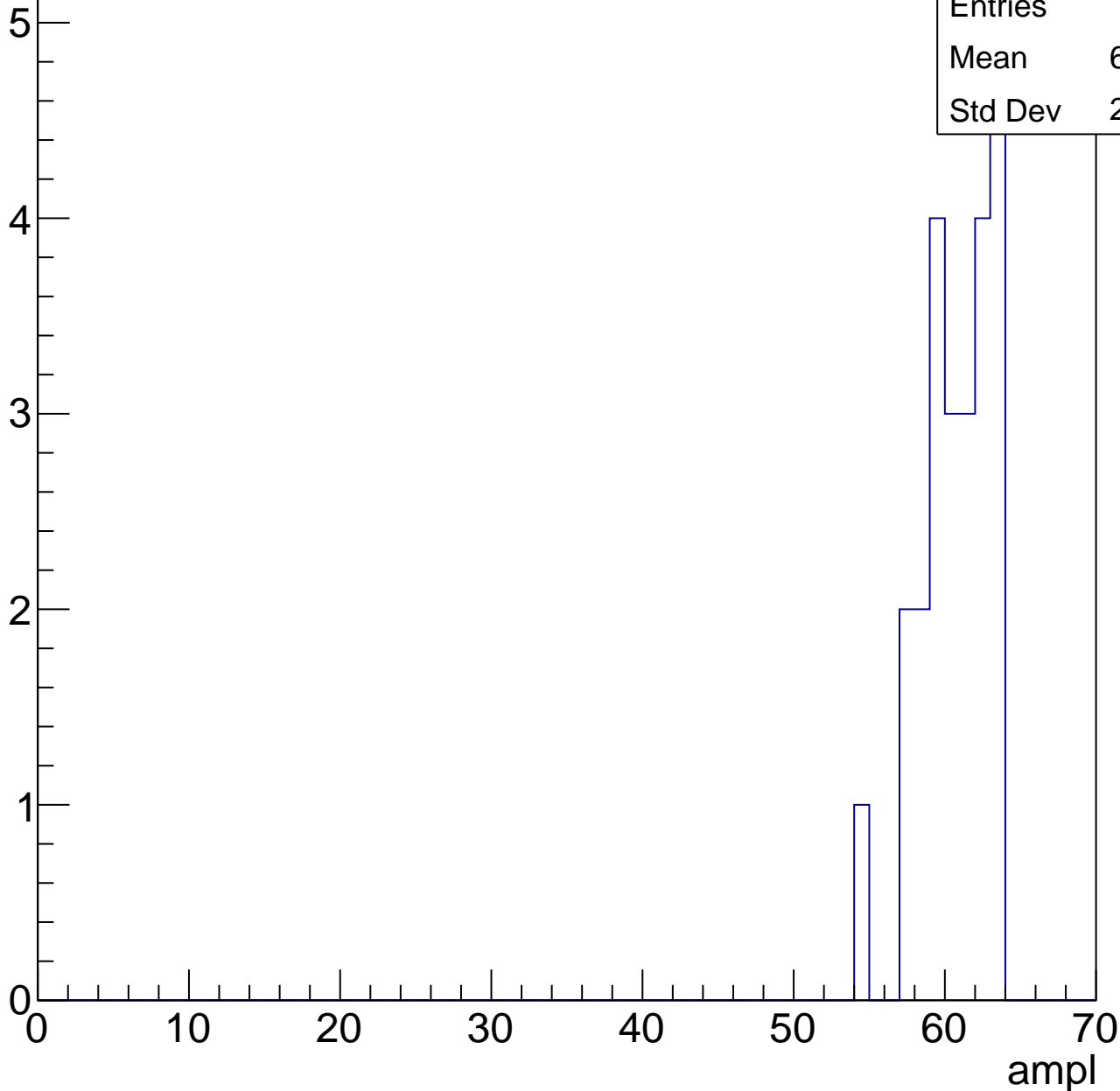


B1L003S, U16-ch54, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

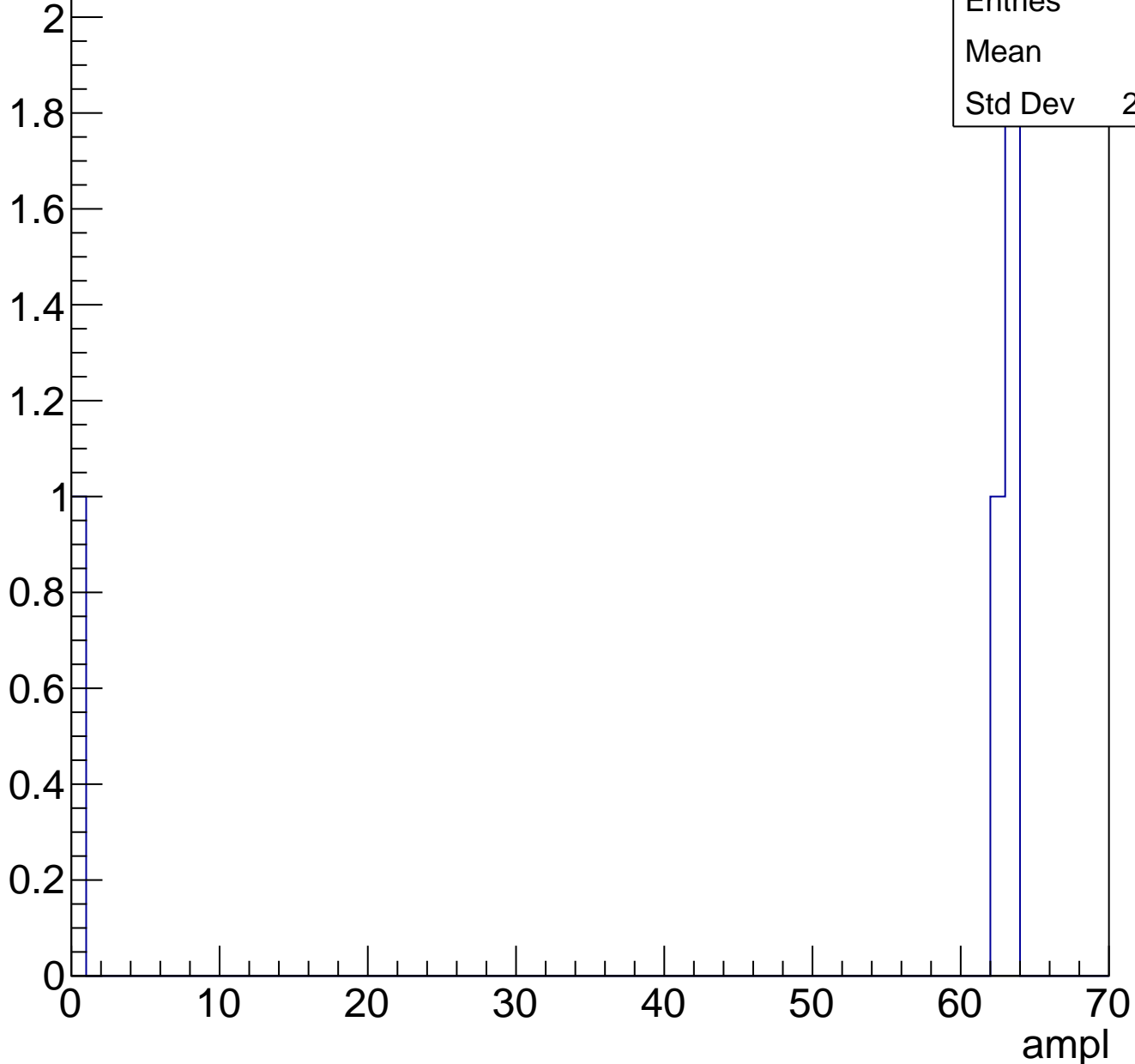
Entries	24
Mean	60.25
Std Dev	2.314



B1L003S, U16-ch54, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch55, adc0

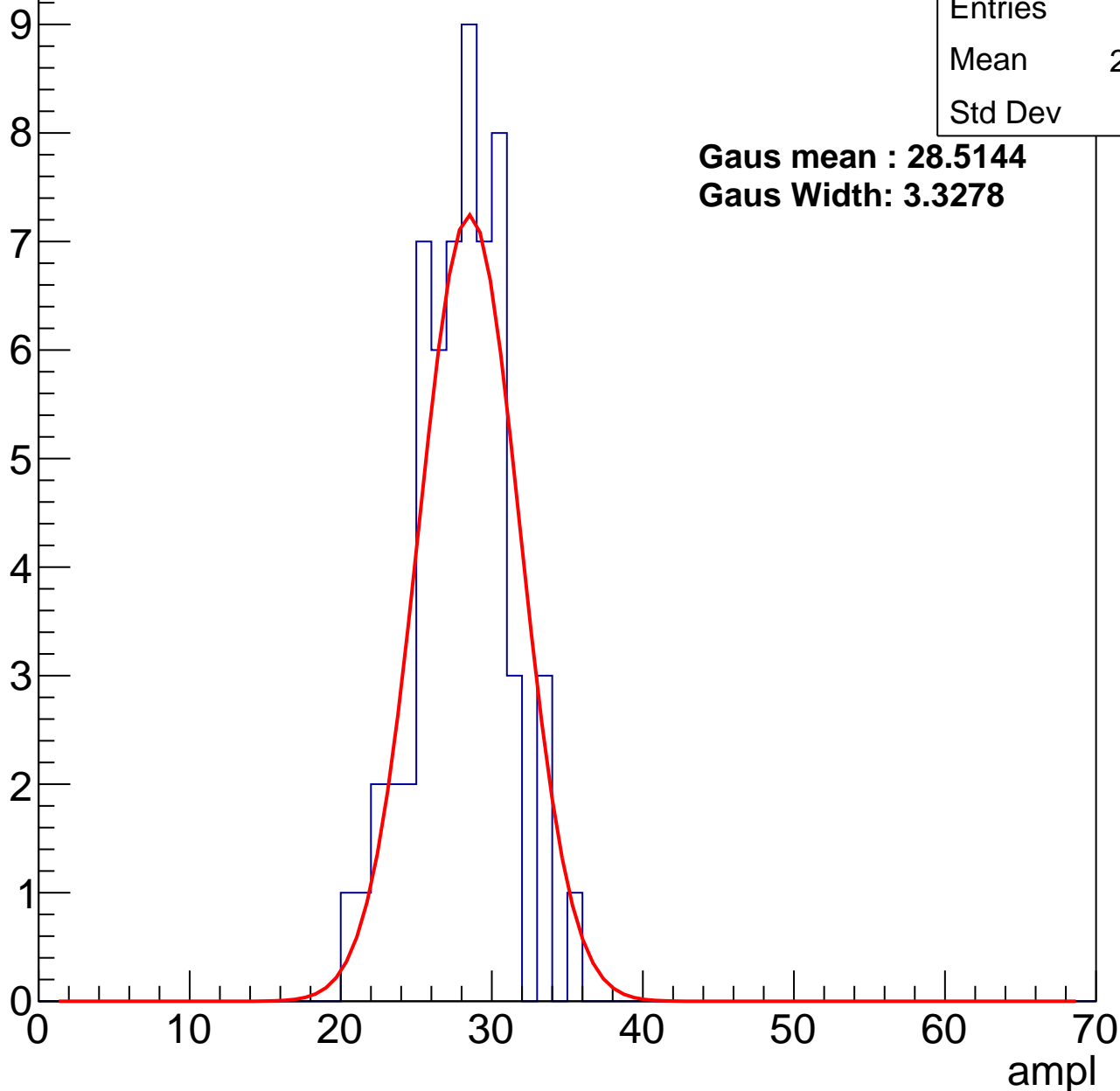
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	27.47
Std Dev	3.01

Gaus mean : 28.5144

Gaus Width: 3.3278



B1L003S, U16-ch55, adc1

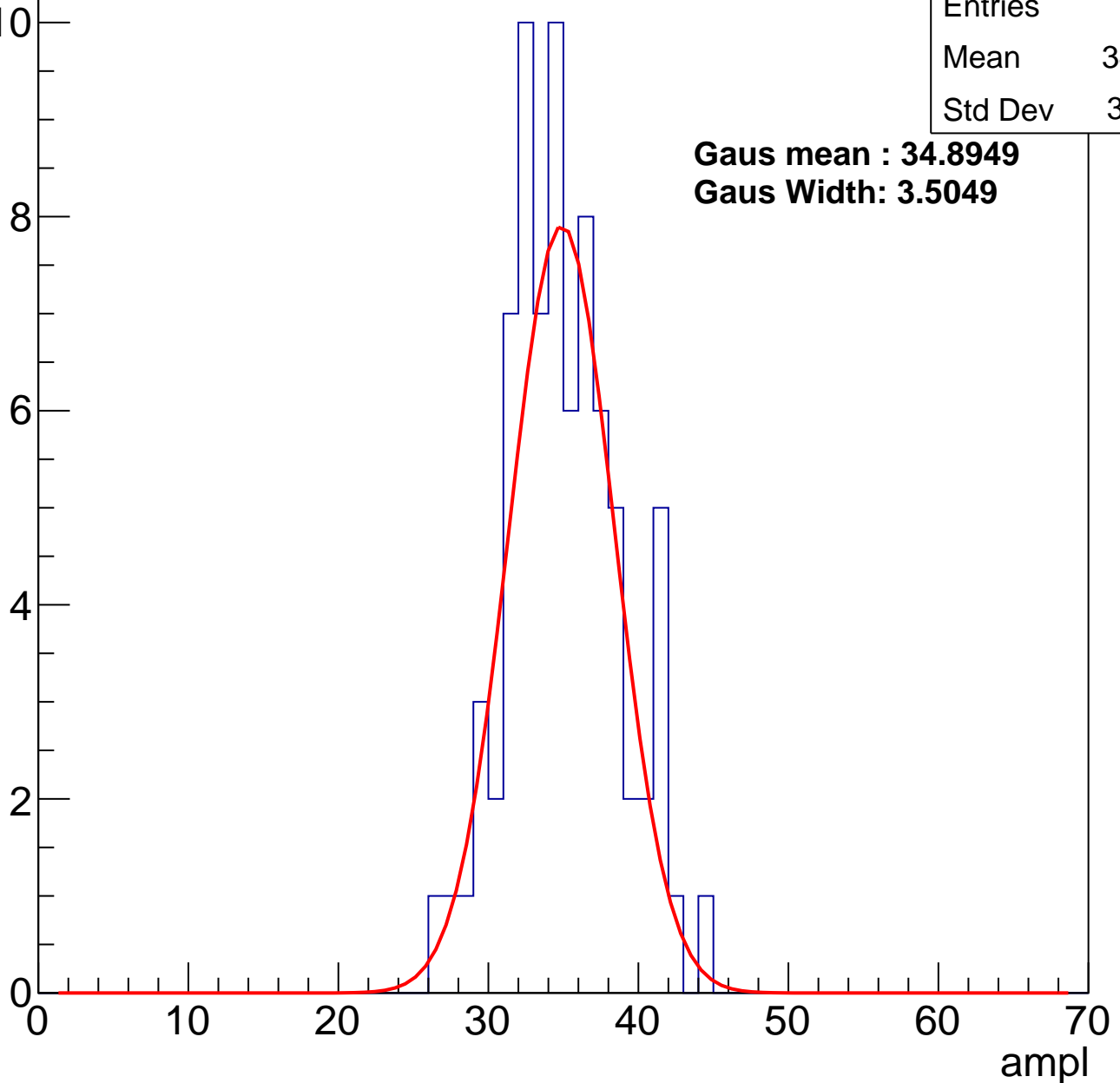
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	34.55
Std Dev	3.661

Gaus mean : 34.8949

Gaus Width: 3.5049



B1L003S, U16-ch55, adc2

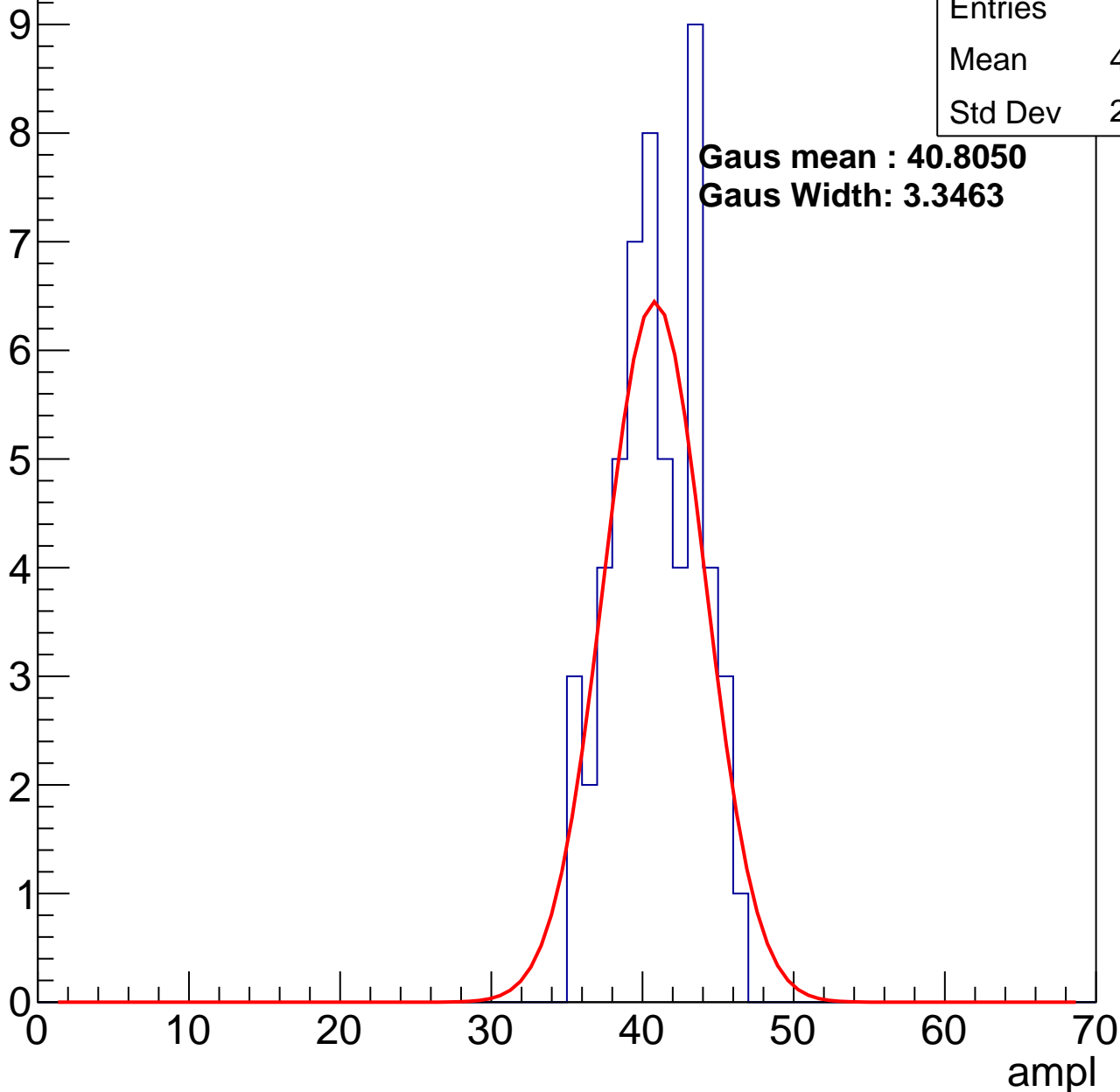
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	40.45
Std Dev	2.814

Gaus mean : 40.8050

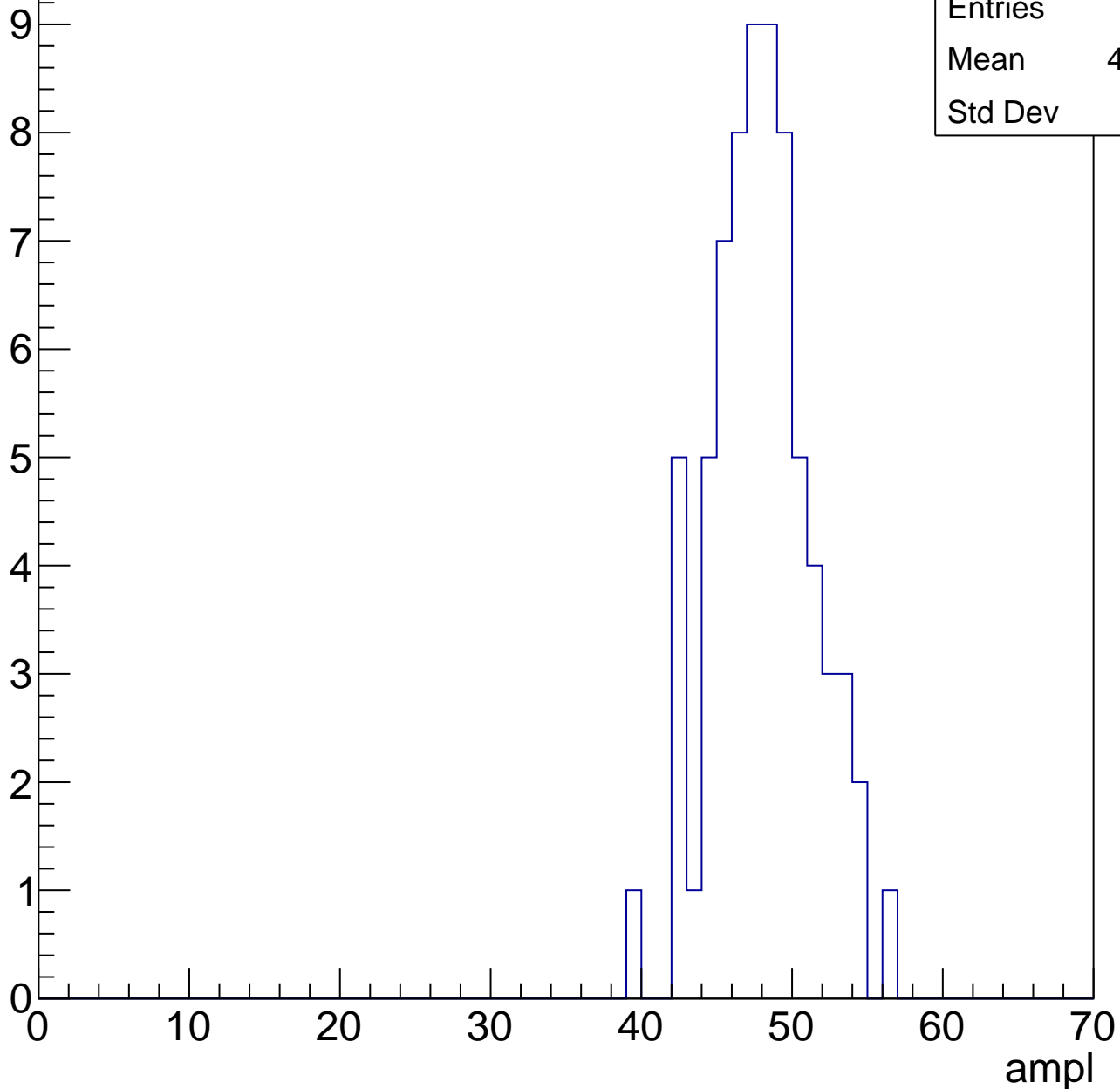
Gaus Width: 3.3463



B1L003S, U16-ch55, adc3

calib_packv5_042523_0143.root, FC#13, port D2

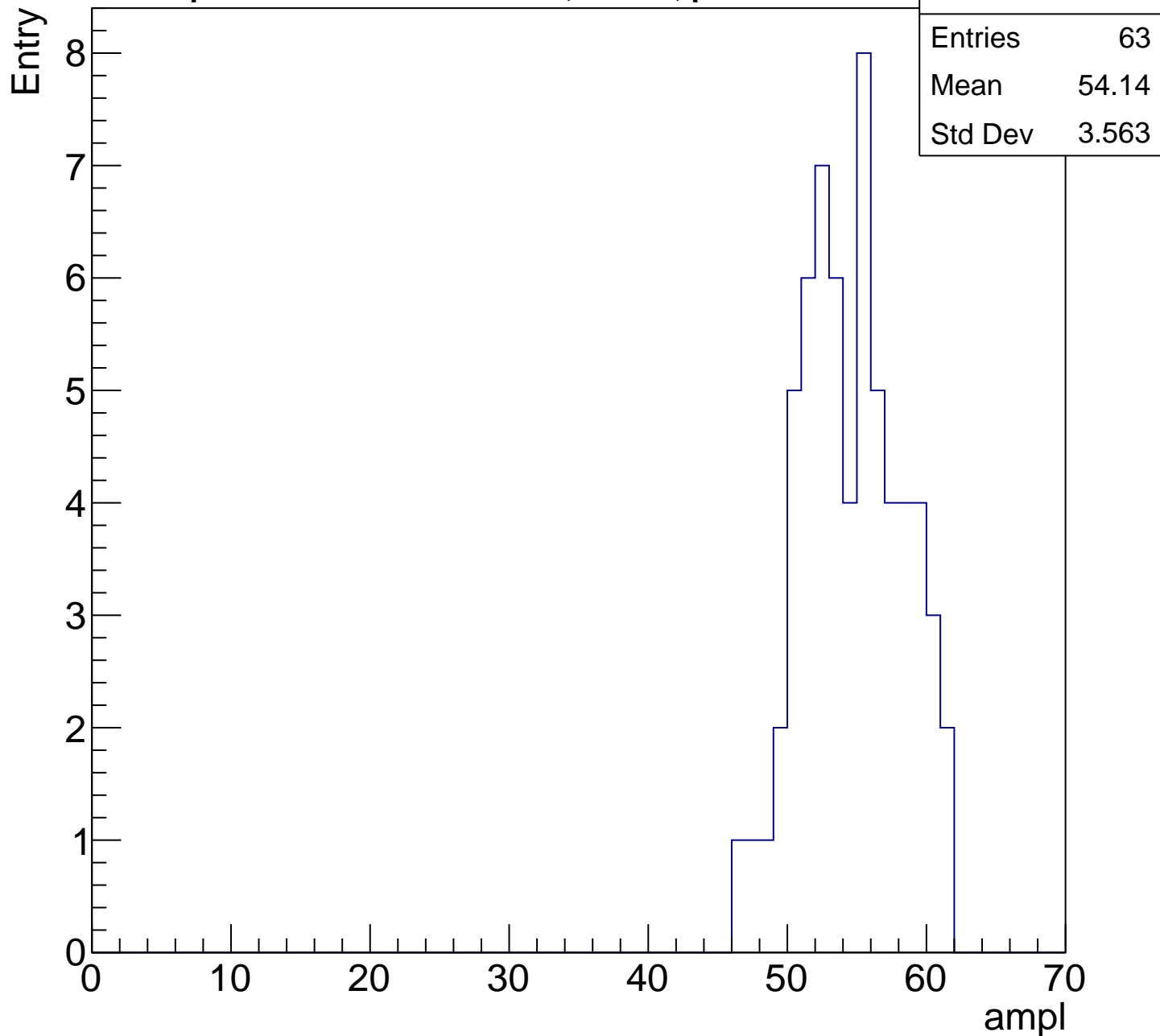
Entry



Entries	71
Mean	47.54
Std Dev	3.31

B1L003S, U16-ch55, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch55, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries

52

Mean

57.6

Std Dev

8.49

ampl

0

10

20

30

40

50

60

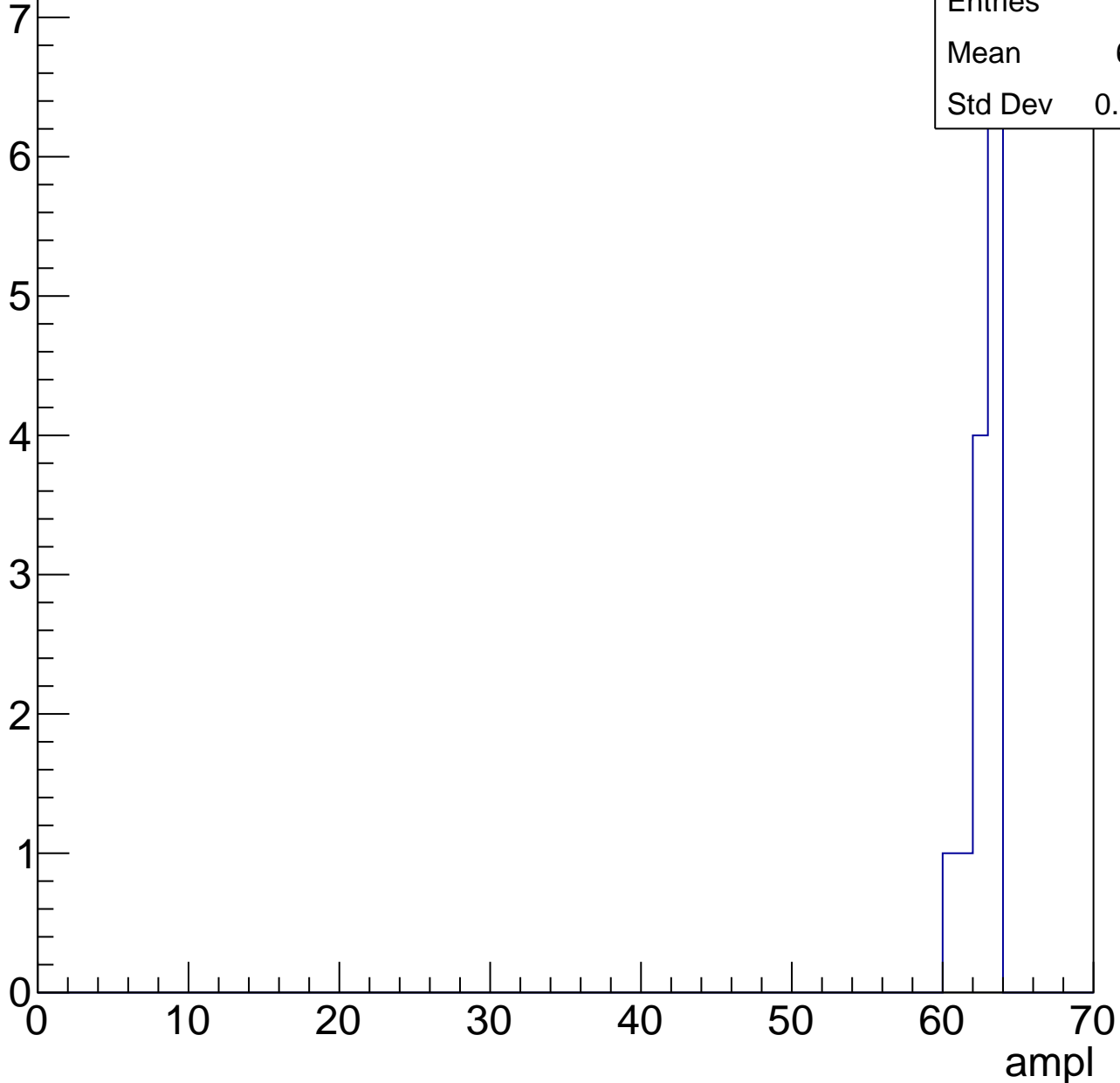
70

B1L003S, U16-ch55, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	62.31
Std Dev	0.9102



B1L003S, U16-ch55, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch56, adc0

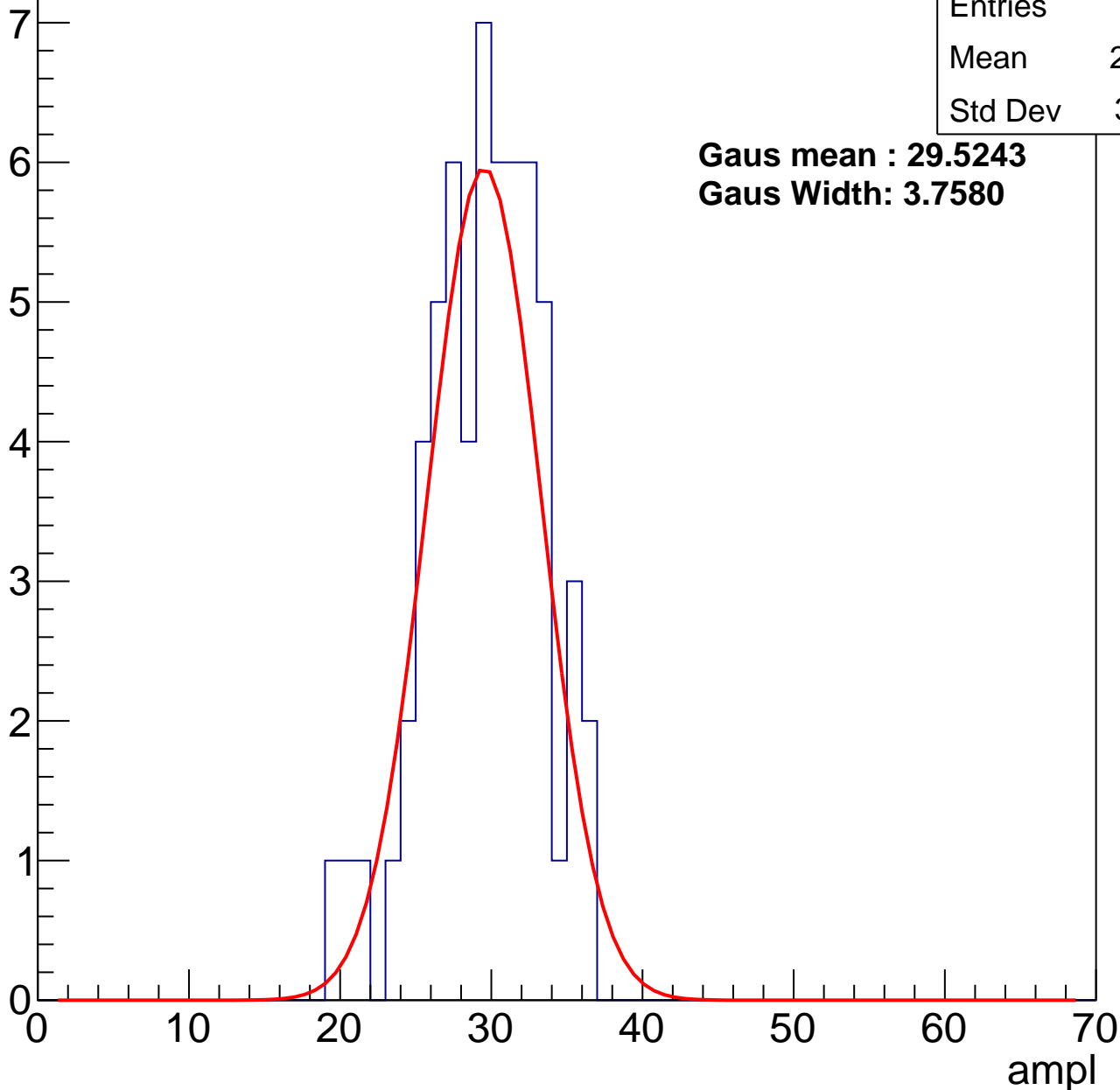
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.05
Std Dev	3.761

Gaus mean : 29.5243

Gaus Width: 3.7580



B1L003S, U16-ch56, adc1

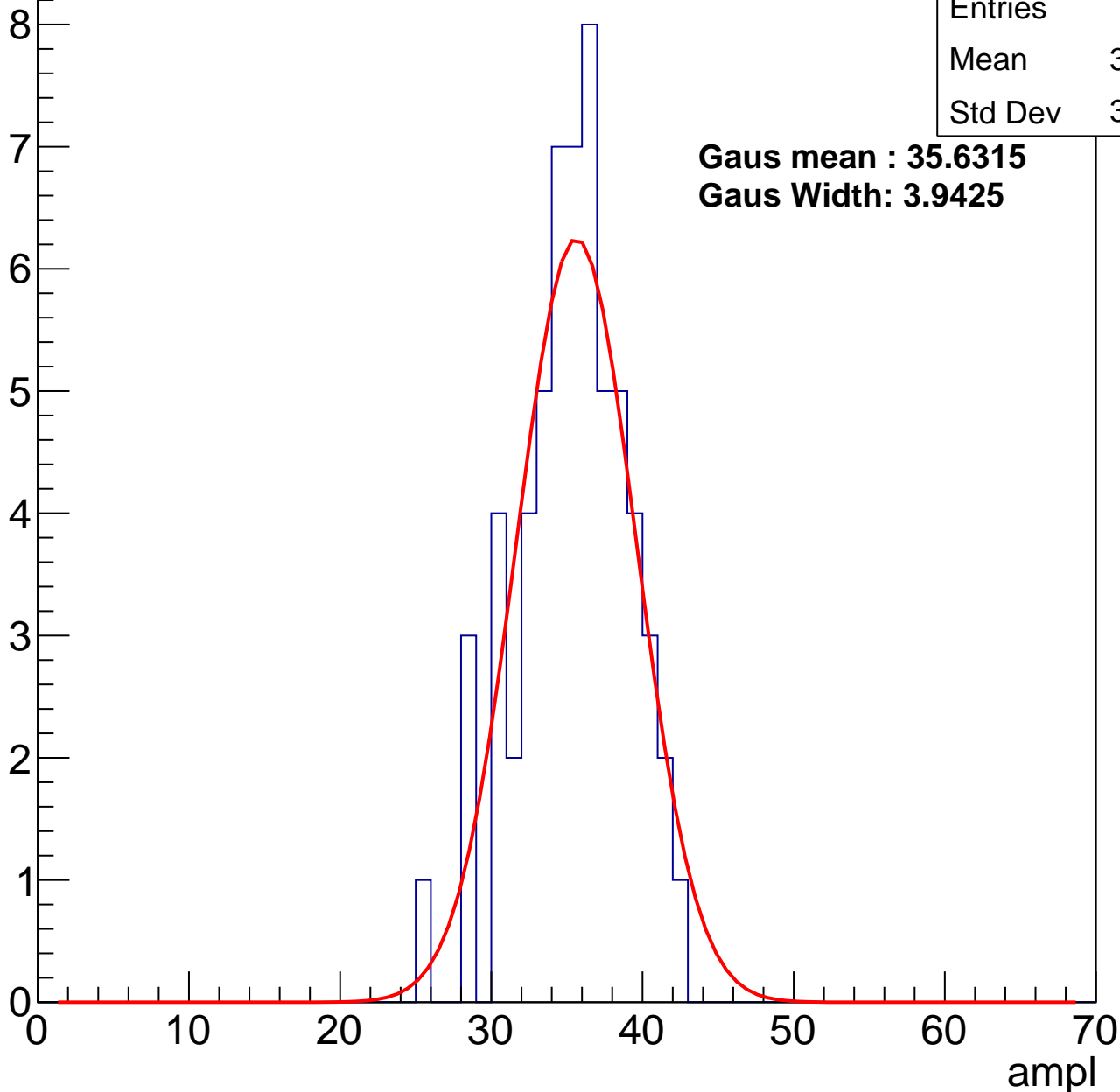
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	34.92
Std Dev	3.563

Gaus mean : 35.6315

Gaus Width: 3.9425



B1L003S, U16-ch56, adc2

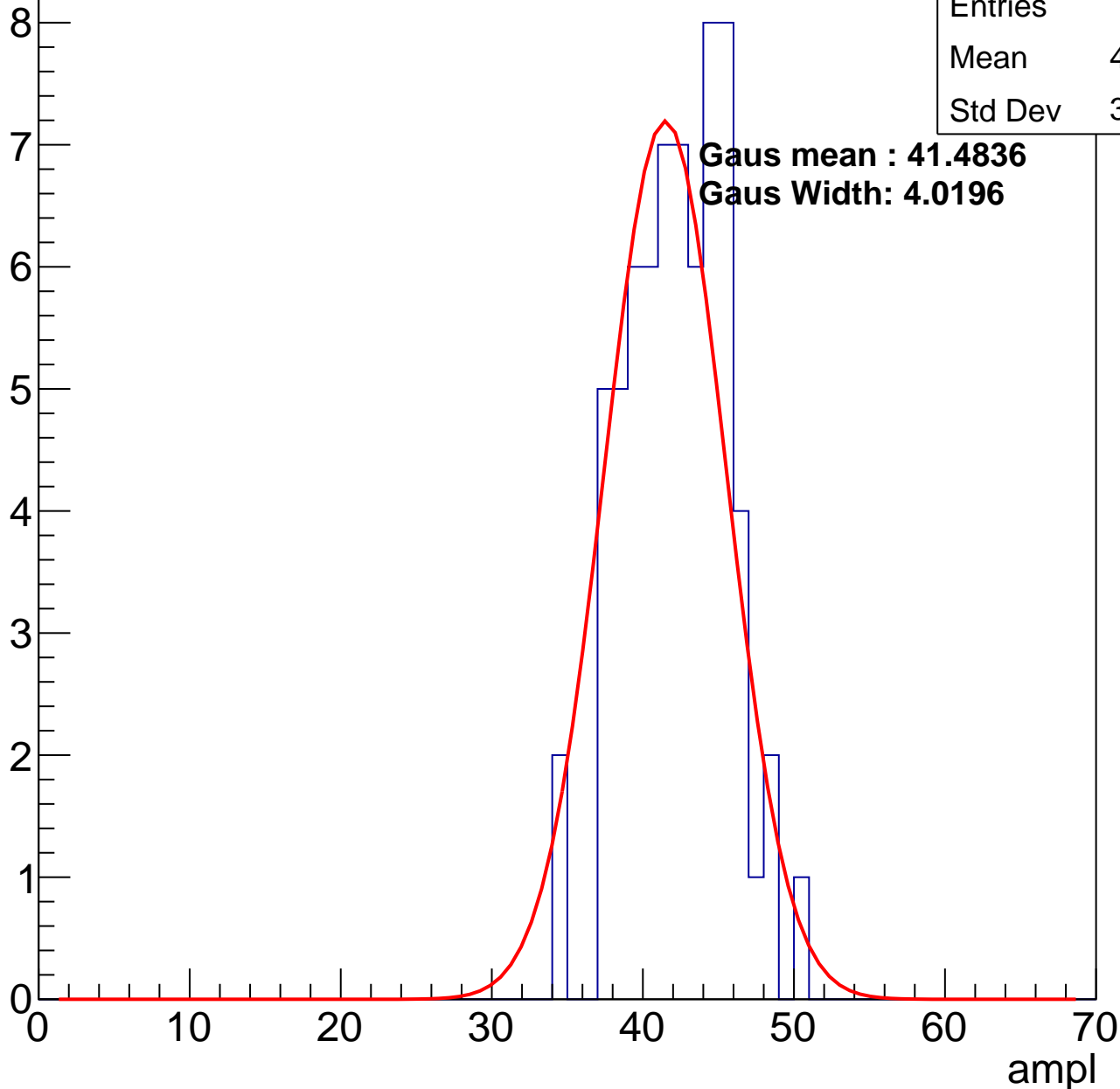
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.84
Std Dev	3.324

Gaus mean : 41.4836

Gaus Width: 4.0196

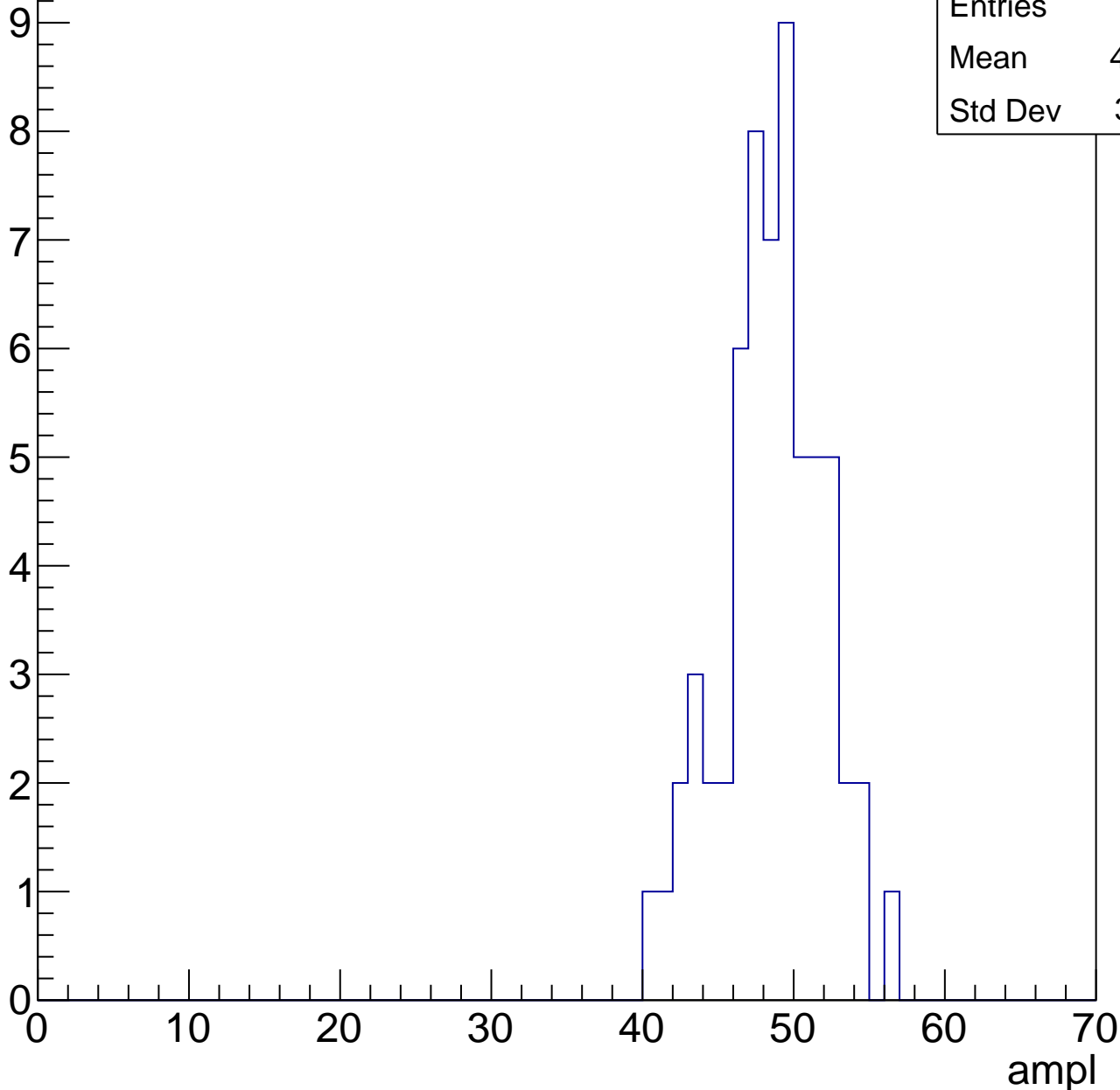


B1L003S, U16-ch56, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.13
Std Dev	3.341

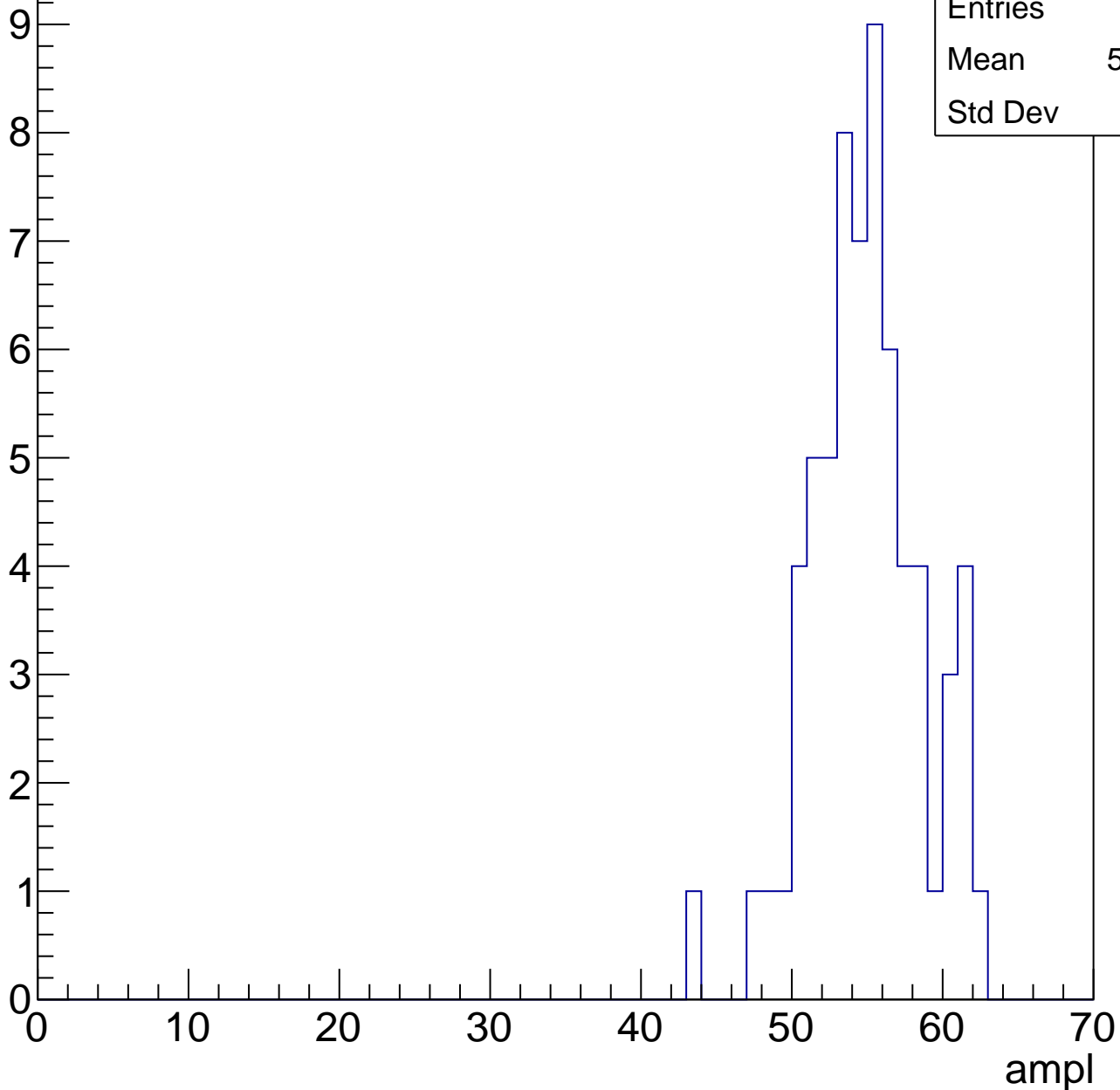


B1L003S, U16-ch56, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

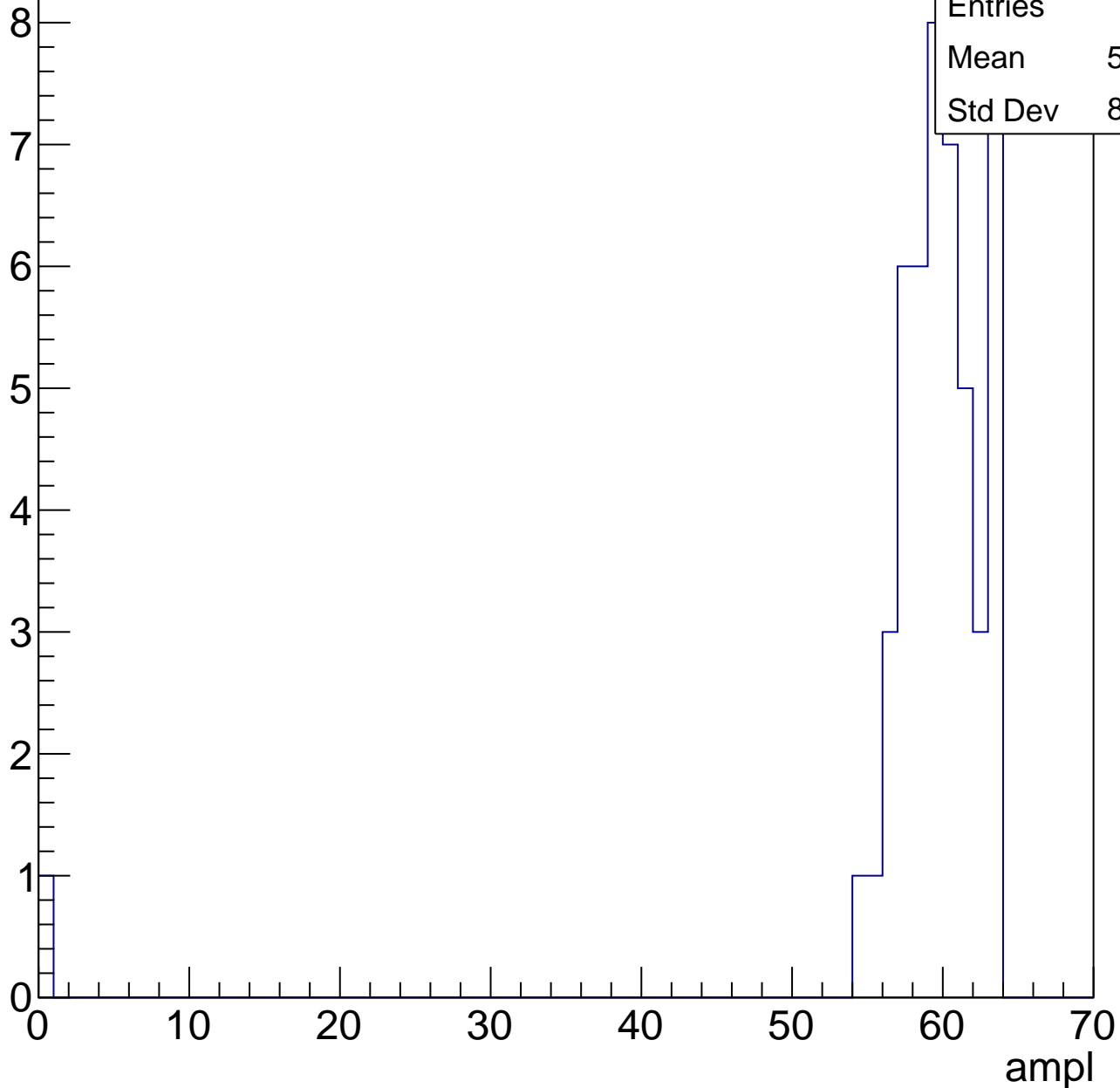
Entries	65
Mean	54.46
Std Dev	3.68



B1L003S, U16-ch56, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



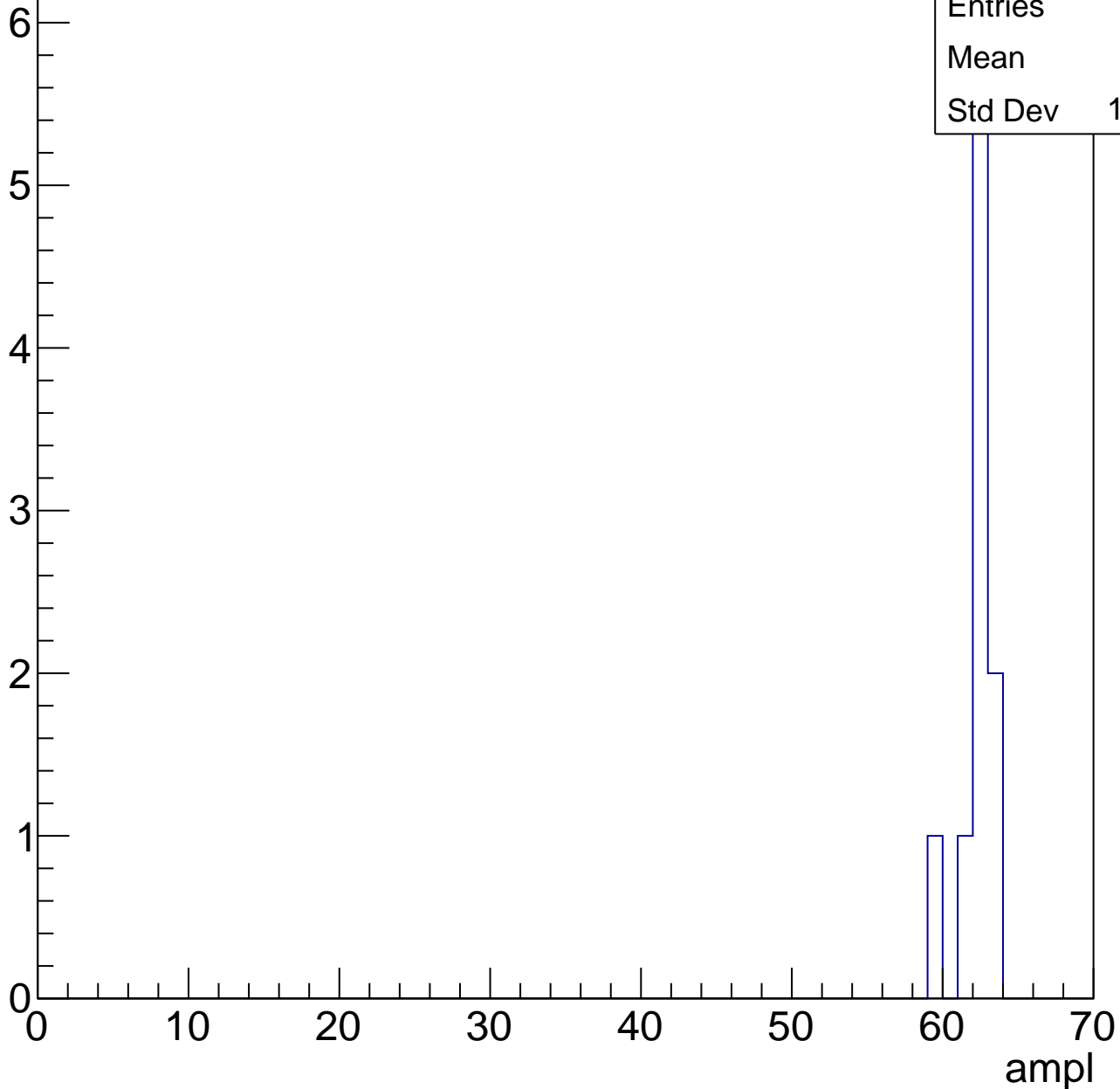
Entries	49
Mean	58.24
Std Dev	8.728

B1L003S, U16-ch56, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.8
Std Dev	1.077



B1L003S, U16-ch56, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch57, adc0

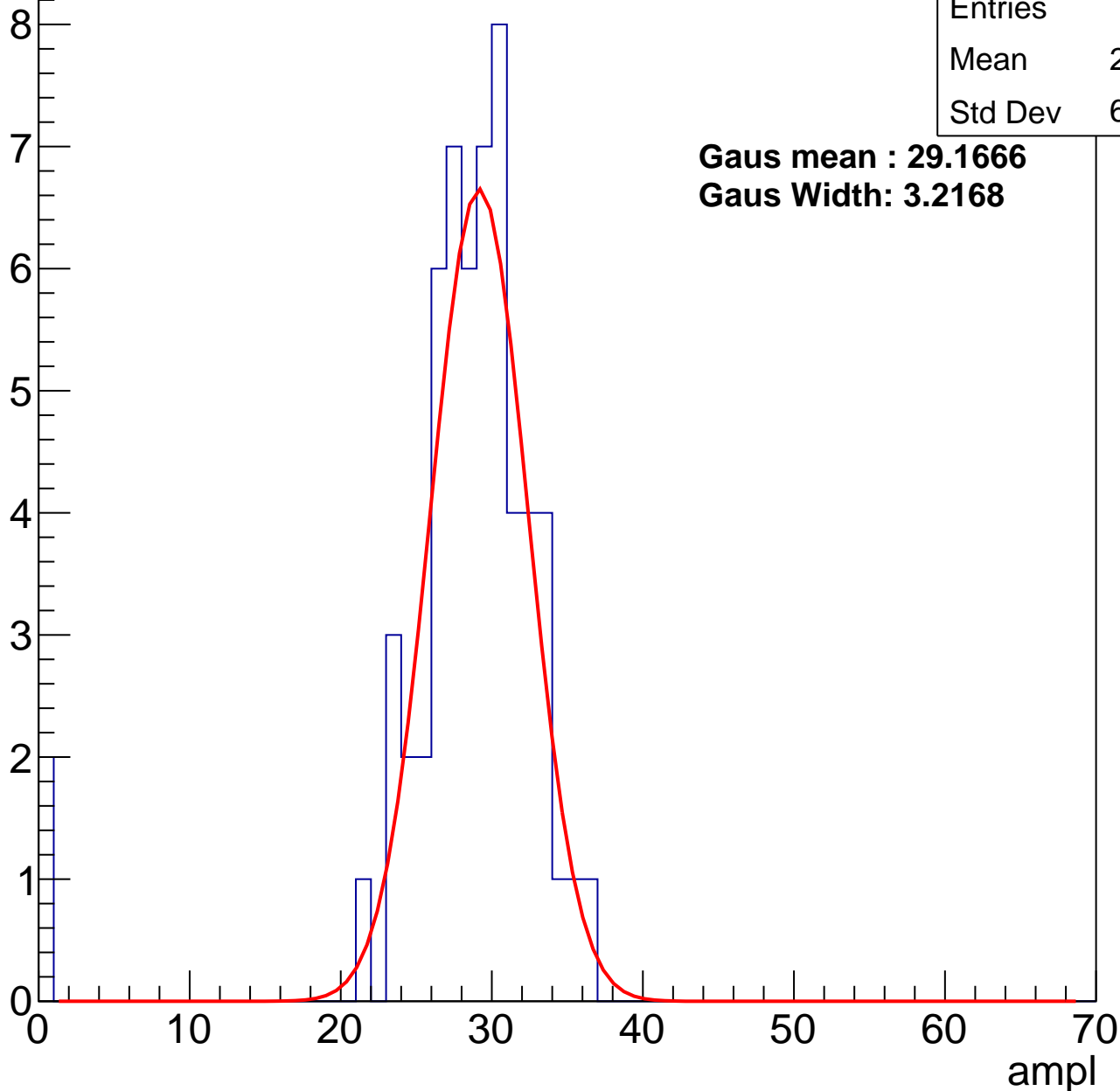
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	27.68
Std Dev	6.043

Gaus mean : 29.1666

Gaus Width: 3.2168



B1L003S, U16-ch57, adc1

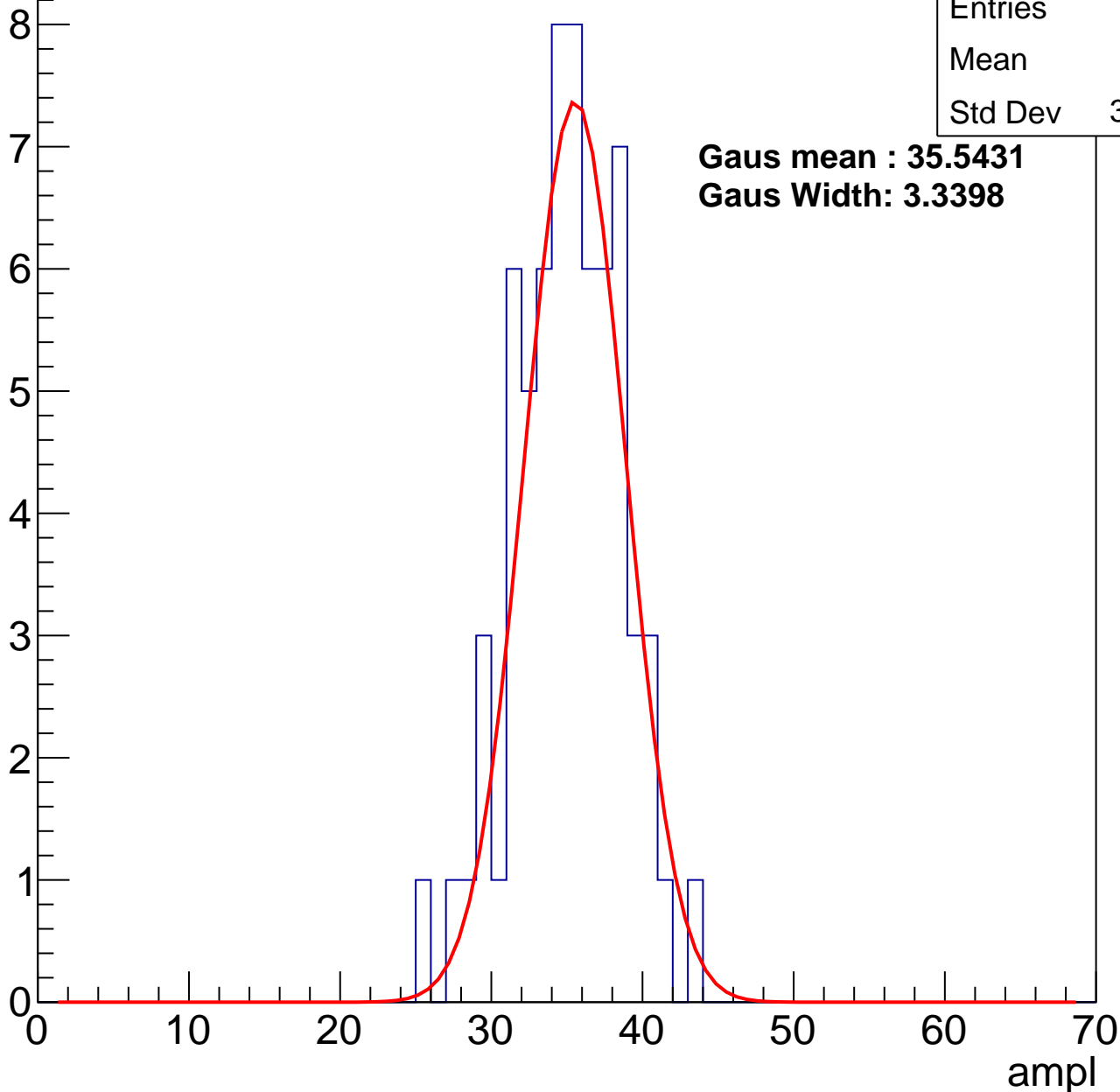
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	34.6
Std Dev	3.507

Gaus mean : 35.5431

Gaus Width: 3.3398



B1L003S, U16-ch57, adc2

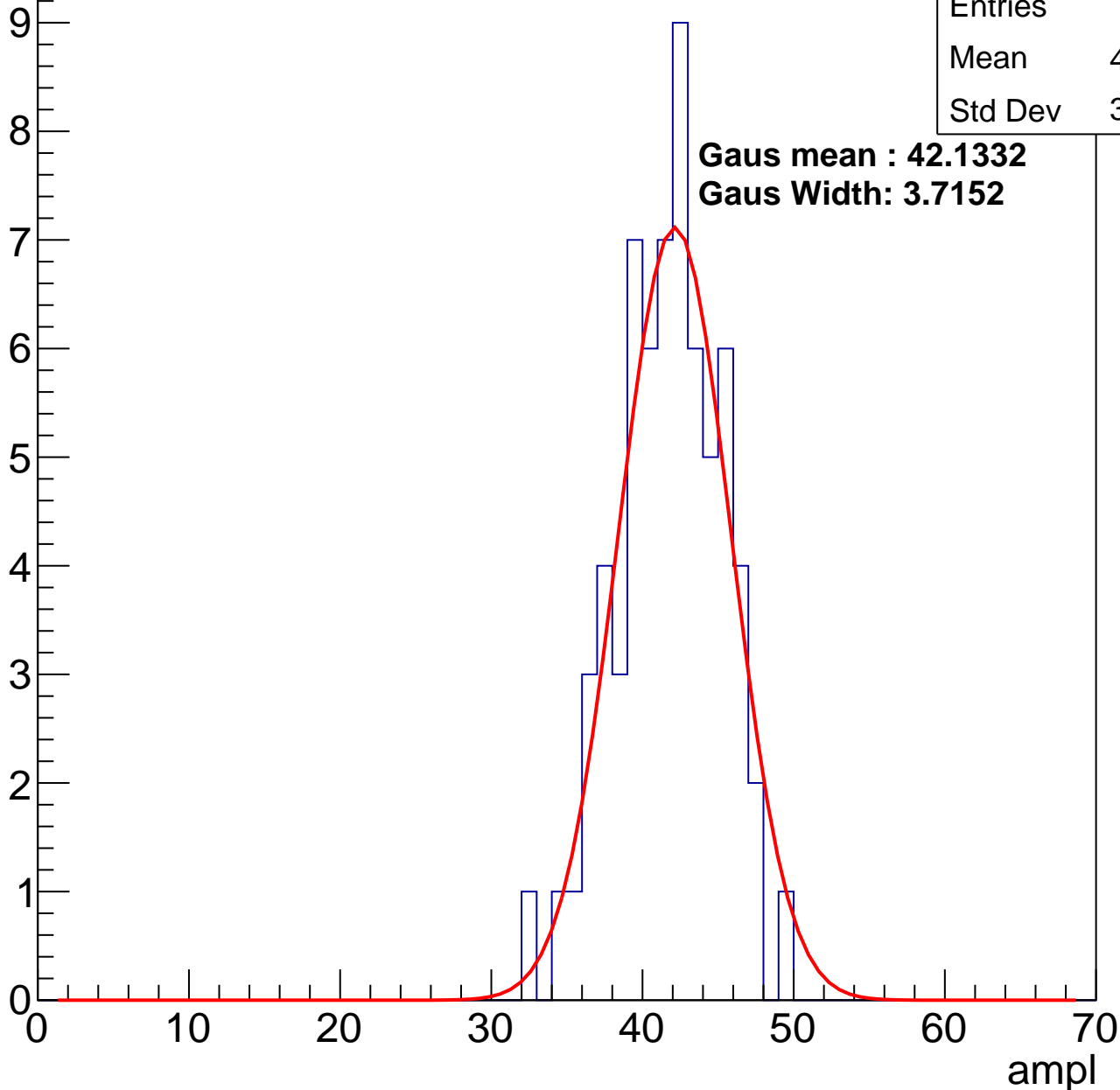
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.27
Std Dev	3.418

Gaus mean : 42.1332

Gaus Width: 3.7152



B1L003S, U16-ch57, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	48.1
Std Dev	3.576

Entry

10

8

6

4

2

0

0

10

20

30

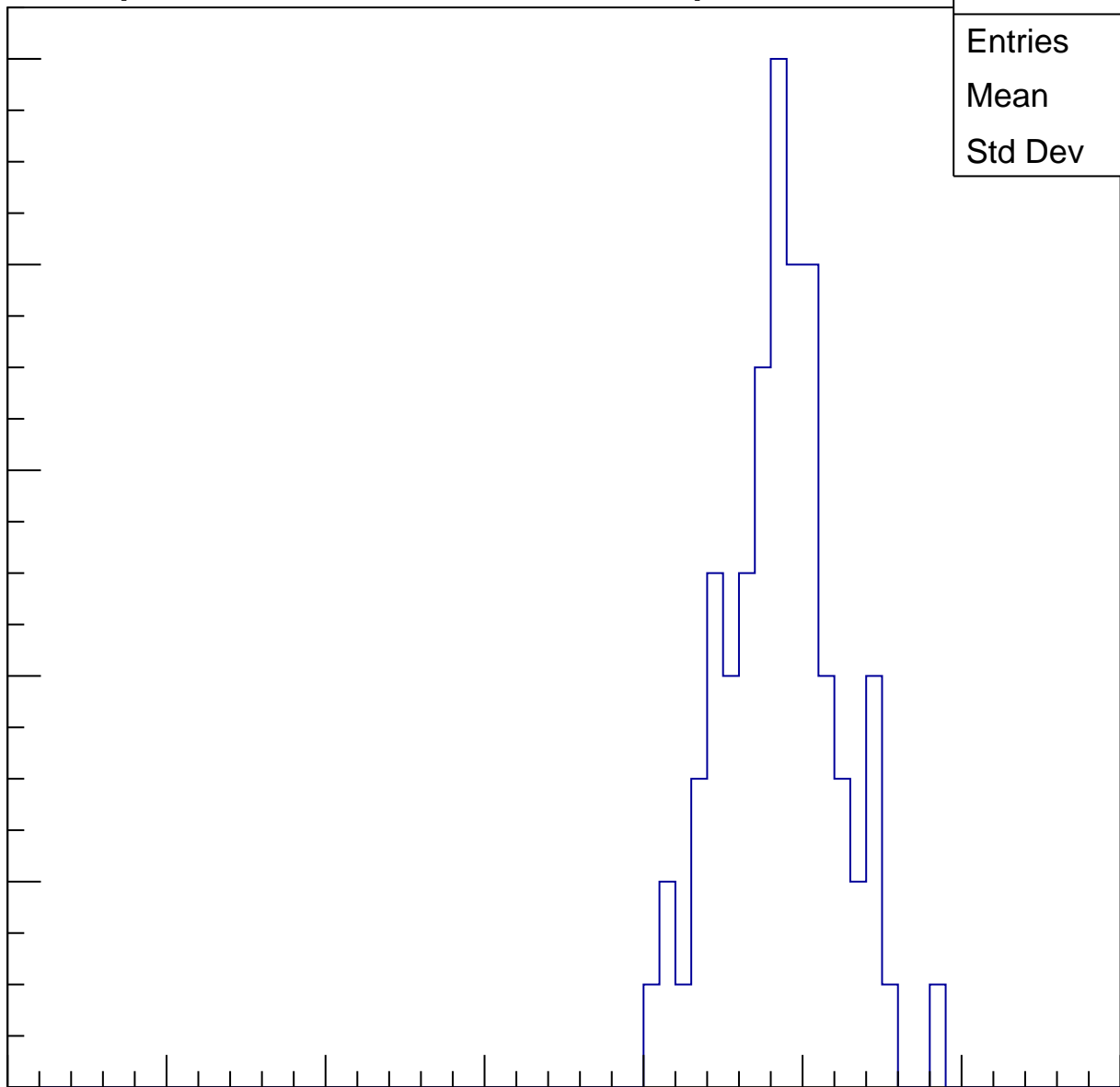
40

50

60

70

ampl

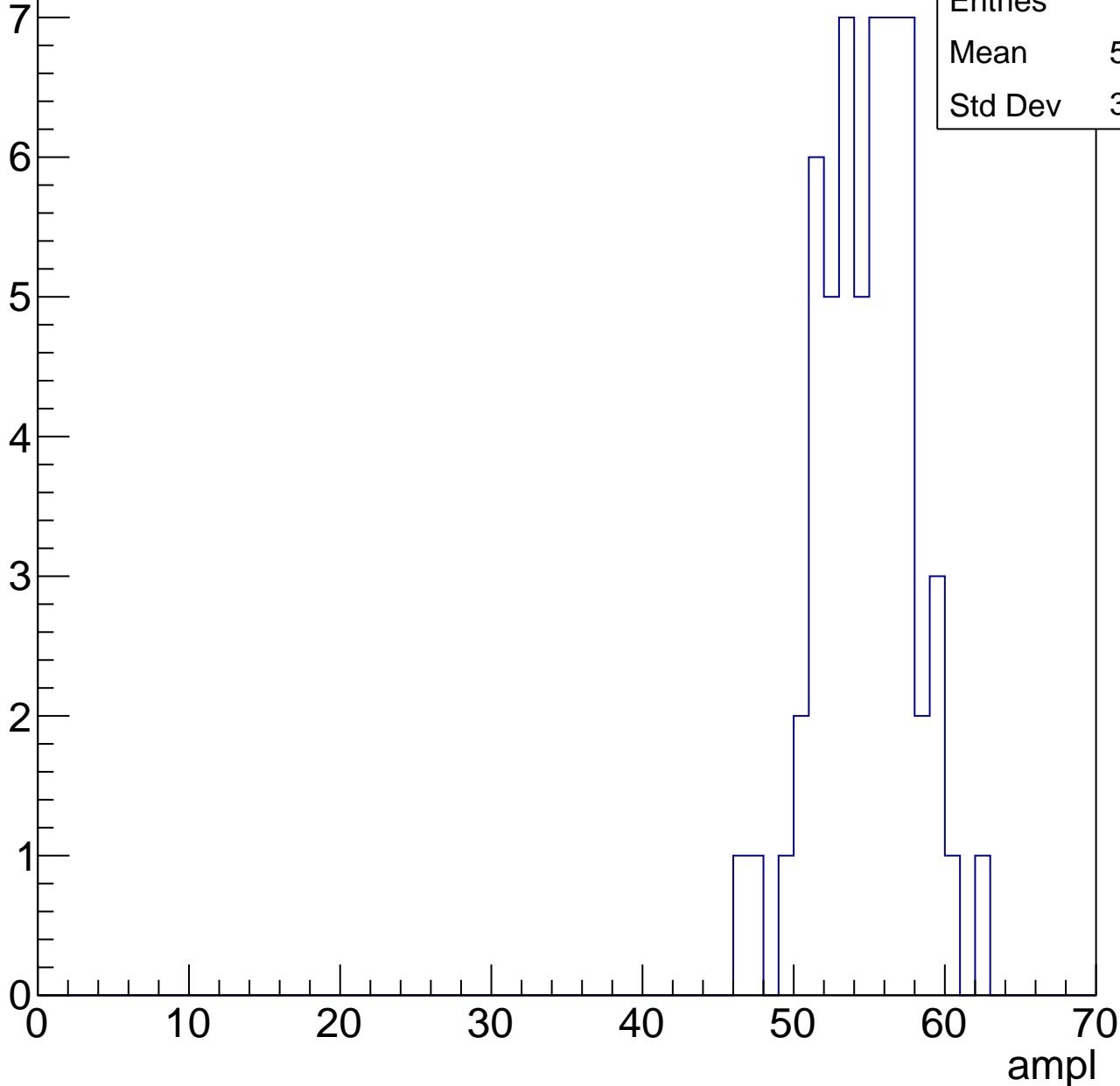


B1L003S, U16-ch57, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.29
Std Dev	3.138

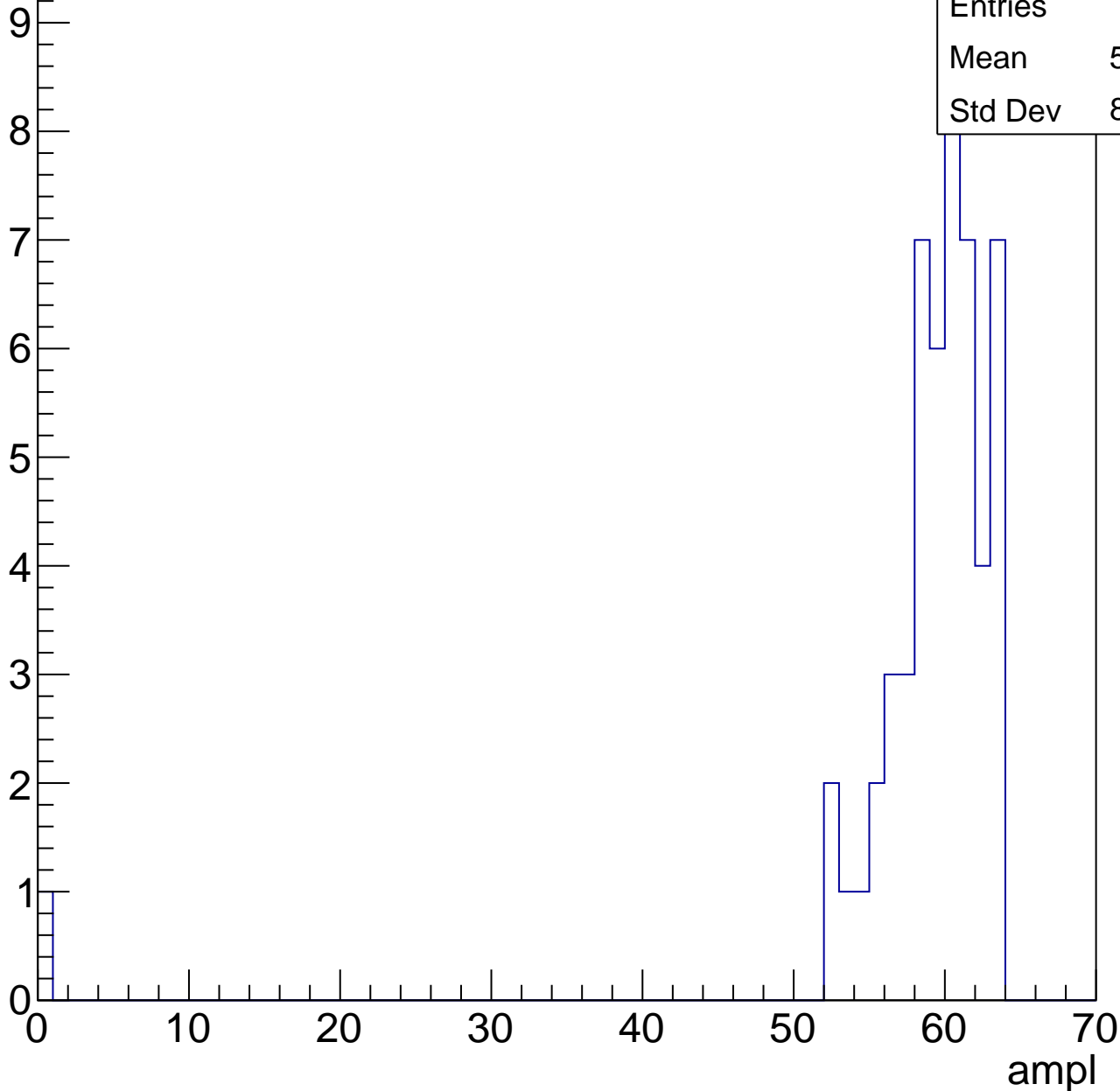


B1L003S, U16-ch57, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.04
Std Dev	8.527

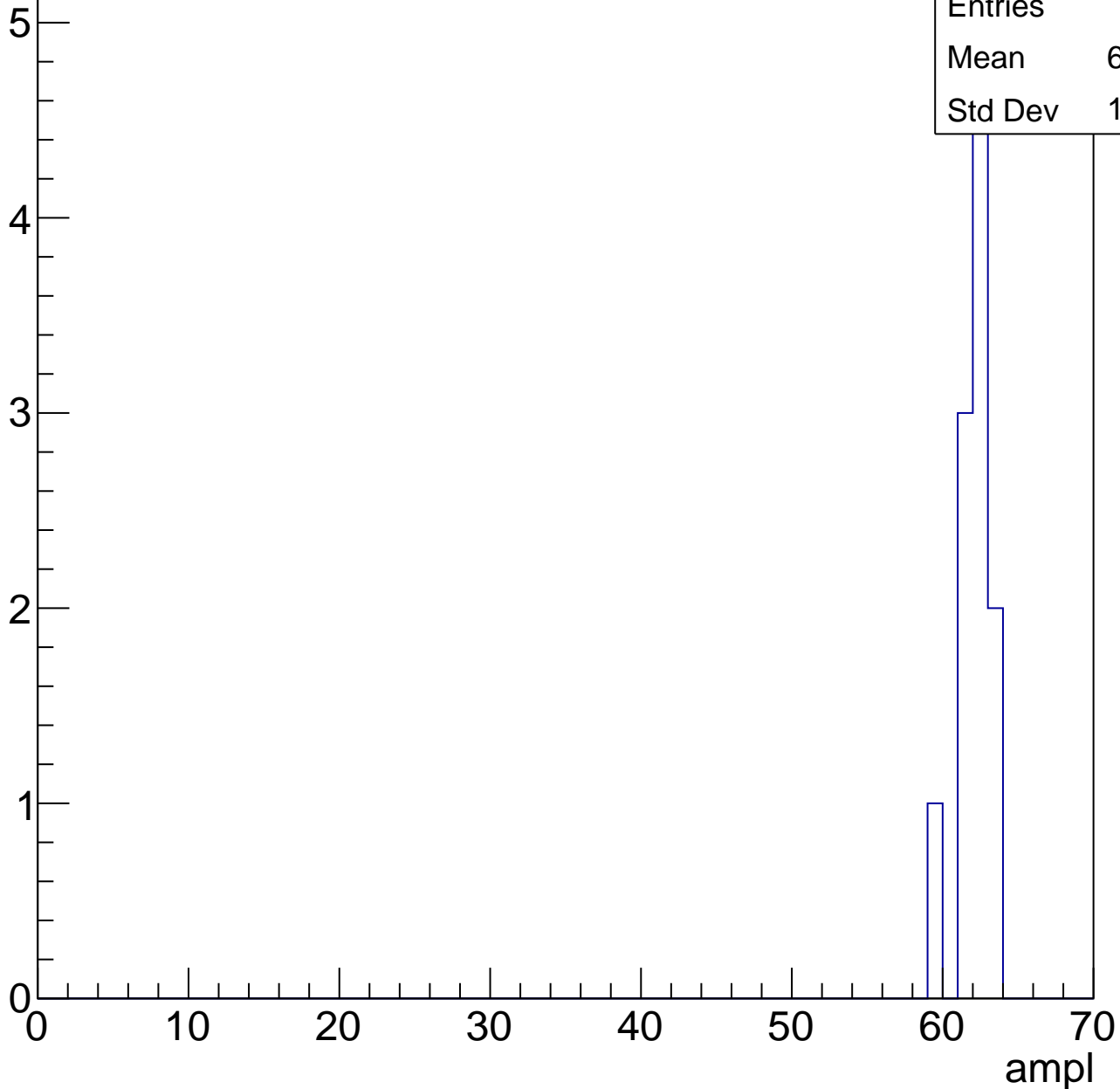


B1L003S, U16-ch57, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	61.64
Std Dev	1.068



B1L003S, U16-ch57, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch58, adc0

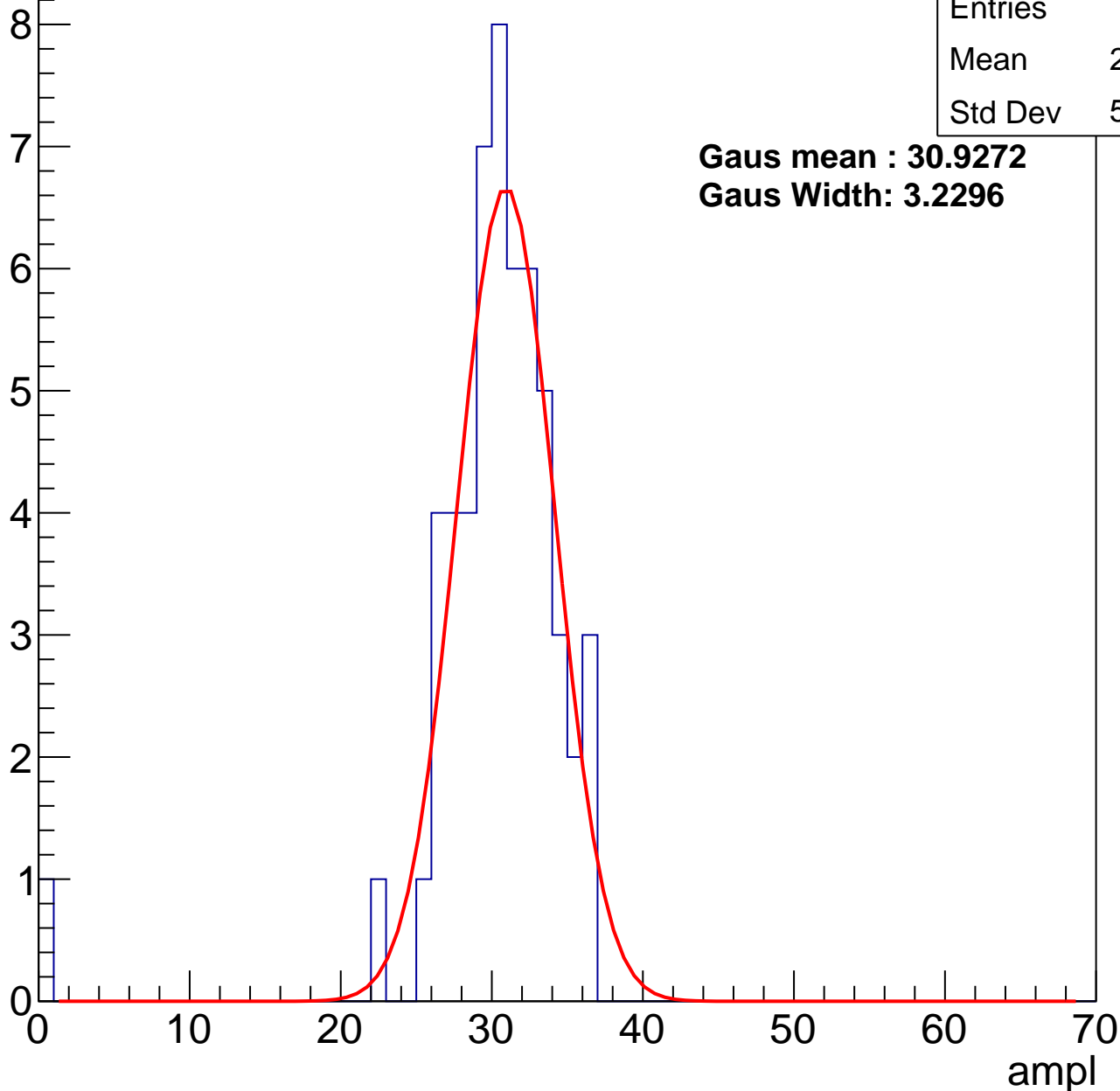
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.76
Std Dev	5.027

Gaus mean : 30.9272

Gaus Width: 3.2296



B1L003S, U16-ch58, adc1

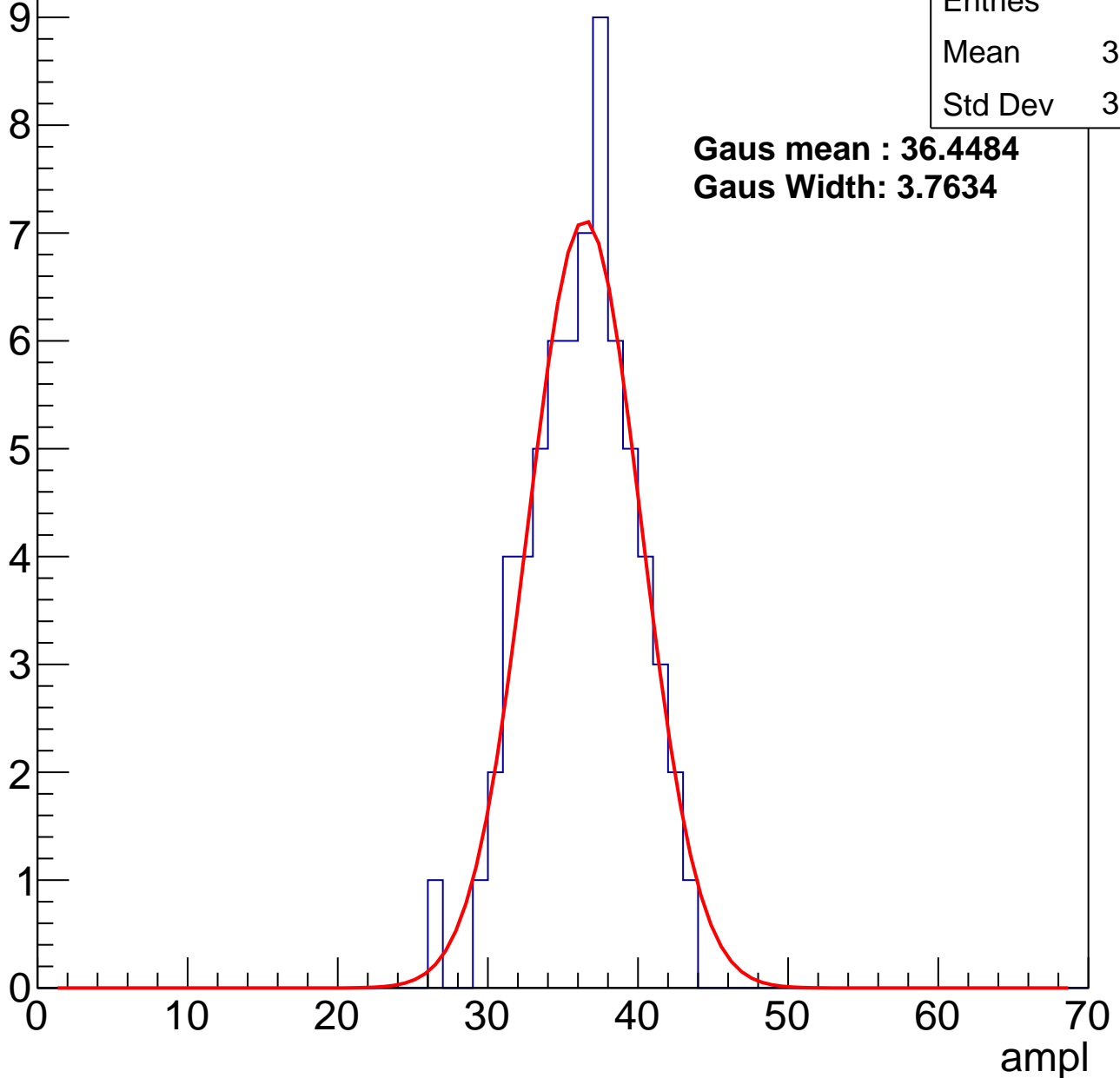
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.82
Std Dev	3.472

Gaus mean : 36.4484

Gaus Width: 3.7634



B1L003S, U16-ch58, adc2

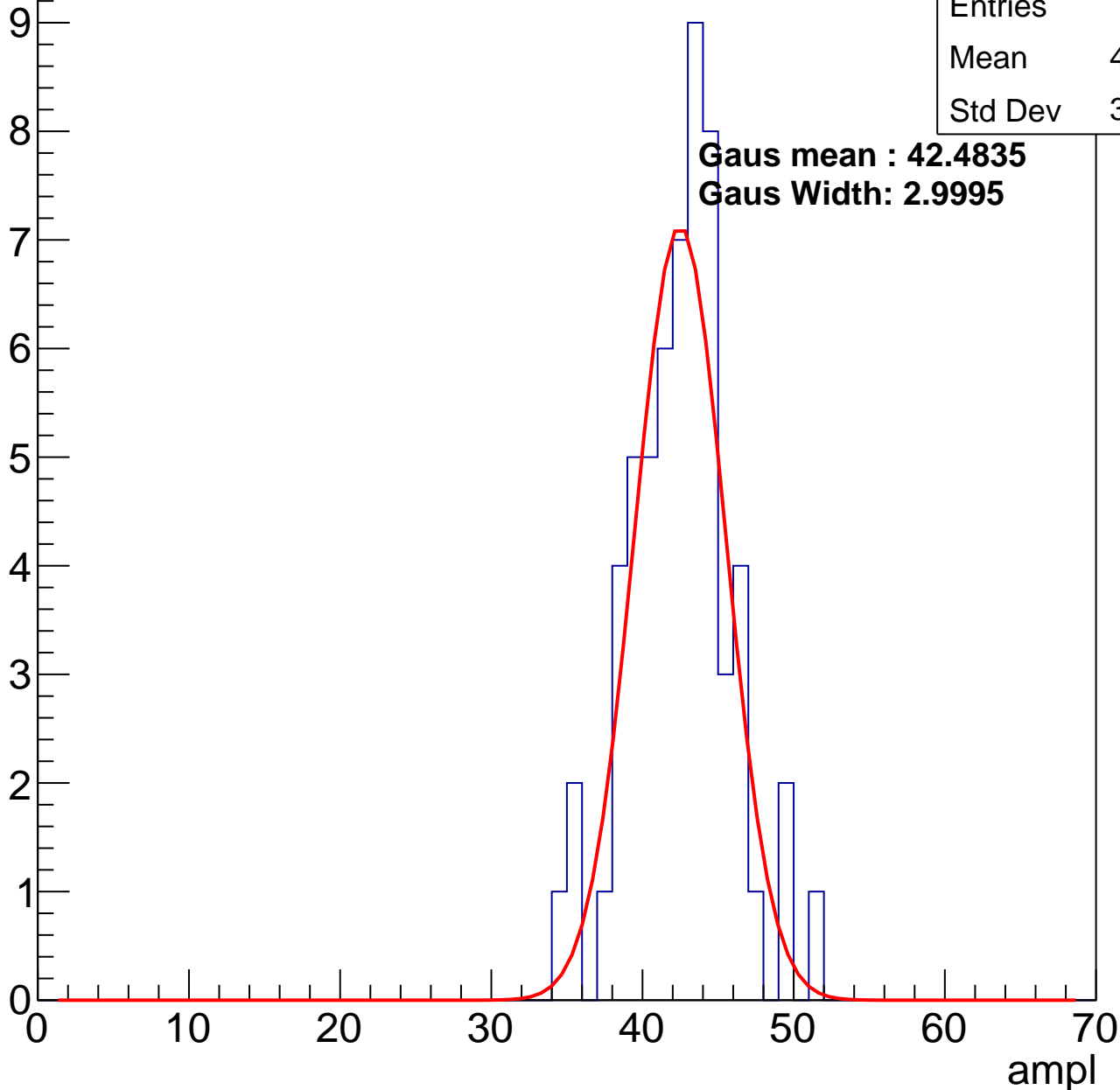
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.07
Std Dev	3.339

Gaus mean : 42.4835

Gaus Width: 2.9995



B1L003S, U16-ch58, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	48.92
Std Dev	3.633

Entry

10

8

6

4

2

0

0

10

20

30

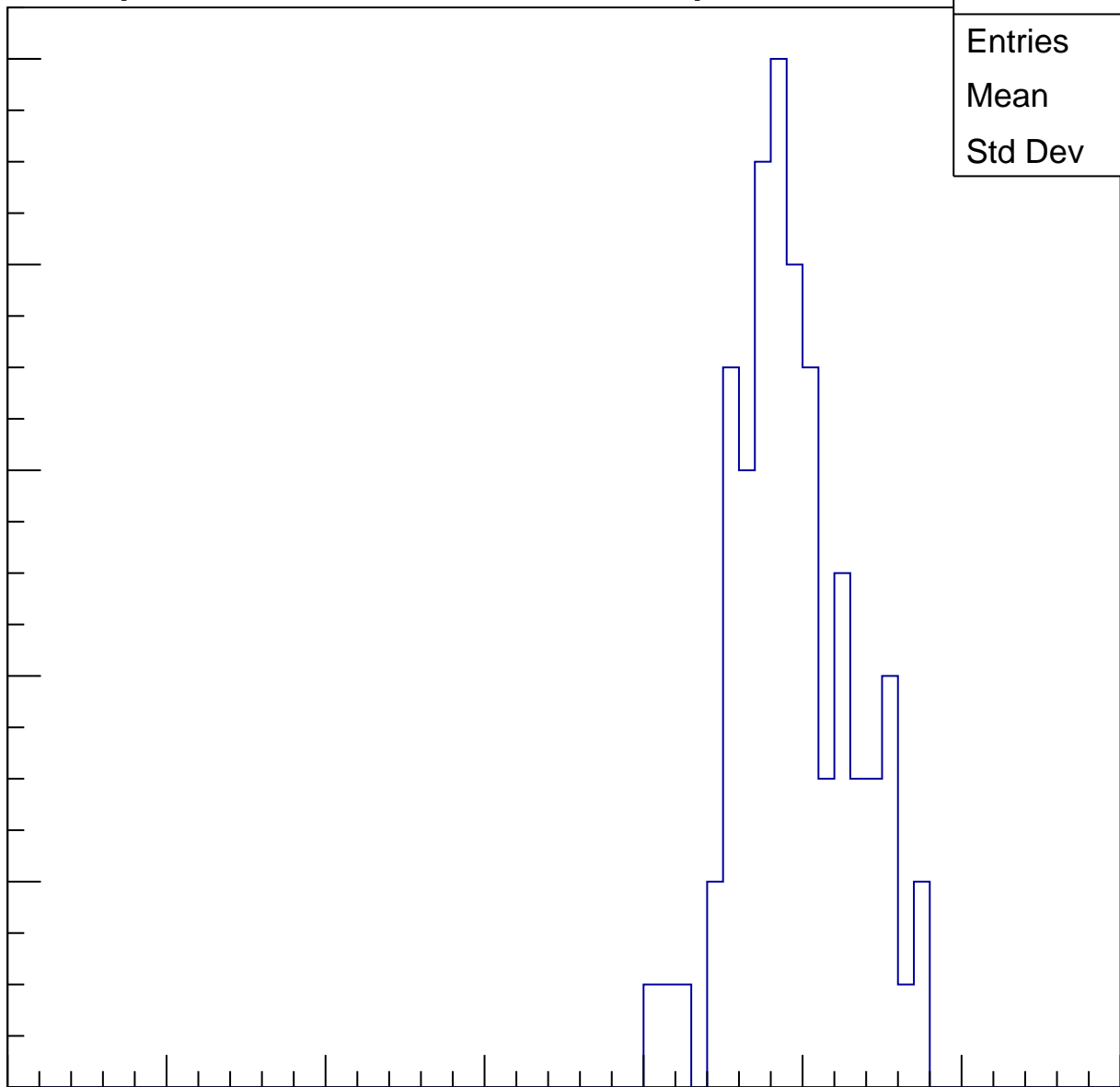
40

50

60

70

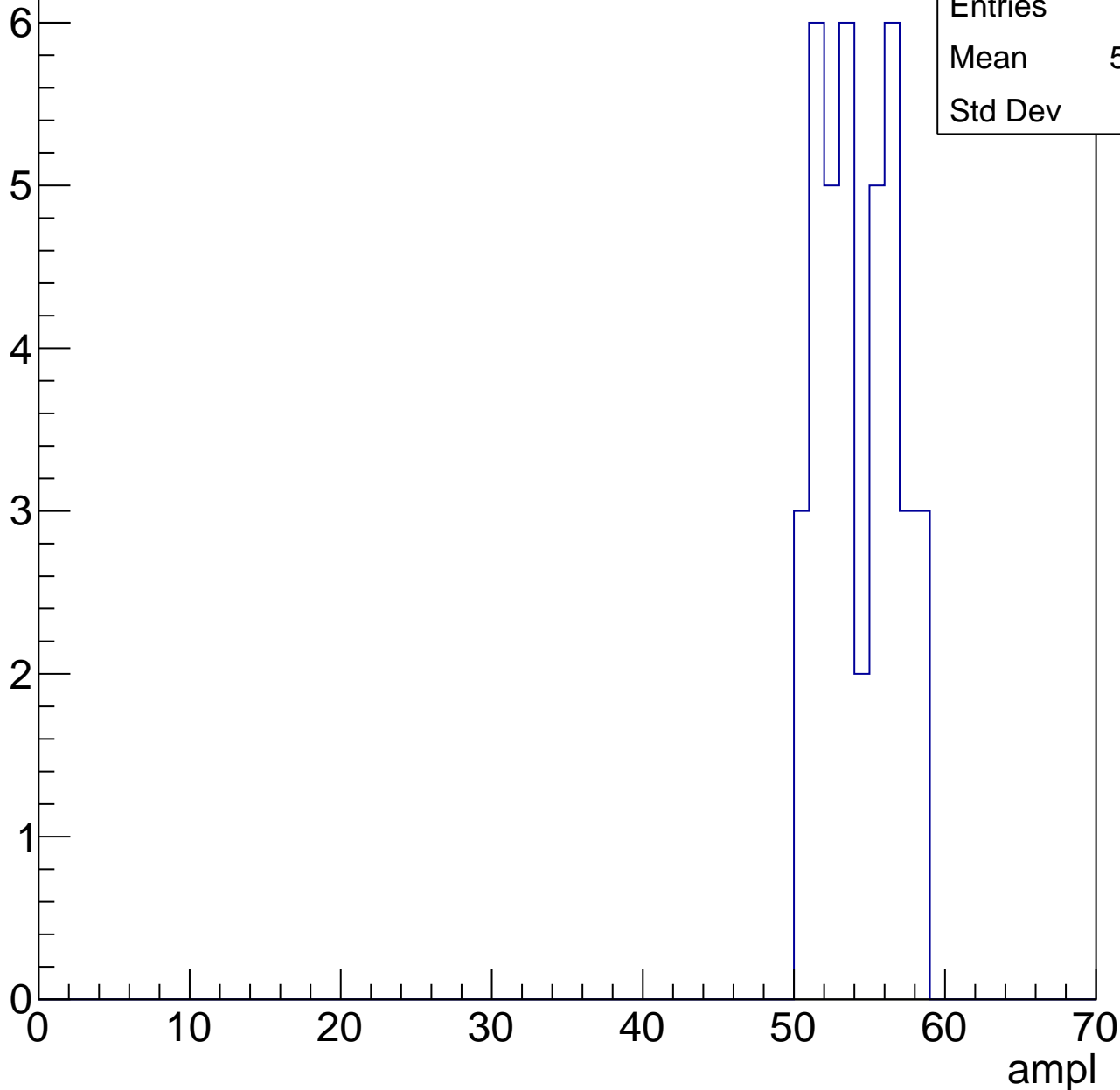
ampl



B1L003S, U16-ch58, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch58, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	58.32
Std Dev	7.75

Entry

10

8

6

4

2

0

0

10

20

30

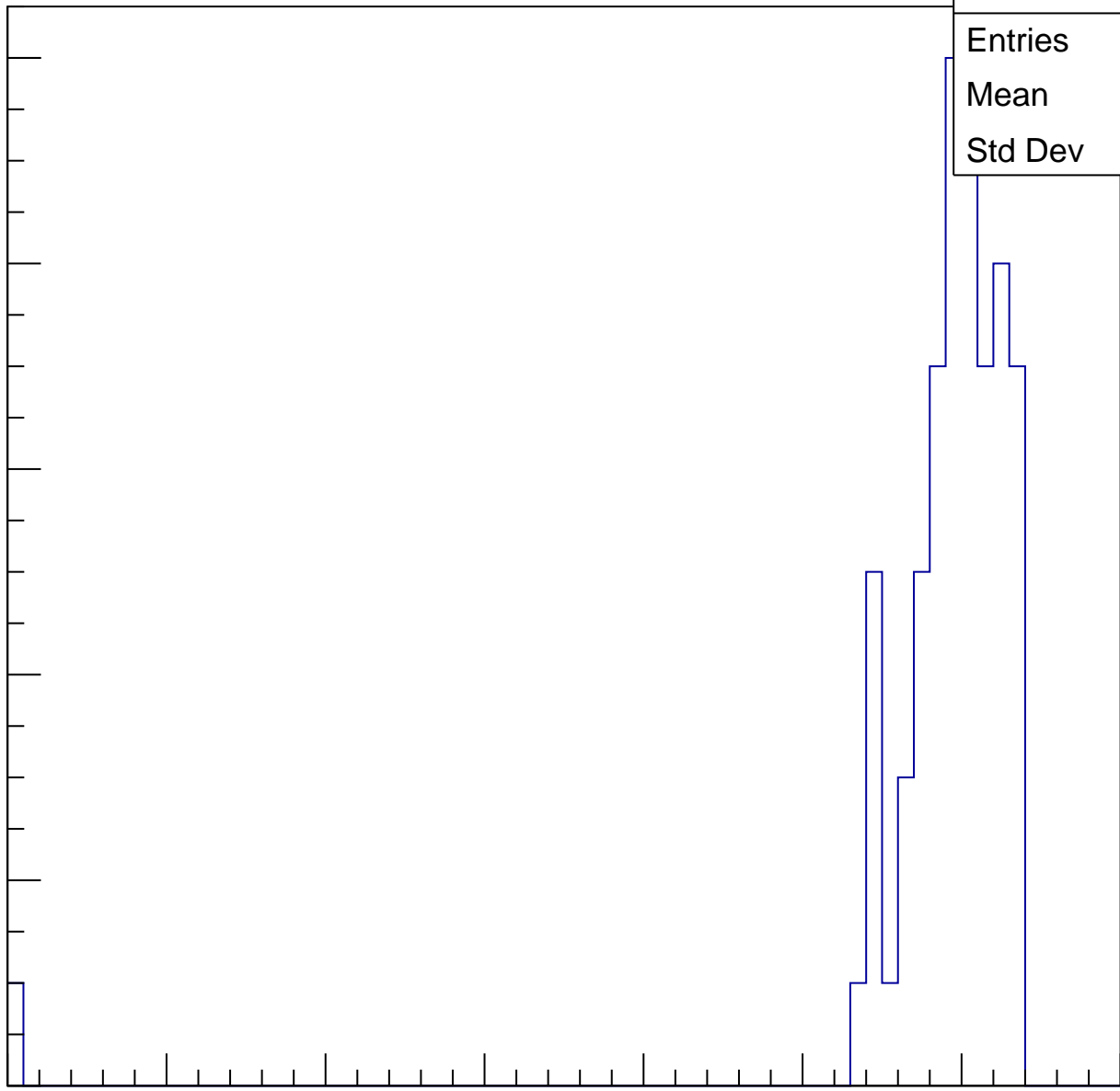
40

50

60

70

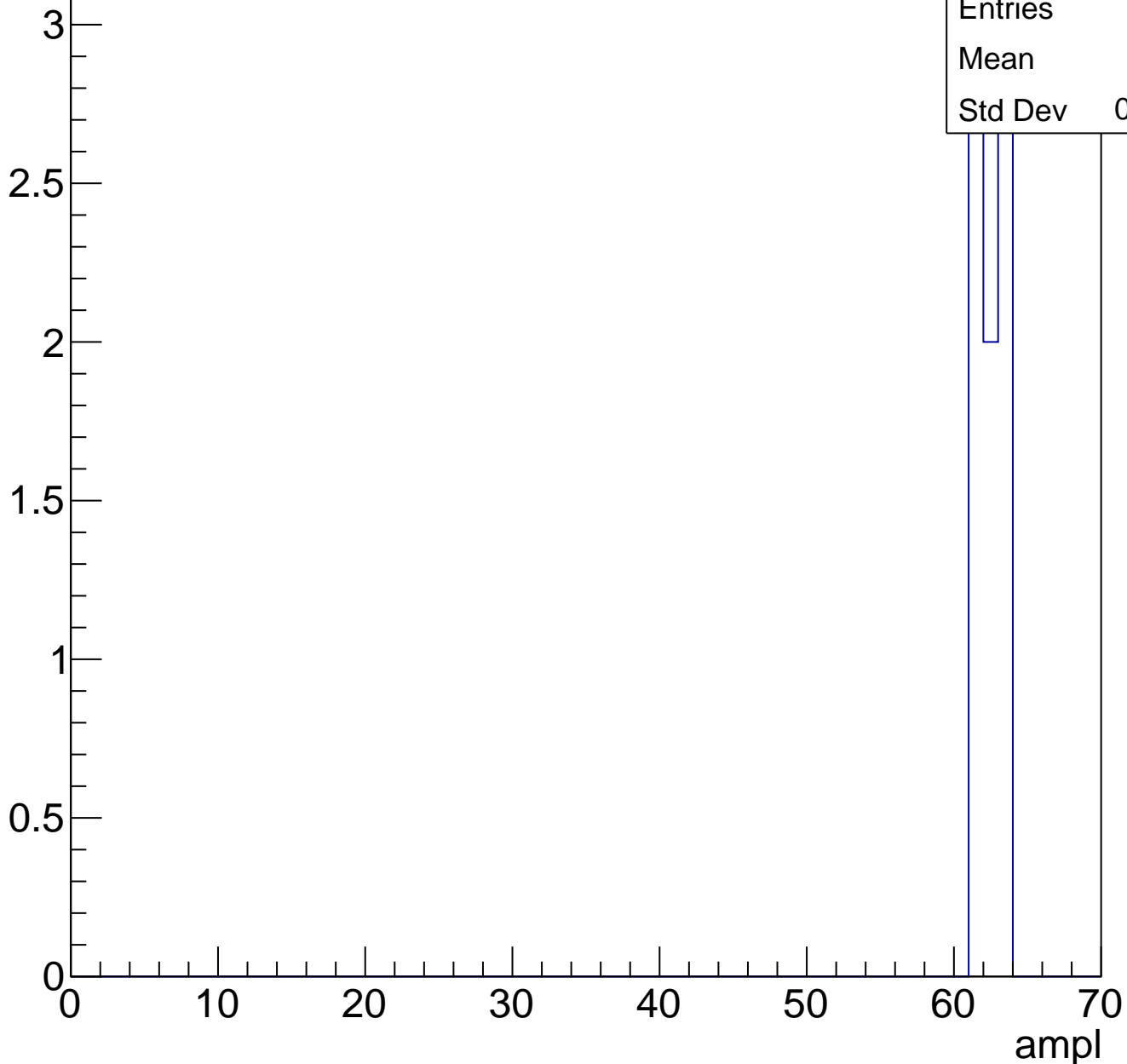
ampl



B1L003S, U16-ch58, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch58, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch59, adc0

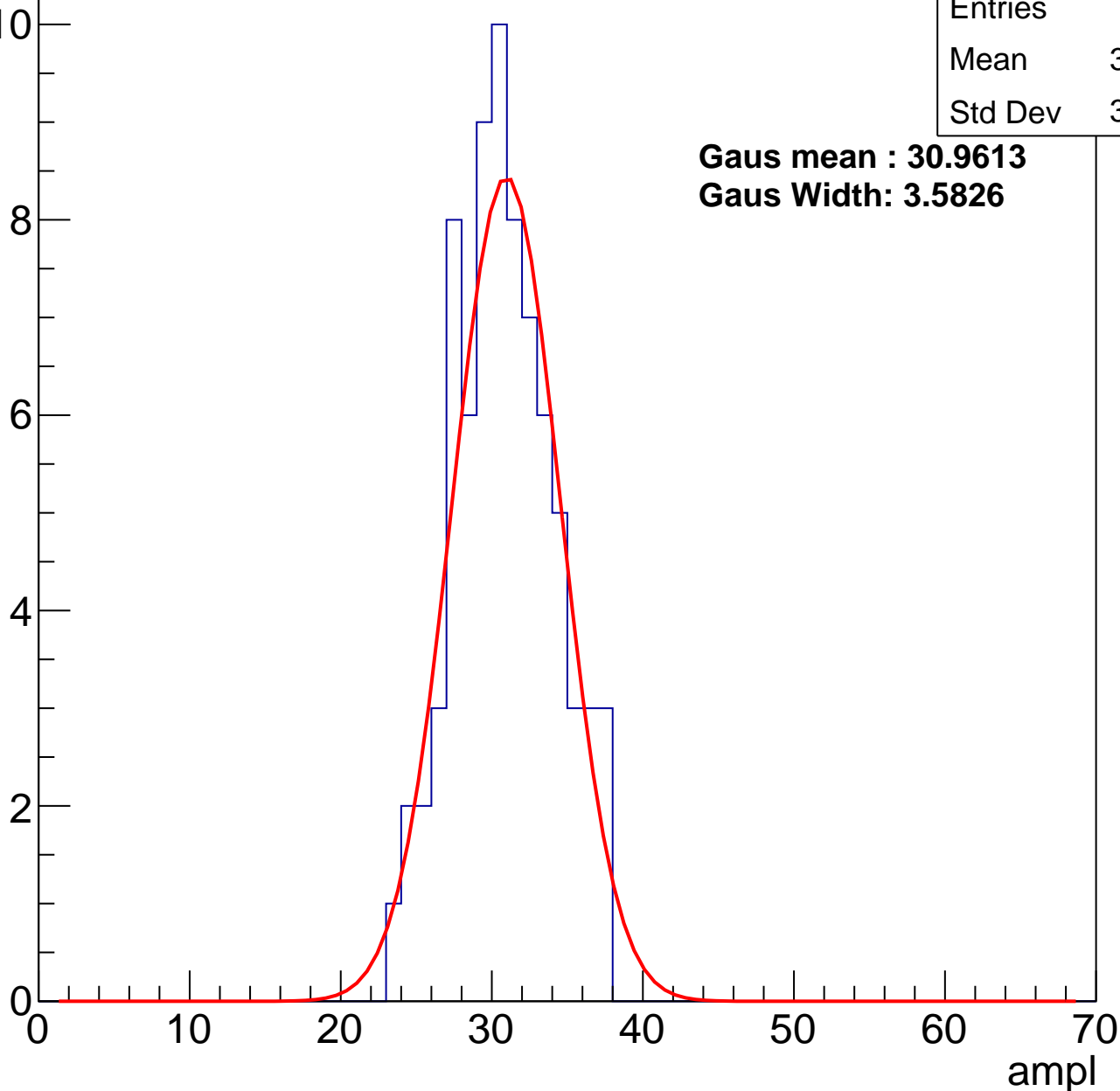
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	30.37
Std Dev	3.272

Gaus mean : 30.9613

Gaus Width: 3.5826



B1L003S, U16-ch59, adc1

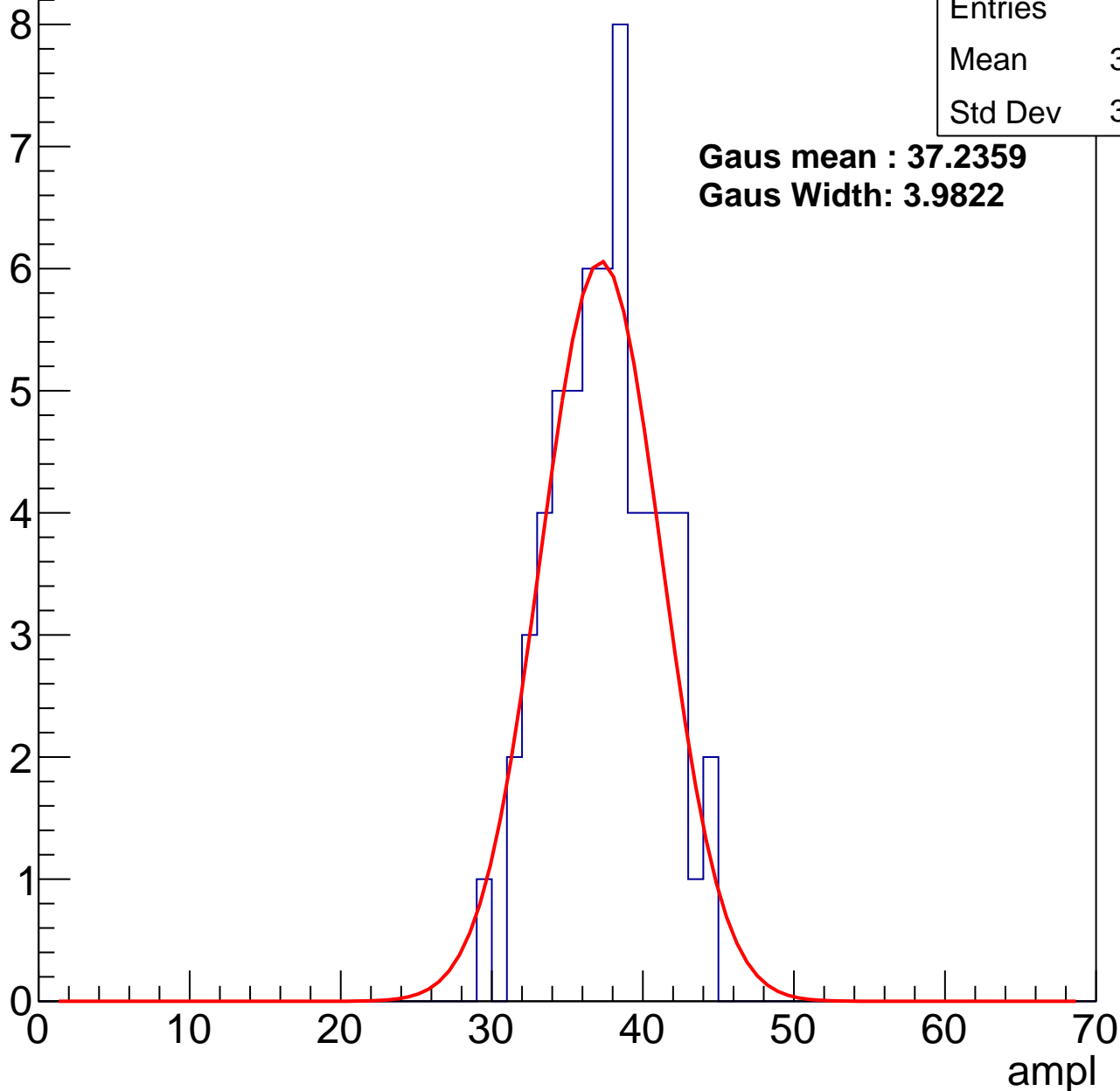
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	37.03
Std Dev	3.454

Gaus mean : 37.2359

Gaus Width: 3.9822



B1L003S, U16-ch59, adc2

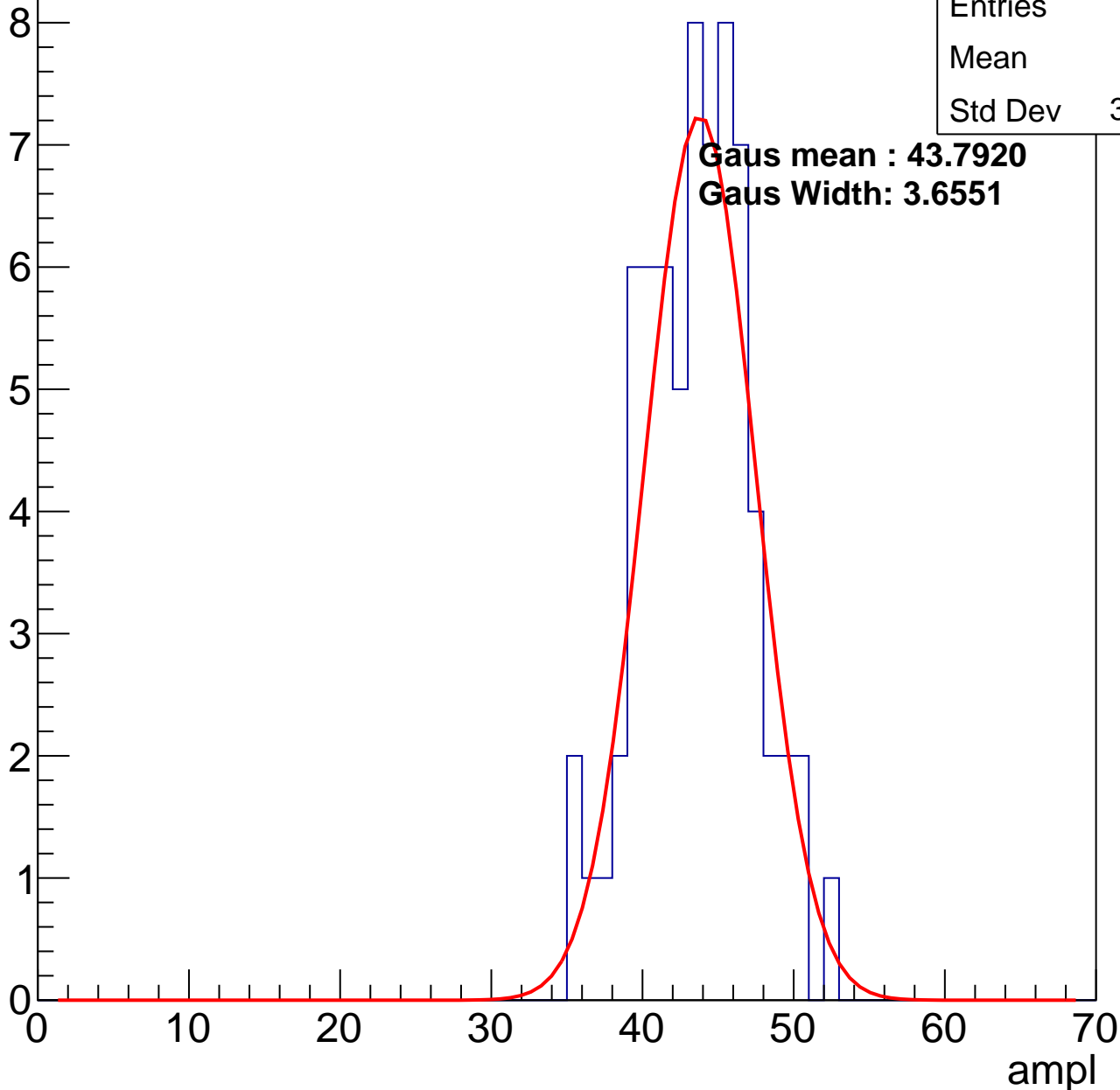
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	43.1
Std Dev	3.602

Gaus mean : 43.7920

Gaus Width: 3.6551

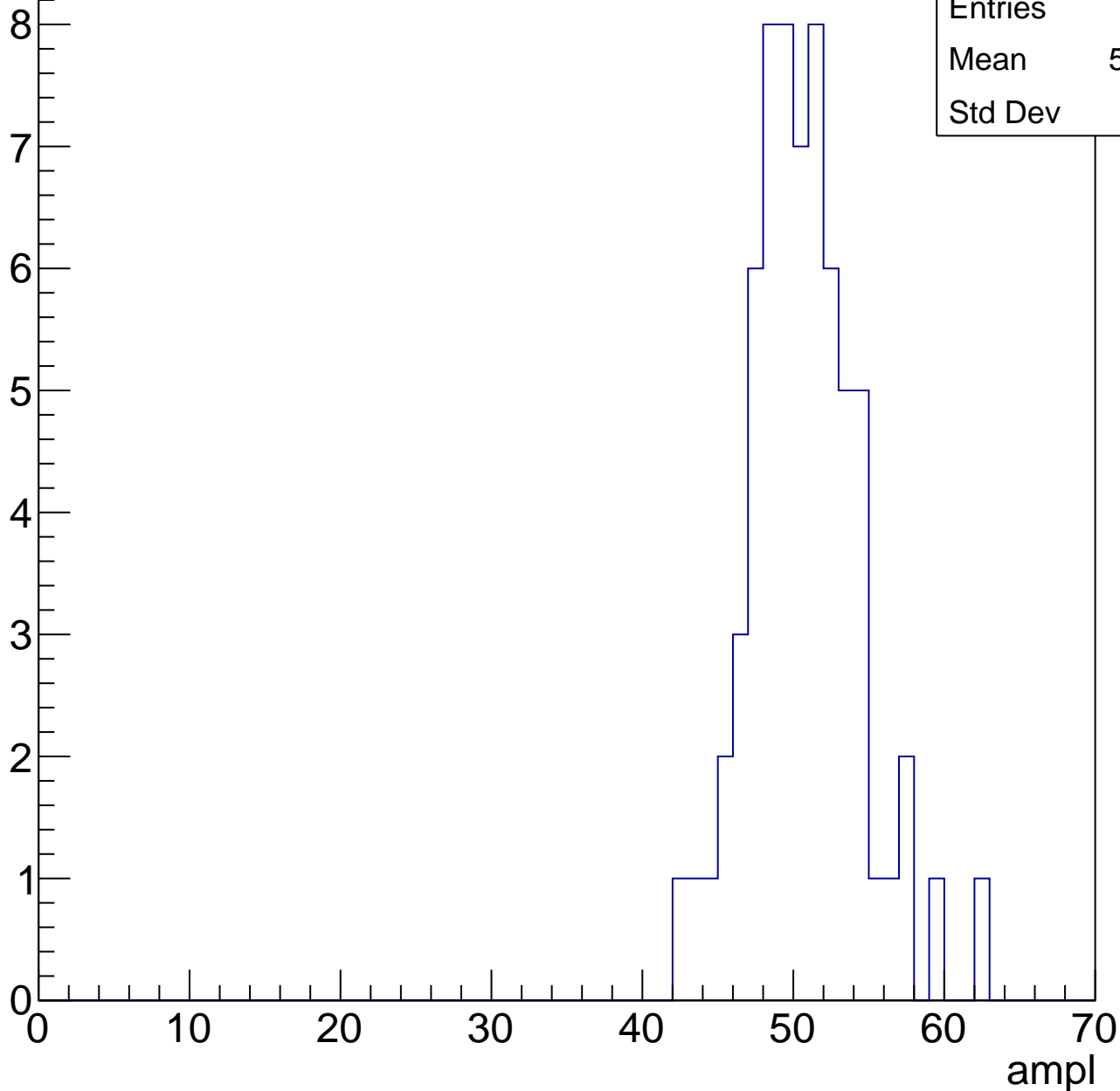


B1L003S, U16-ch59, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	50.24
Std Dev	3.62

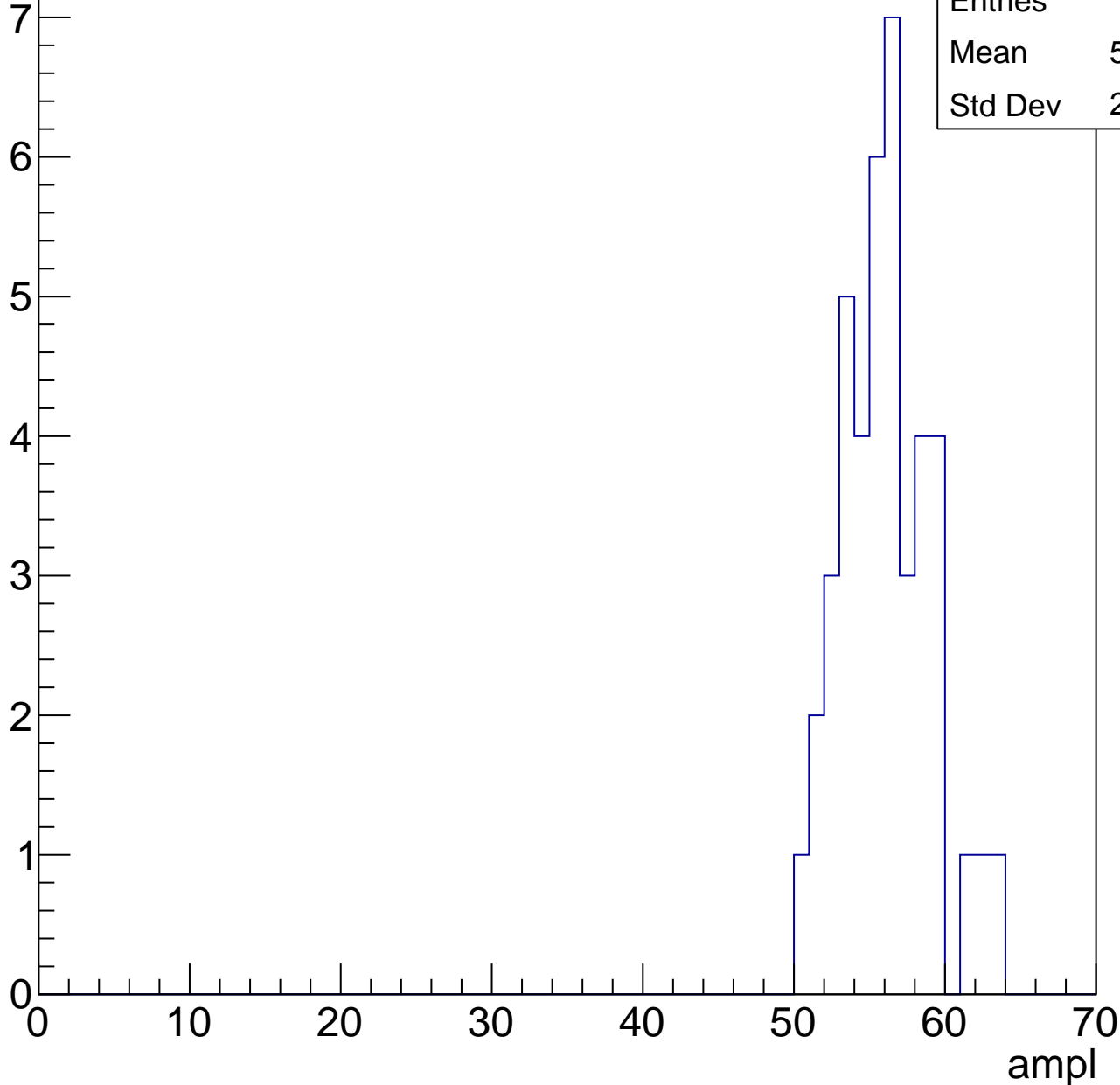


B1L003S, U16-ch59, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	55.62
Std Dev	2.927

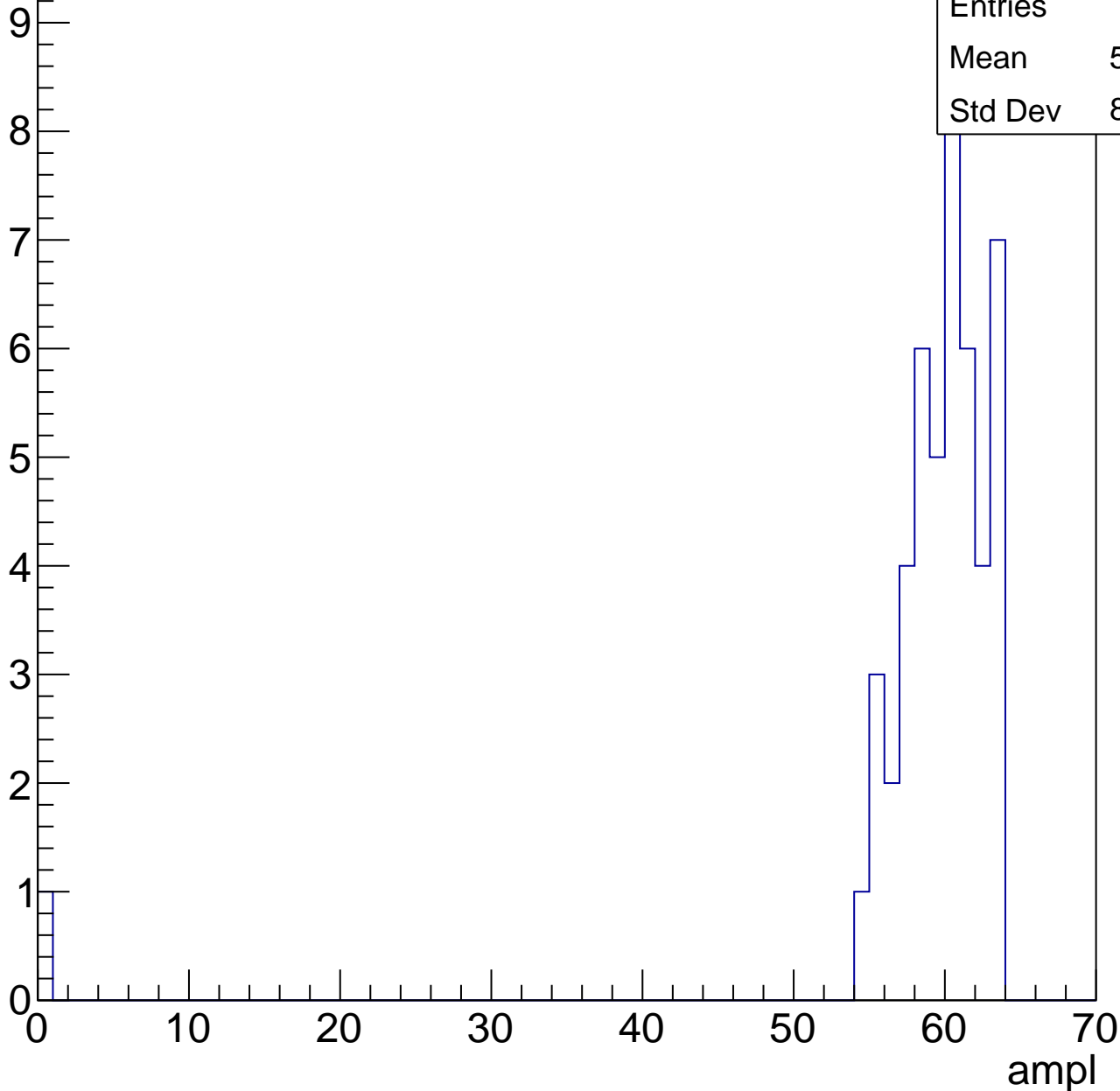


B1L003S, U16-ch59, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

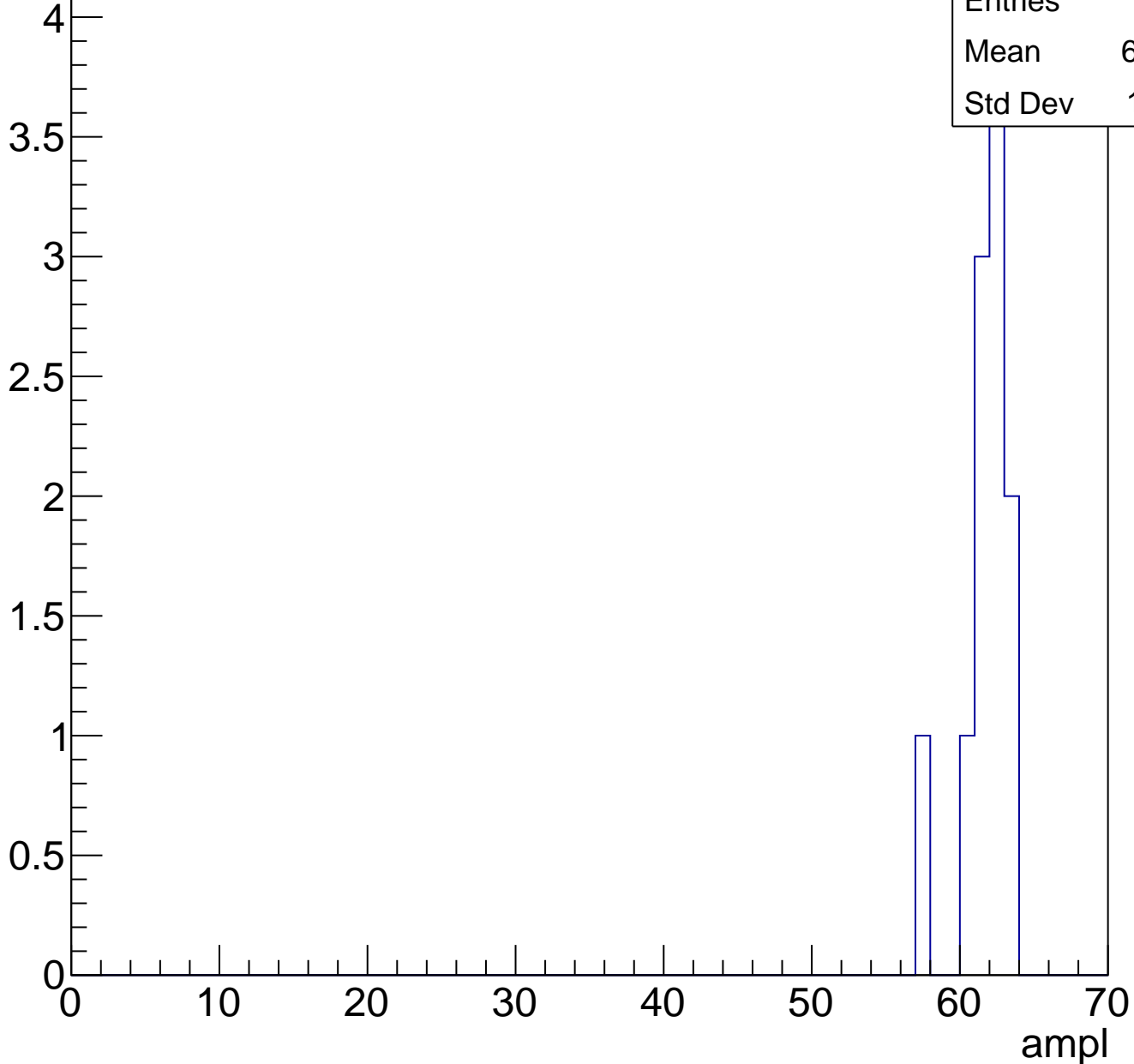
Entries	48
Mean	58.27
Std Dev	8.838



B1L003S, U16-ch59, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch59, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch60, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	28.85
Std Dev	4.935

Gaus mean : 28.8104

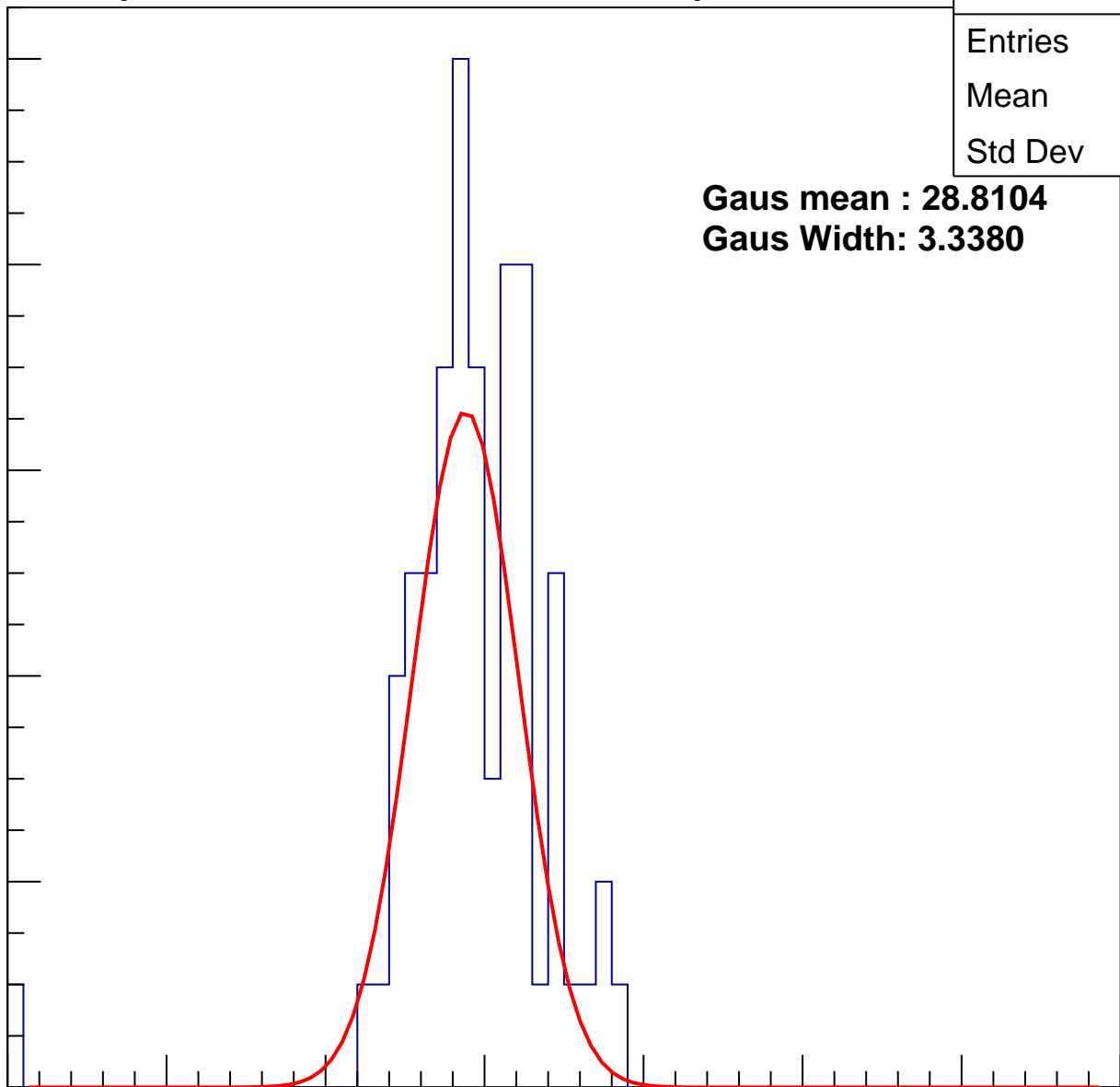
Gaus Width: 3.3380

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch60, adc1

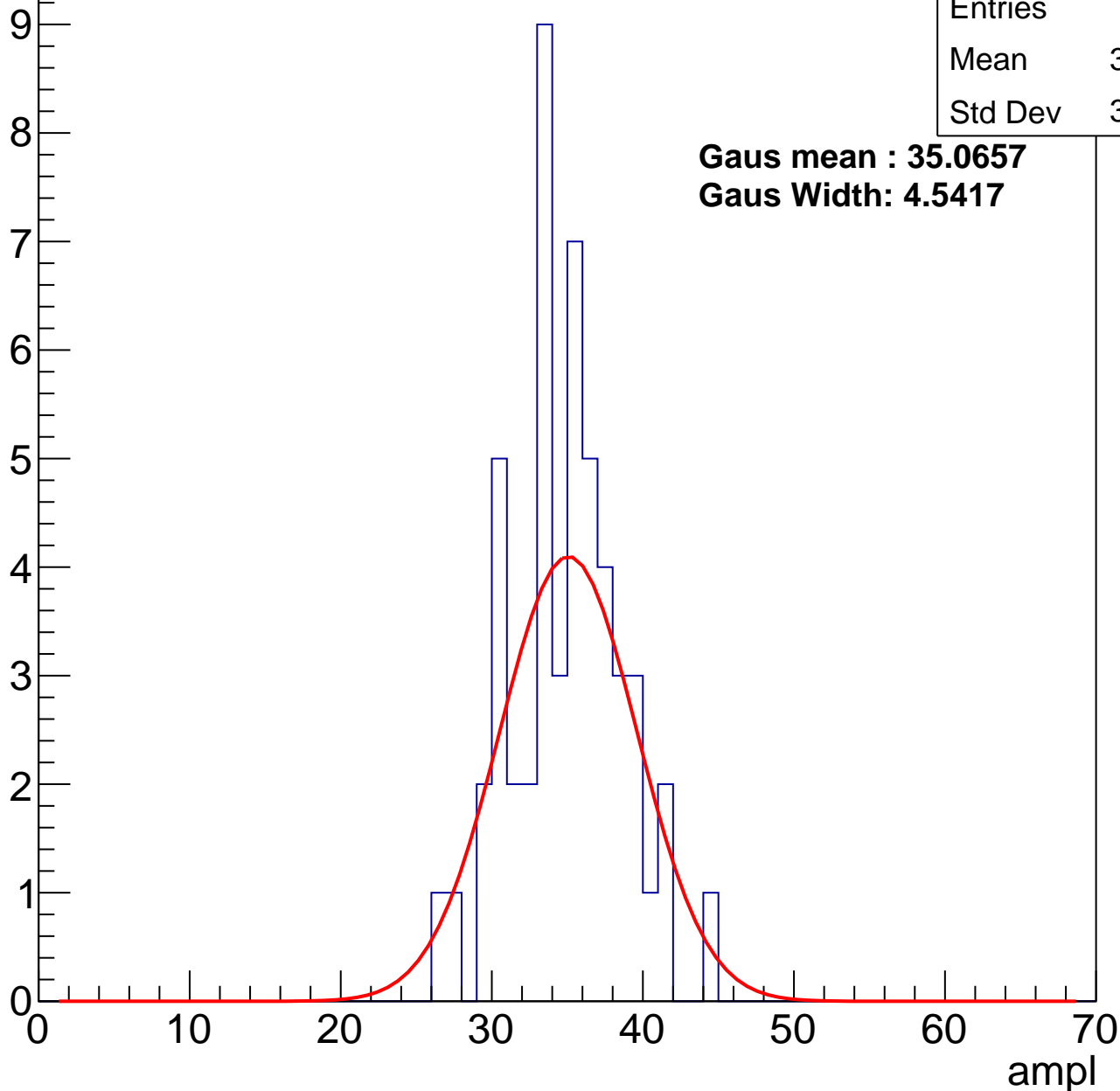
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	34.43
Std Dev	3.674

Gaus mean : 35.0657

Gaus Width: 4.5417



B1L003S, U16-ch60, adc2

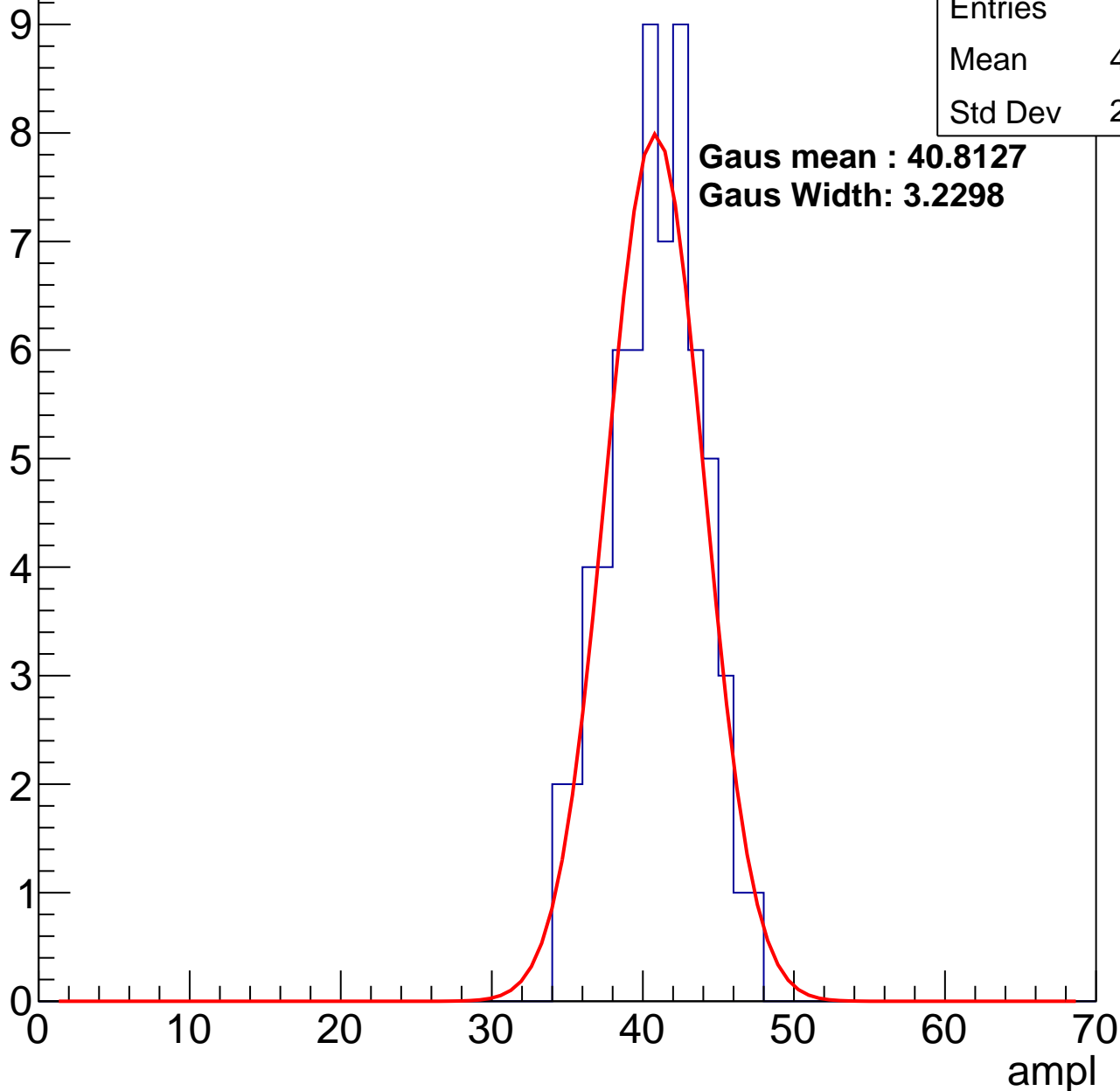
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	40.35
Std Dev	2.989

Gaus mean : 40.8127

Gaus Width: 3.2298



B1L003S, U16-ch60, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	47.3
Std Dev	3.131

Entry

10

8

6

4

2

0

0

10

20

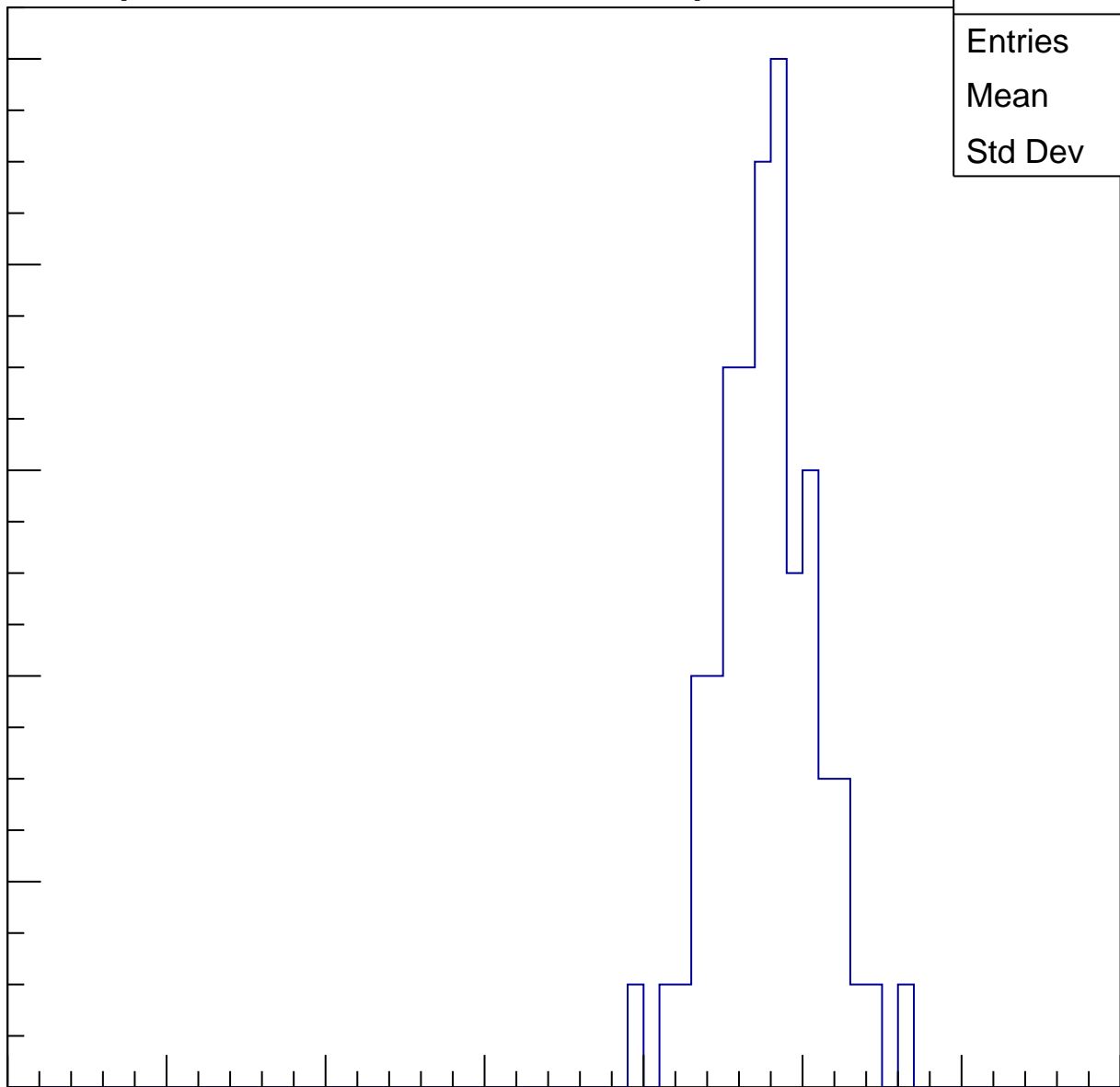
30

40

50

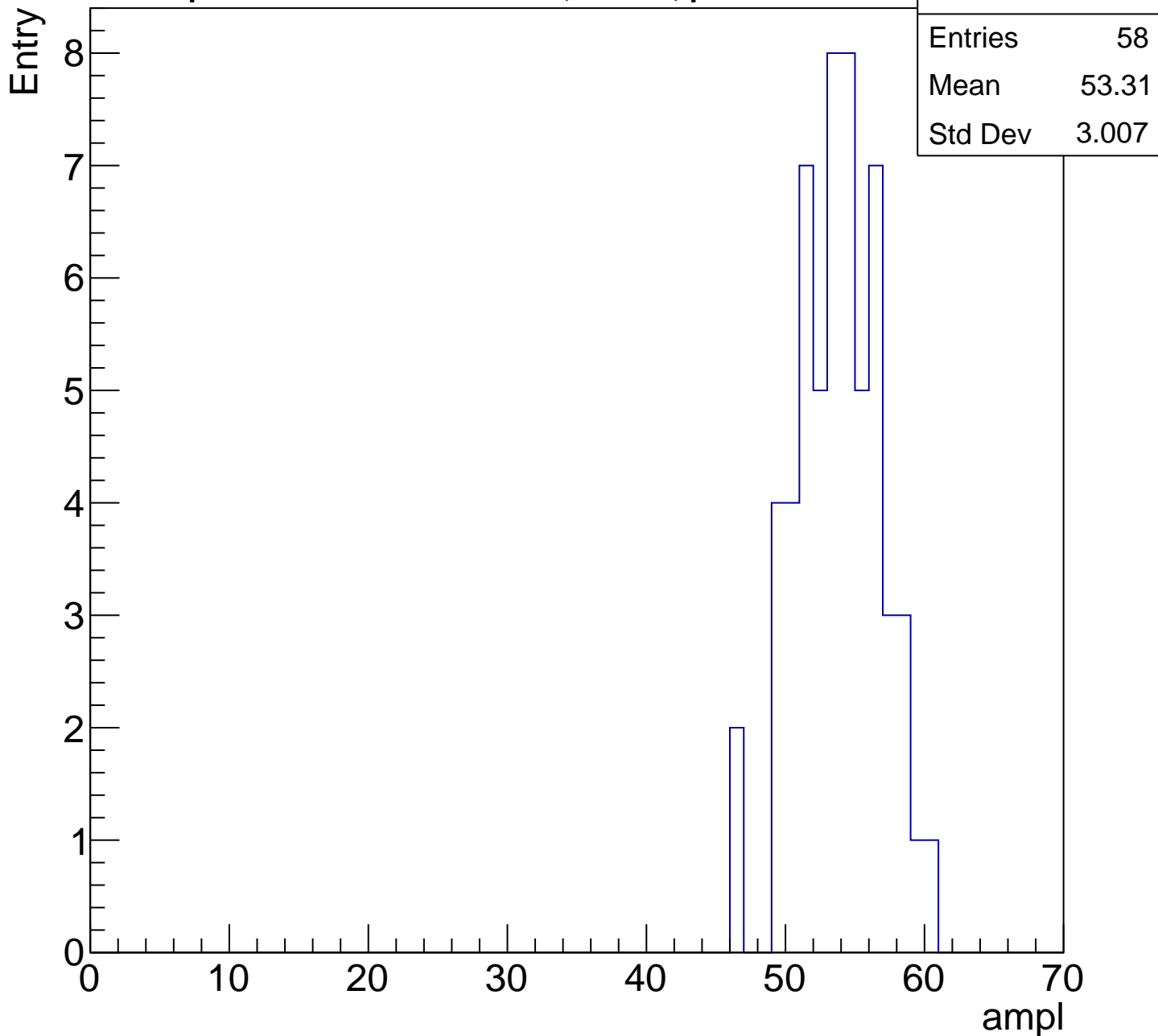
60

ampl



B1L003S, U16-ch60, adc4

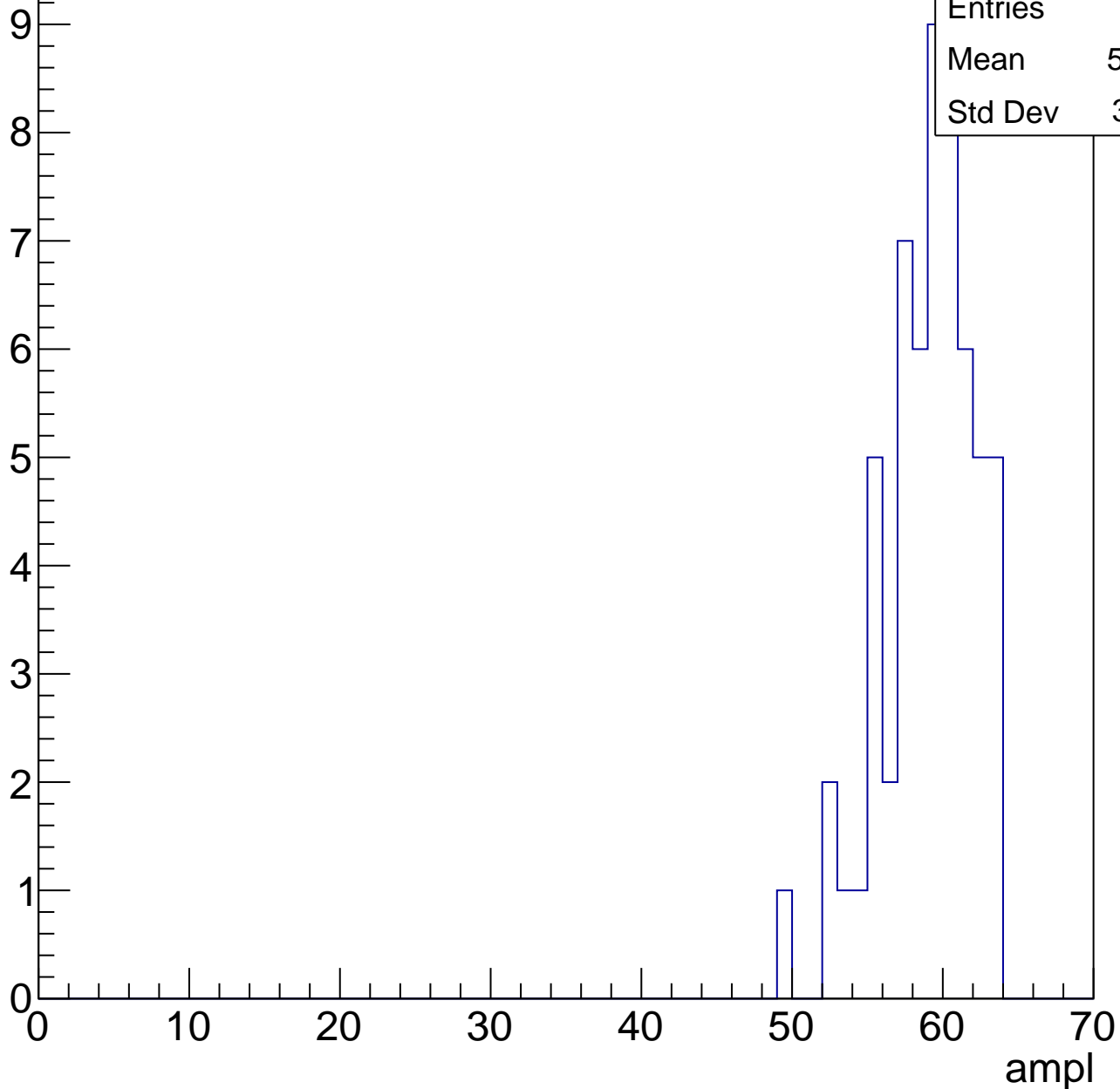
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch60, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

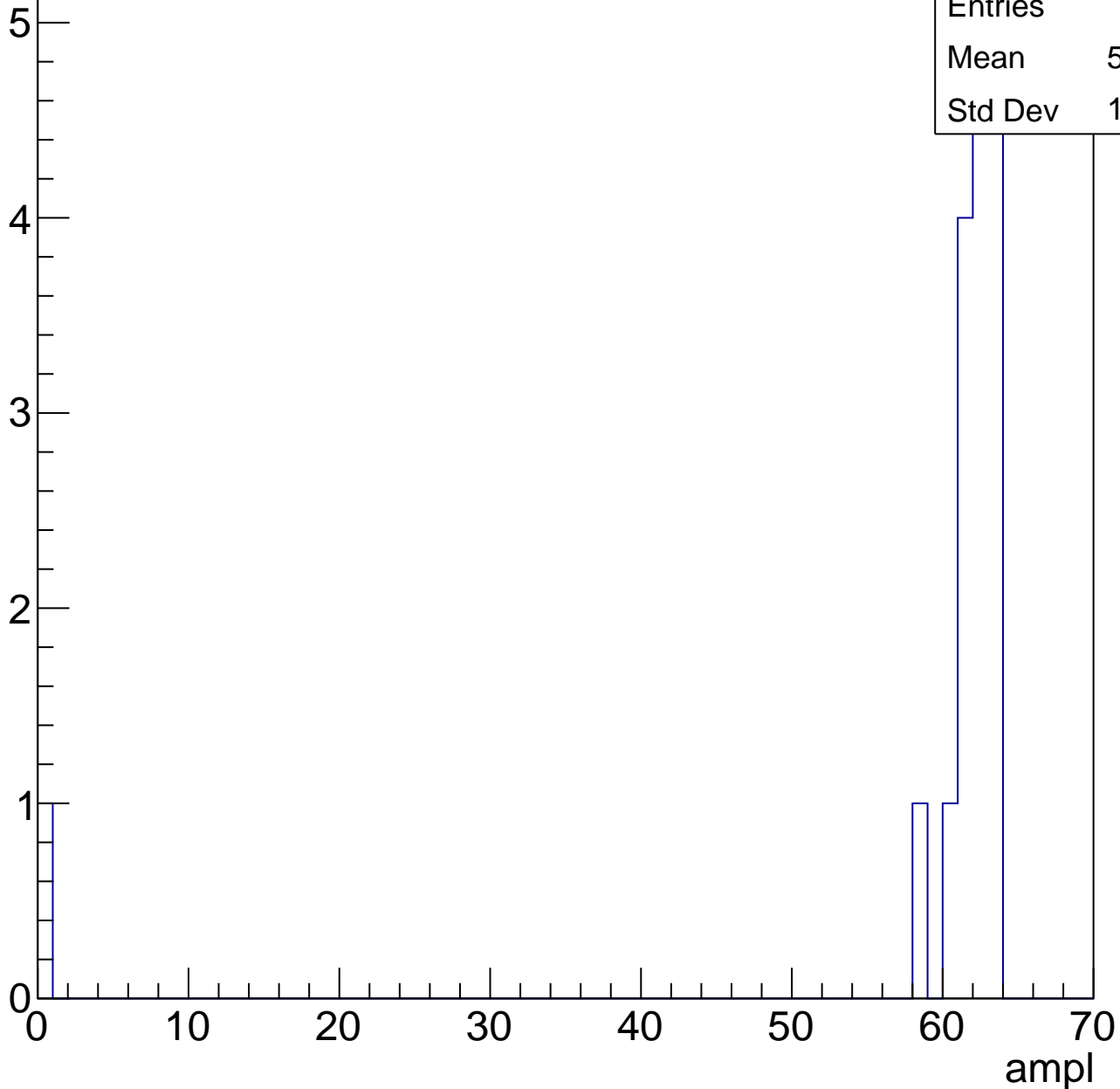


B1L003S, U16-ch60, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.06
Std Dev	14.57



B1L003S, U16-ch60, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch61, adc0

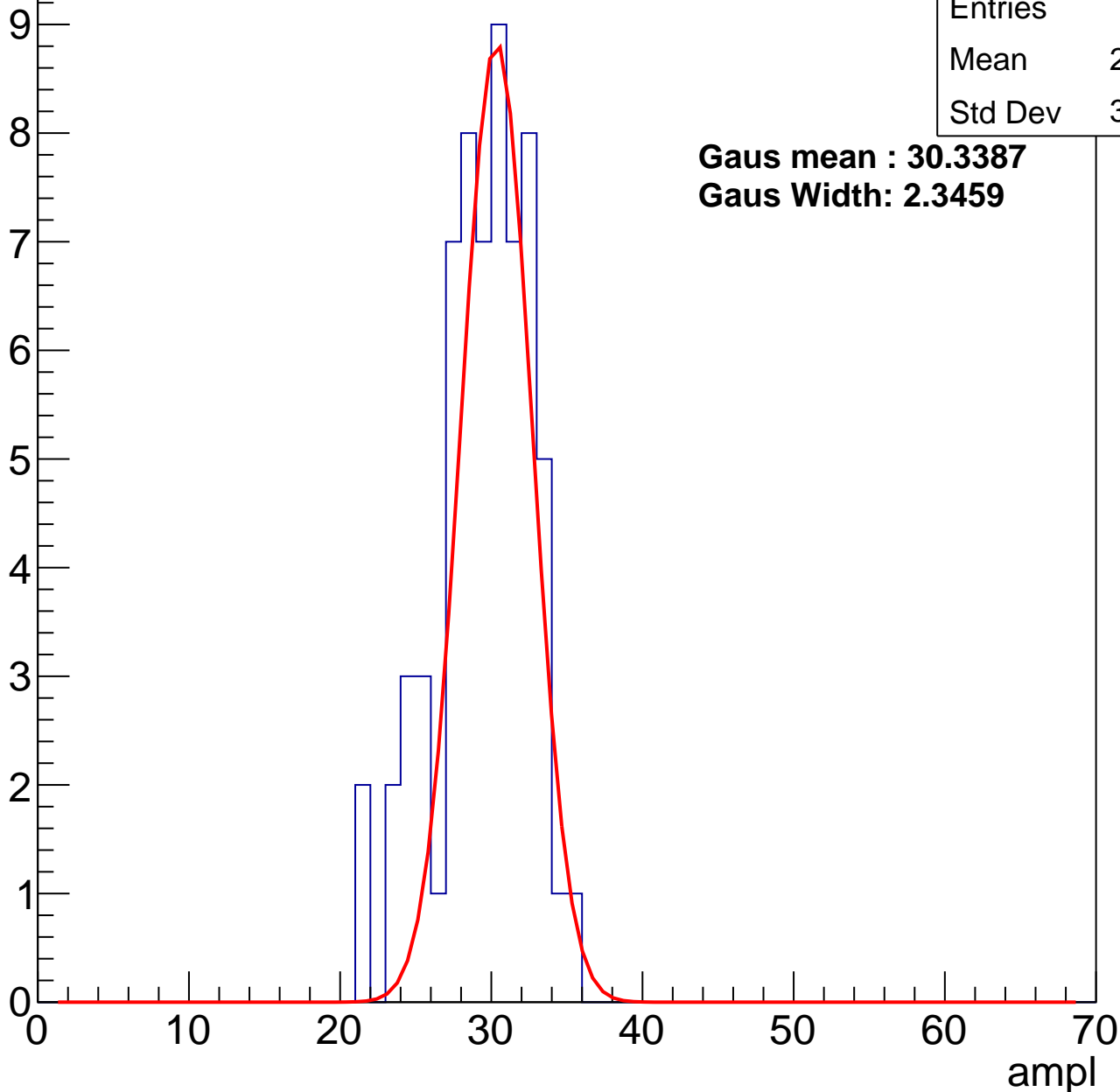
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.97
Std Dev	3.107

Gaus mean : 30.3387

Gaus Width: 2.3459

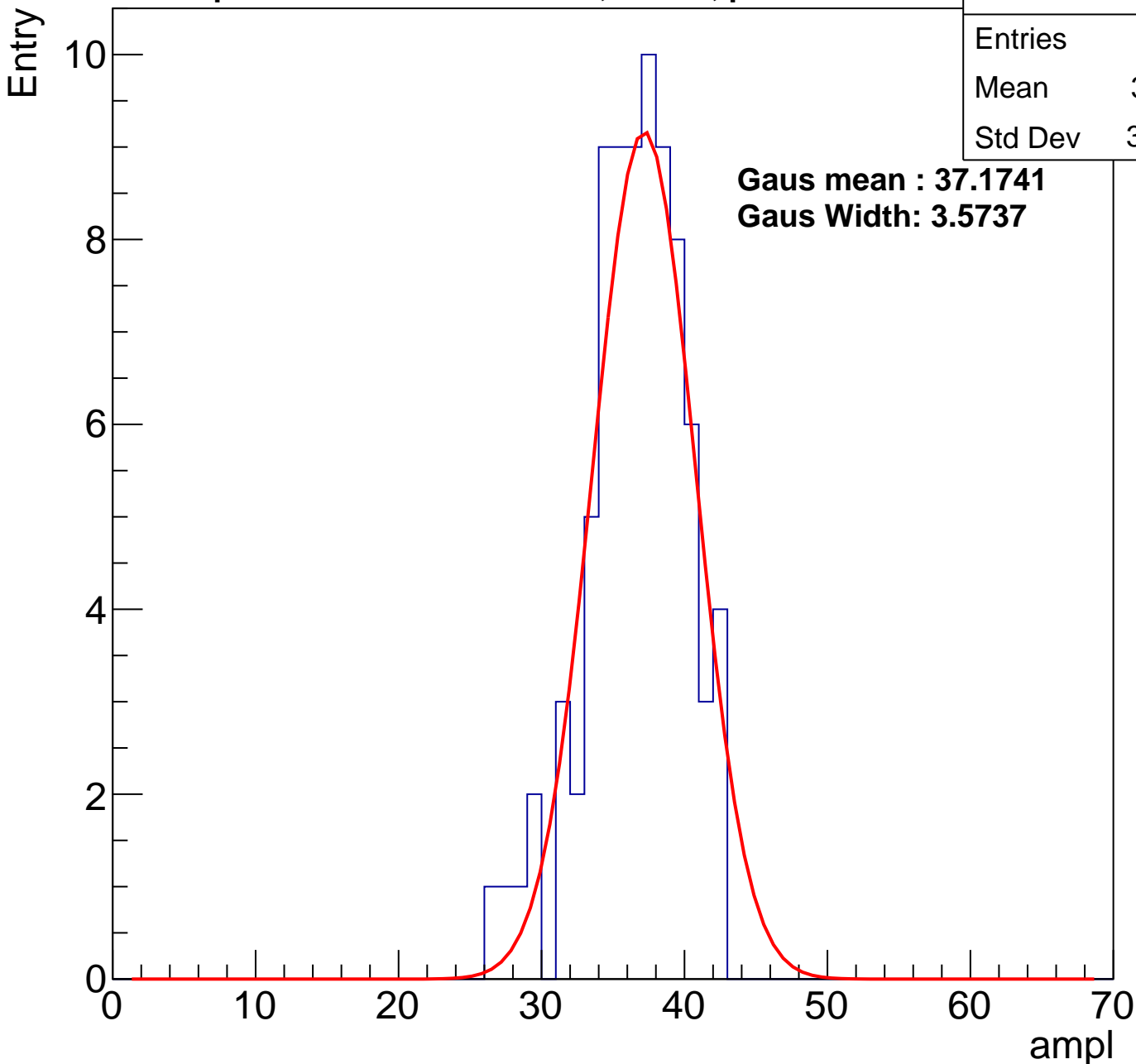


B1L003S, U16-ch61, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	36.11
Std Dev	3.443

Gaus mean : 37.1741
Gaus Width: 3.5737



B1L003S, U16-ch61, adc2

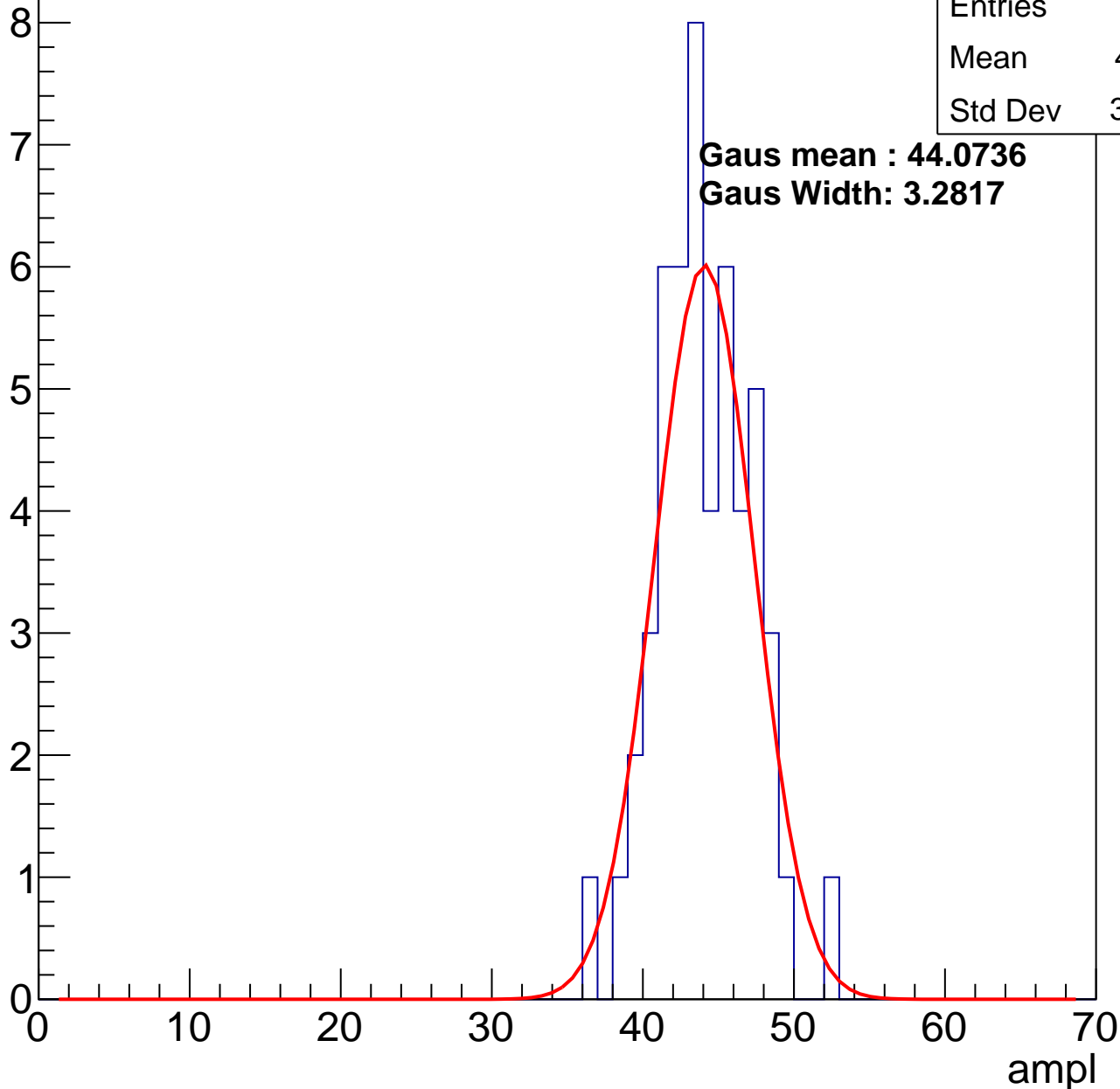
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.61
Std Dev	3.068

Gaus mean : 44.0736

Gaus Width: 3.2817

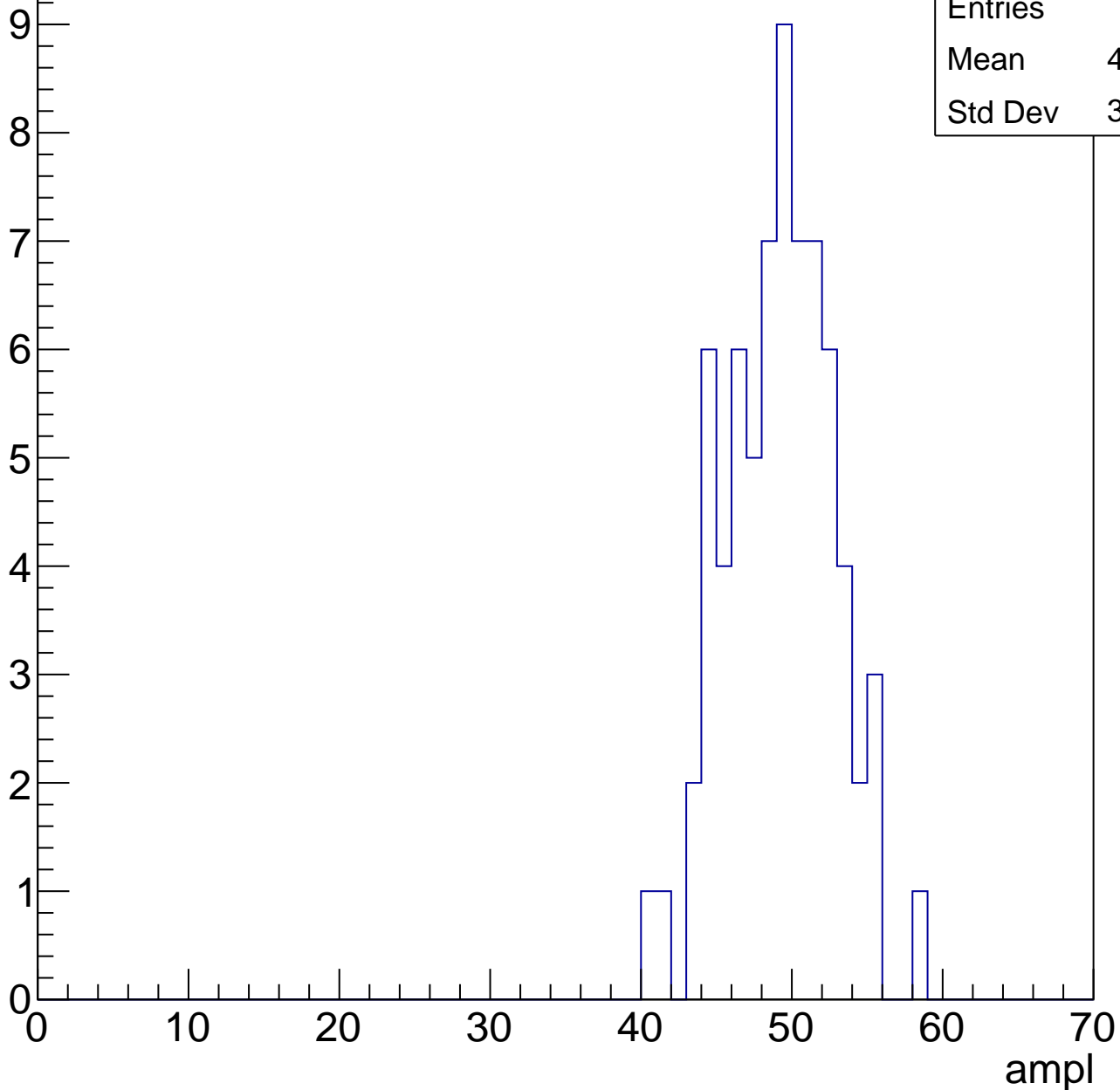


B1L003S, U16-ch61, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	48.75
Std Dev	3.559

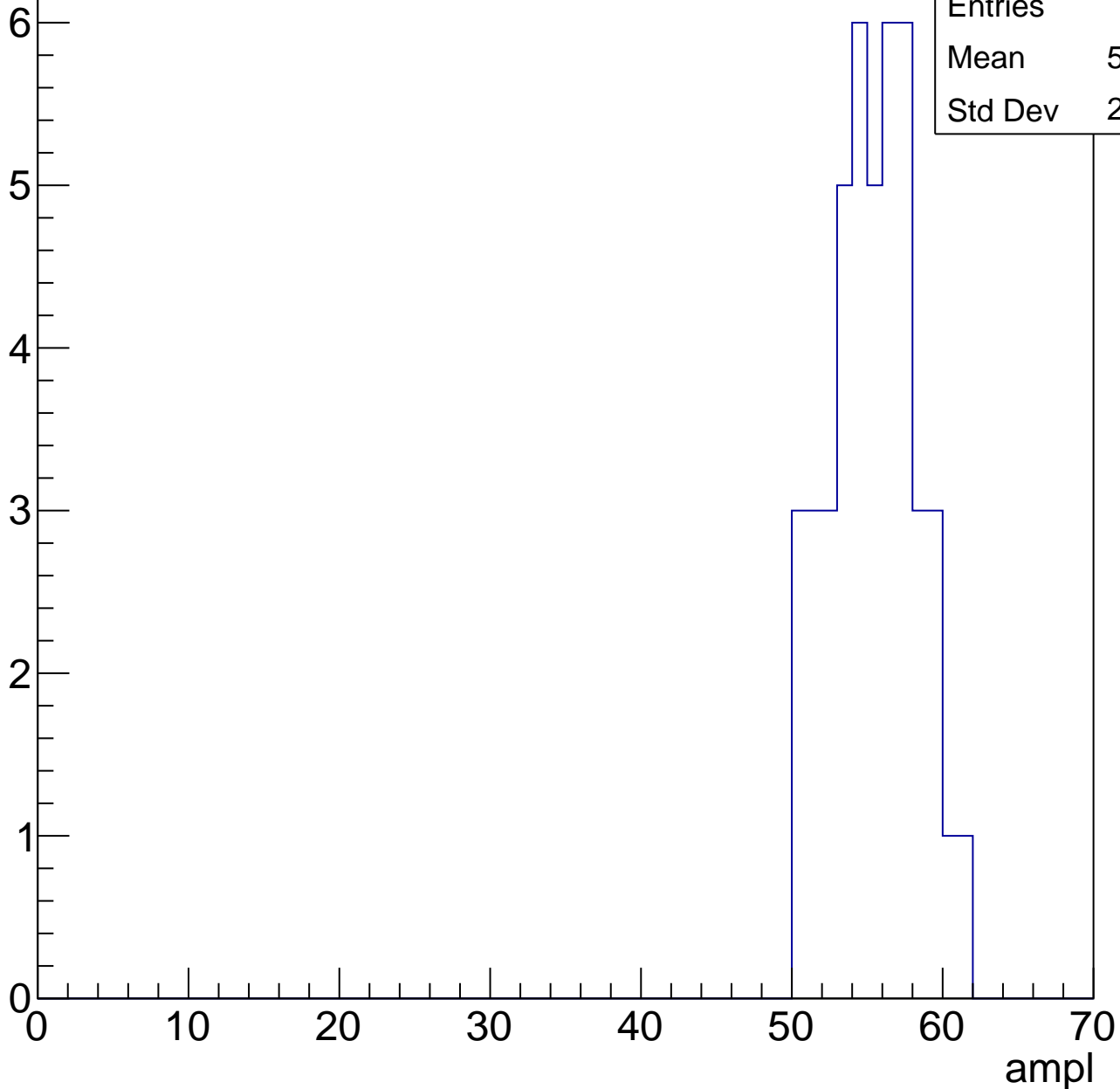


B1L003S, U16-ch61, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	54.96
Std Dev	2.756



B1L003S, U16-ch61, adc5

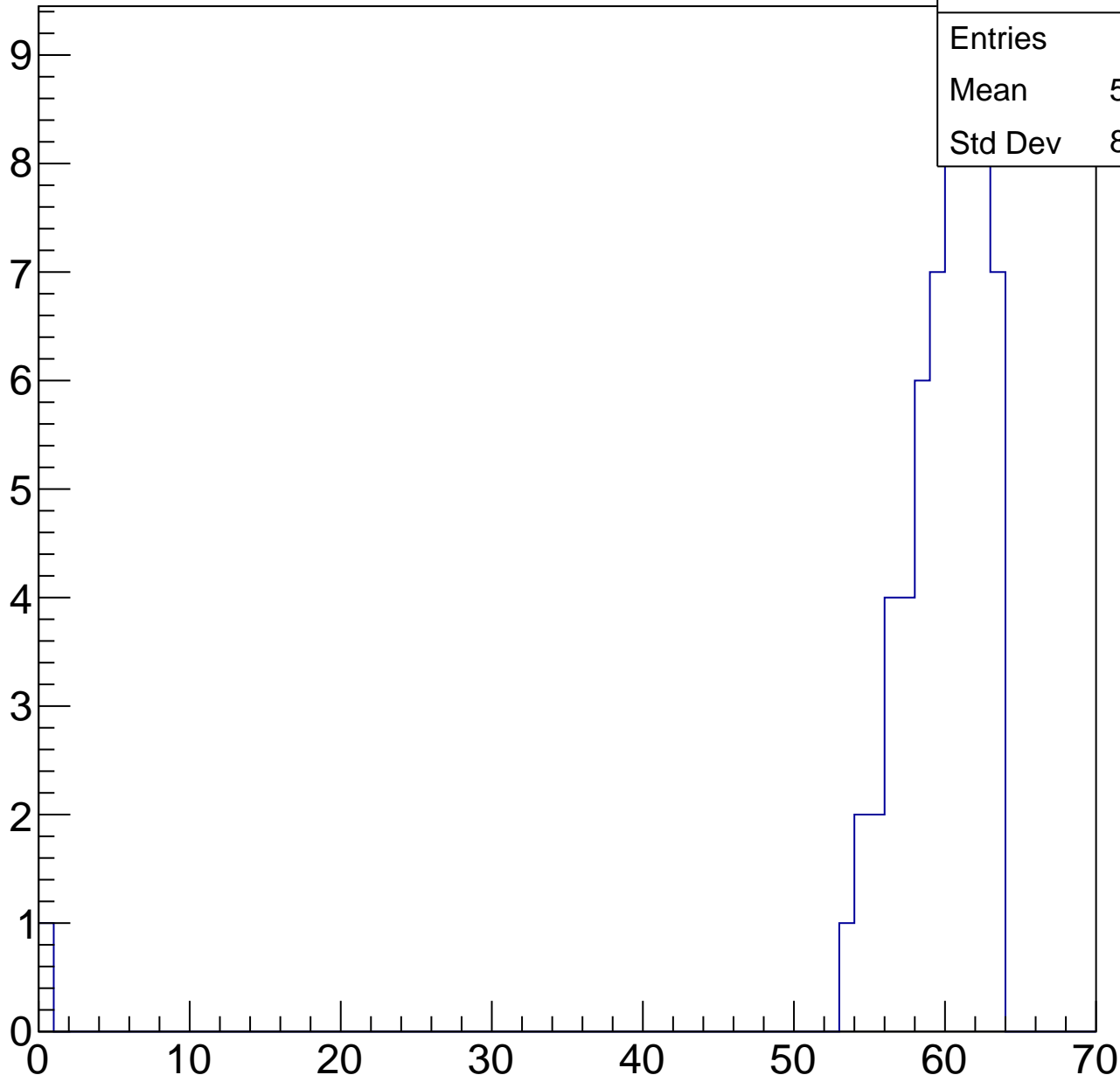
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.47
Std Dev	8.098

ampl



B1L003S, U16-ch61, adc6

calib_packv5_042523_0143.root, FC#13, port D2

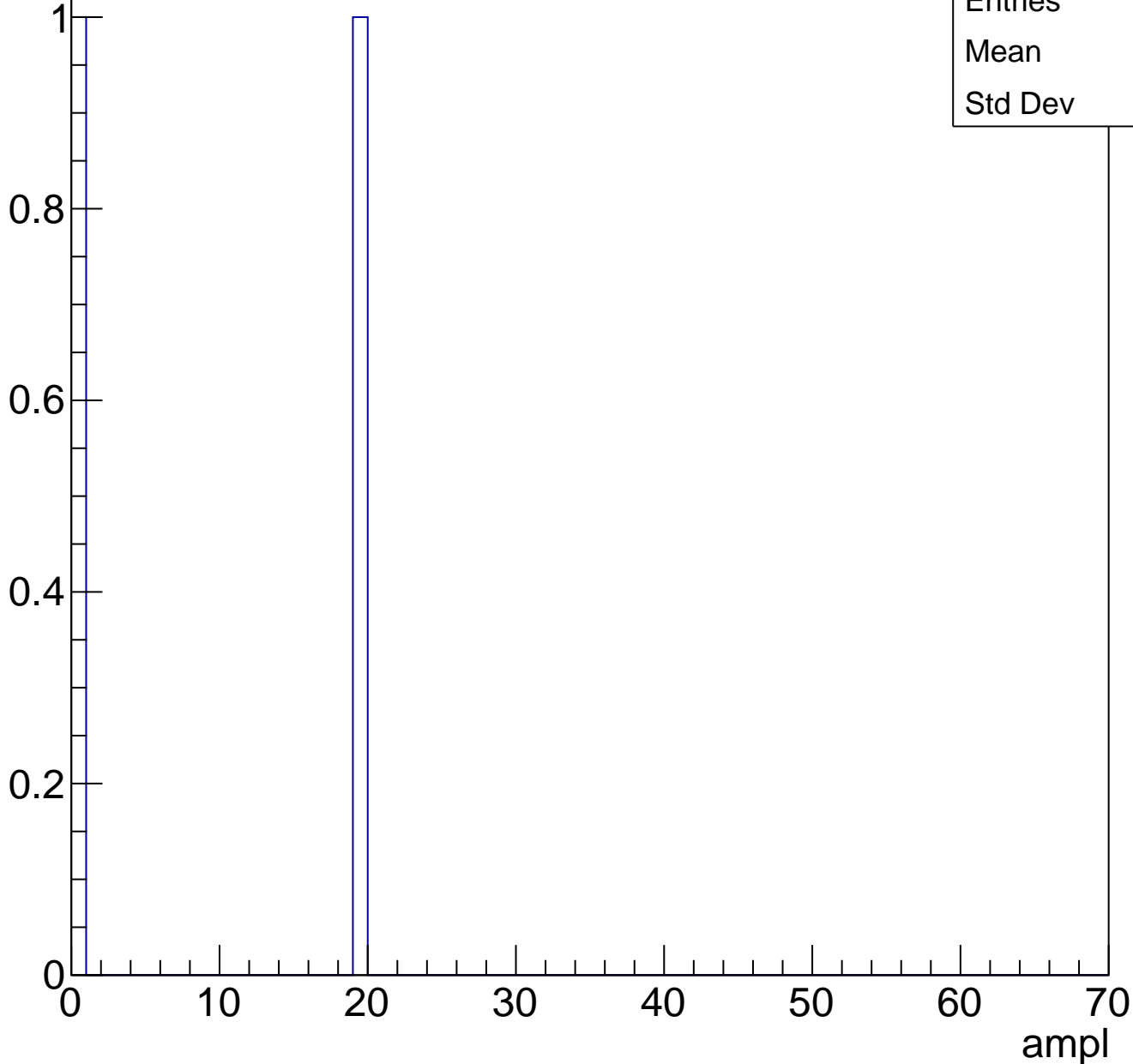
Entry



B1L003S, U16-ch61, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L003S, U16-ch62, adc0

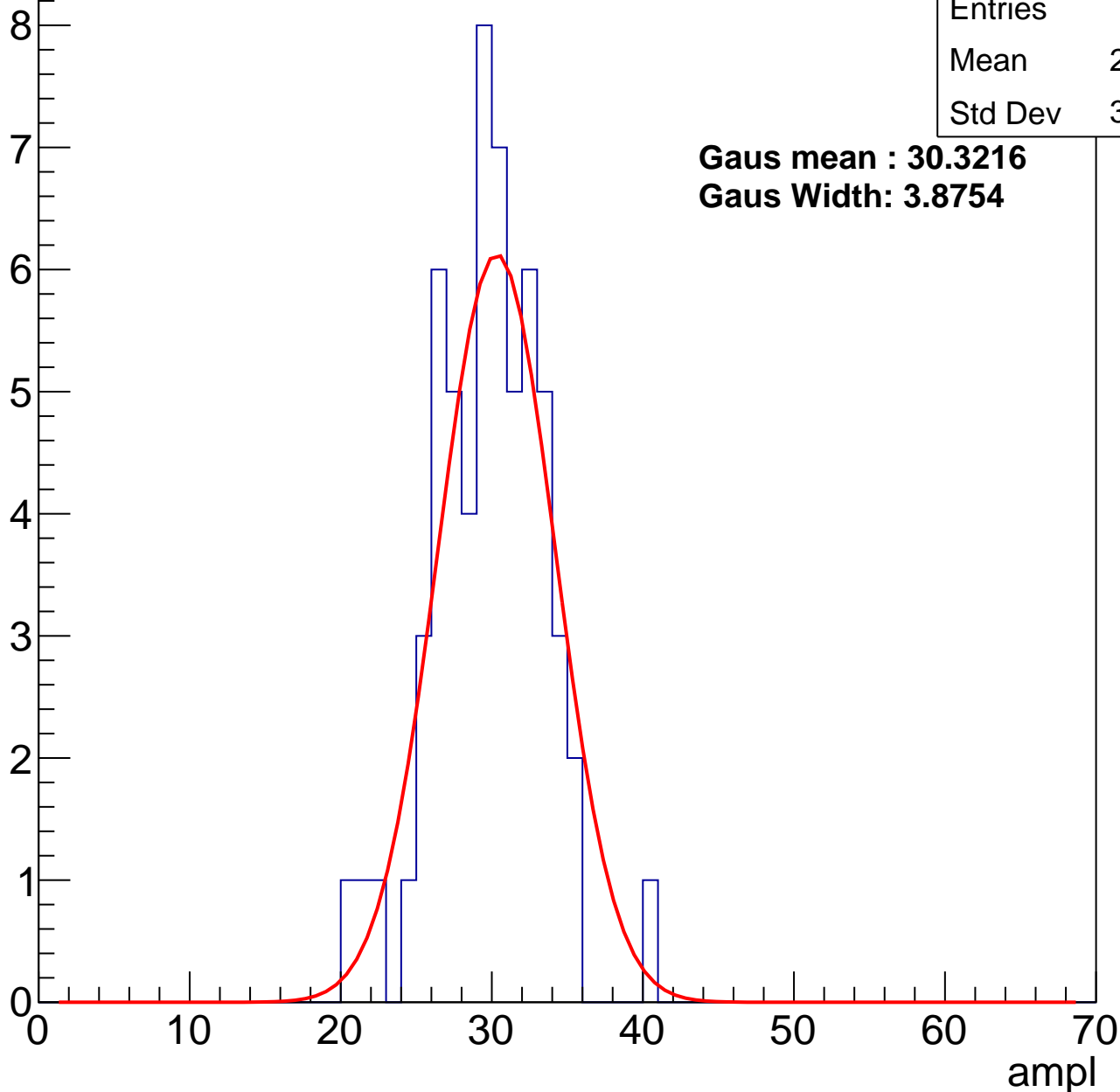
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.34
Std Dev	3.606

Gaus mean : 30.3216

Gaus Width: 3.8754



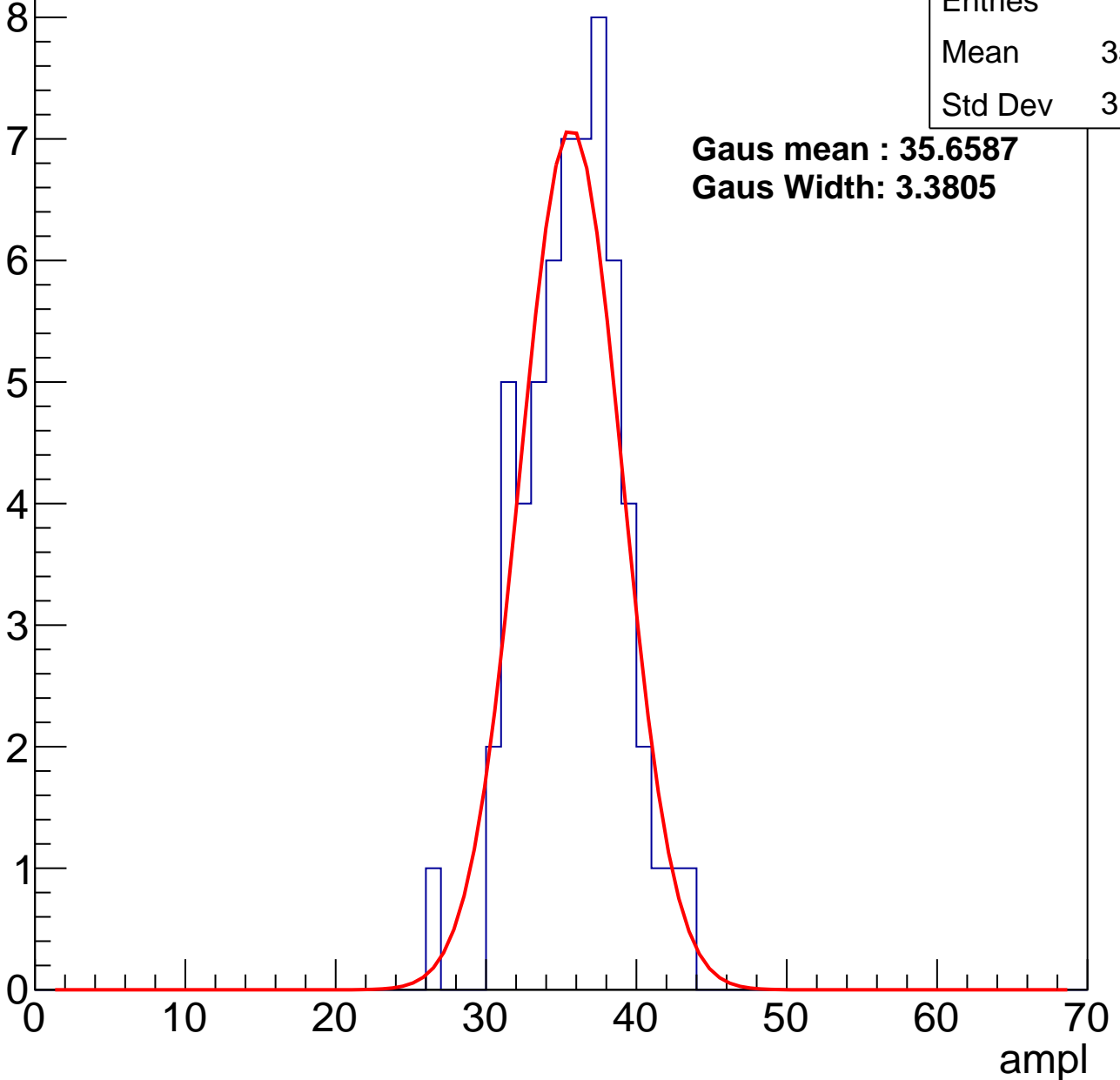
B1L003S, U16-ch62, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.35
Std Dev	3.219

Gaus mean : 35.6587
Gaus Width: 3.3805



B1L003S, U16-ch62, adc2

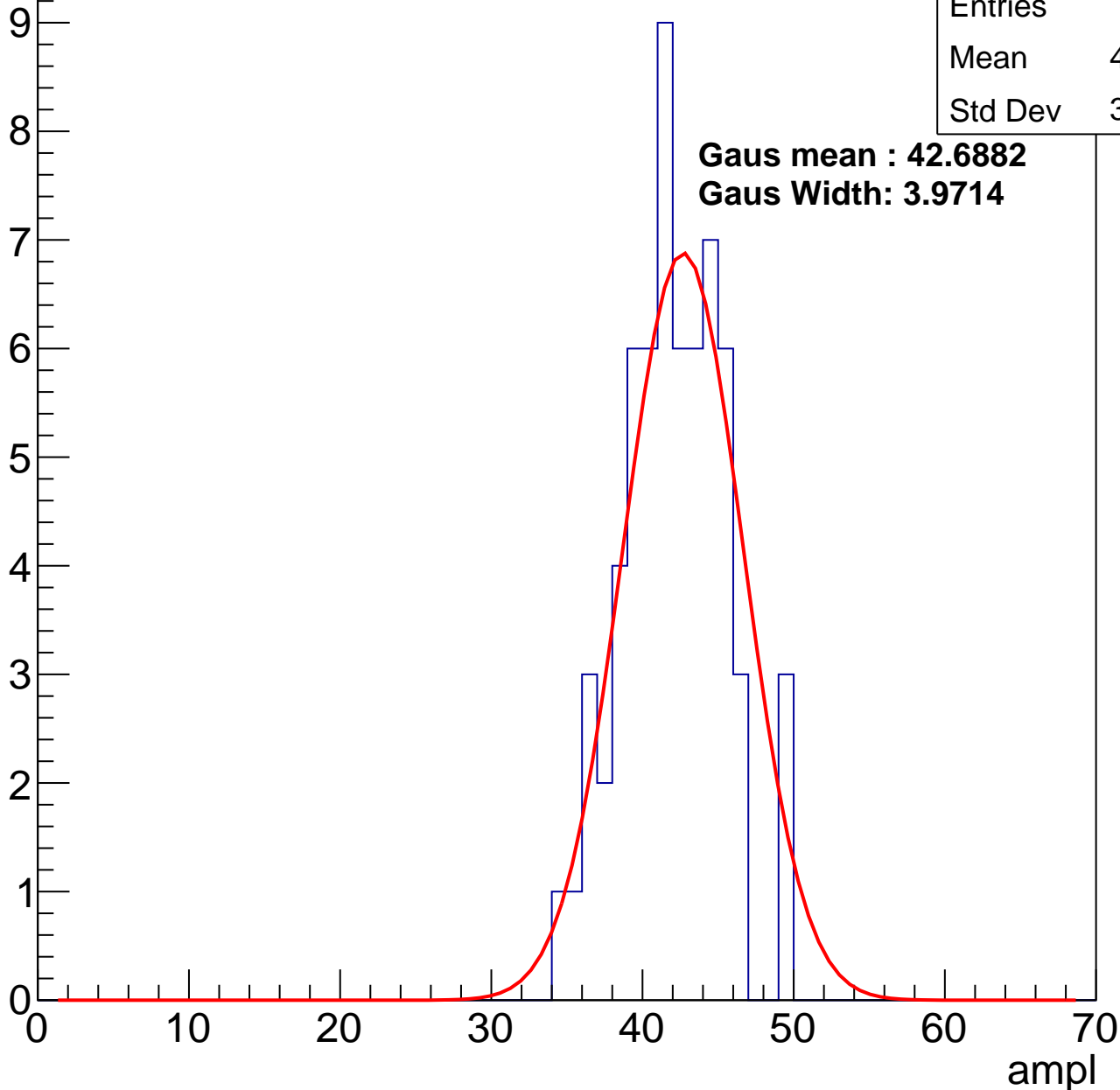
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.57
Std Dev	3.318

Gaus mean : 42.6882

Gaus Width: 3.9714

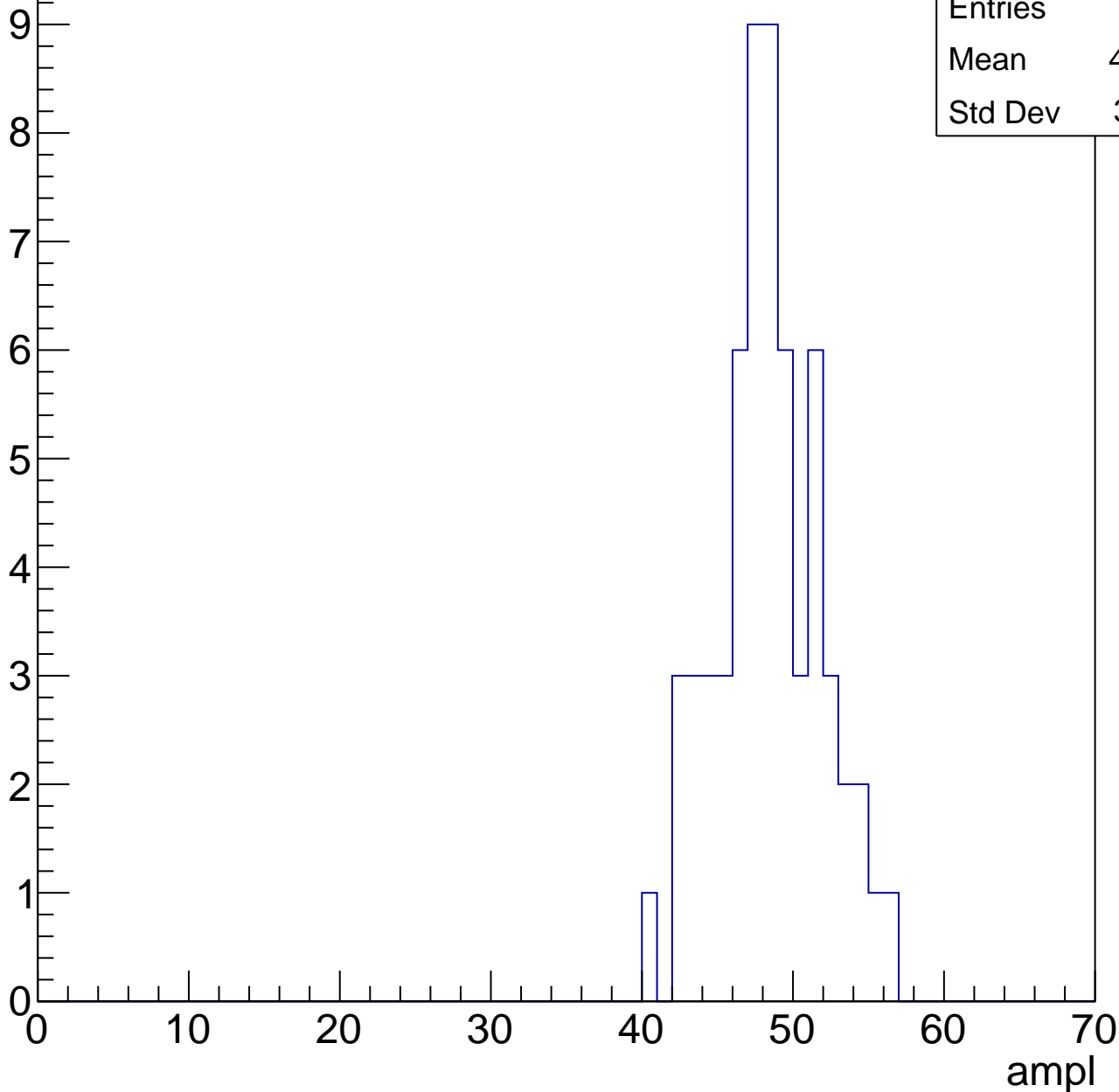


B1L003S, U16-ch62, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	47.93
Std Dev	3.411

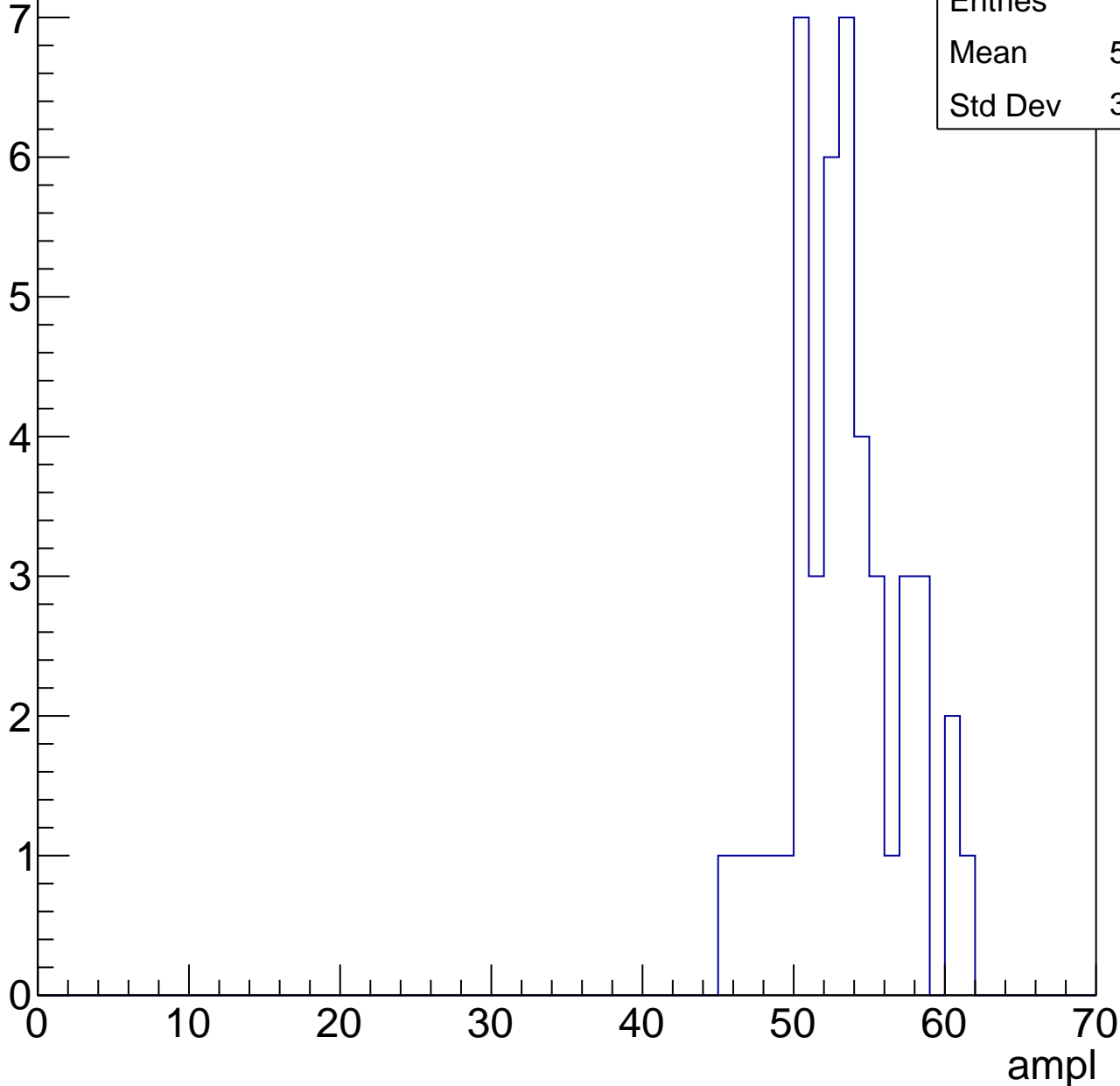


B1L003S, U16-ch62, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	52.98
Std Dev	3.593

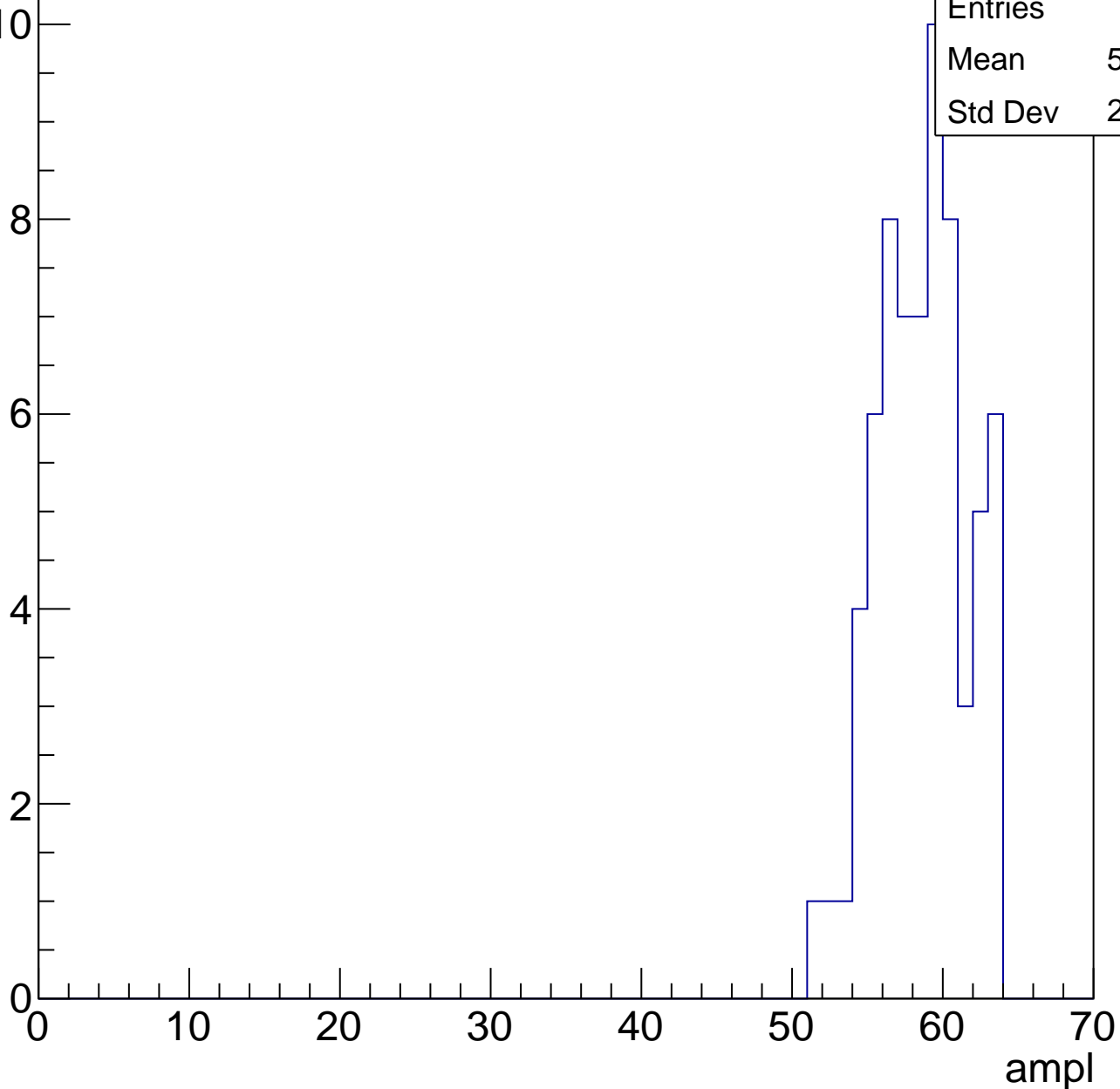


B1L003S, U16-ch62, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	58.15
Std Dev	2.903

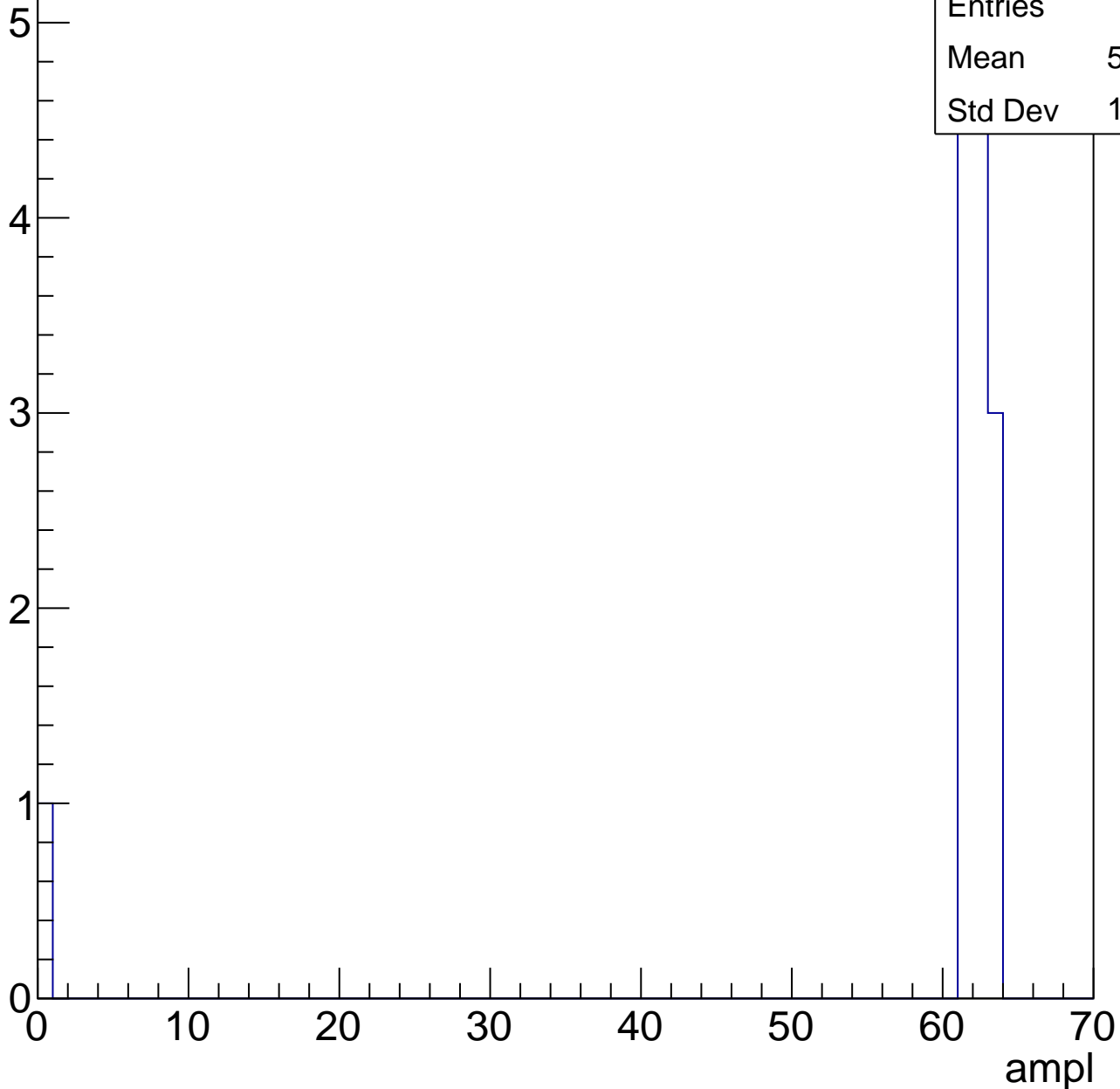


B1L003S, U16-ch62, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

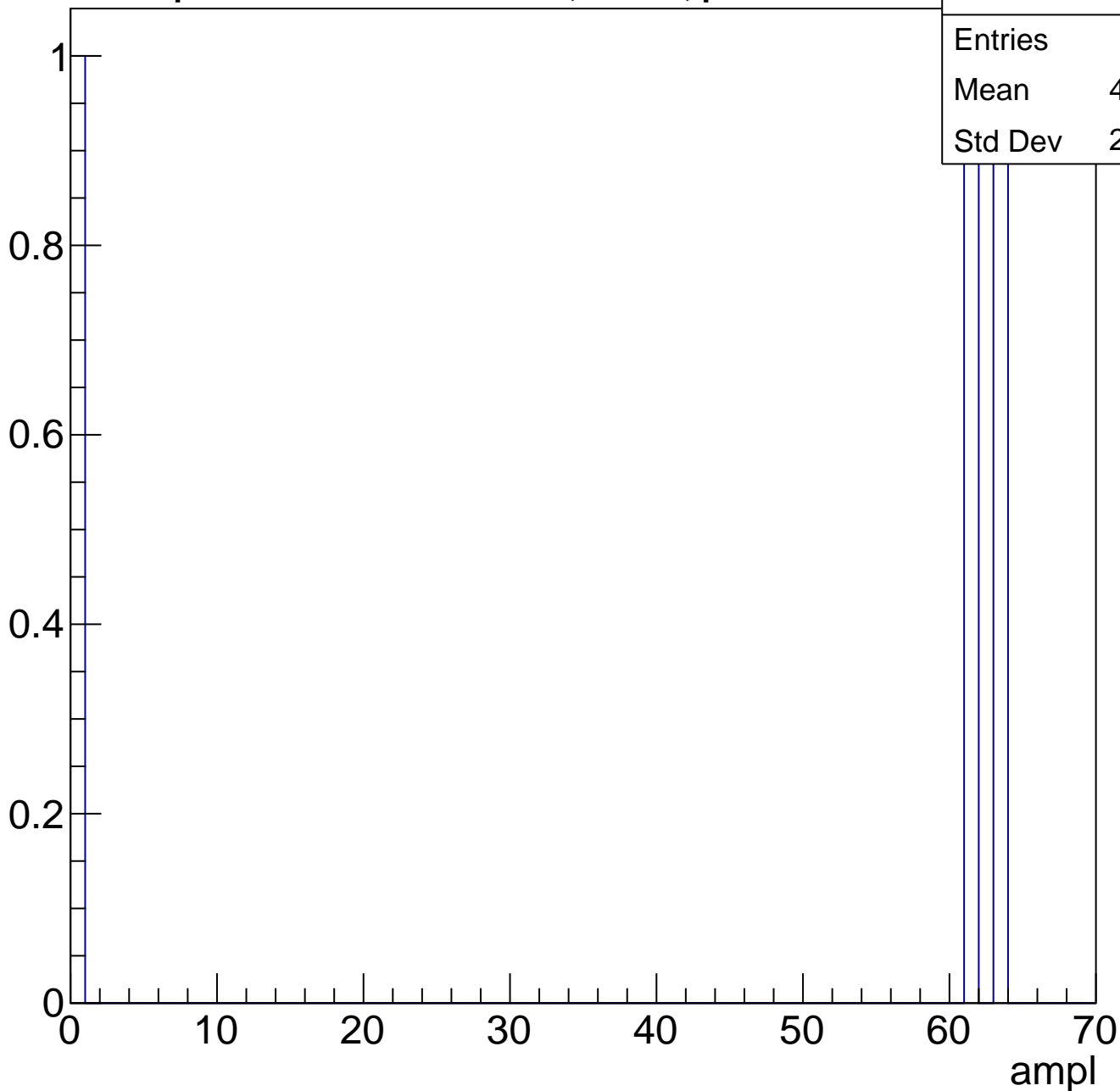
Entries	14
Mean	57.43
Std Dev	15.95



B1L003S, U16-ch62, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch63, adc0

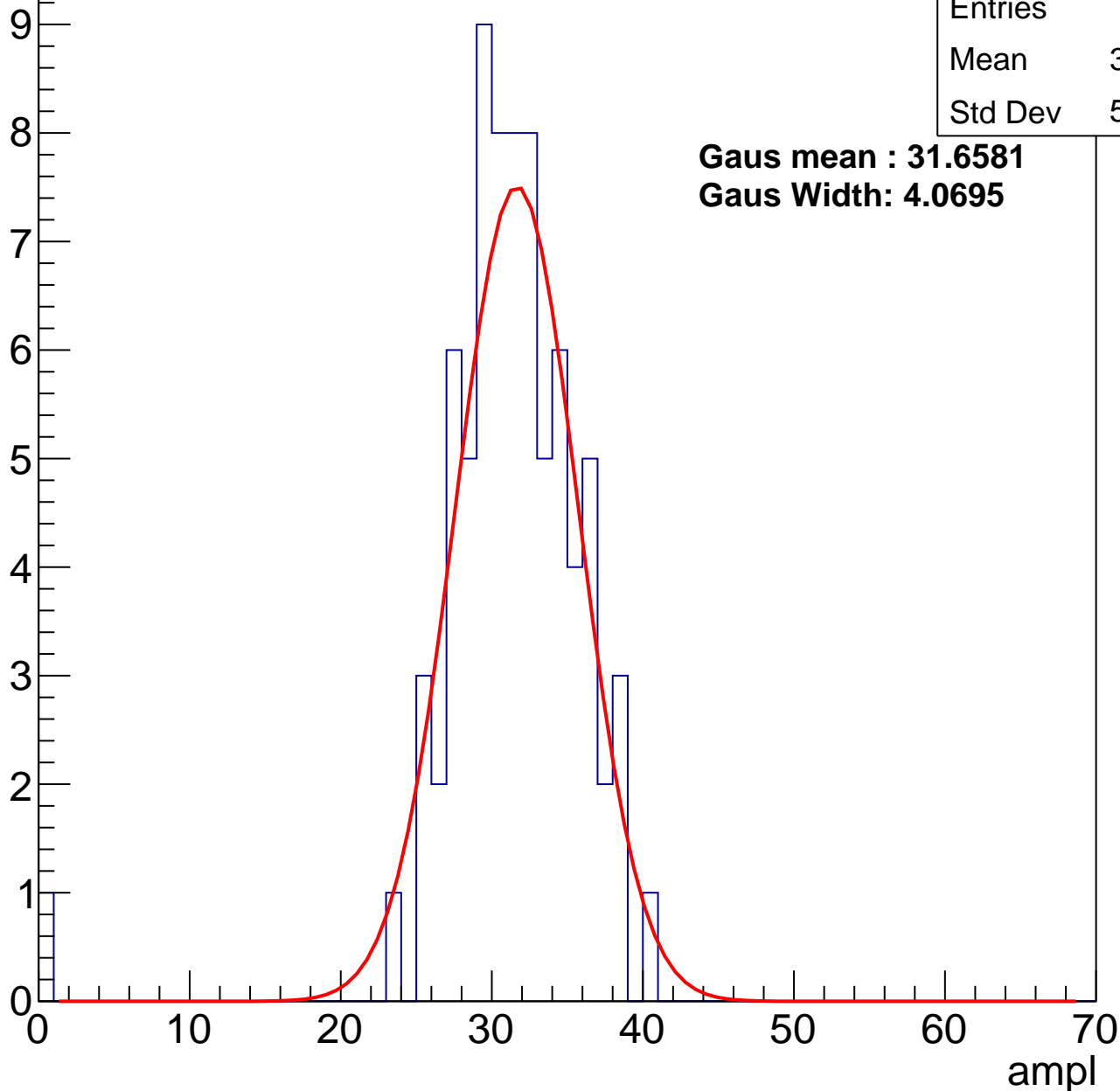
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	30.83
Std Dev	5.015

Gaus mean : 31.6581

Gaus Width: 4.0695



B1L003S, U16-ch63, adc1

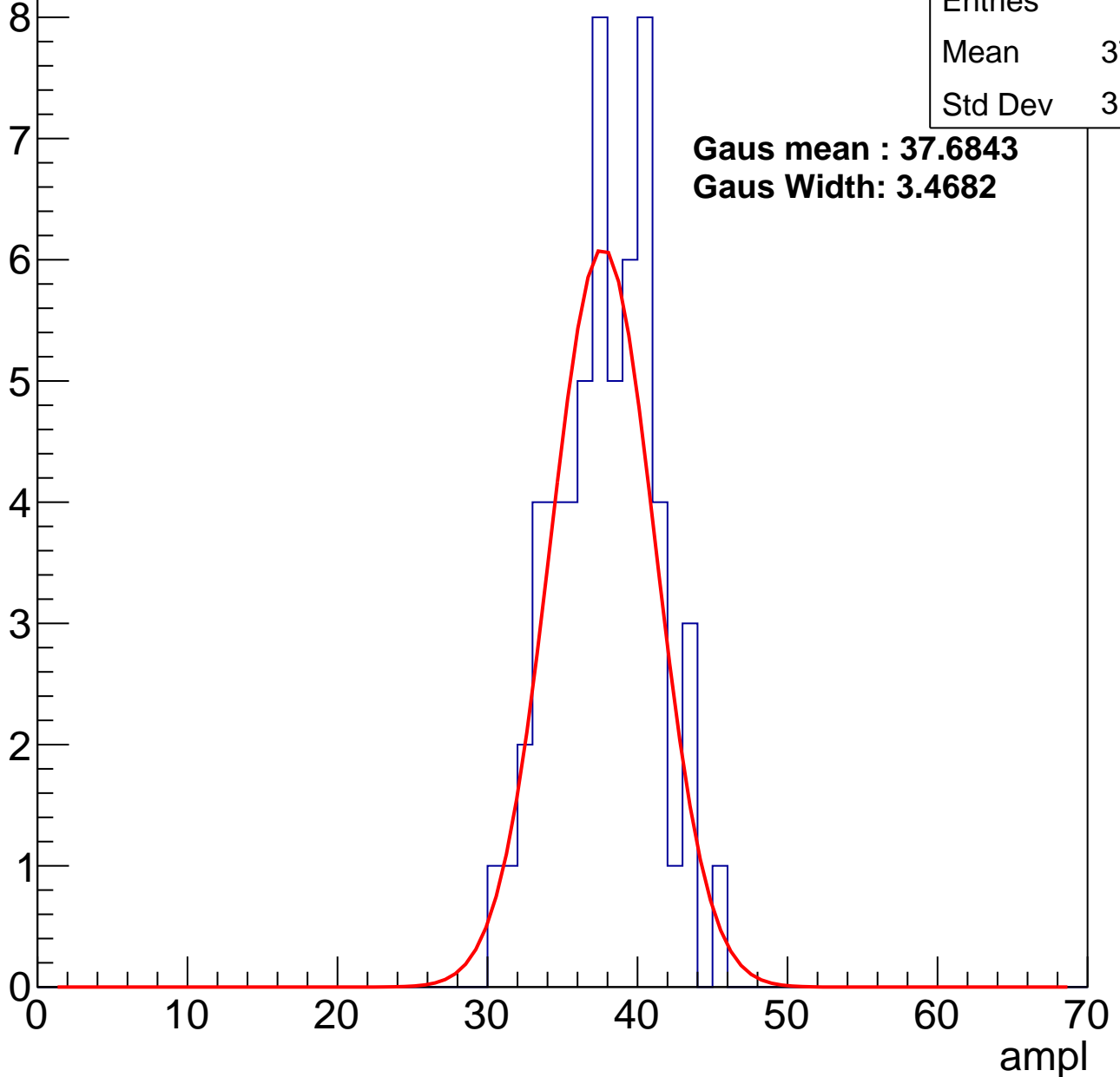
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	37.42
Std Dev	3.266

Gaus mean : 37.6843

Gaus Width: 3.4682



B1L003S, U16-ch63, adc2

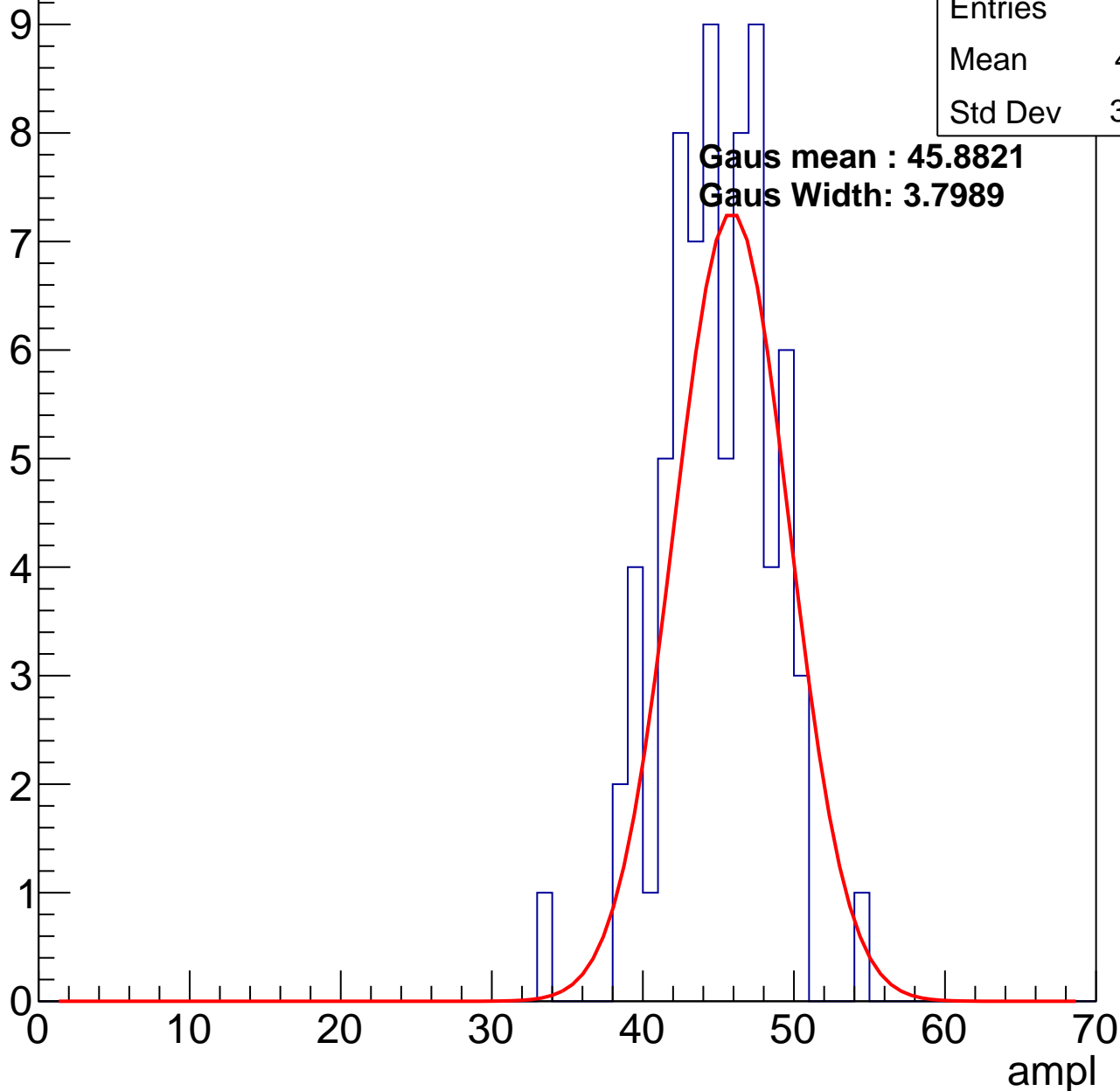
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	44.51
Std Dev	3.539

Gaus mean : 45.8821

Gaus Width: 3.7989

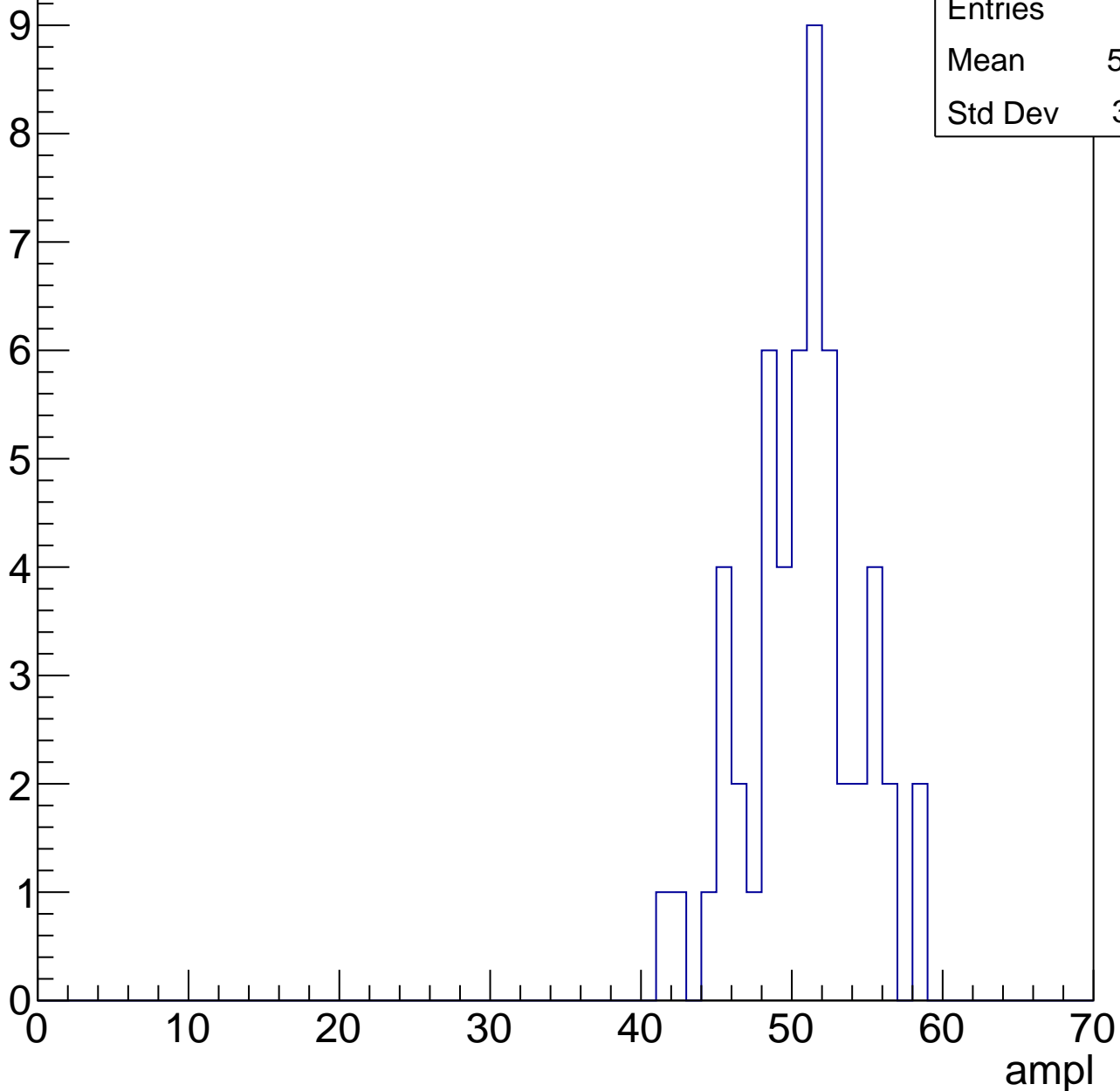


B1L003S, U16-ch63, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

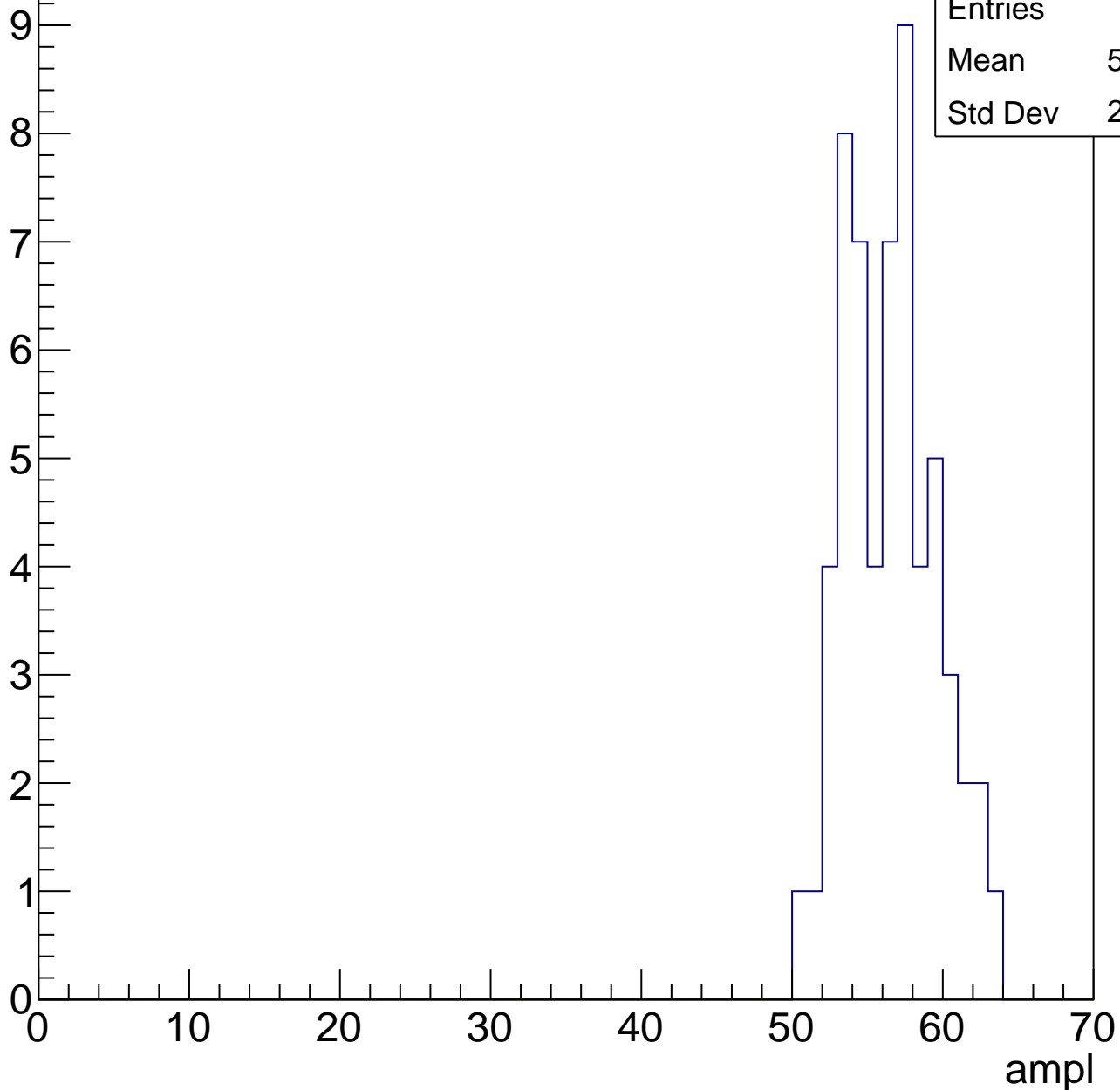
Entries	53
Mean	50.25
Std Dev	3.711



B1L003S, U16-ch63, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



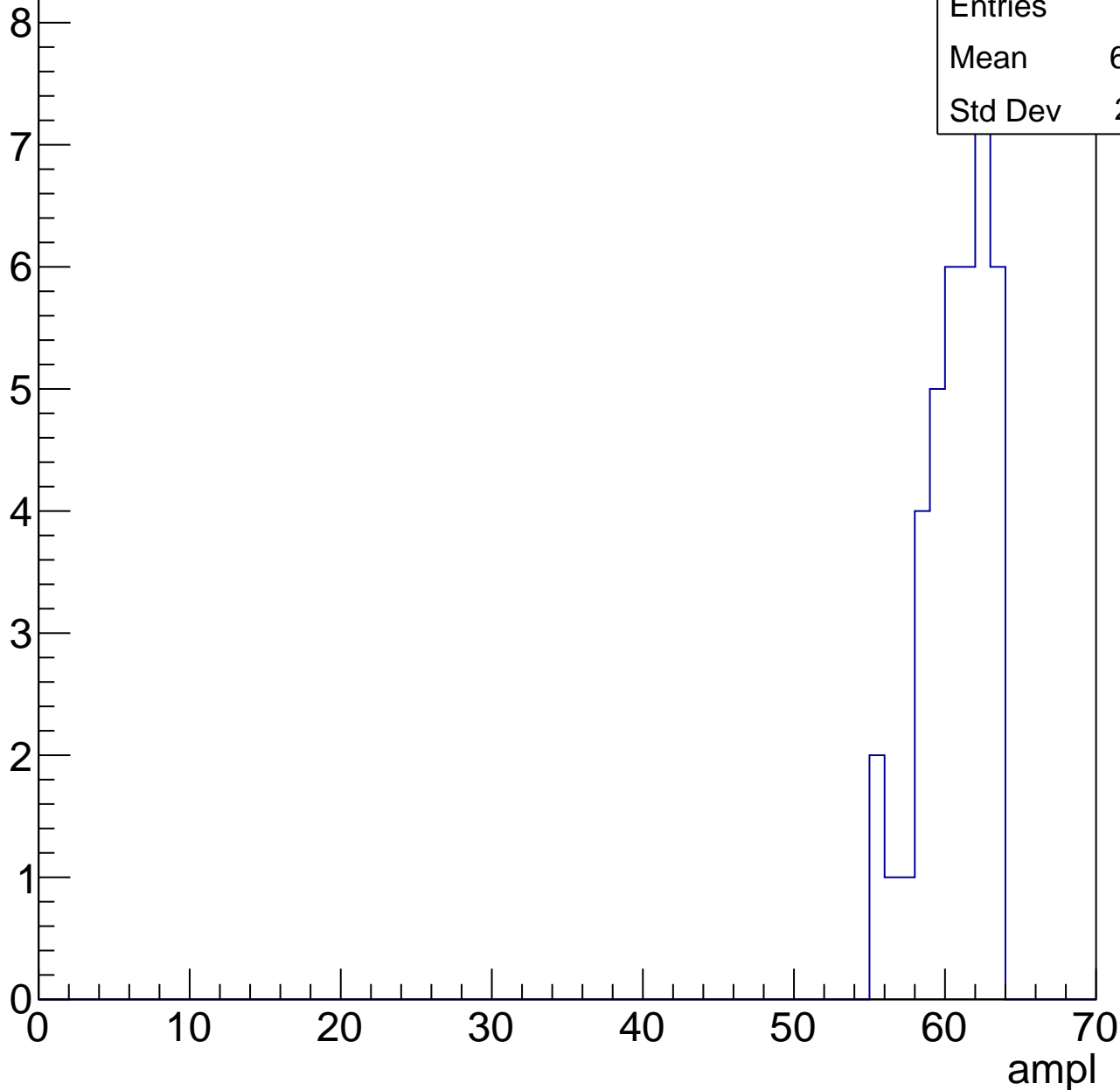
Entries	58
Mean	56.07
Std Dev	2.988

B1L003S, U16-ch63, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

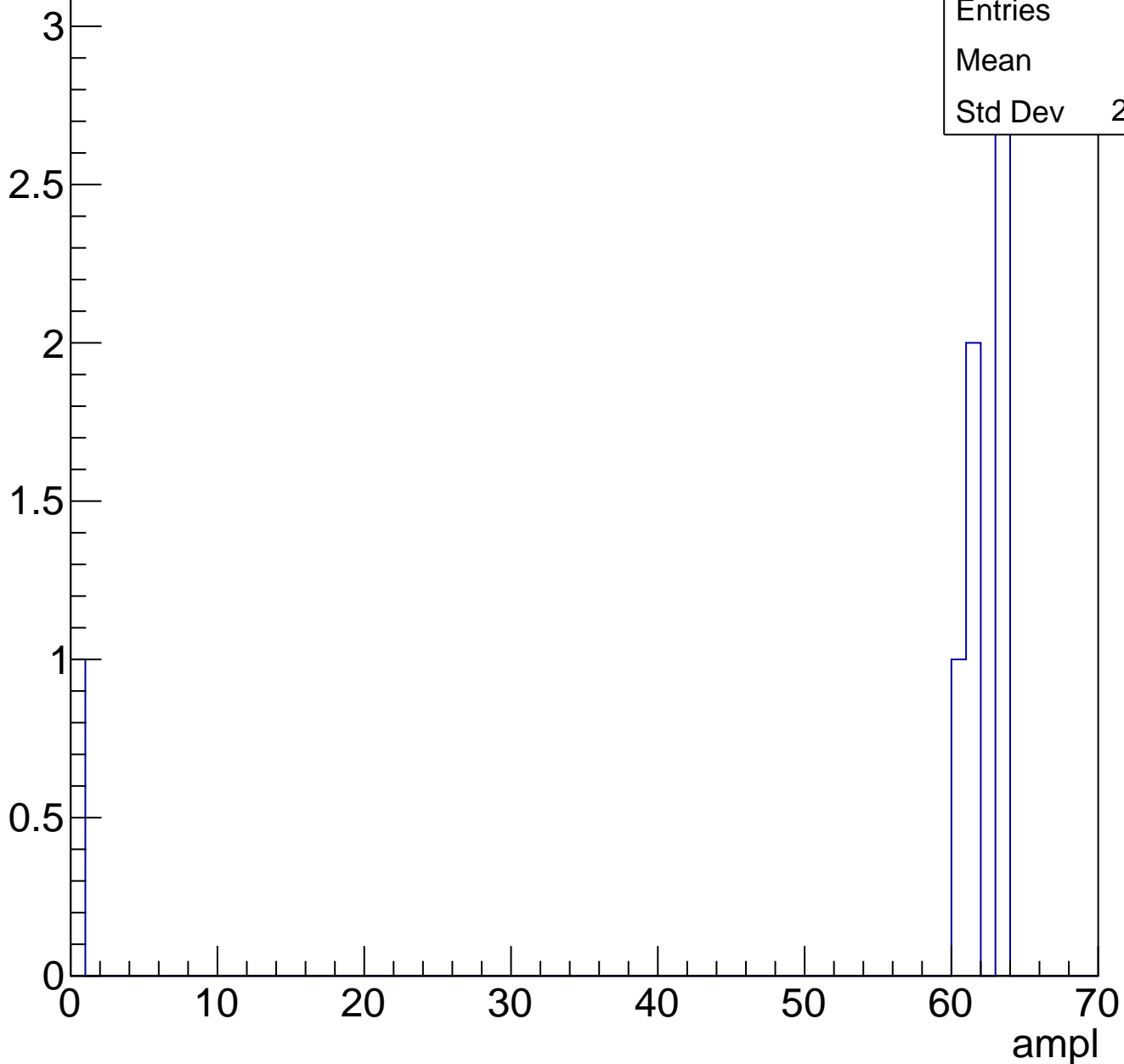
Entries	39
Mean	60.26
Std Dev	2.181



B1L003S, U16-ch63, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch63, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch64, adc0

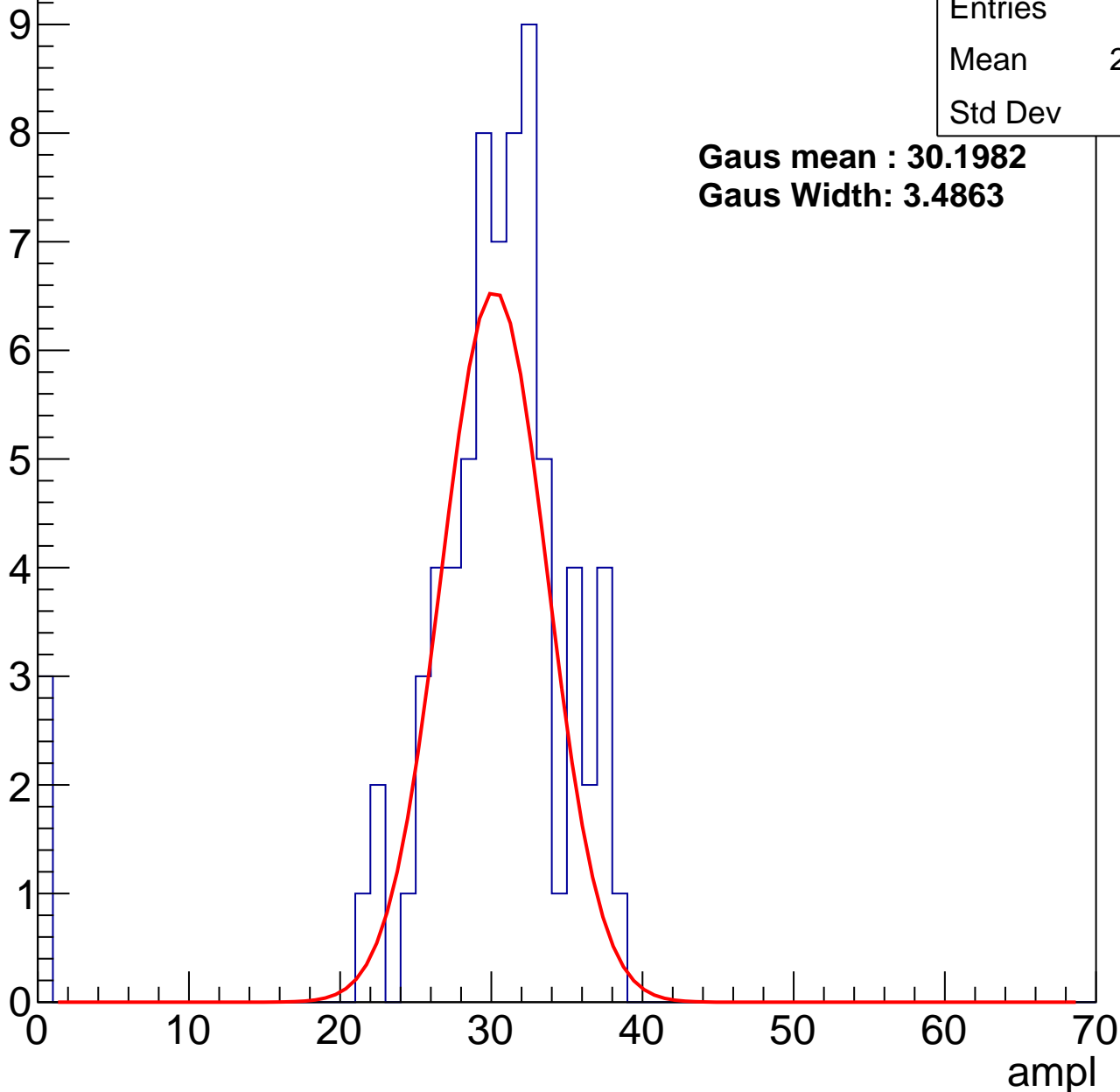
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.04
Std Dev	7.09

Gaus mean : 30.1982

Gaus Width: 3.4863



B1L003S, U16-ch64, adc1

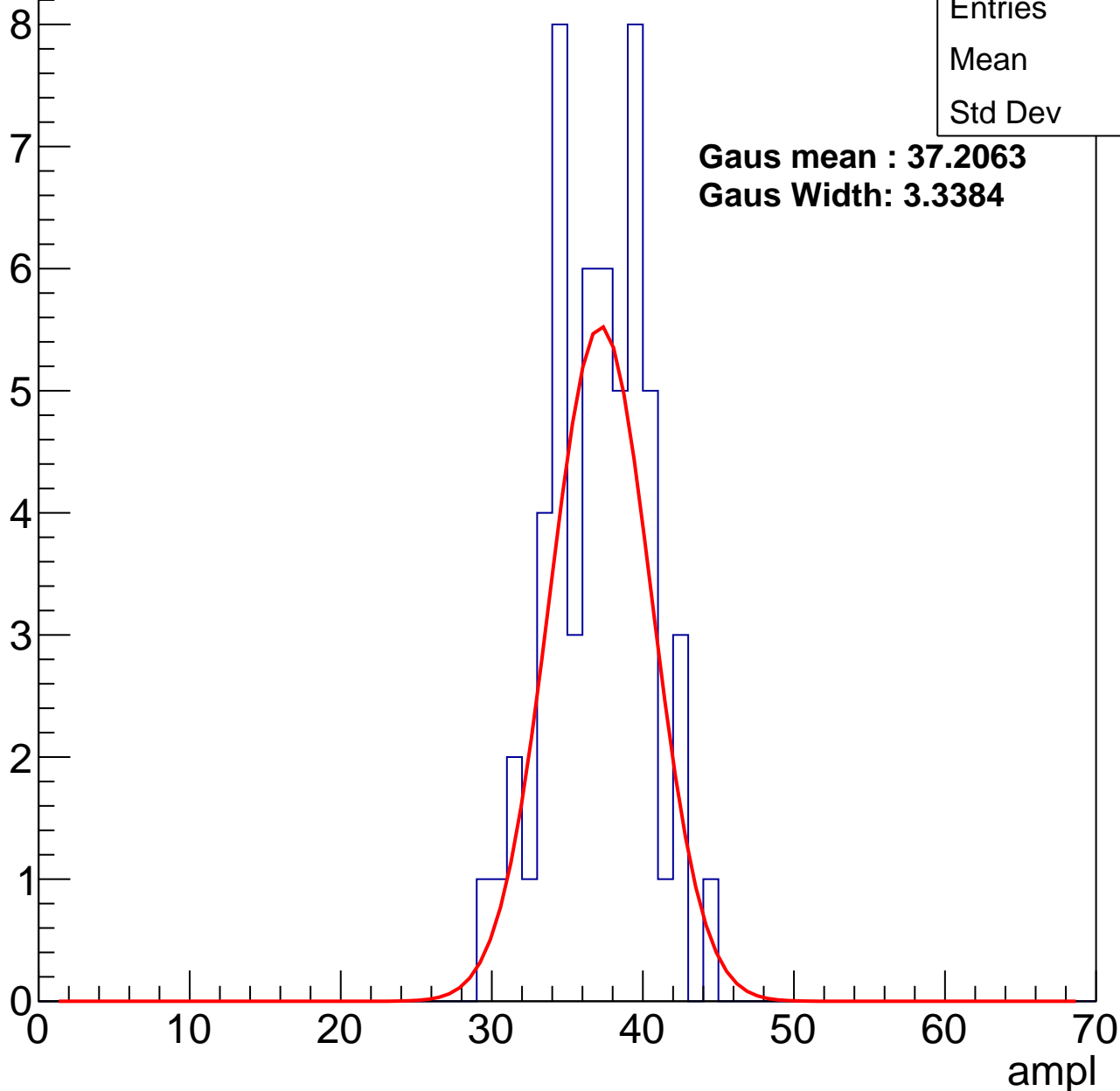
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.6
Std Dev	3.24

Gaus mean : 37.2063

Gaus Width: 3.3384



B1L003S, U16-ch64, adc2

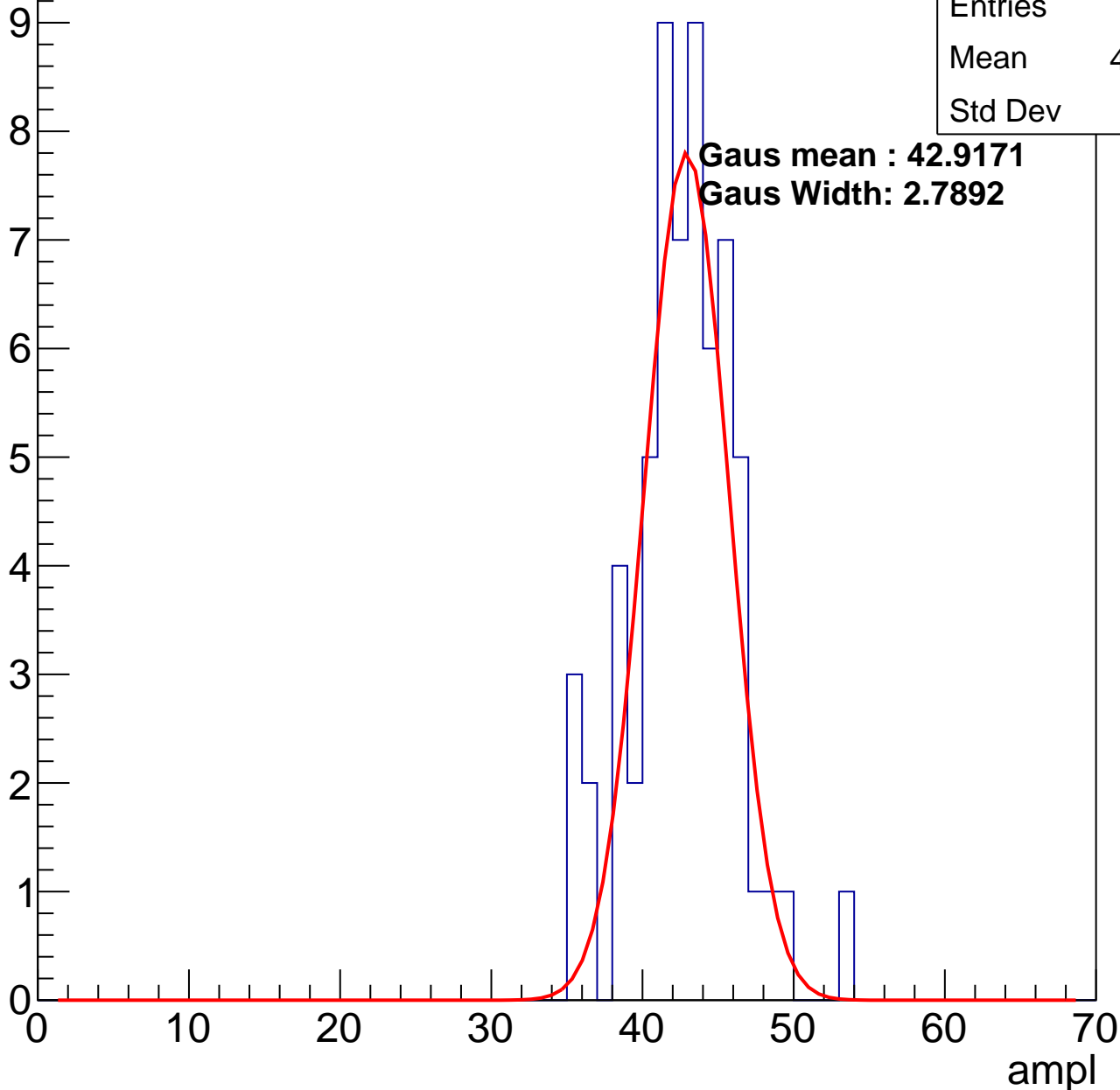
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.27
Std Dev	3.41

Gaus mean : 42.9171

Gaus Width: 2.7892

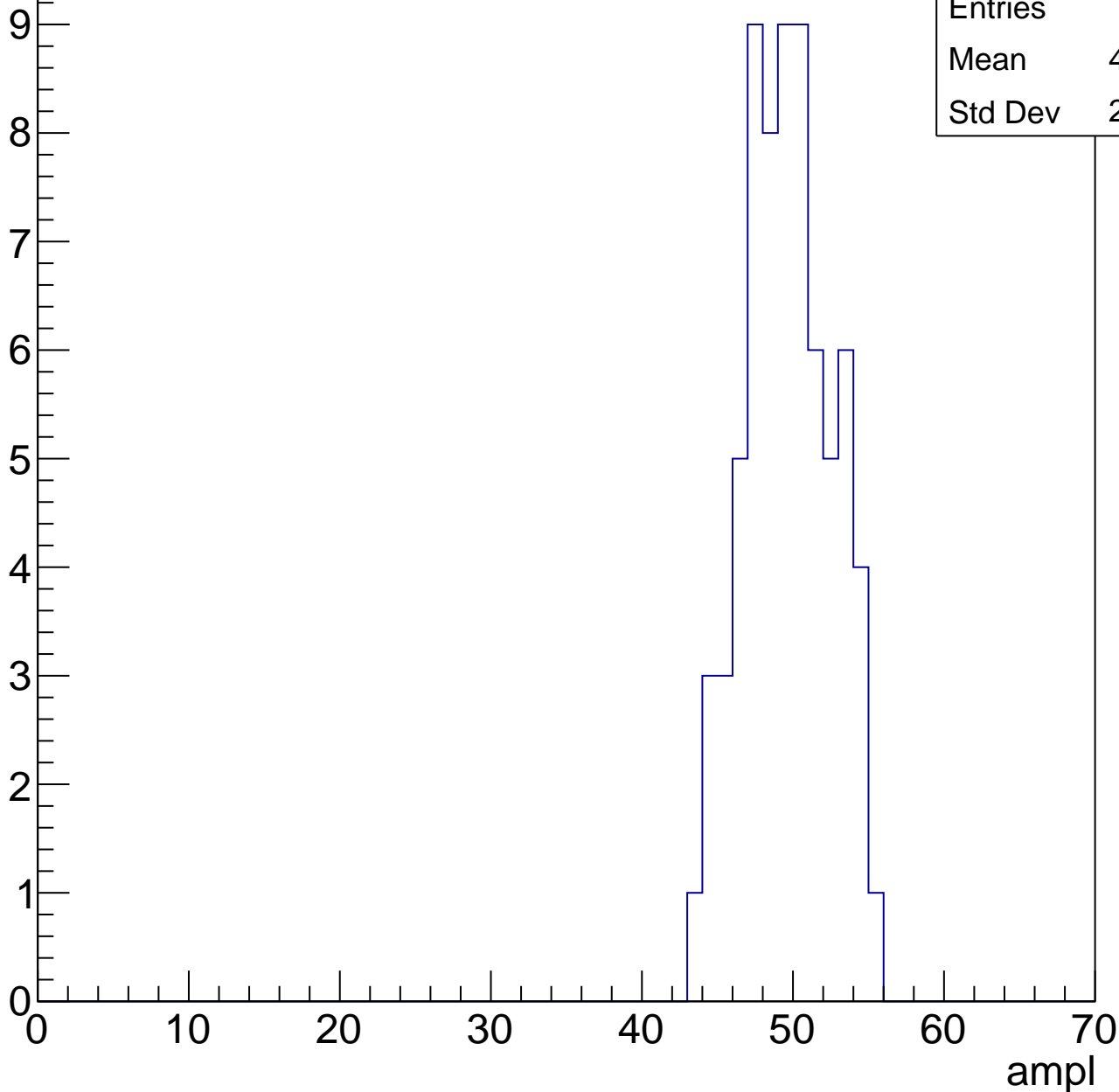


B1L003S, U16-ch64, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.17
Std Dev	2.838

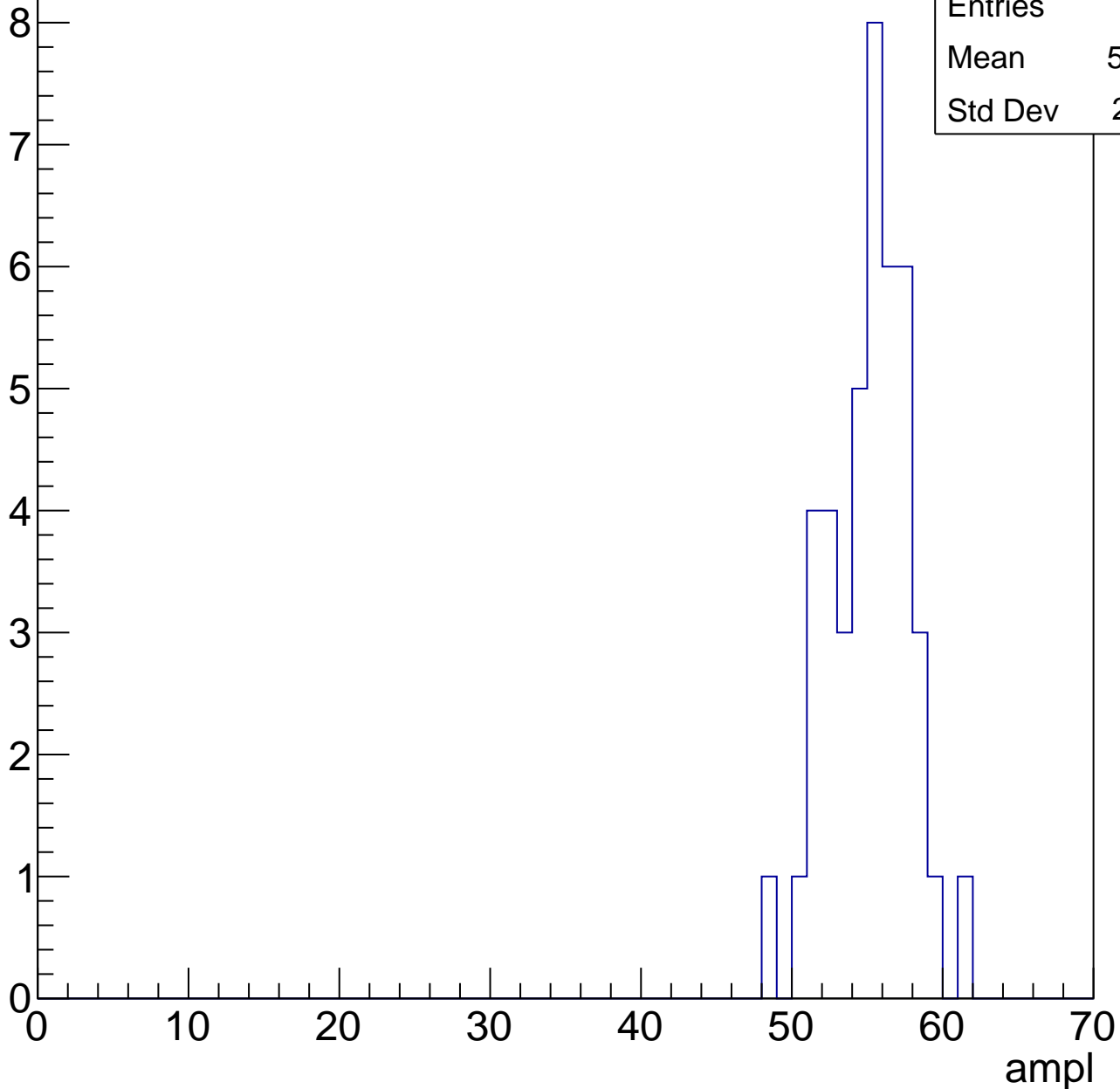


B1L003S, U16-ch64, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

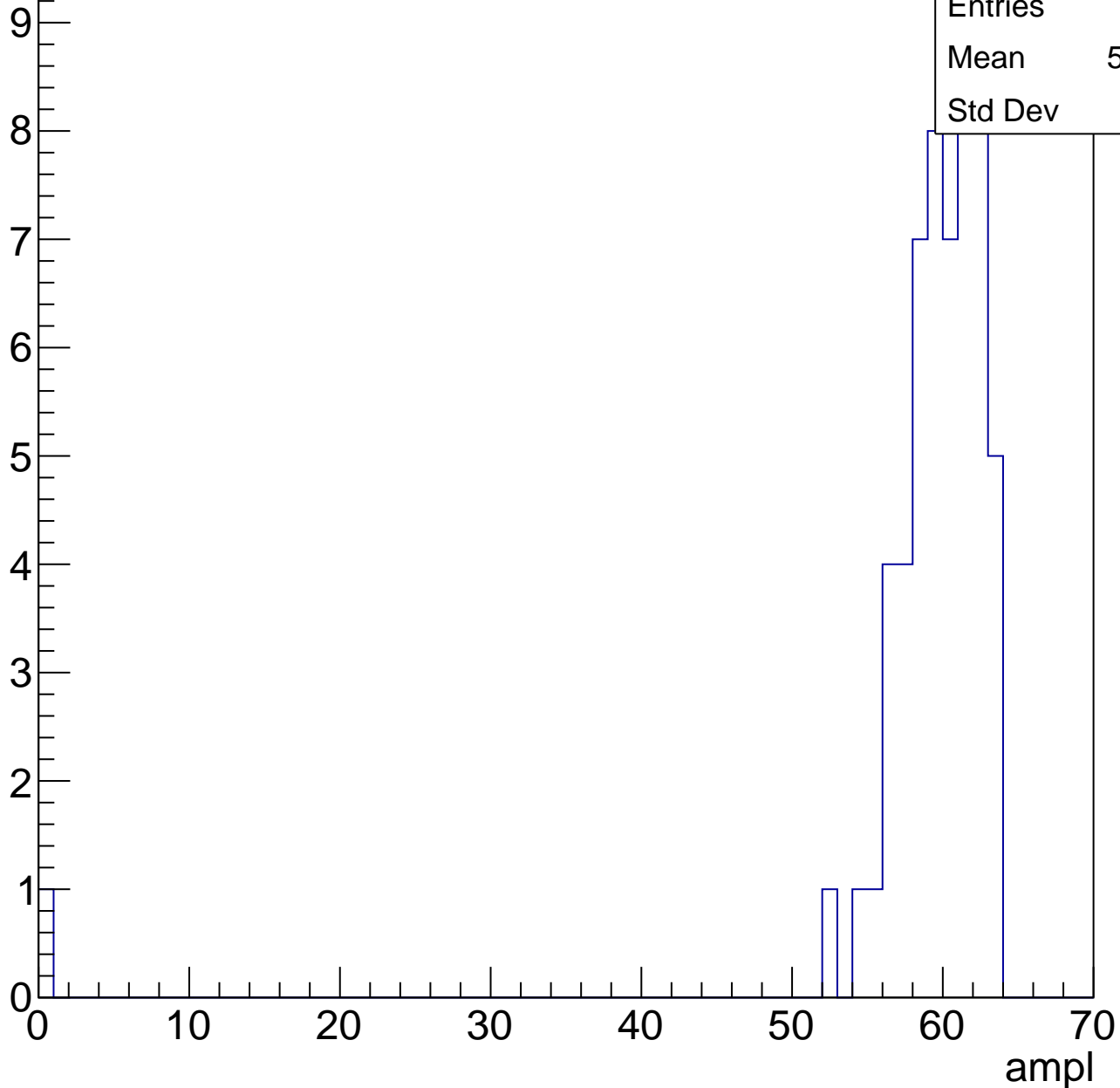
Entries	43
Mean	54.67
Std Dev	2.621



B1L003S, U16-ch64, adc5

calib_packv5_042523_0143.root, FC#13, port D2

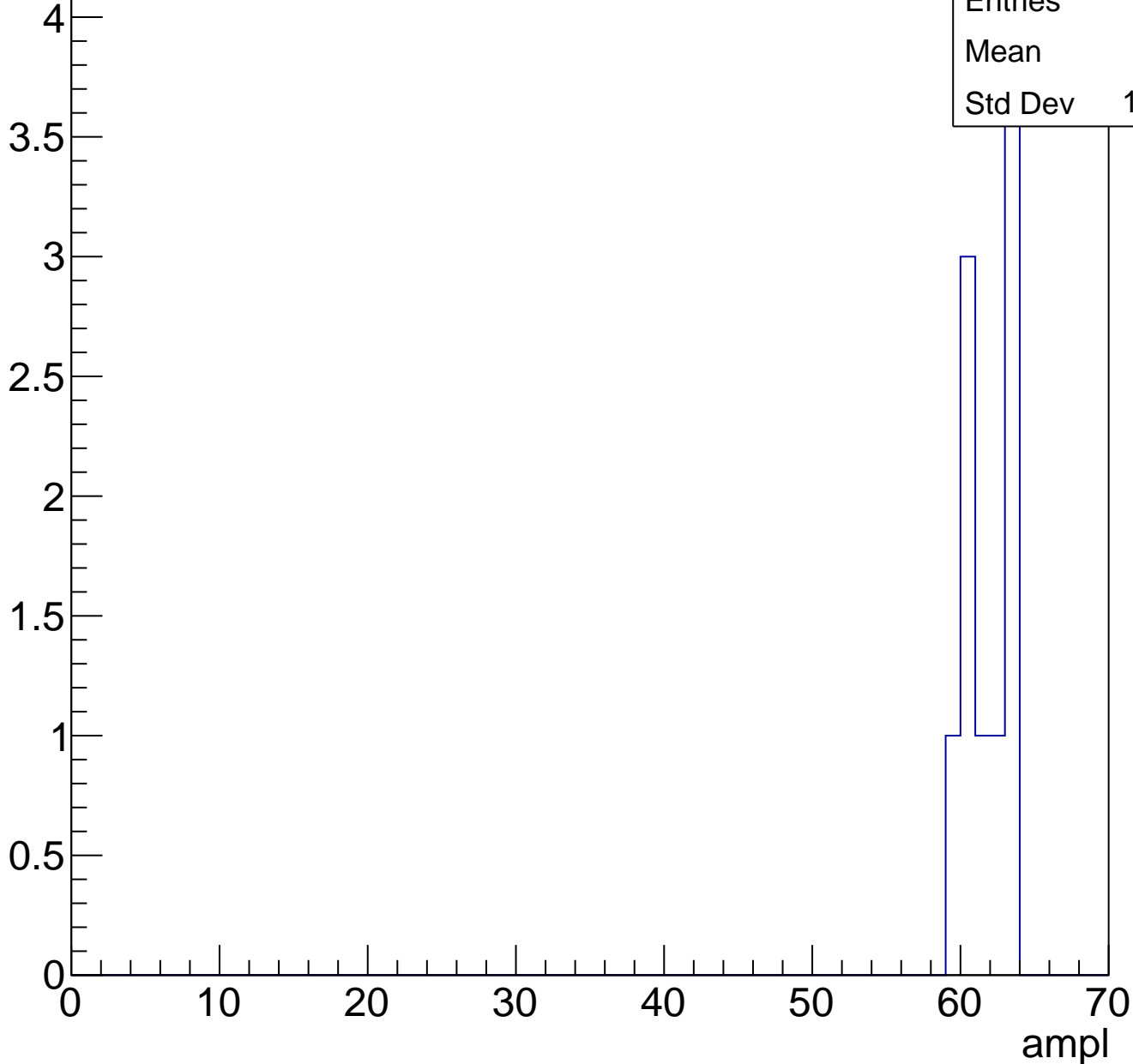
Entry



B1L003S, U16-ch64, adc6

calib_packv5_042523_0143.root, FC#13, port D2

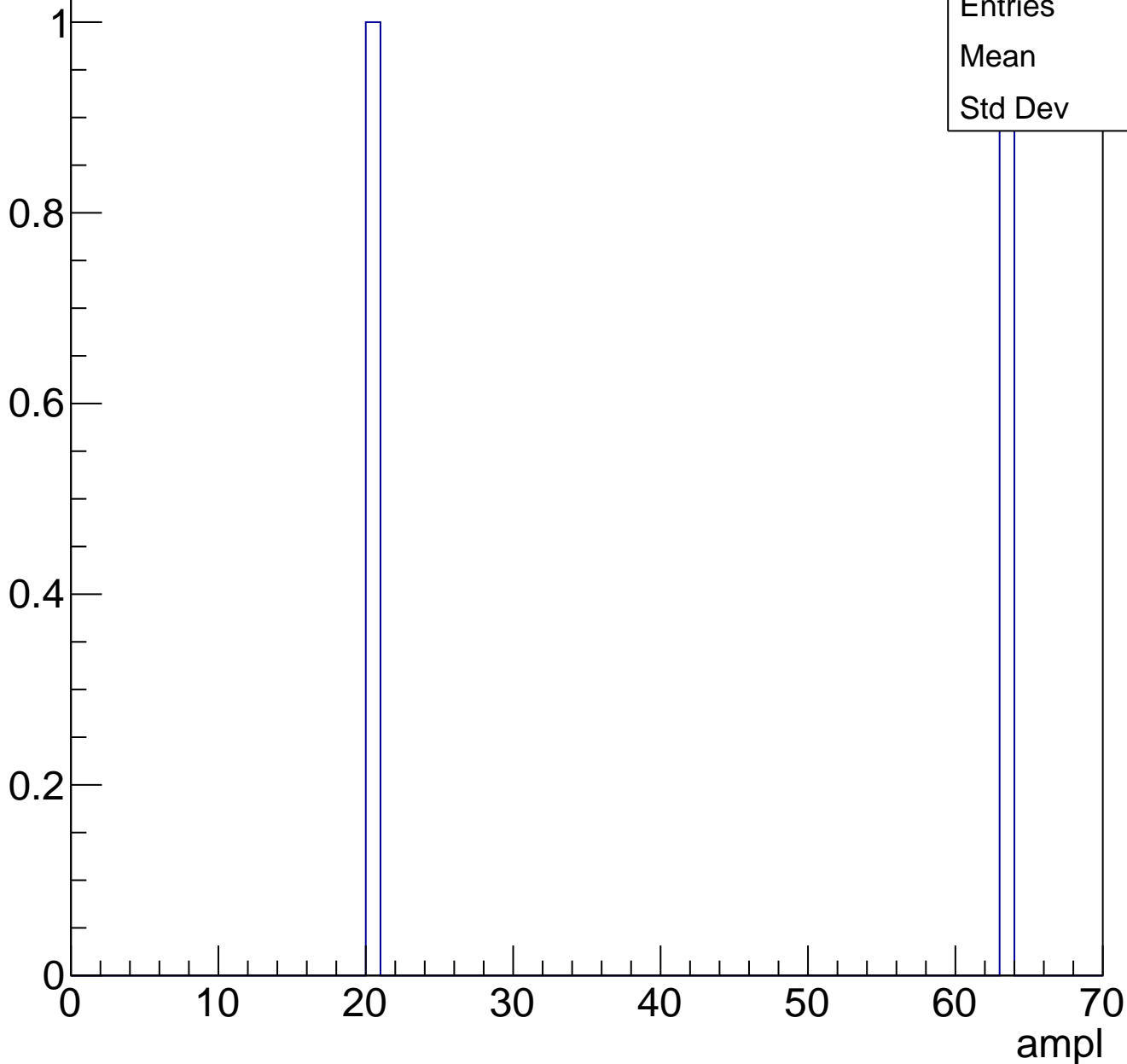
Entry



B1L003S, U16-ch64, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch65, adc0

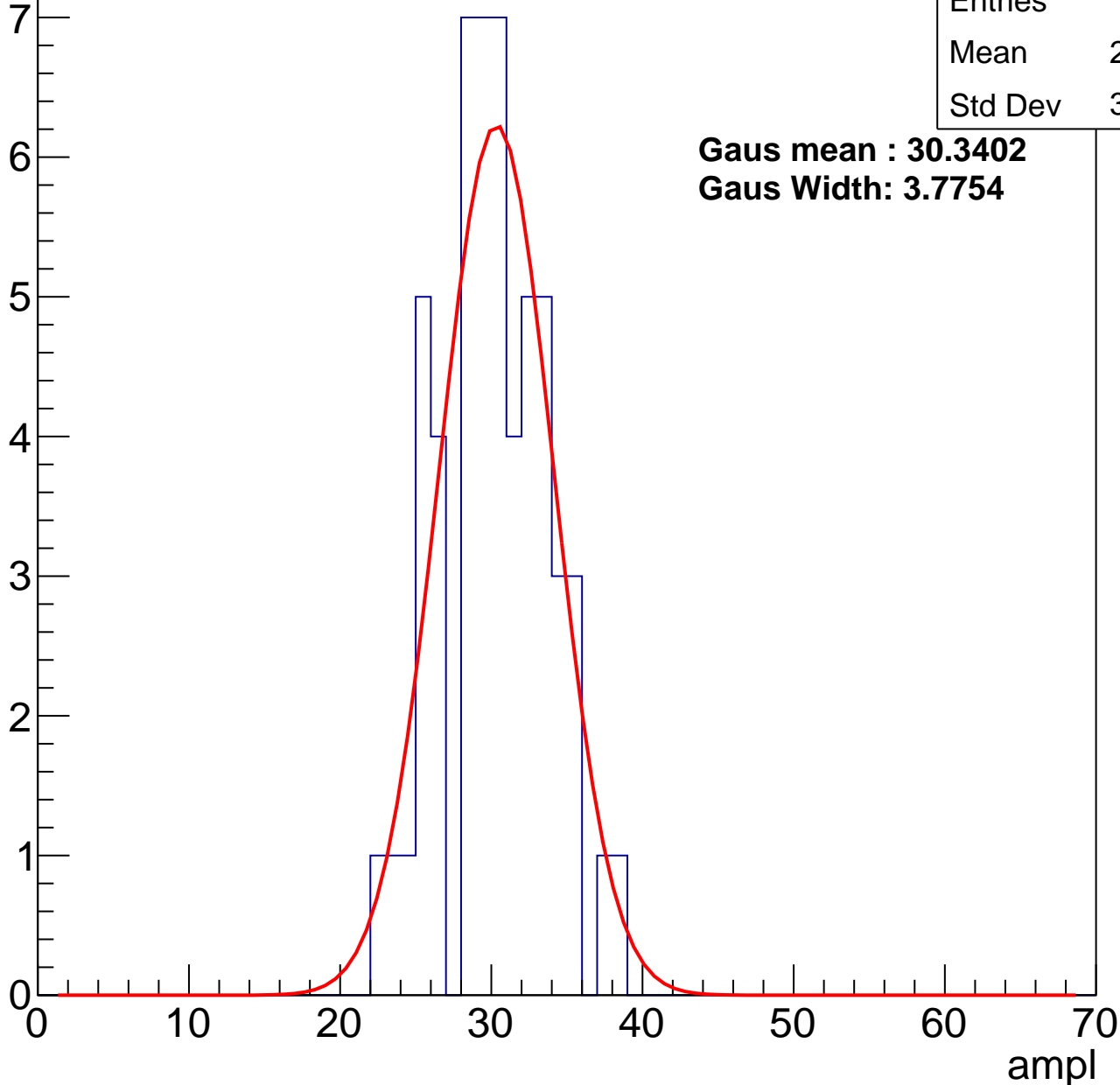
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.78
Std Dev	3.504

Gaus mean : 30.3402

Gaus Width: 3.7754



B1L003S, U16-ch65, adc1

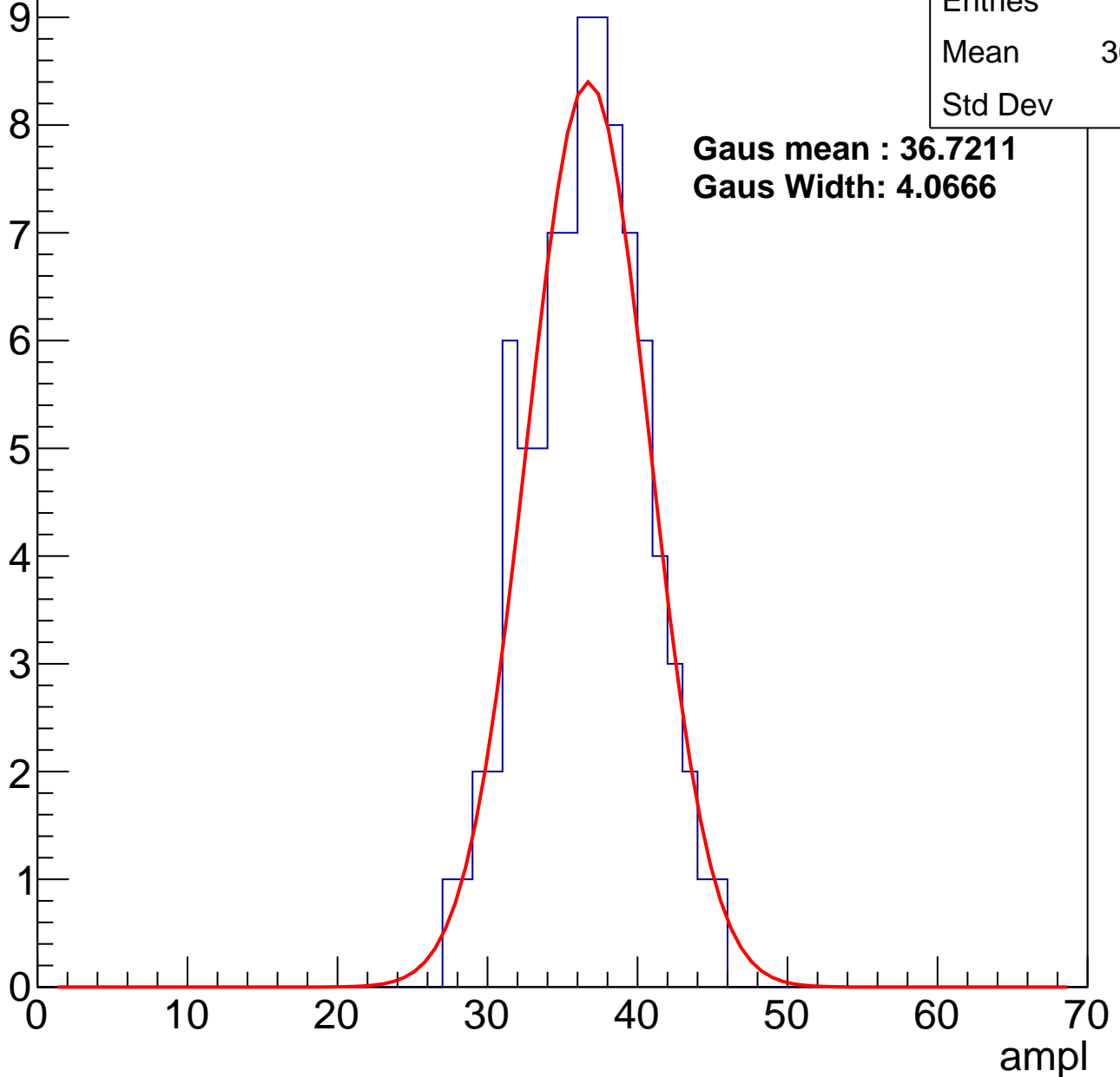
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	36.12
Std Dev	3.85

Gaus mean : 36.7211

Gaus Width: 4.0666



B1L003S, U16-ch65, adc2

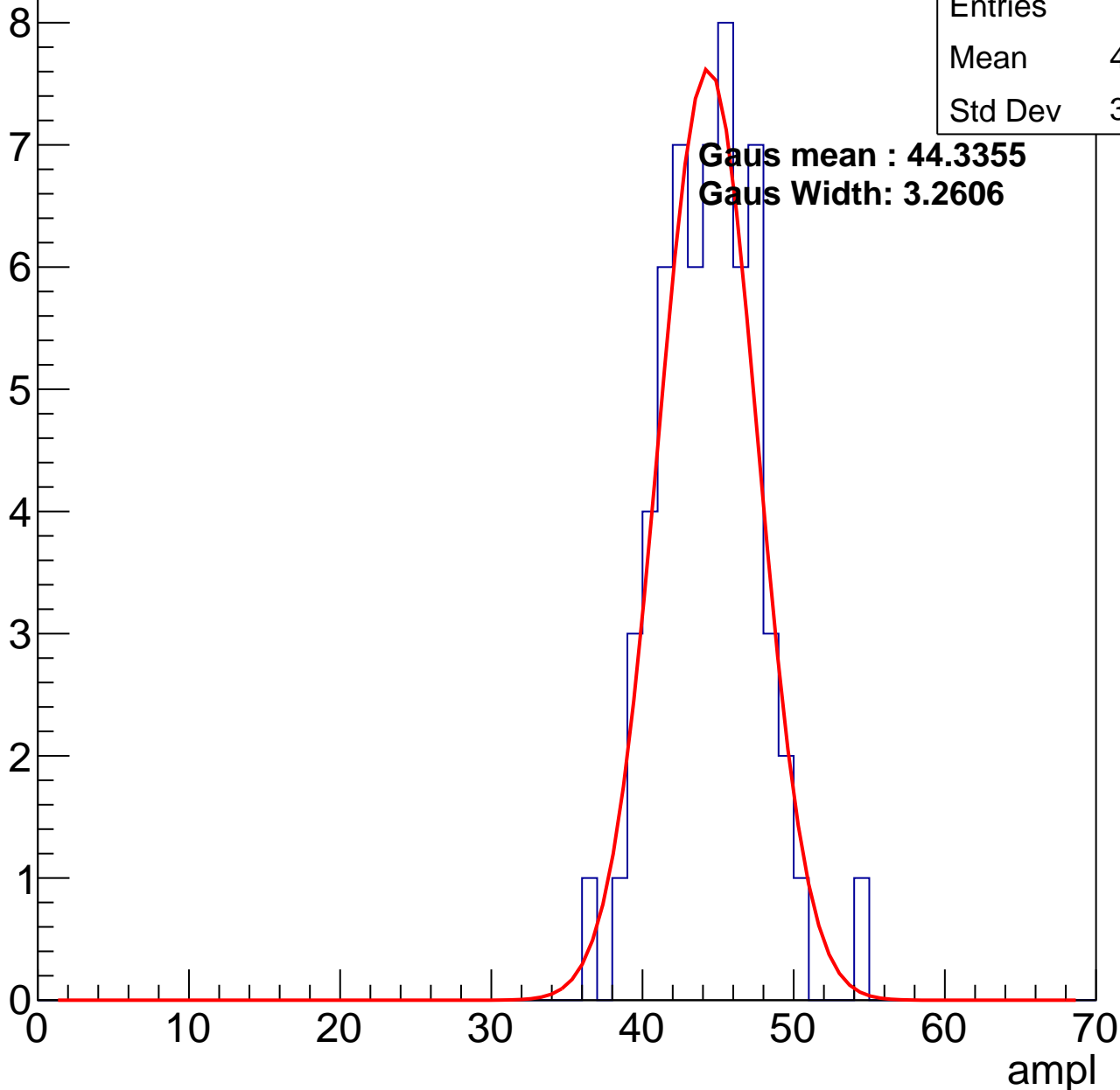
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.94
Std Dev	3.226

Gaus mean : 44.3355

Gaus Width: 3.2606

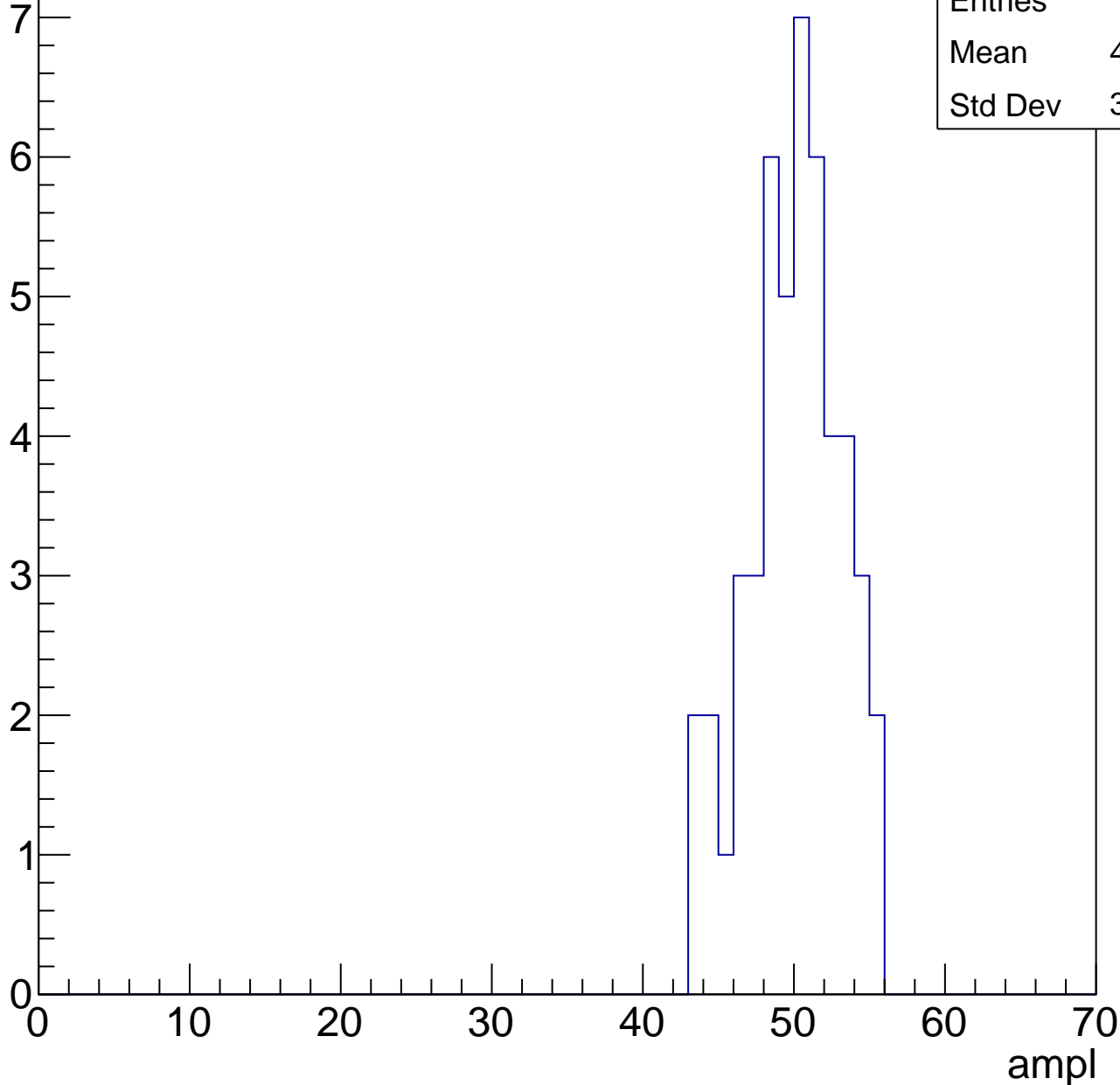


B1L003S, U16-ch65, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	49.56
Std Dev	3.048

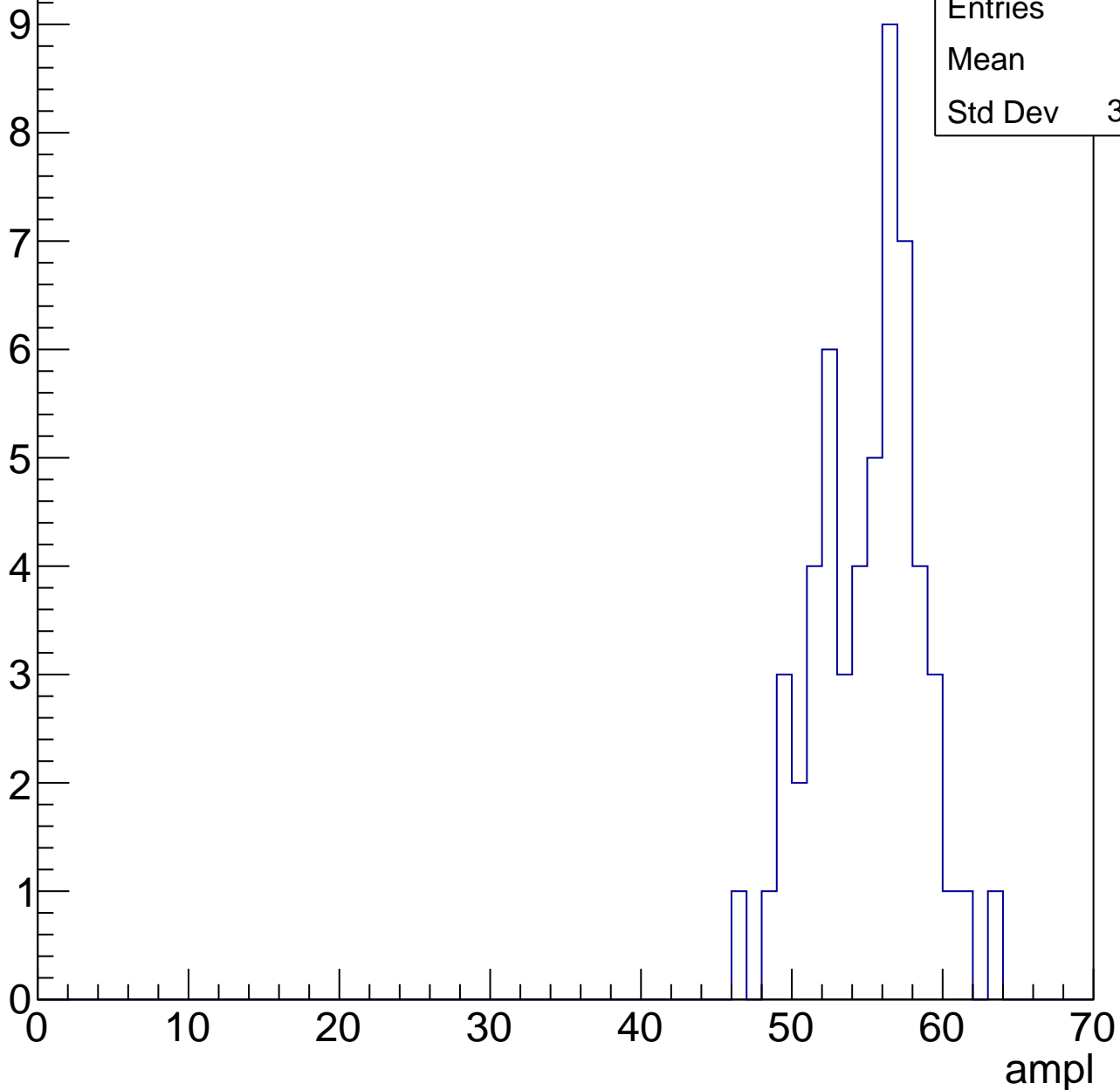


B1L003S, U16-ch65, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.6
Std Dev	3.462

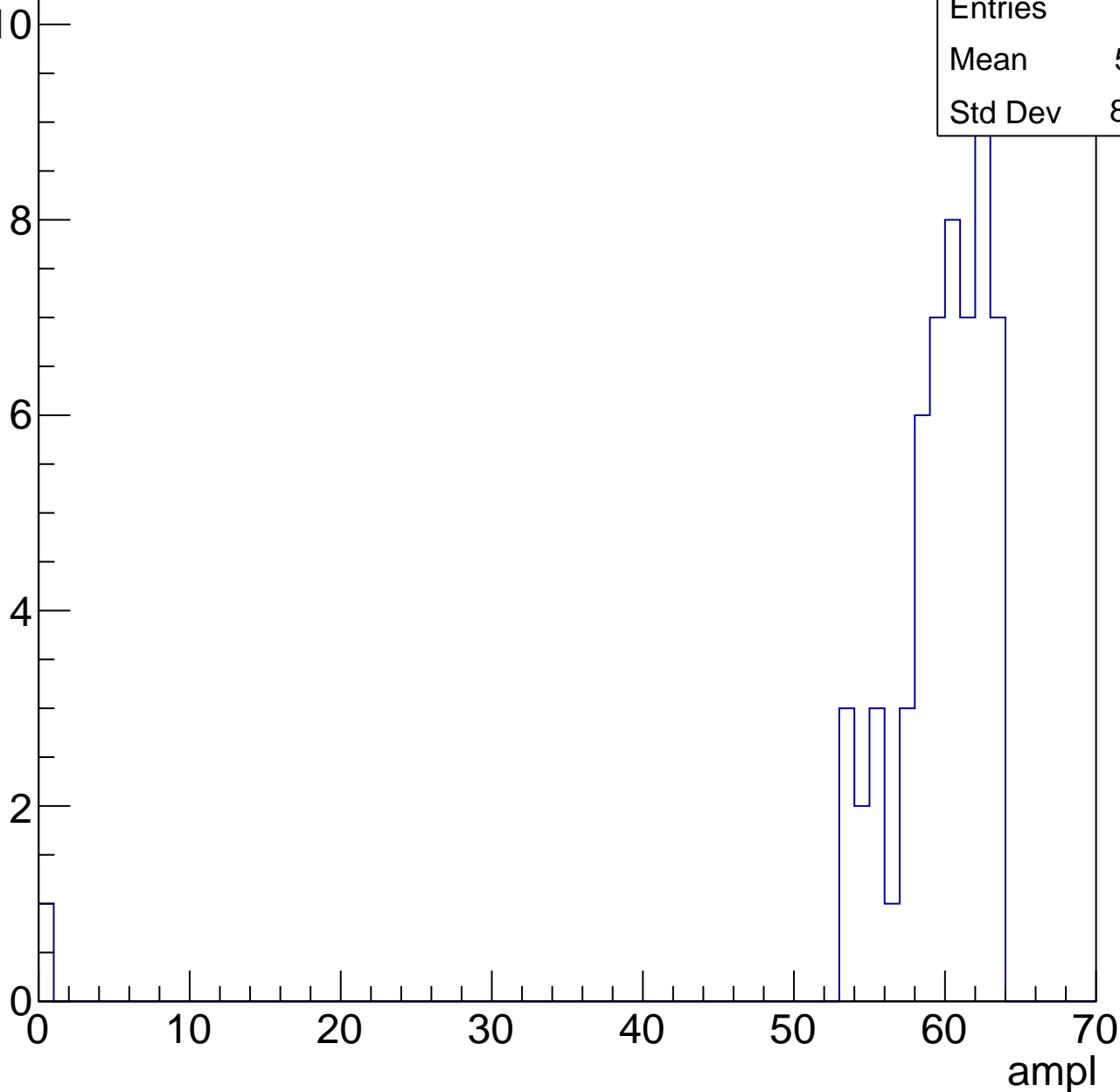


B1L003S, U16-ch65, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.41
Std Dev	8.234



B1L003S, U16-ch65, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B1L003S, U16-ch65, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L003S, U16-ch66, adc0

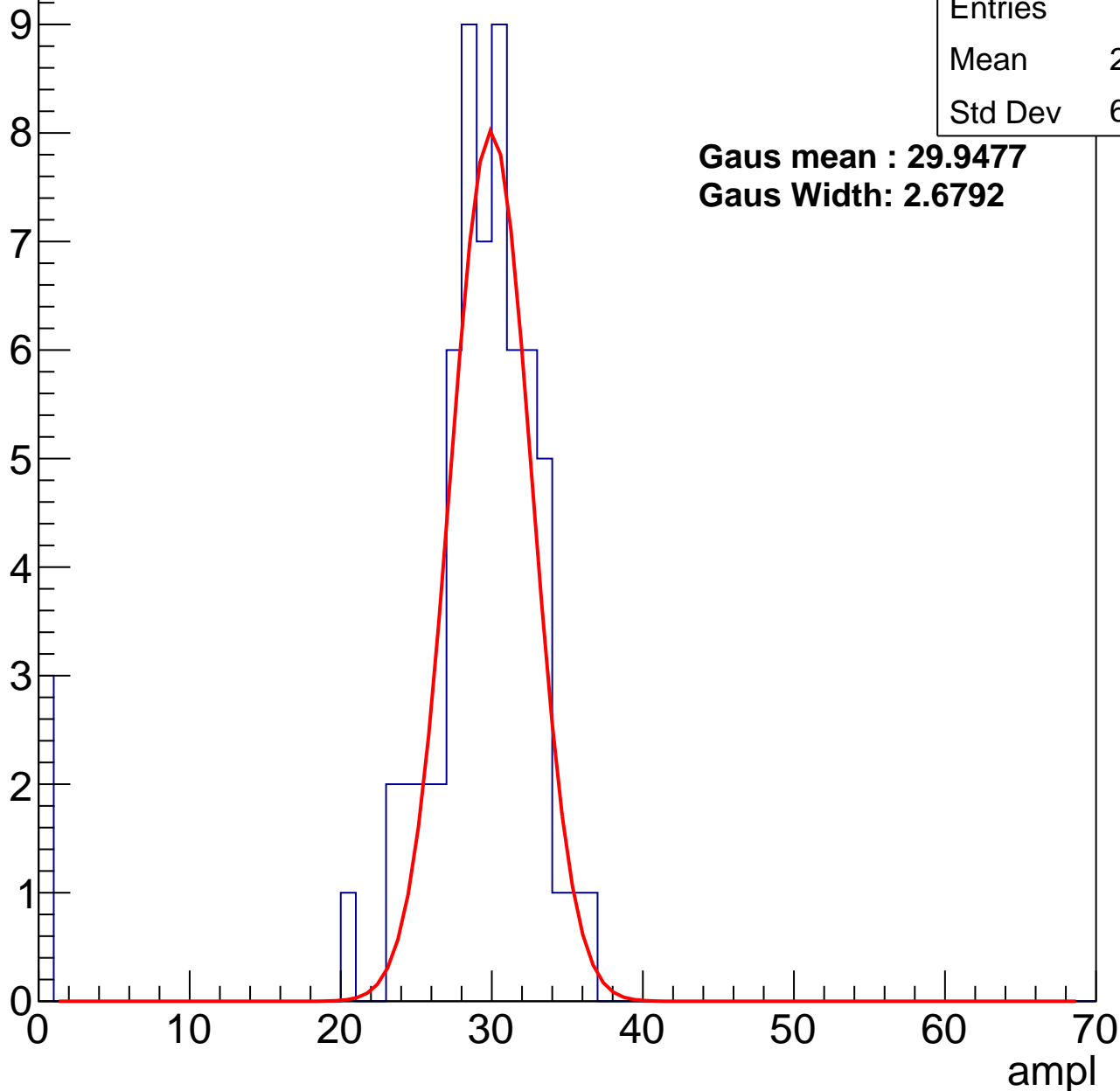
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	27.79
Std Dev	6.894

Gaus mean : 29.9477

Gaus Width: 2.6792



B1L003S, U16-ch66, adc1

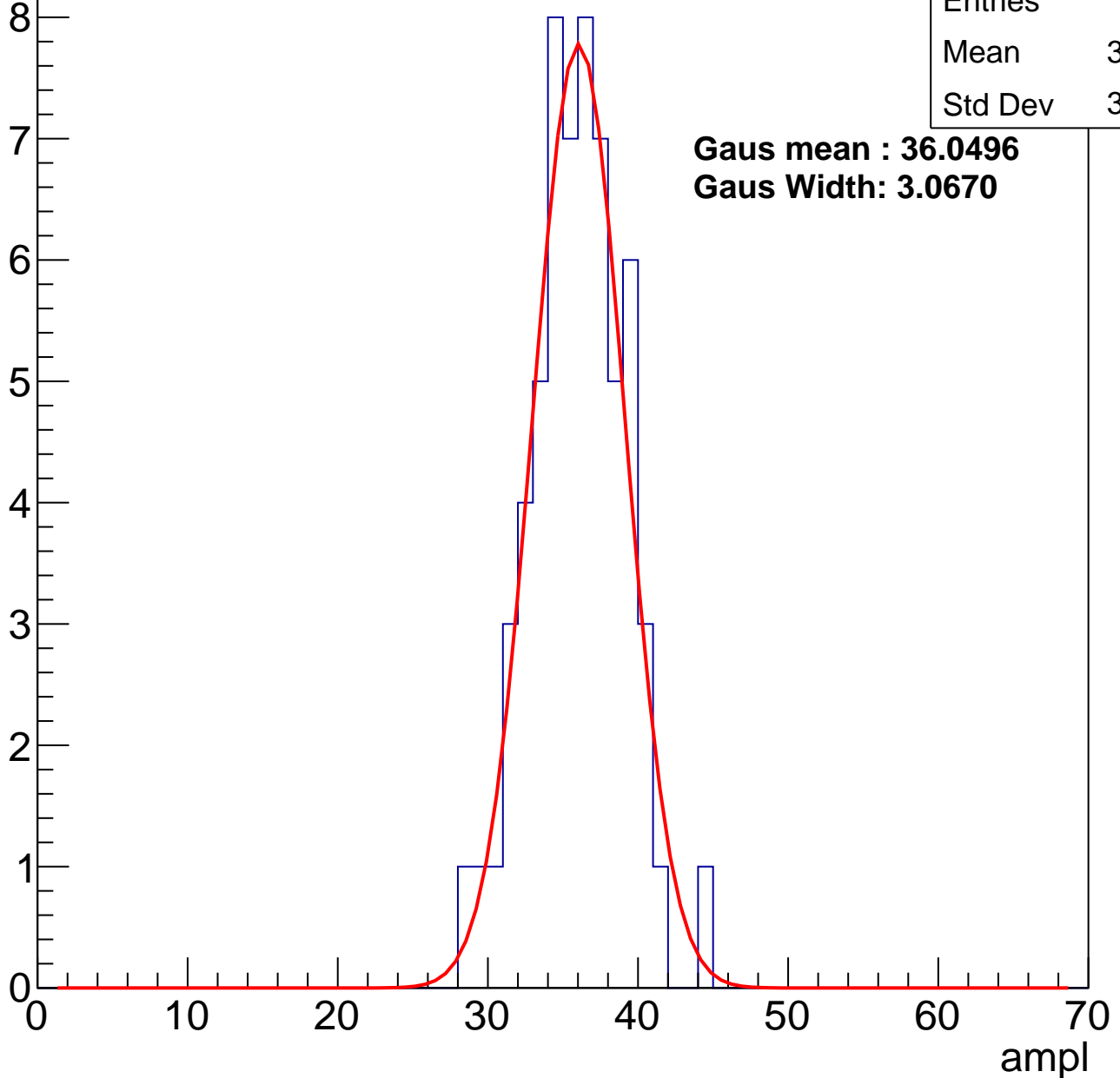
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.51
Std Dev	3.071

Gaus mean : 36.0496

Gaus Width: 3.0670



B1L003S, U16-ch66, adc2

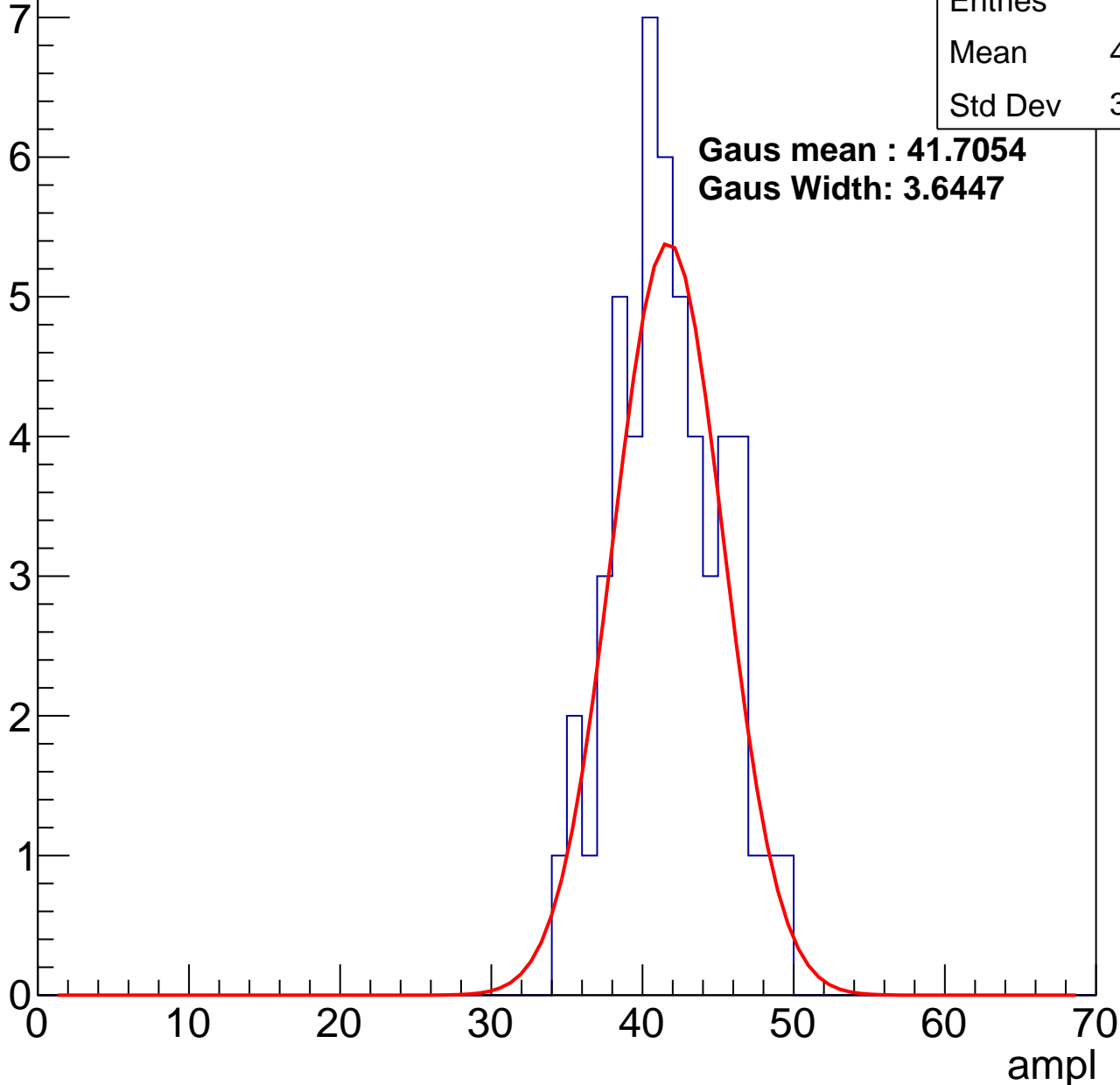
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	41.25
Std Dev	3.452

Gaus mean : 41.7054

Gaus Width: 3.6447

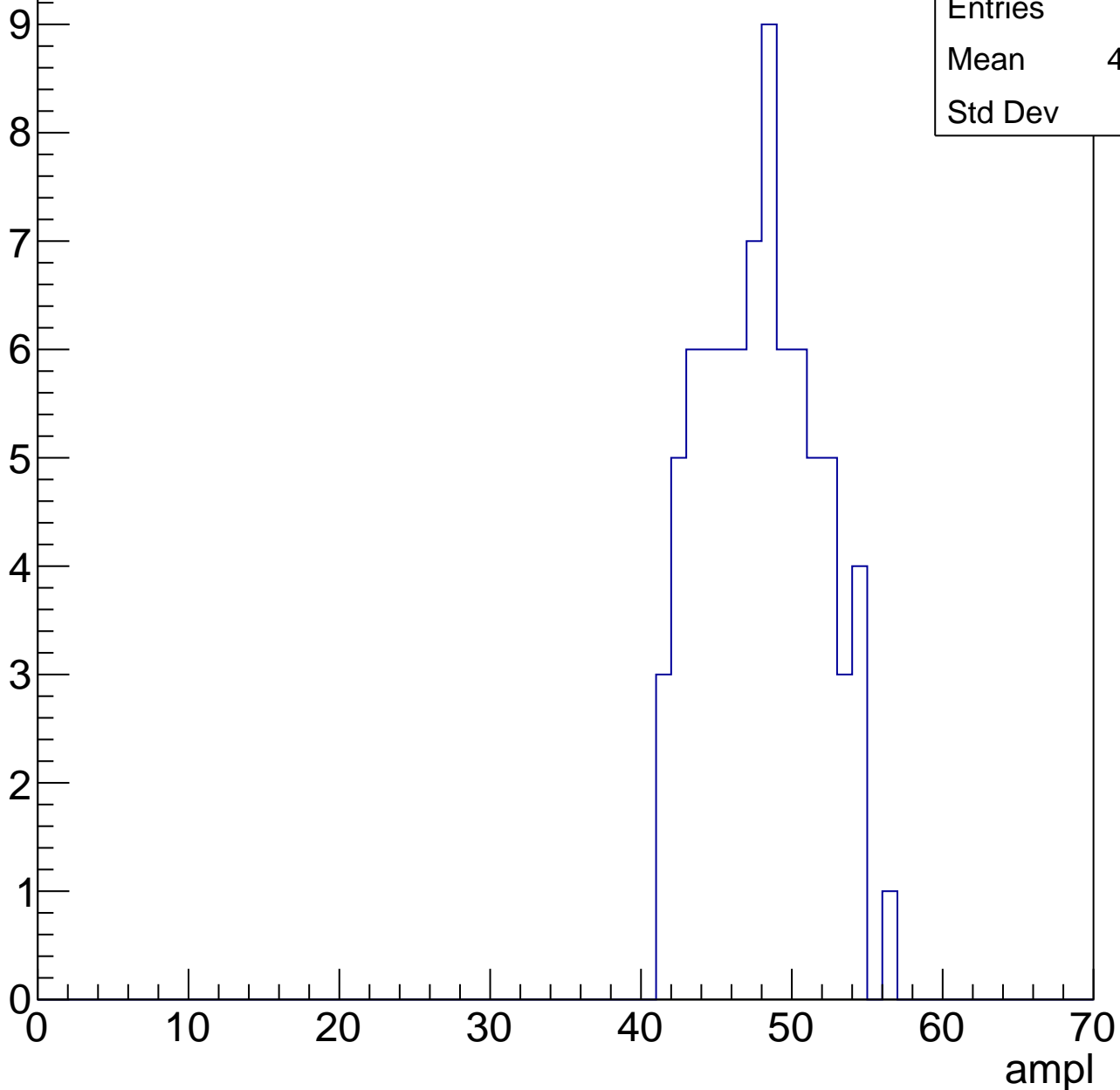


B1L003S, U16-ch66, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	47.46
Std Dev	3.71

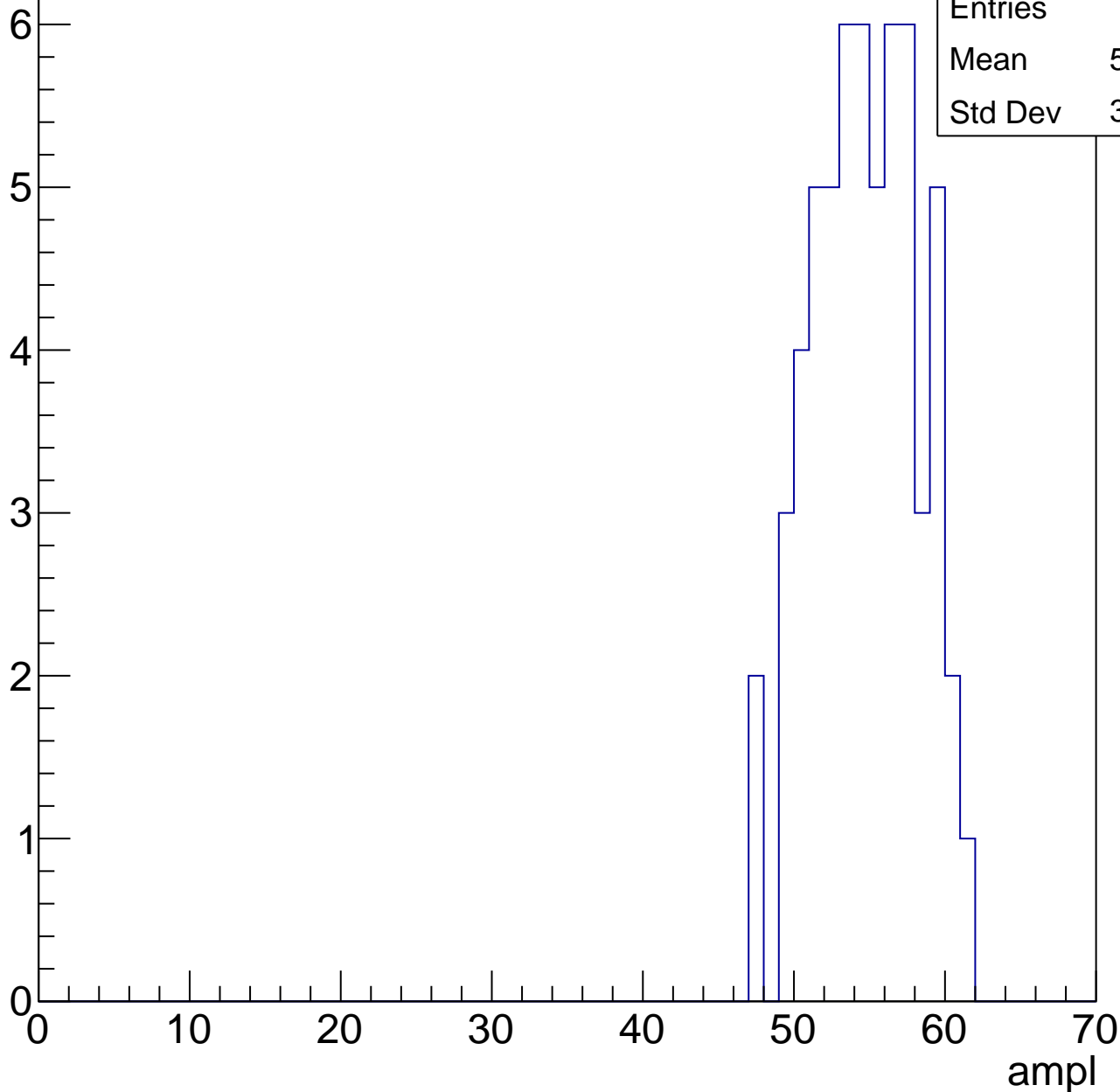


B1L003S, U16-ch66, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.25
Std Dev	3.398



B1L003S, U16-ch66, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	51
Mean	59.41
Std Dev	2.68

ampl

B1L003S, U16-ch66, adc6

calib_packv5_042523_0143.root, FC#13, port D2

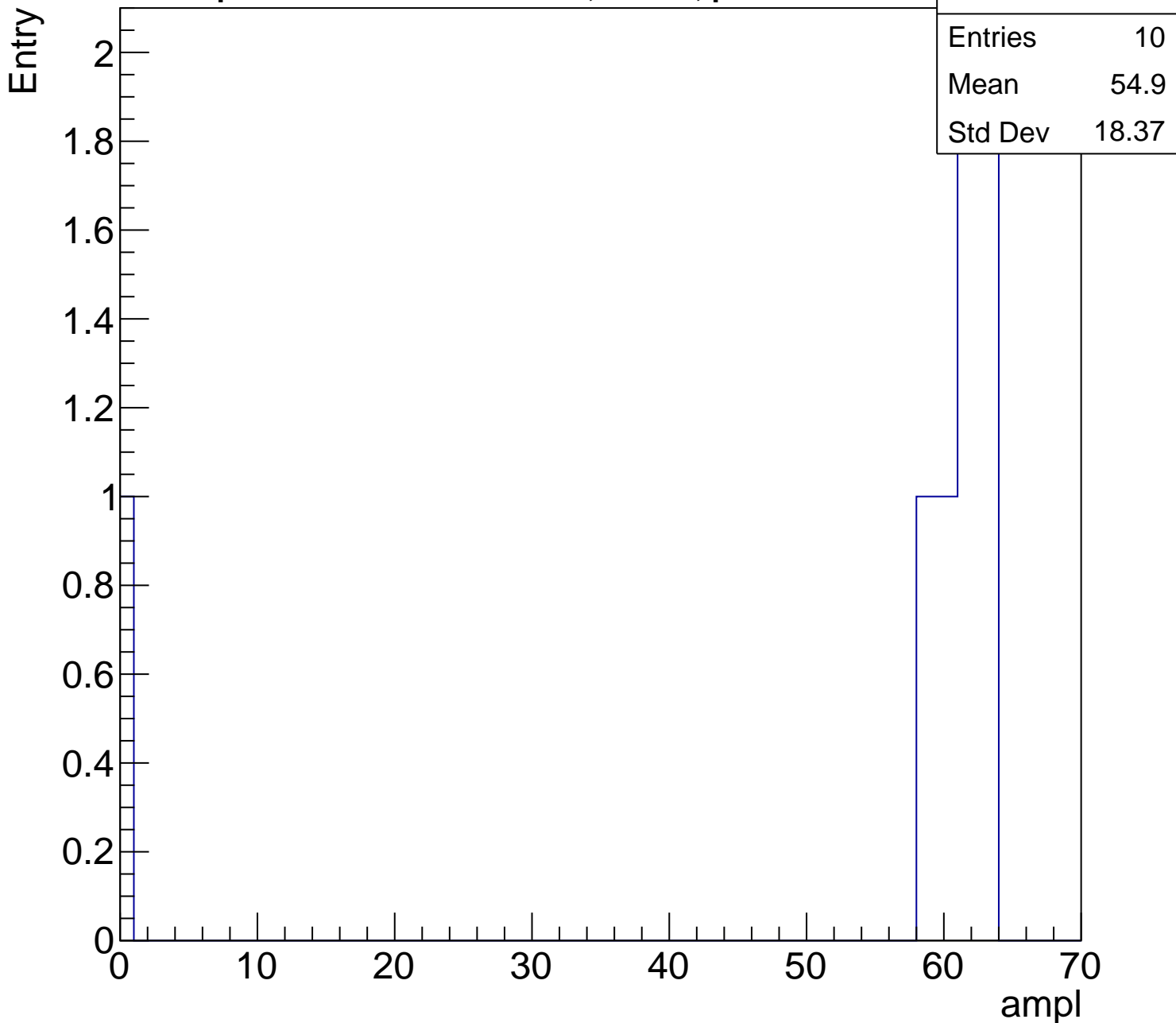
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	54.9
Std Dev	18.37

ampl

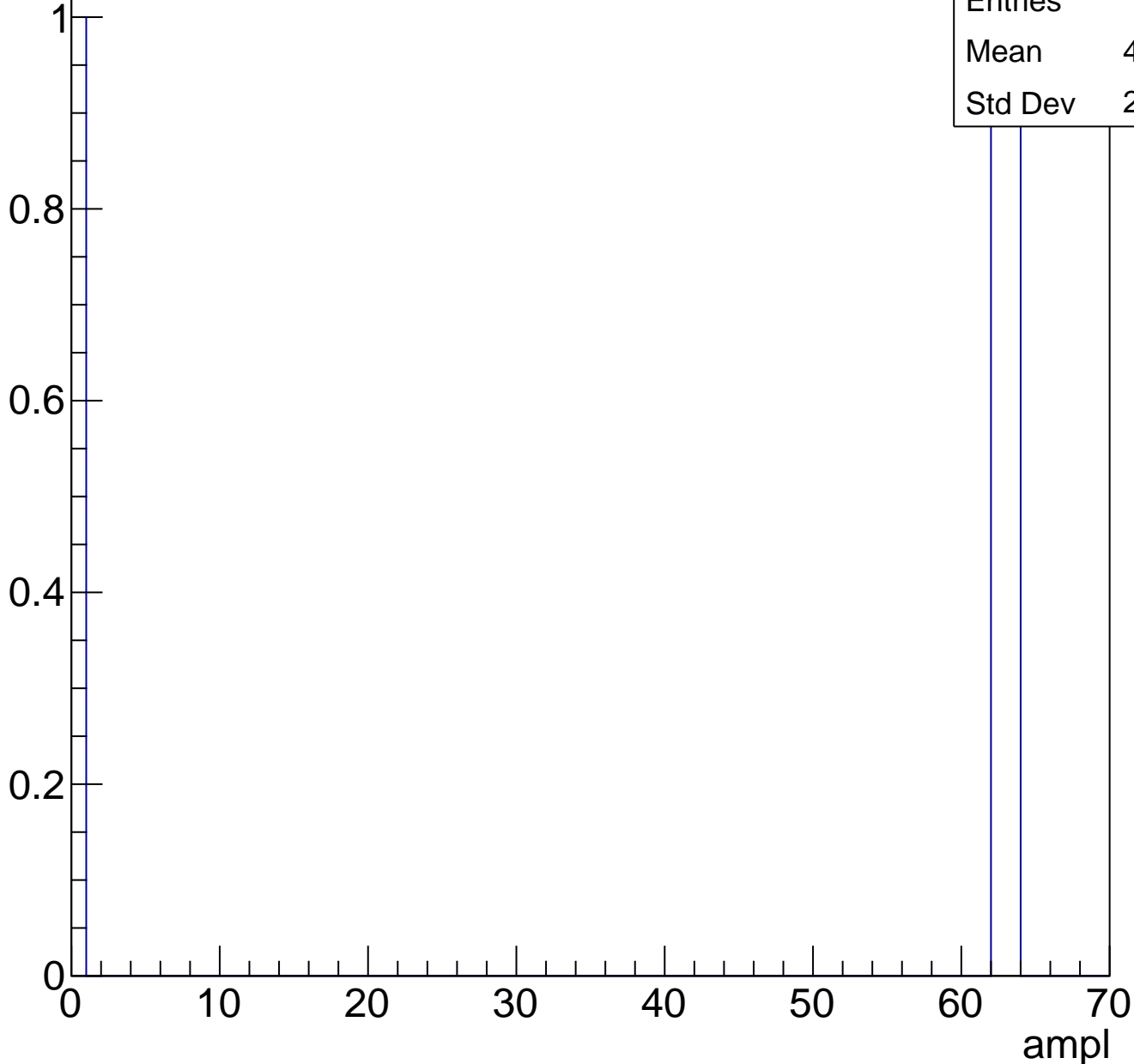
0 10 20 30 40 50 60 70



B1L003S, U16-ch66, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch67, adc0

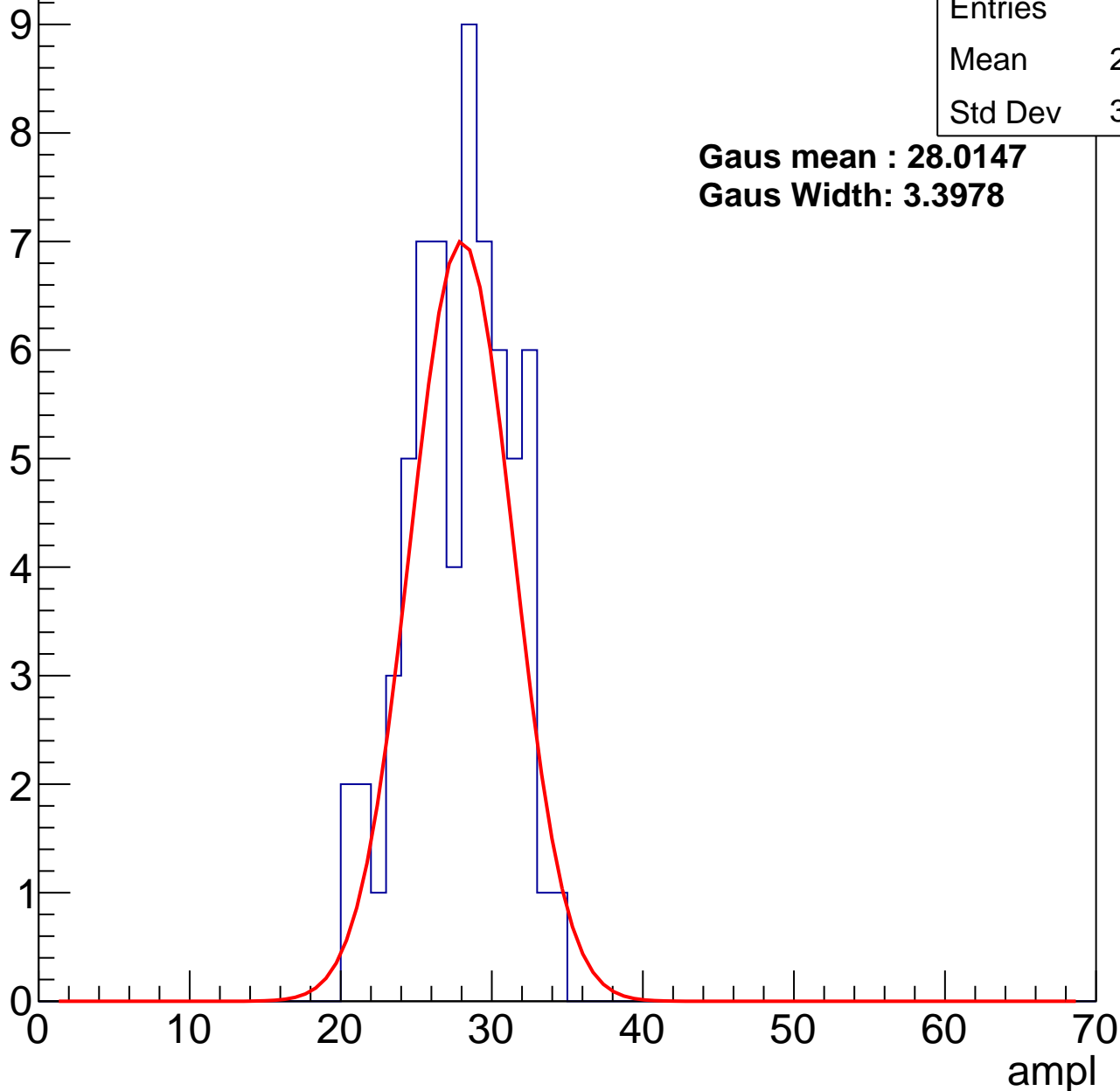
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.38
Std Dev	3.302

Gaus mean : 28.0147

Gaus Width: 3.3978



B1L003S, U16-ch67, adc1

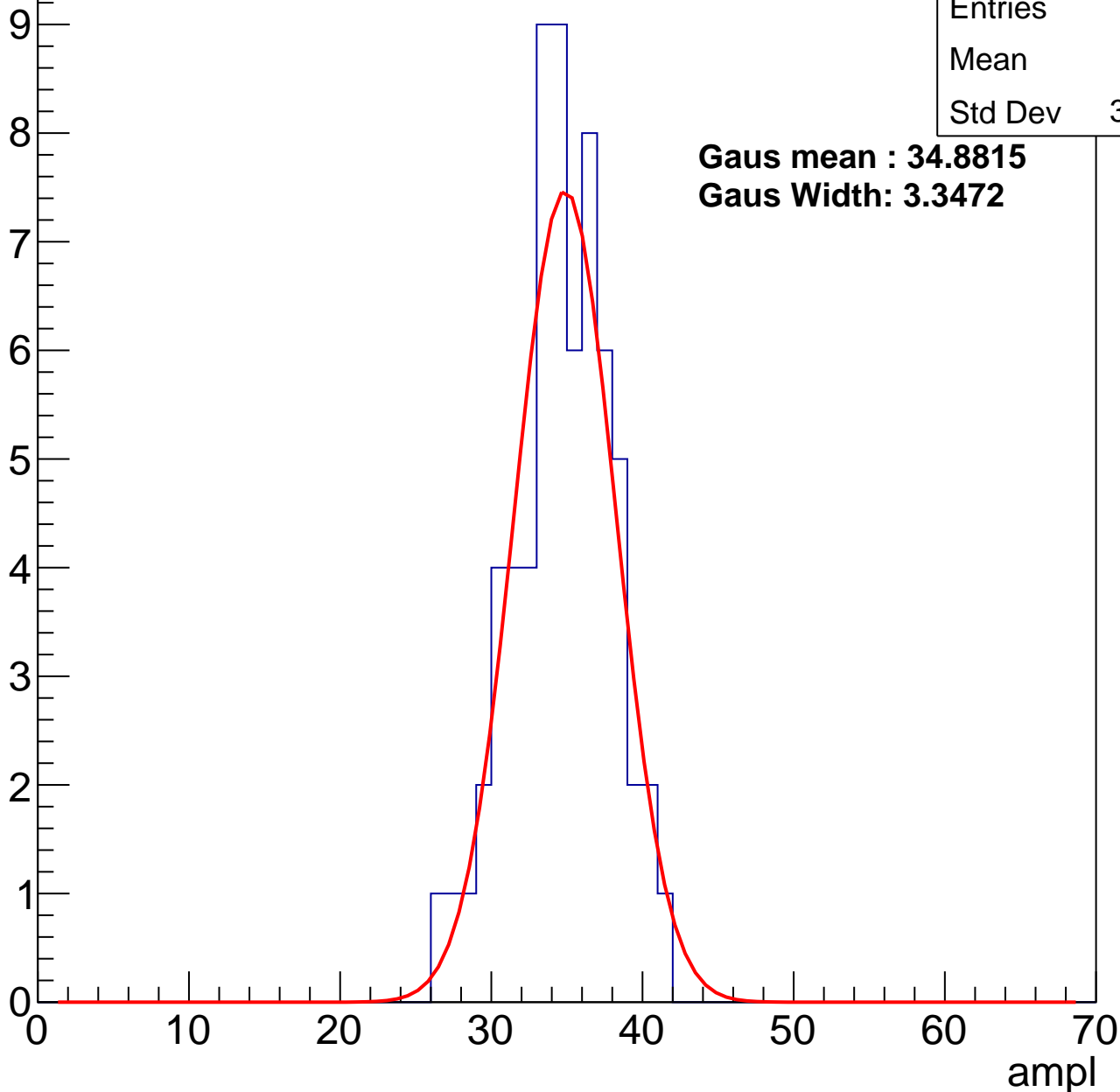
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	34.2
Std Dev	3.197

Gaus mean : 34.8815

Gaus Width: 3.3472



B1L003S, U16-ch67, adc2

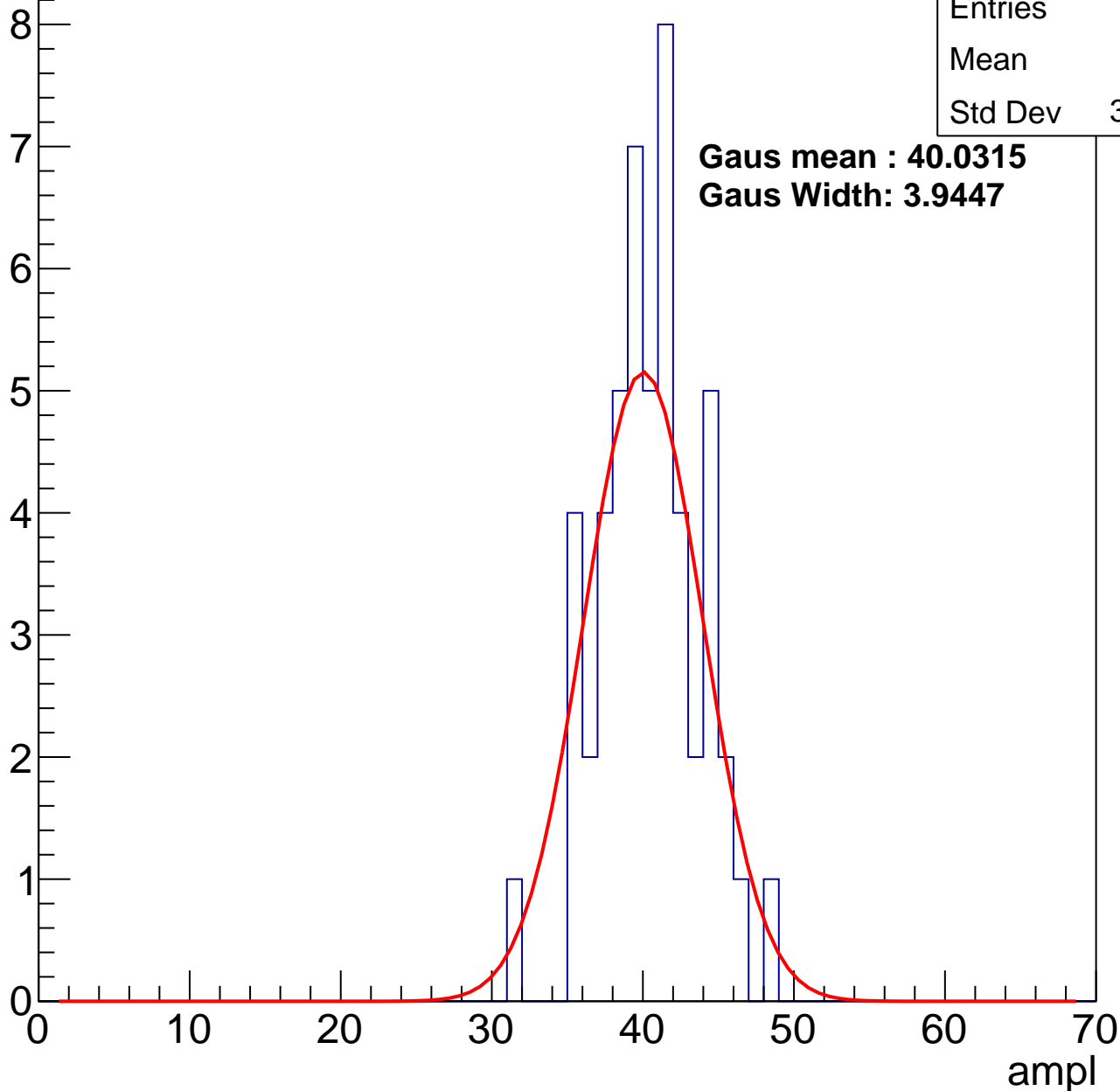
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	40
Std Dev	3.278

Gaus mean : 40.0315

Gaus Width: 3.9447

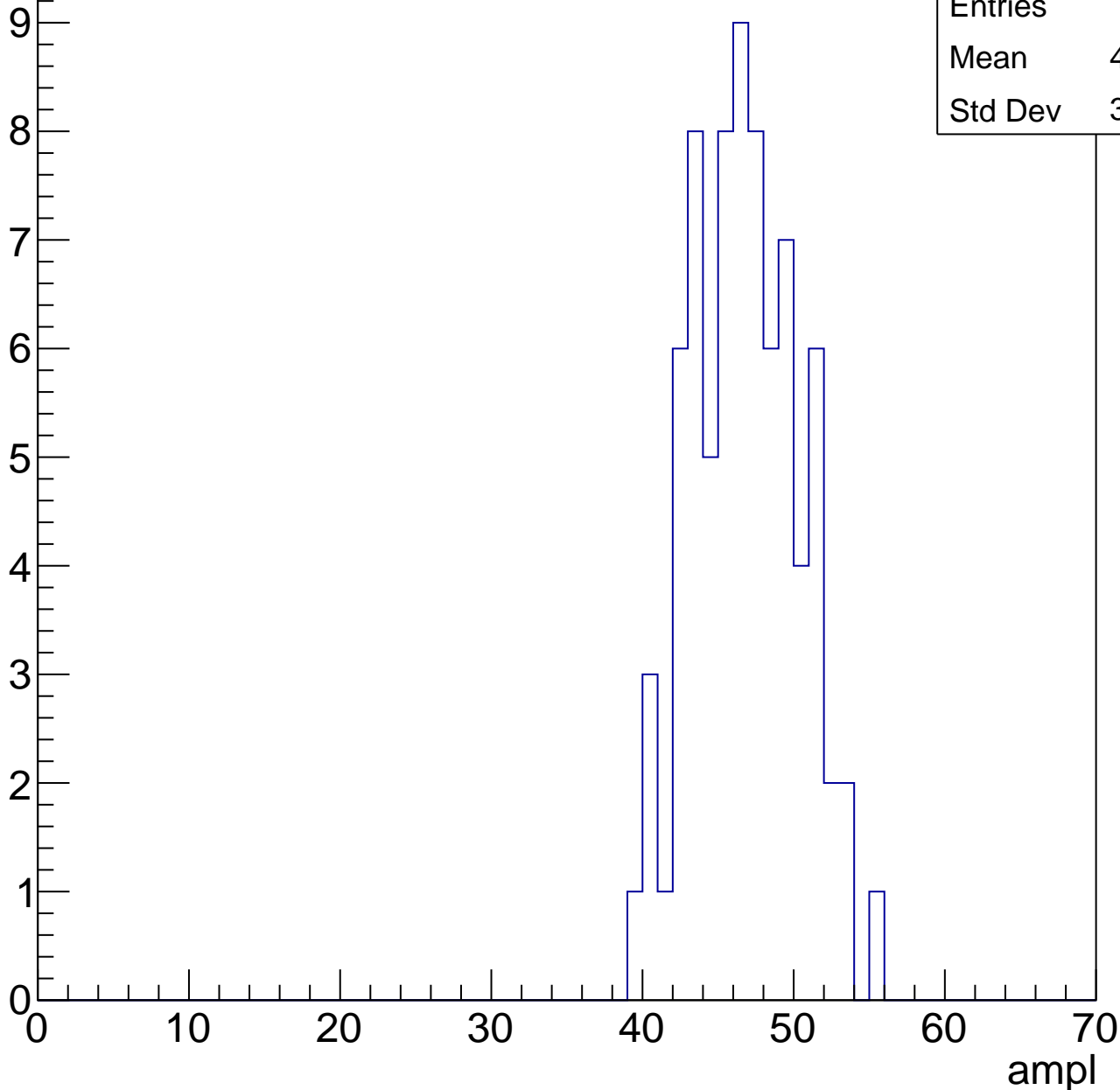


B1L003S, U16-ch67, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

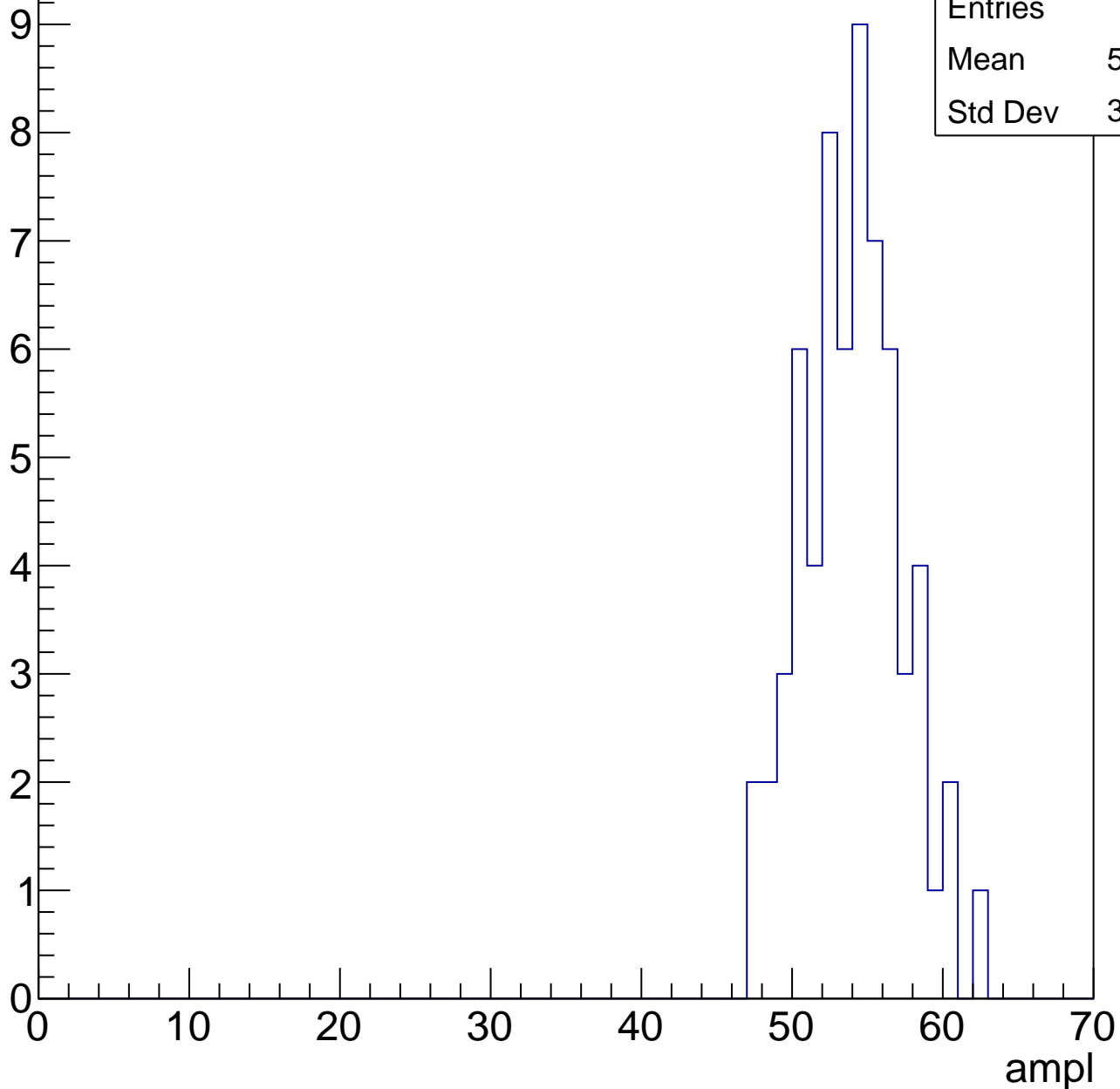
Entries	77
Mean	46.34
Std Dev	3.474



B1L003S, U16-ch67, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



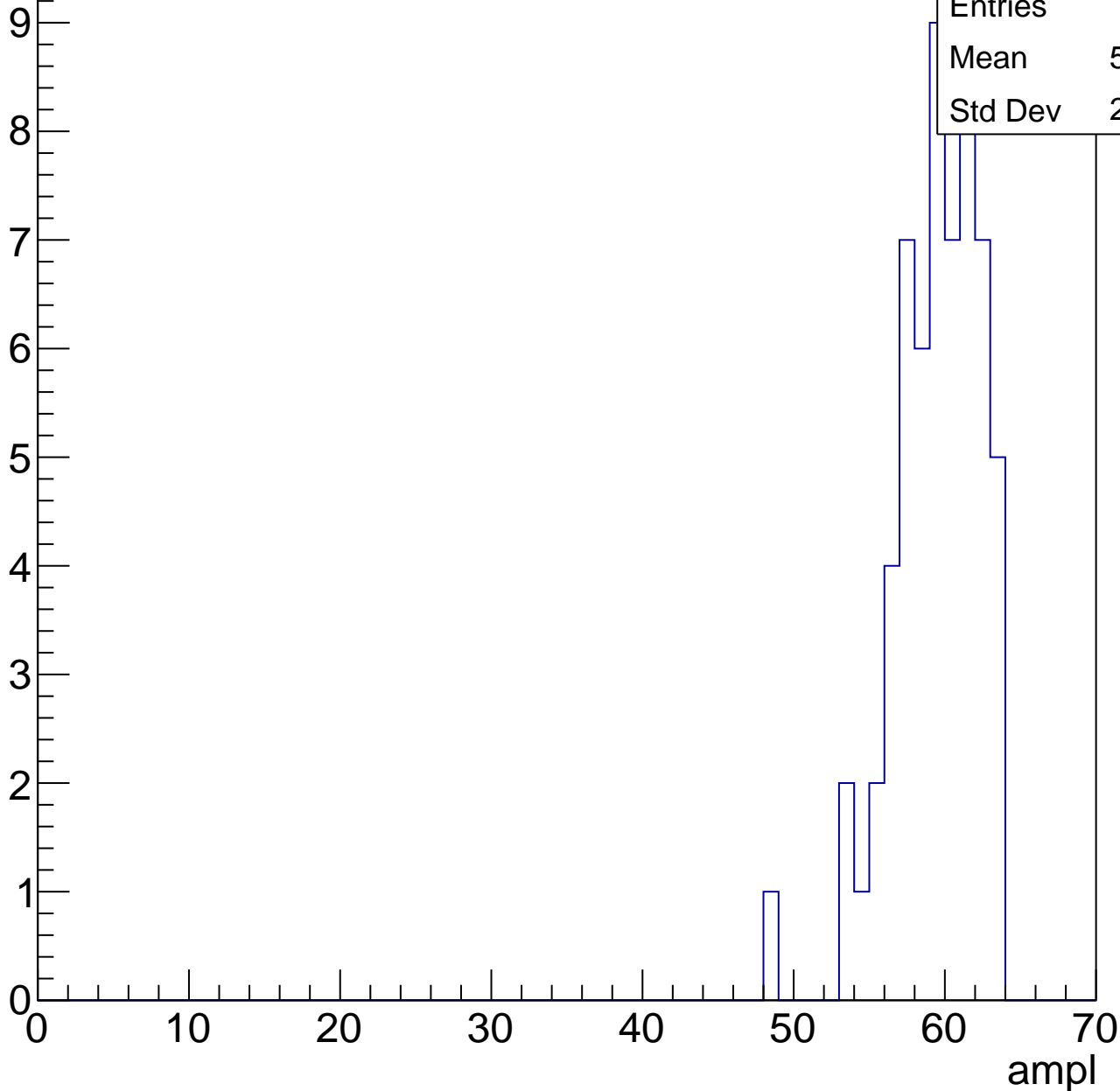
Entries	64
Mean	53.53
Std Dev	3.269

B1L003S, U16-ch67, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	58.97
Std Dev	2.904

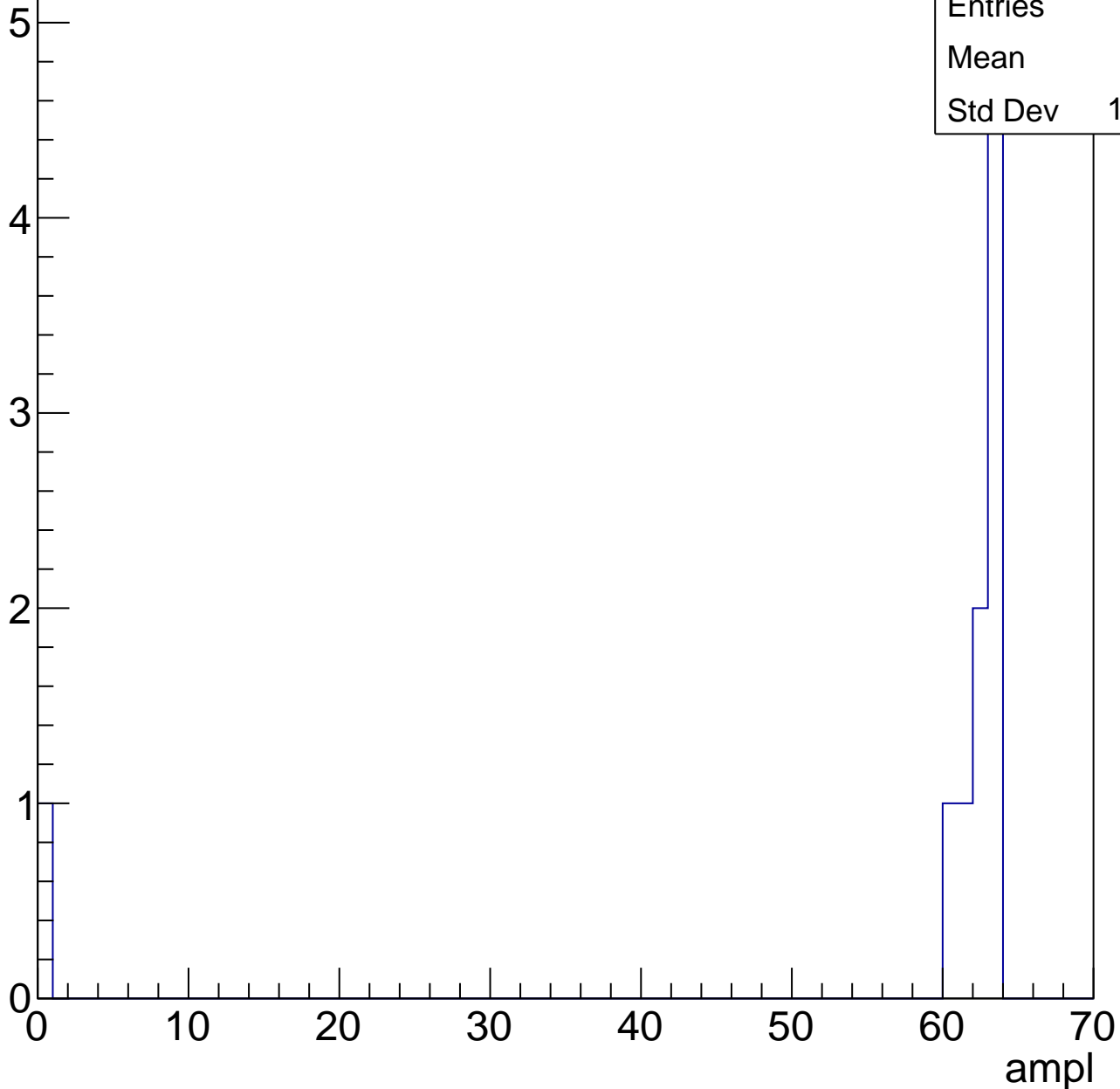


B1L003S, U16-ch67, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	56
Std Dev	18.69



B1L003S, U16-ch67, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch68, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	29.09
Std Dev	3.683

Gaus mean : 29.6352

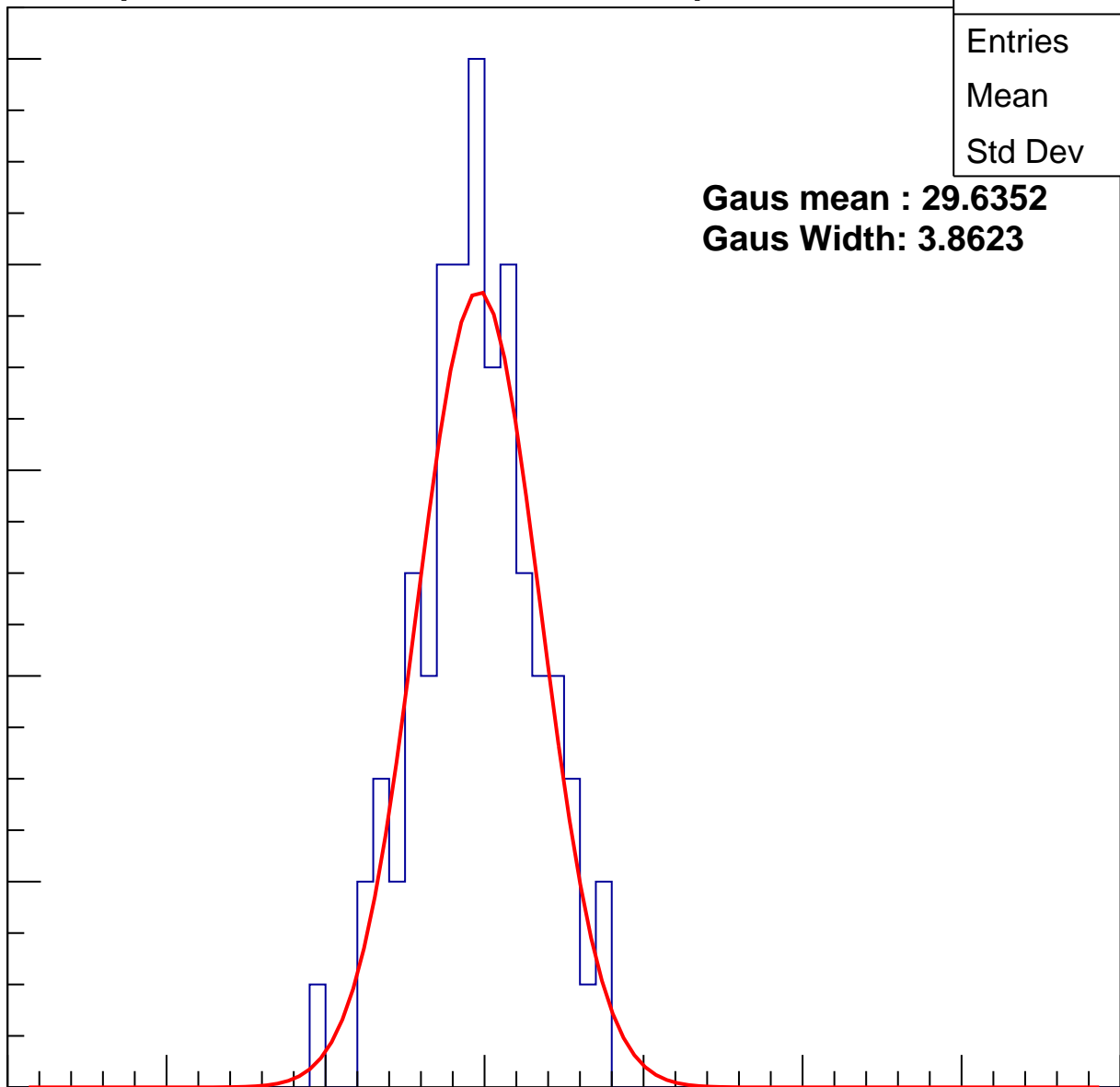
Gaus Width: 3.8623

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch68, adc1

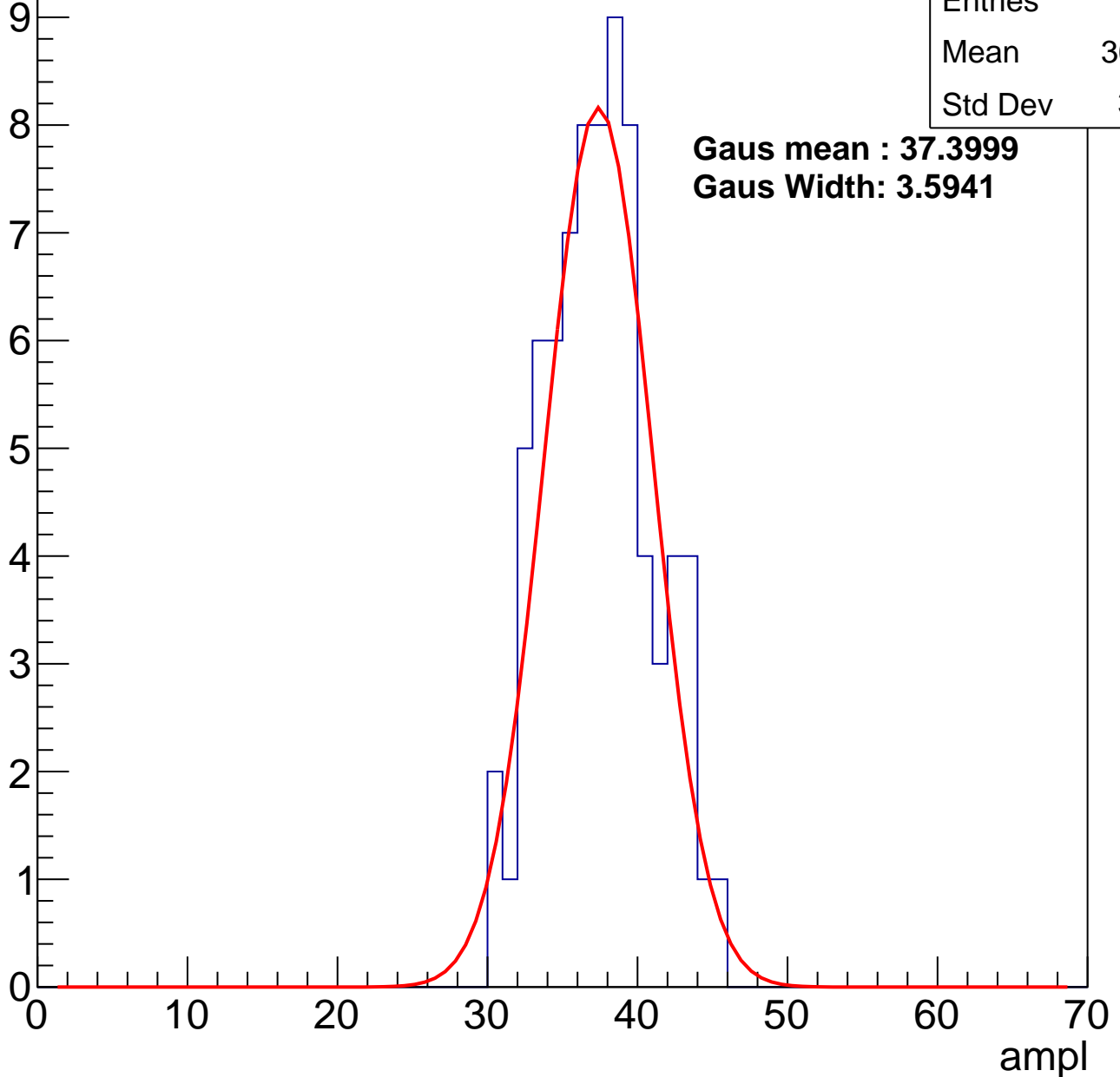
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	36.99
Std Dev	3.47

Gaus mean : 37.3999

Gaus Width: 3.5941

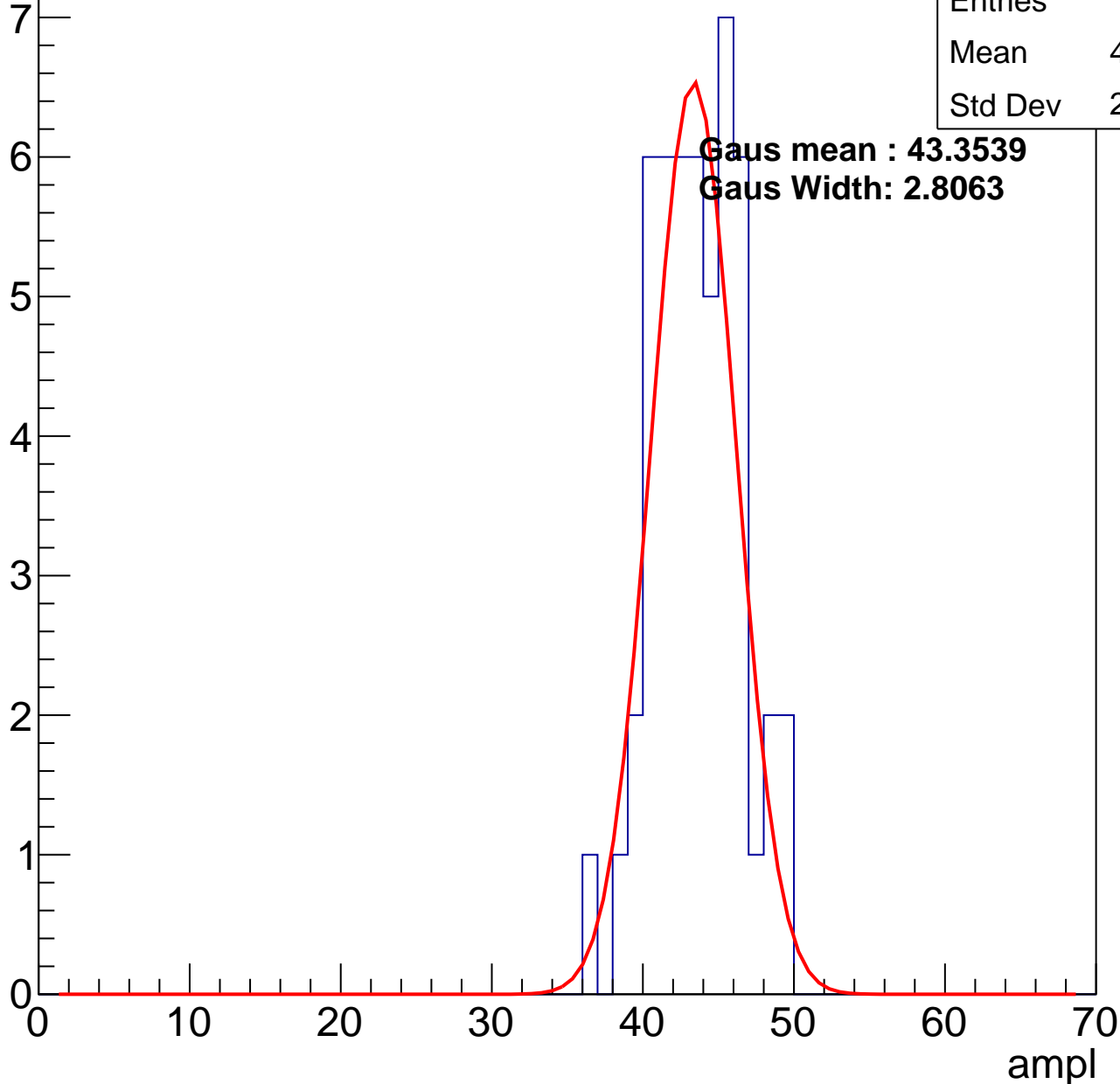


B1L003S, U16-ch68, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.14
Std Dev	2.849

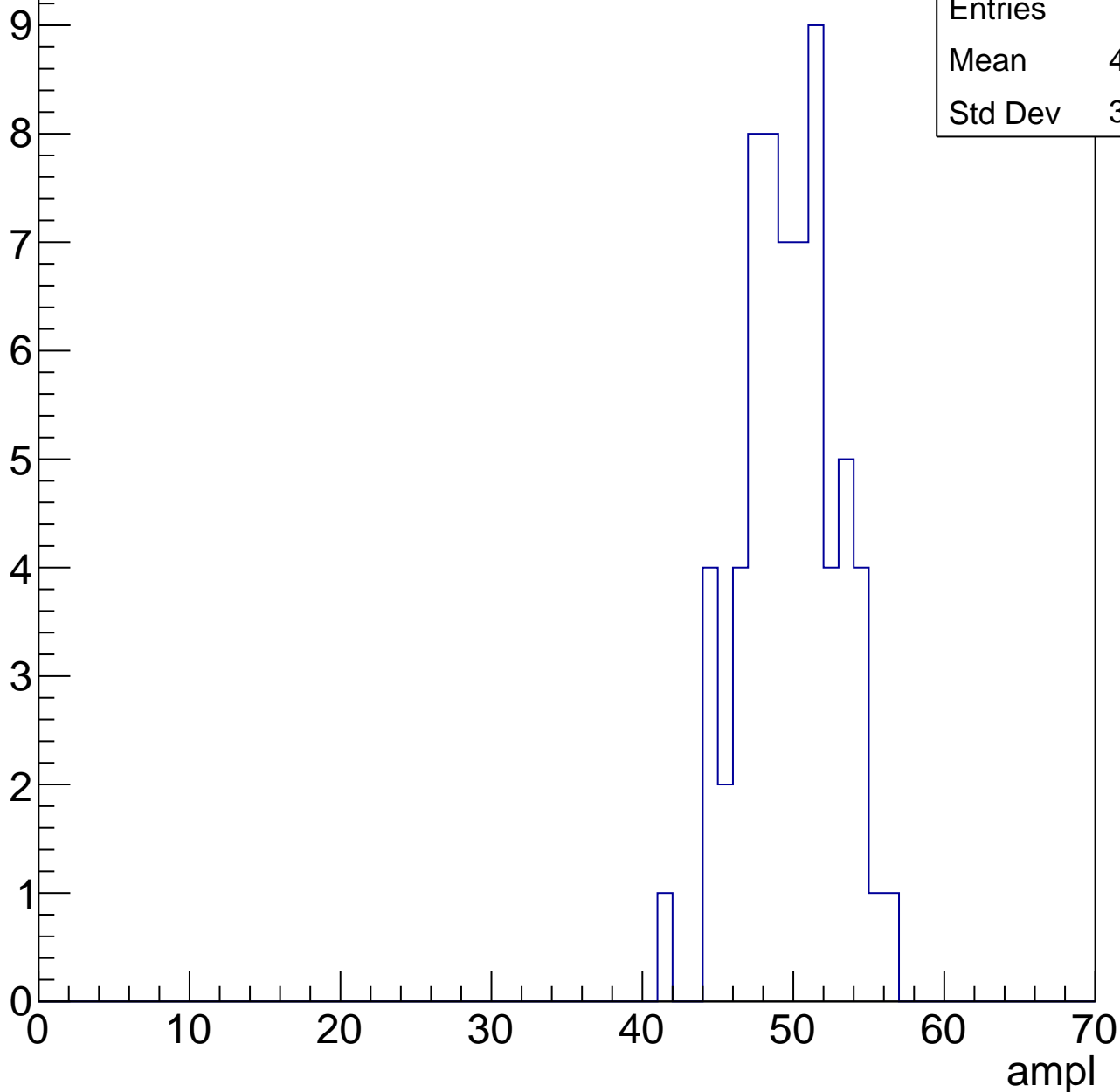


B1L003S, U16-ch68, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.28
Std Dev	3.066

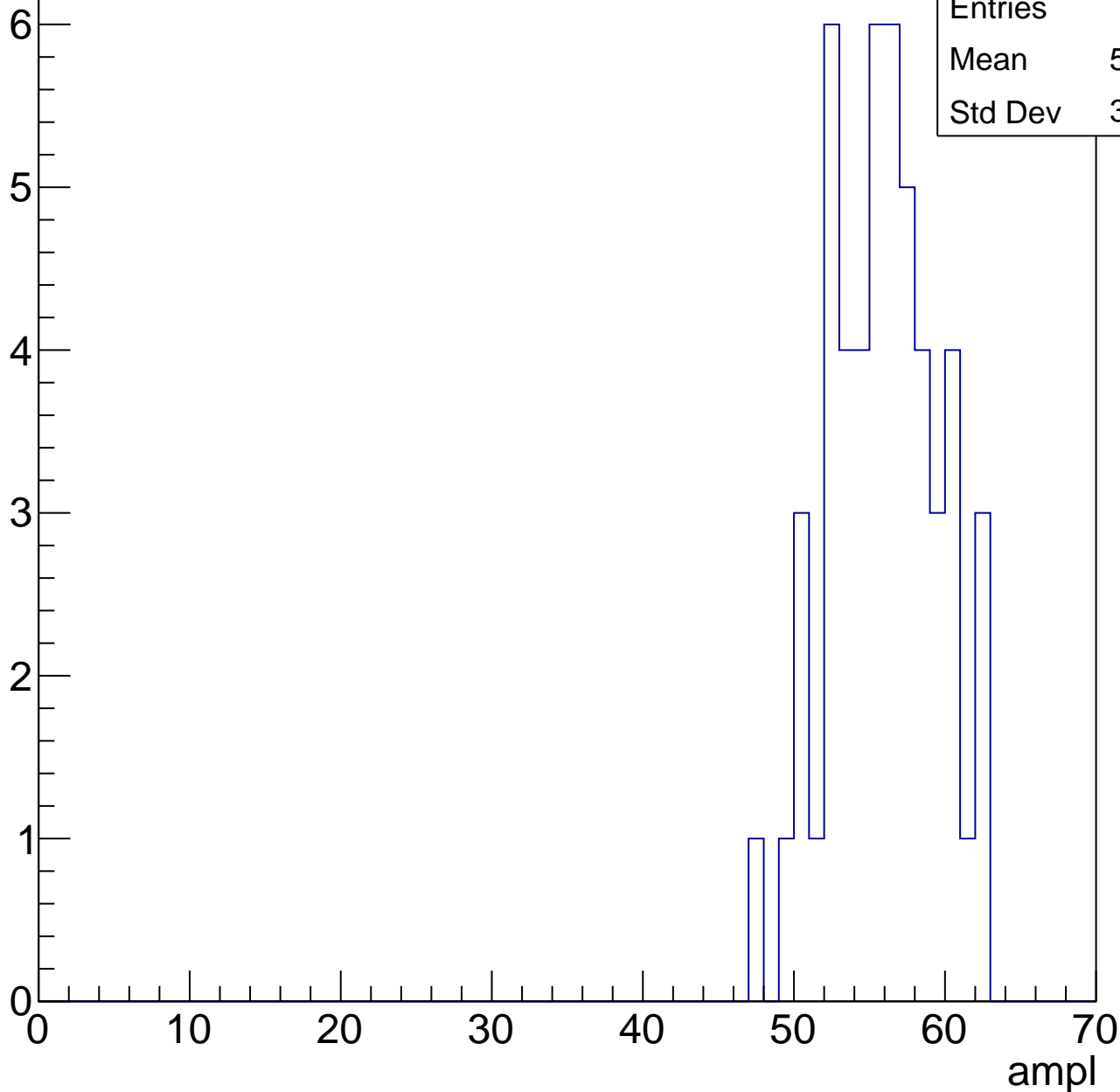


B1L003S, U16-ch68, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.46
Std Dev	3.527

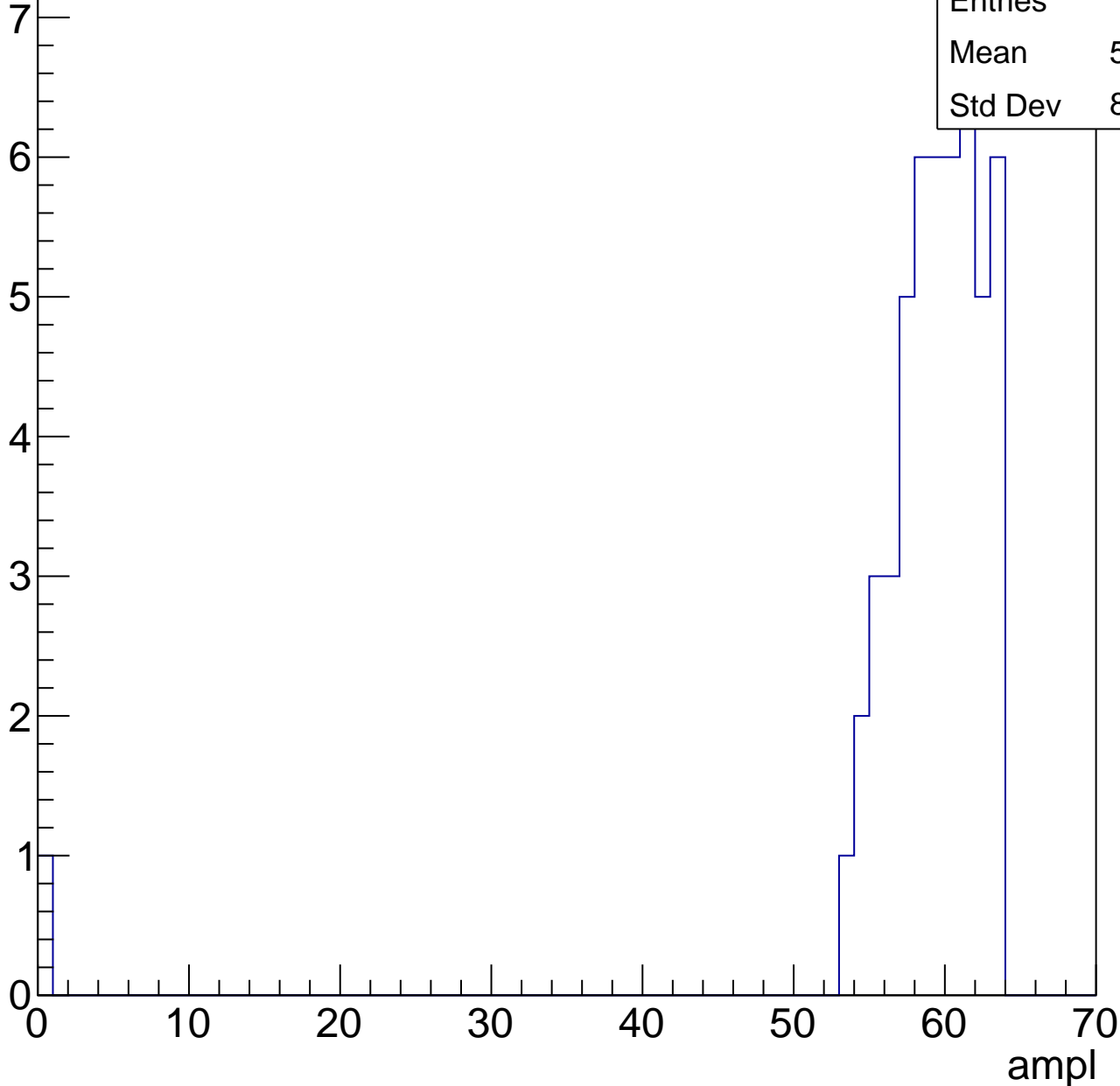


B1L003S, U16-ch68, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

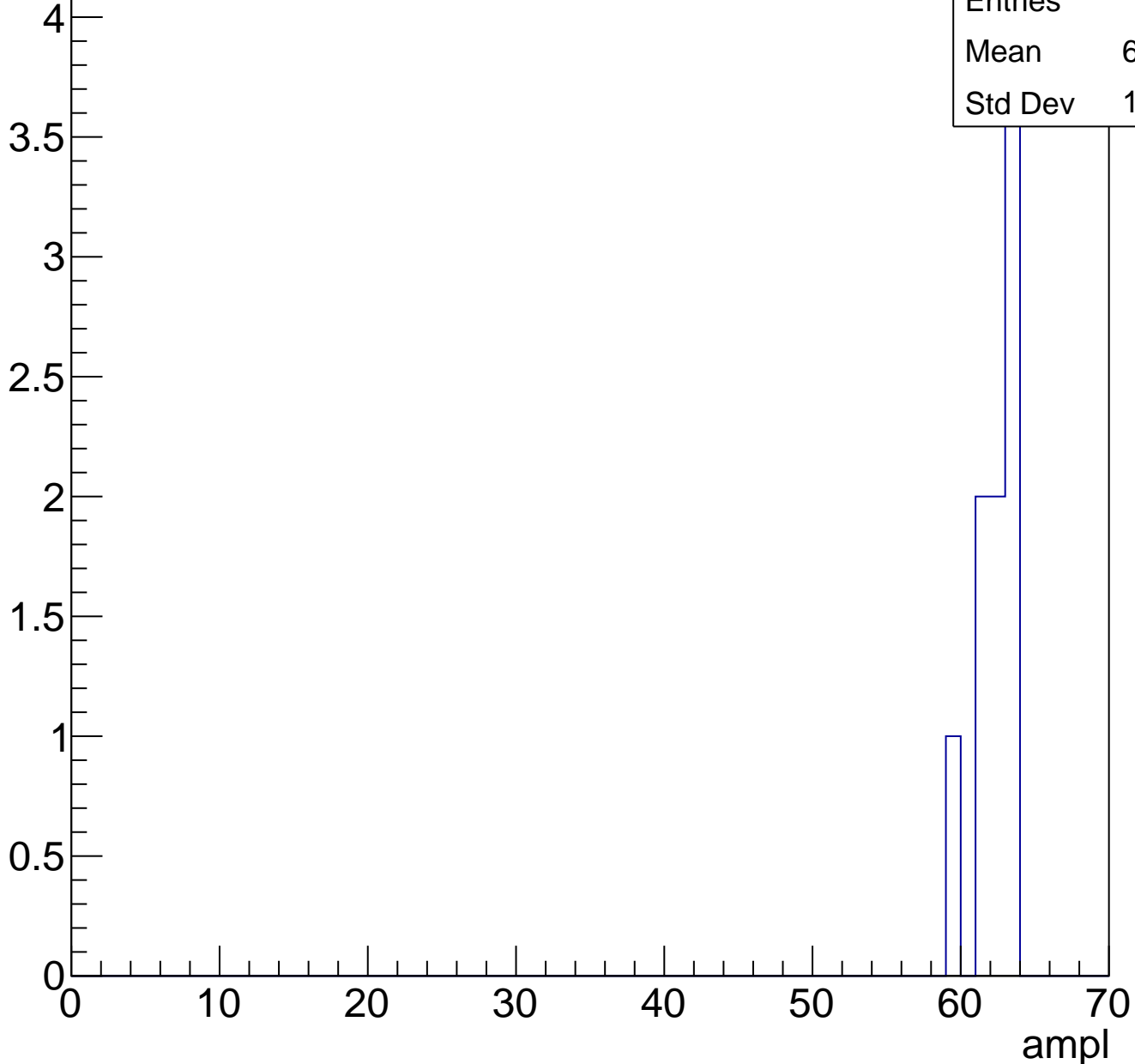
Entries	51
Mean	57.96
Std Dev	8.618



B1L003S, U16-ch68, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch68, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L003S, U16-ch69, adc0

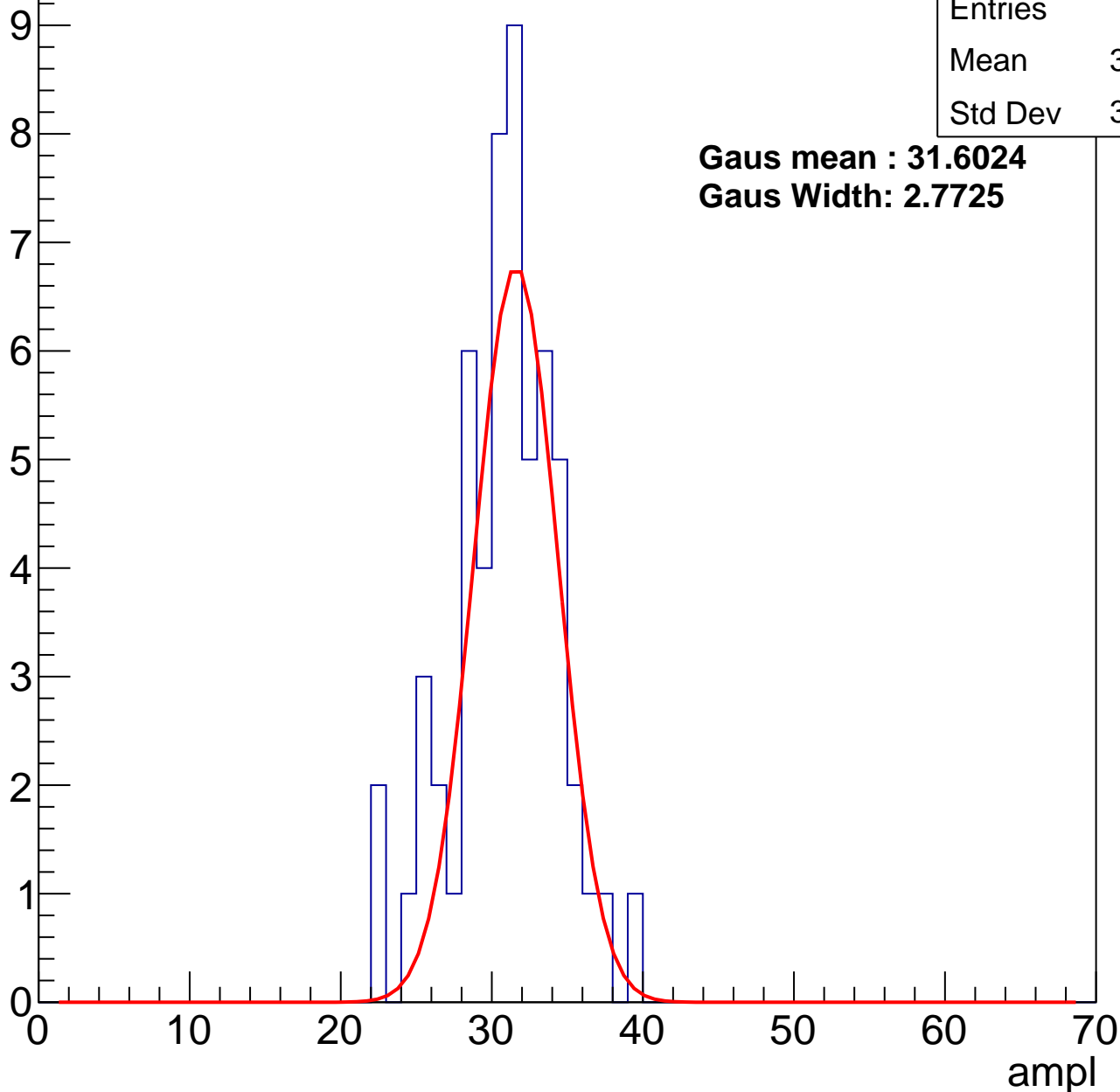
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	30.44
Std Dev	3.444

Gaus mean : 31.6024

Gaus Width: 2.7725



B1L003S, U16-ch69, adc1

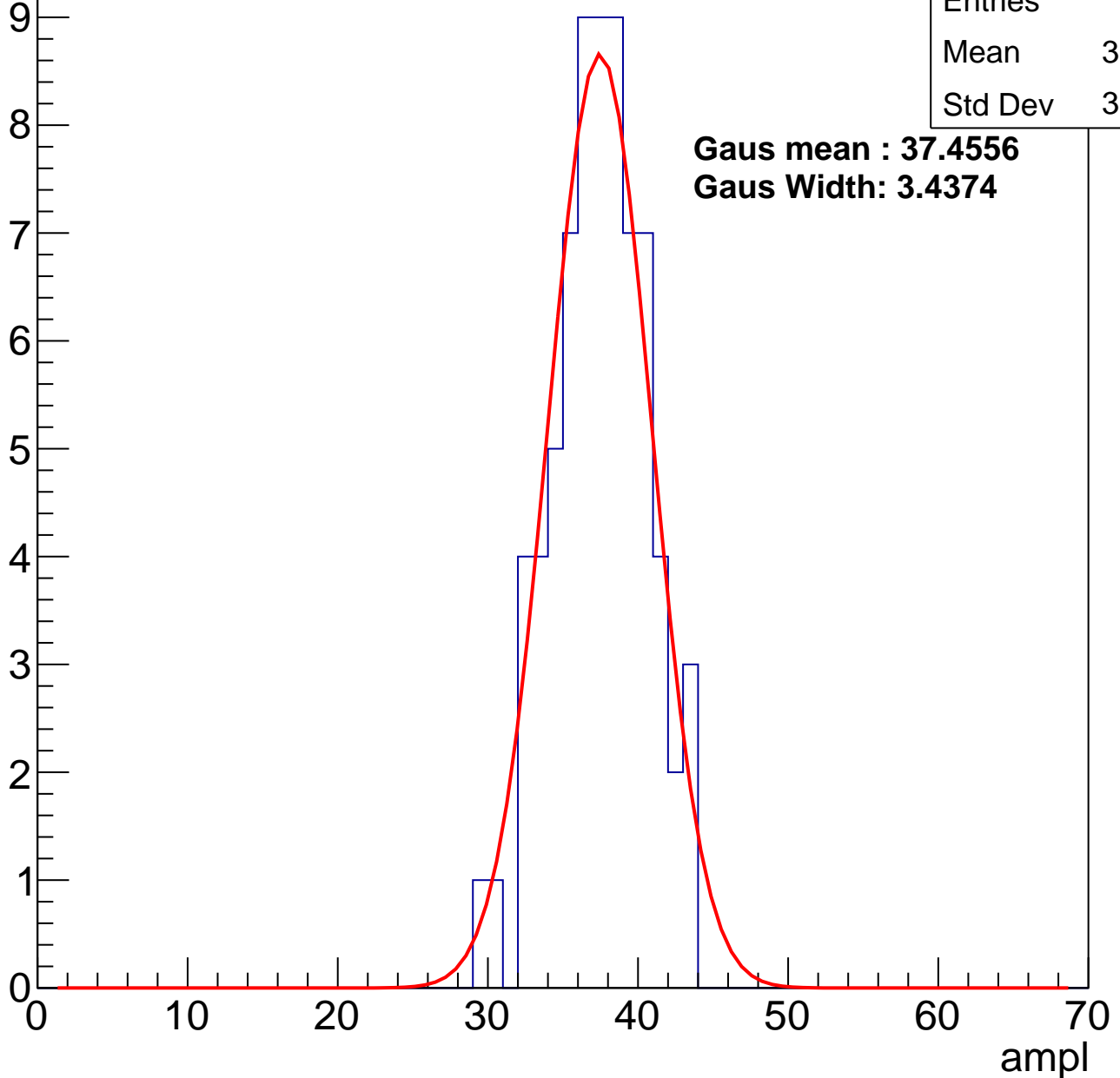
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.99
Std Dev	3.075

Gaus mean : 37.4556

Gaus Width: 3.4374



B1L003S, U16-ch69, adc2

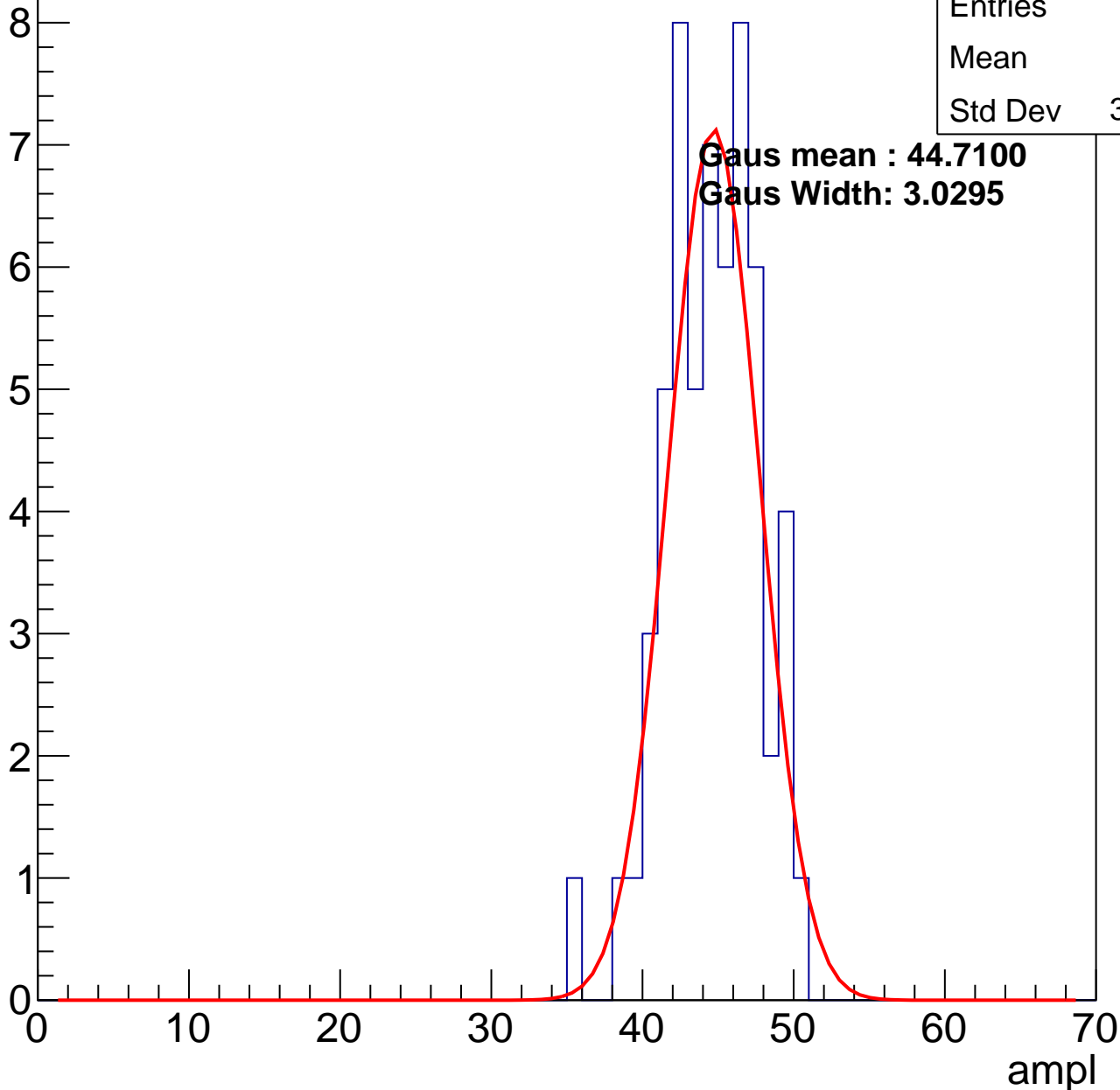
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.1
Std Dev	3.027

Gaus mean : 44.7100

Gaus Width: 3.0295

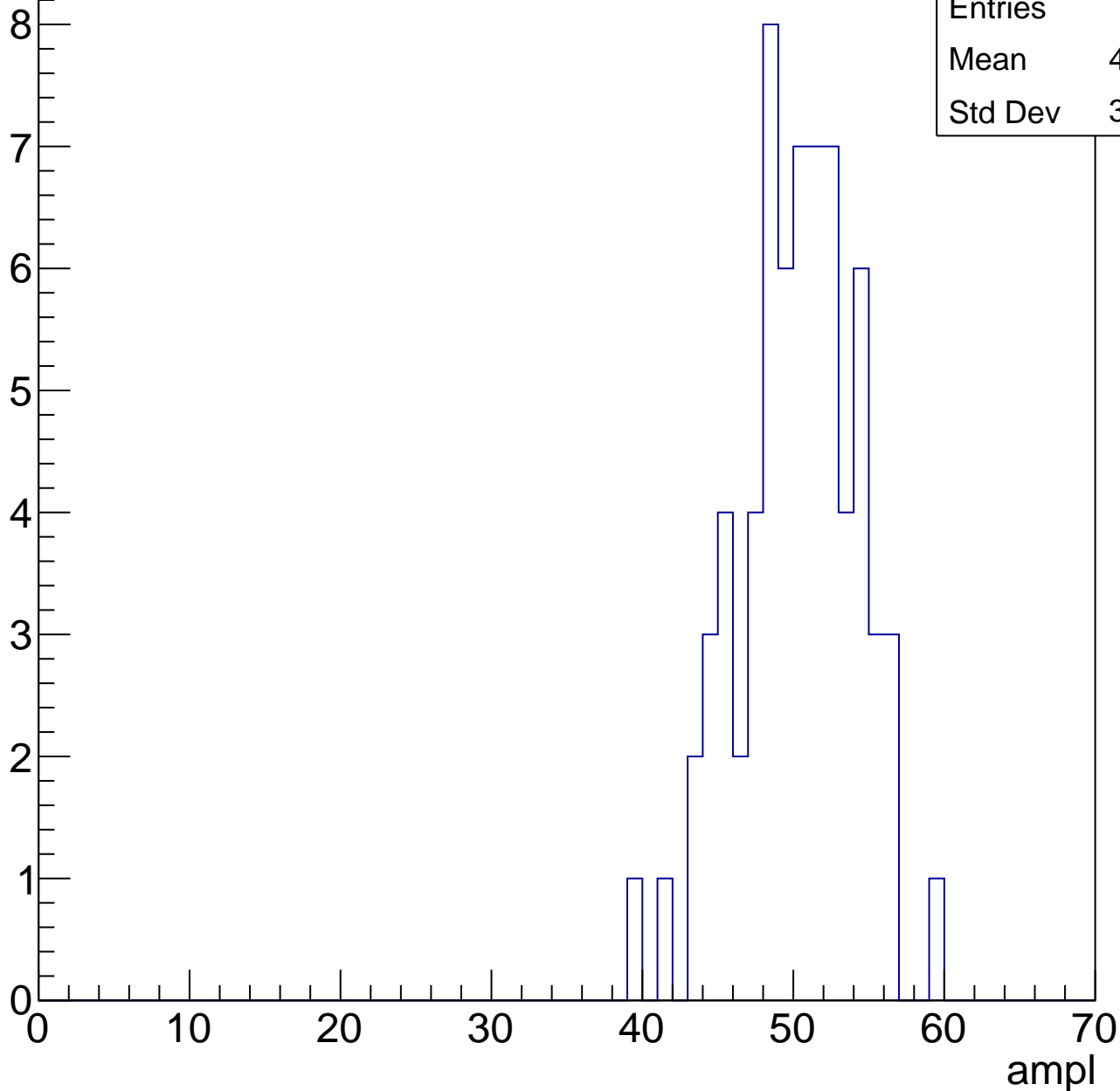


B1L003S, U16-ch69, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.78
Std Dev	3.886

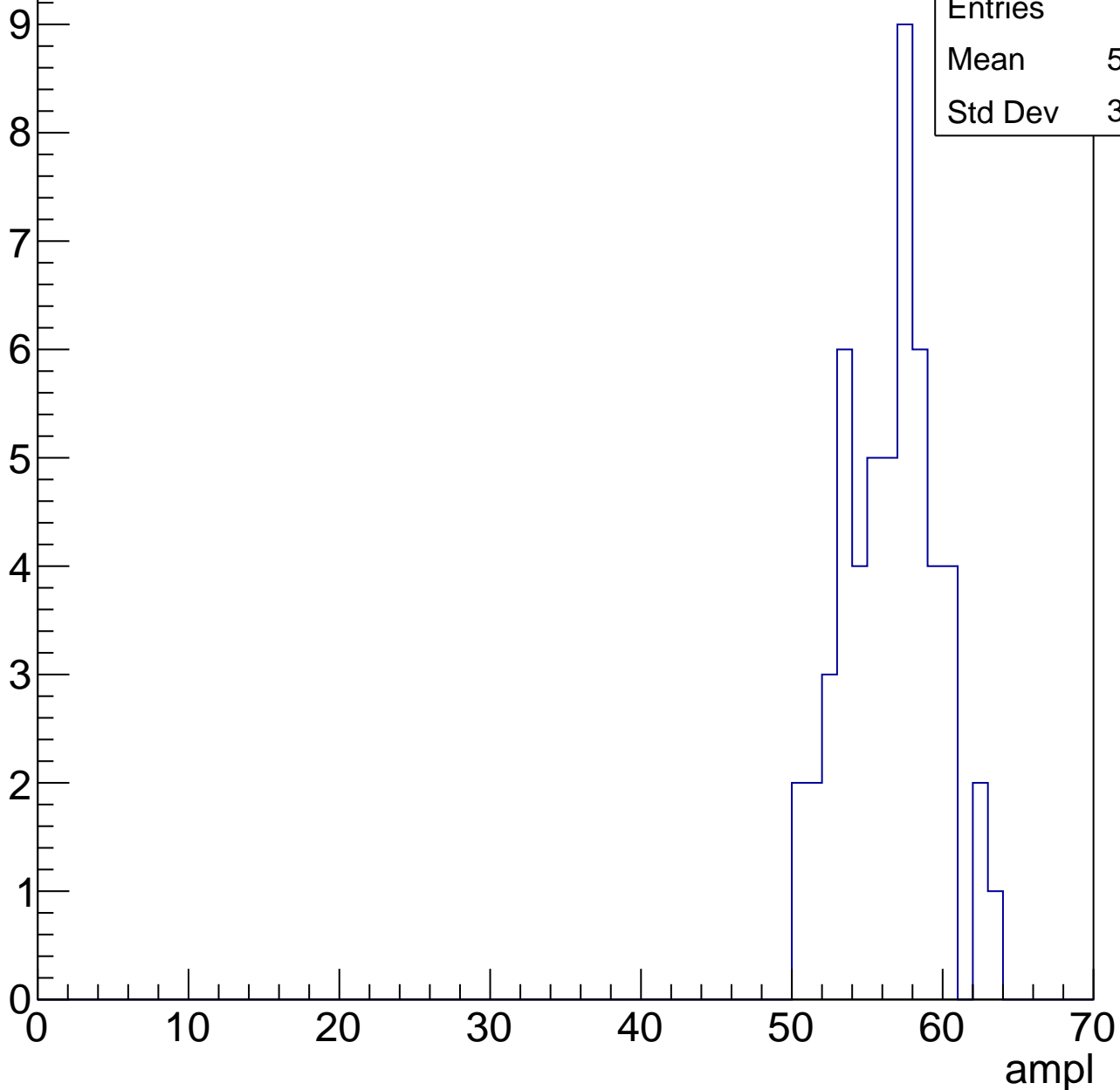


B1L003S, U16-ch69, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56.06
Std Dev	3.068

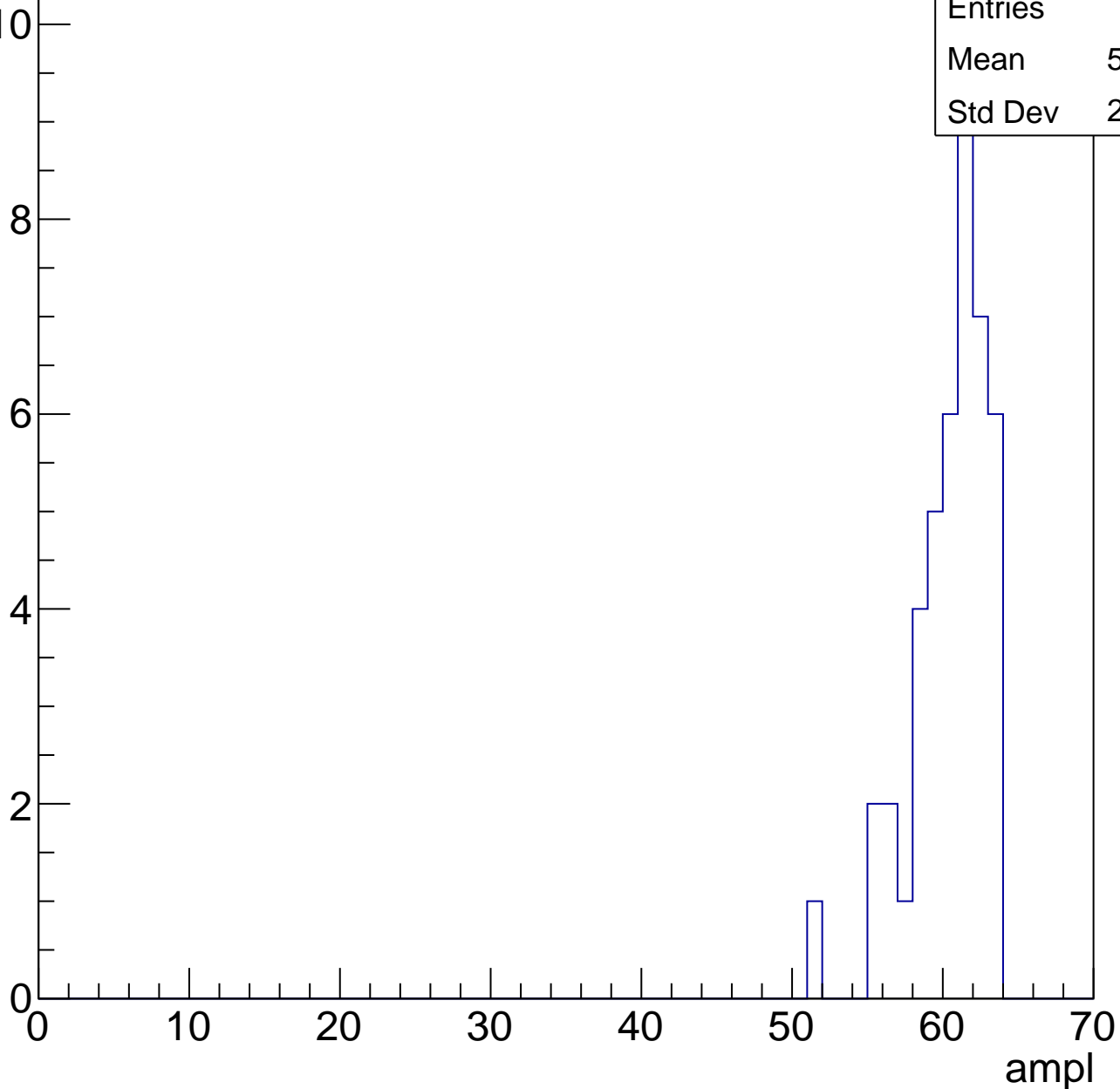


B1L003S, U16-ch69, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.98
Std Dev	2.545



B1L003S, U16-ch69, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch69, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch70, adc0

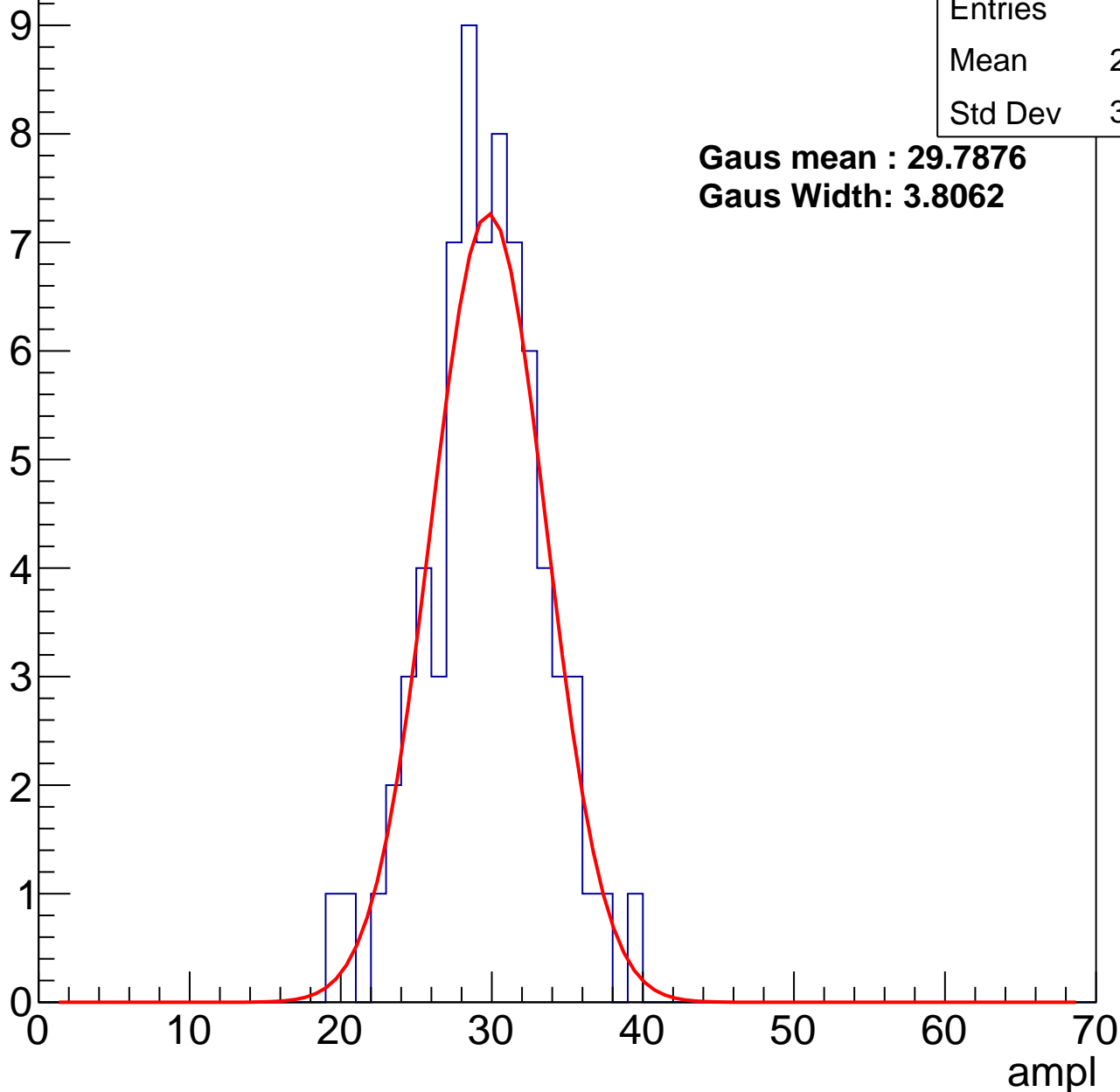
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.18
Std Dev	3.824

Gaus mean : 29.7876

Gaus Width: 3.8062



B1L003S, U16-ch70, adc1

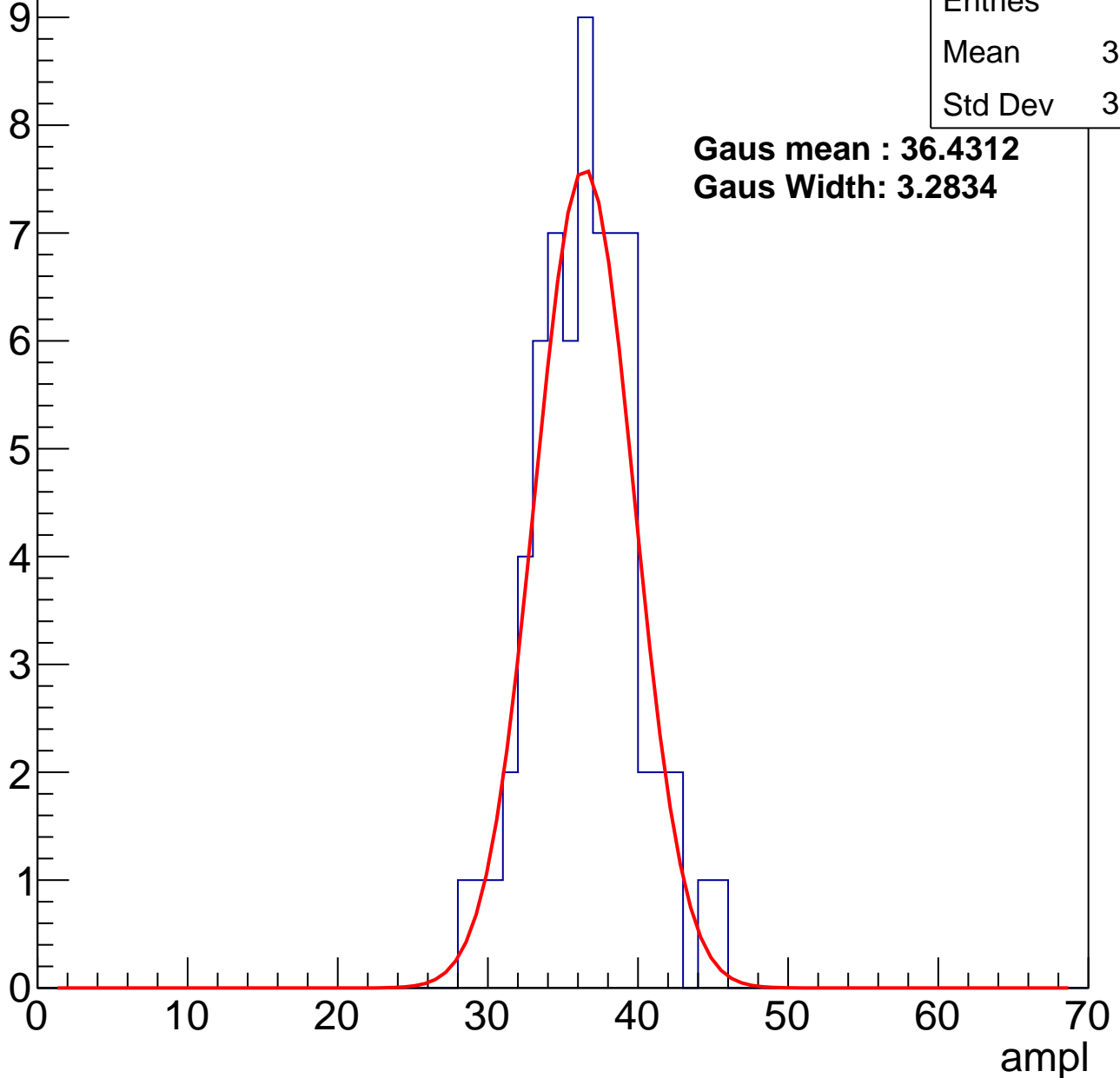
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.06
Std Dev	3.366

Gaus mean : 36.4312

Gaus Width: 3.2834



B1L003S, U16-ch70, adc2

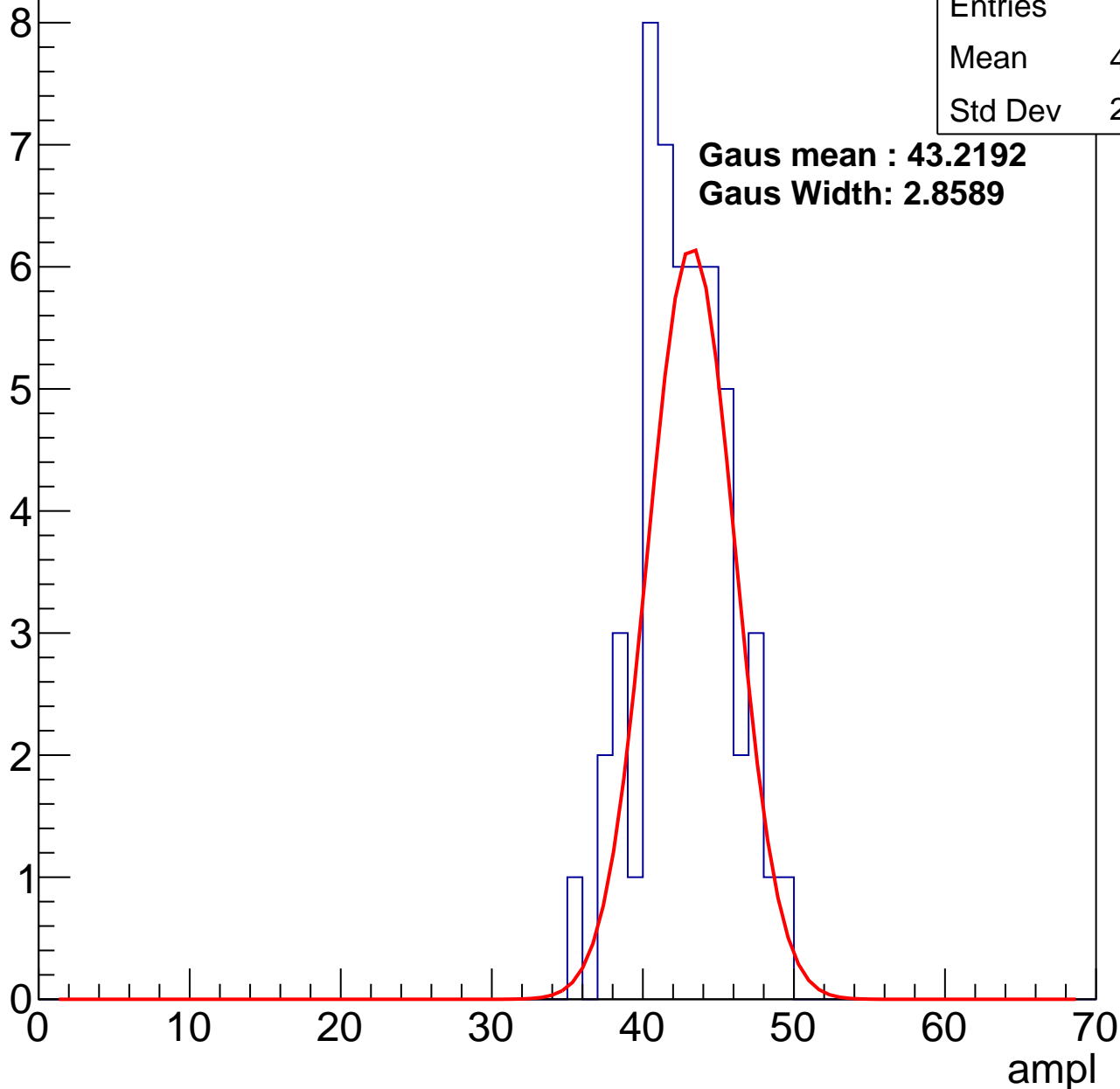
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.27
Std Dev	2.969

Gaus mean : 43.2192

Gaus Width: 2.8589

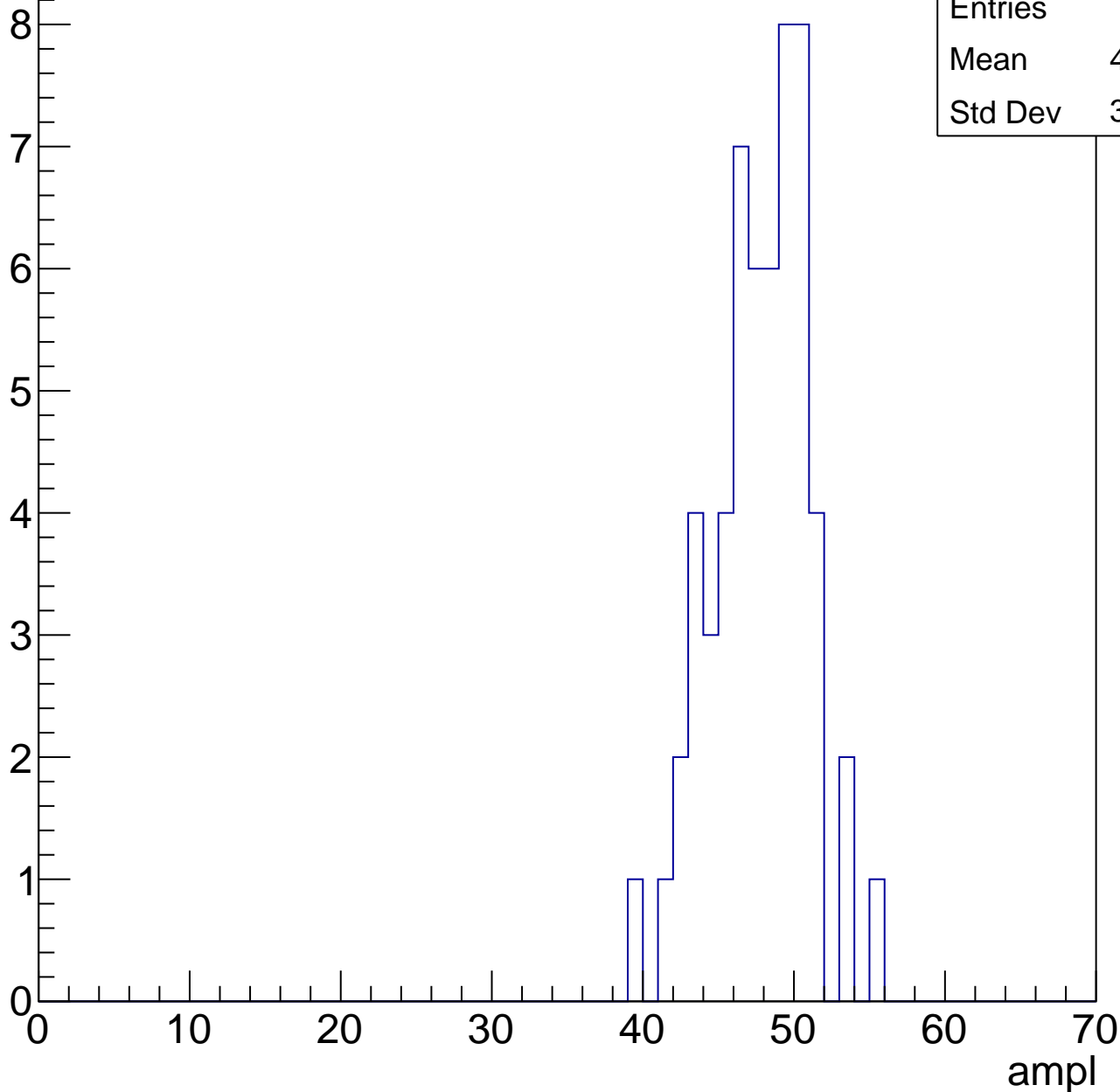


B1L003S, U16-ch70, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.32
Std Dev	3.158



B1L003S, U16-ch70, adc4

calib_packv5_042523_0143.root, FC#13, port D2

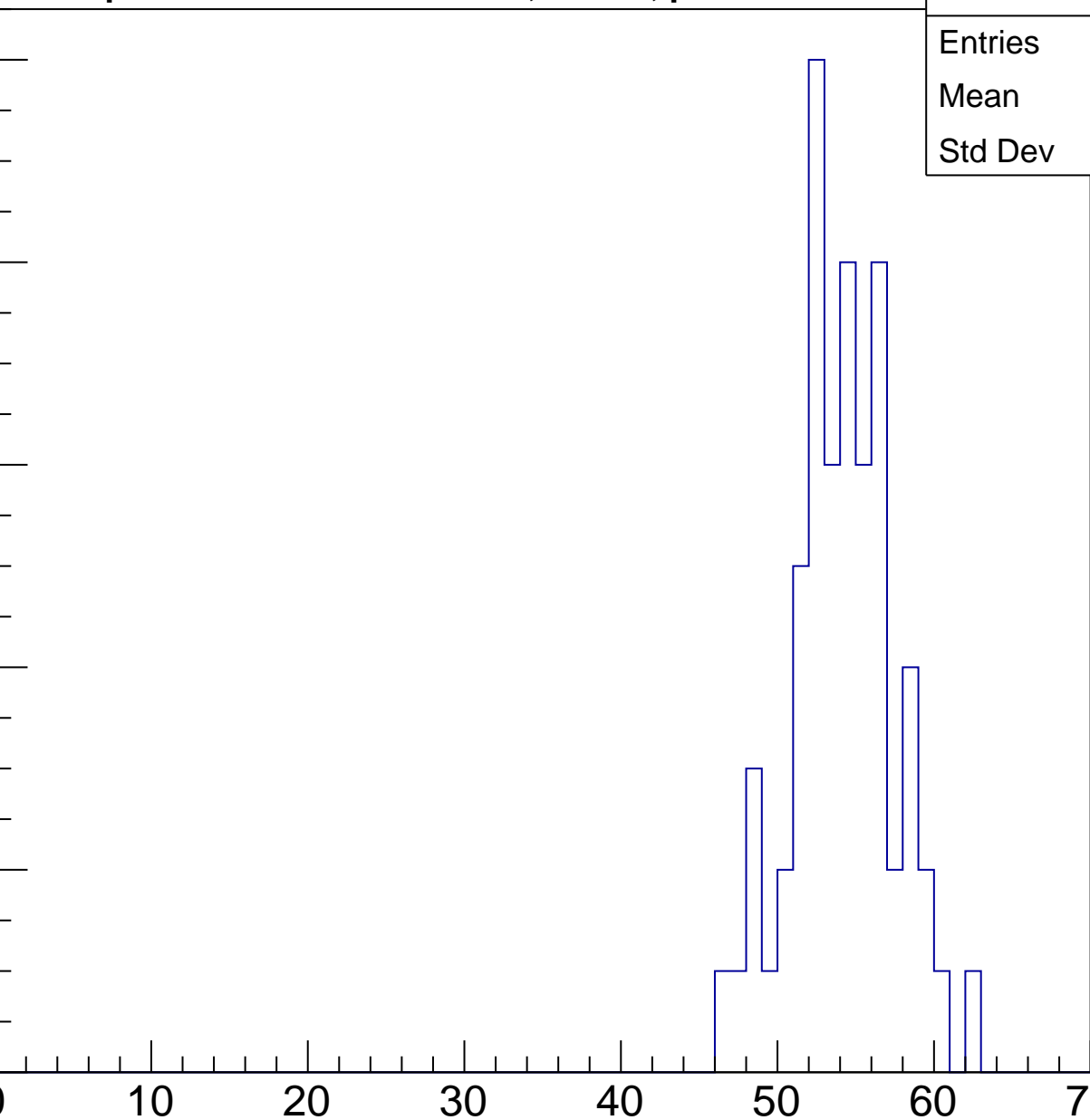
Entries	61
Mean	53.69
Std Dev	3.222

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch70, adc5

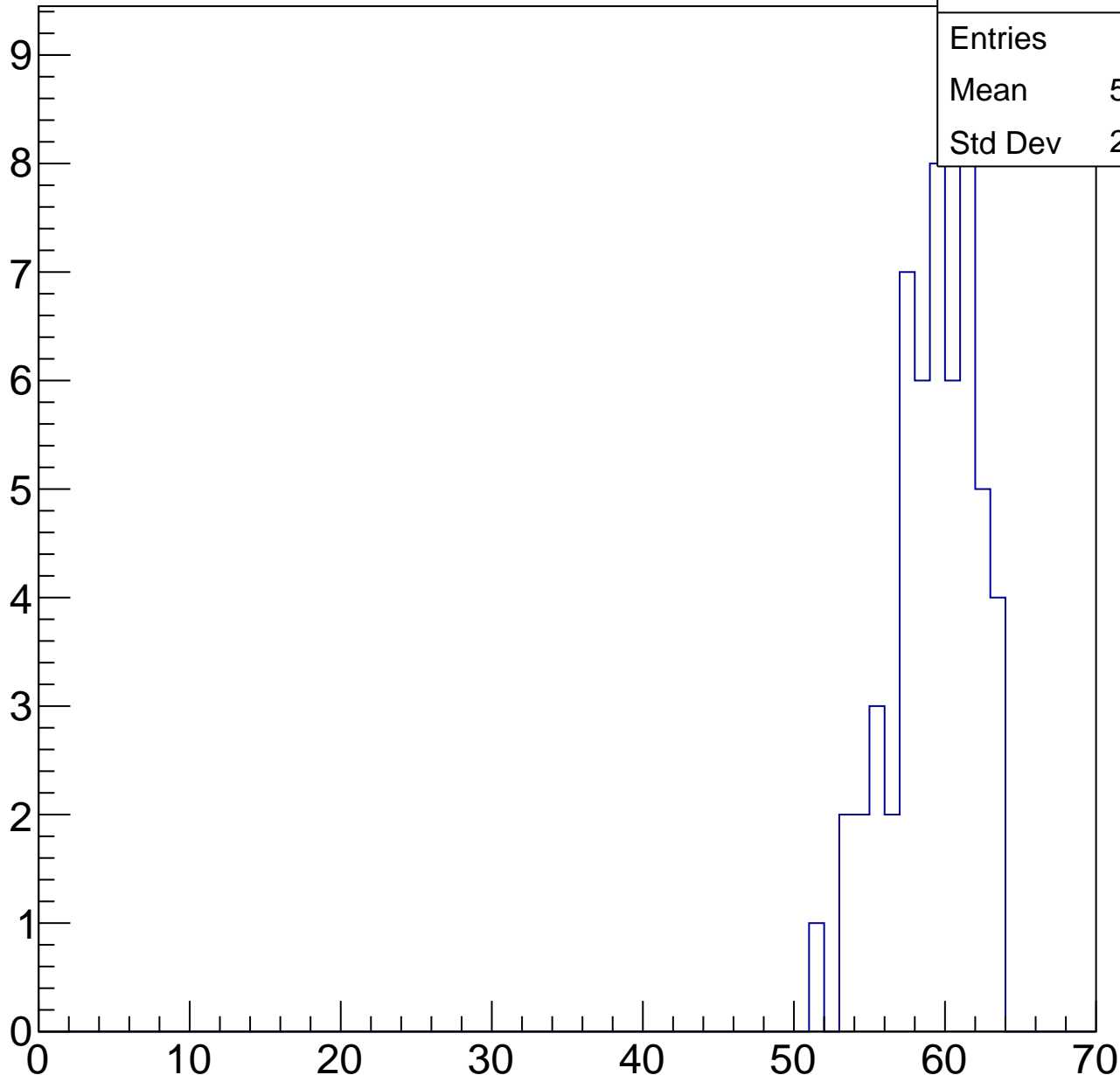
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.76
Std Dev	2.809

ampl

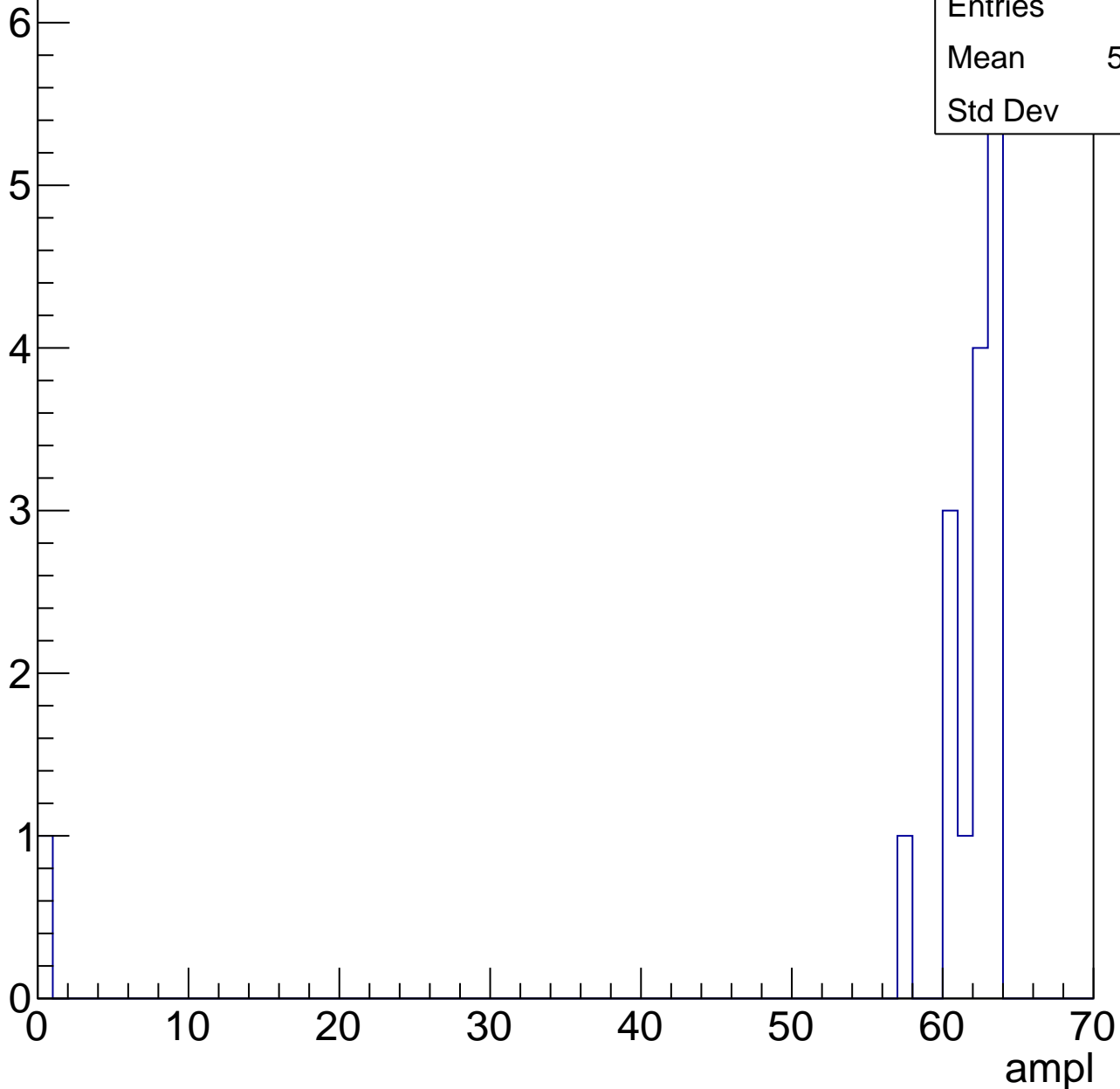


B1L003S, U16-ch70, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.75
Std Dev	15



B1L003S, U16-ch70, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch71, adc0

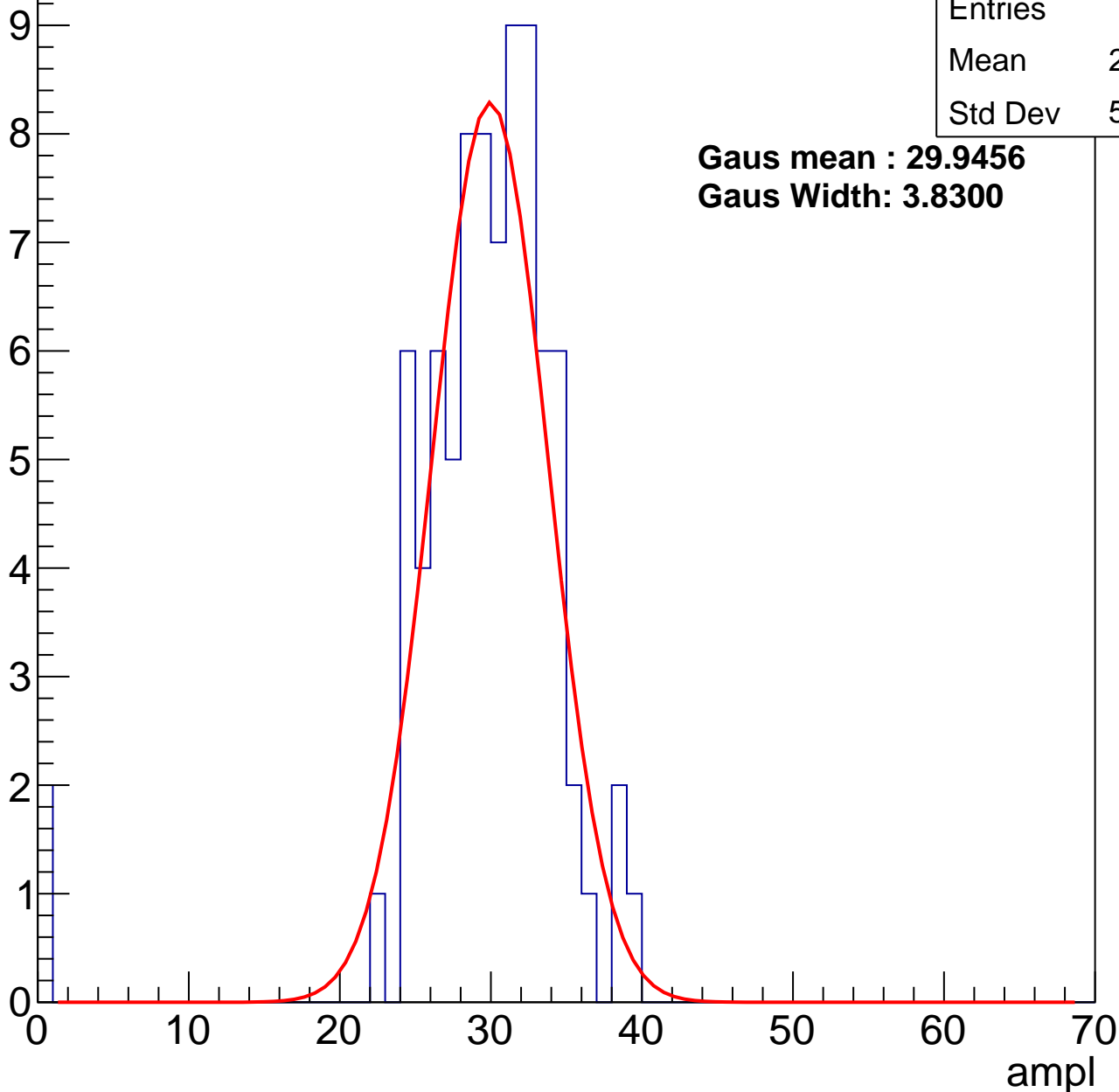
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	29.07
Std Dev	5.787

Gaus mean : 29.9456

Gaus Width: 3.8300



B1L003S, U16-ch71, adc1

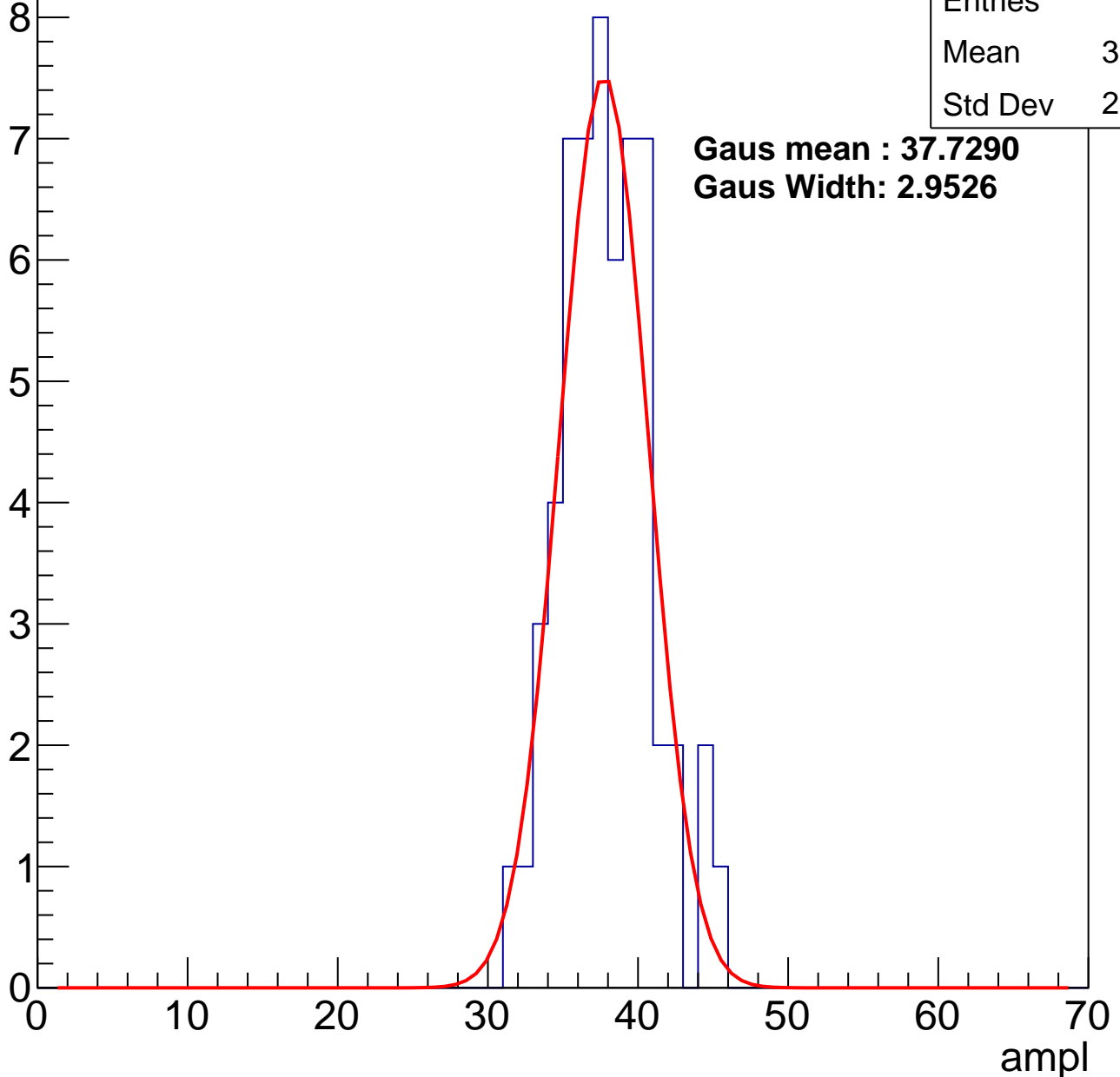
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	37.43
Std Dev	2.966

Gaus mean : 37.7290

Gaus Width: 2.9526



B1L003S, U16-ch71, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.36
Std Dev	3.812

Gaus mean : 43.7482

Gaus Width: 3.0614

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

10

8

6

4

2

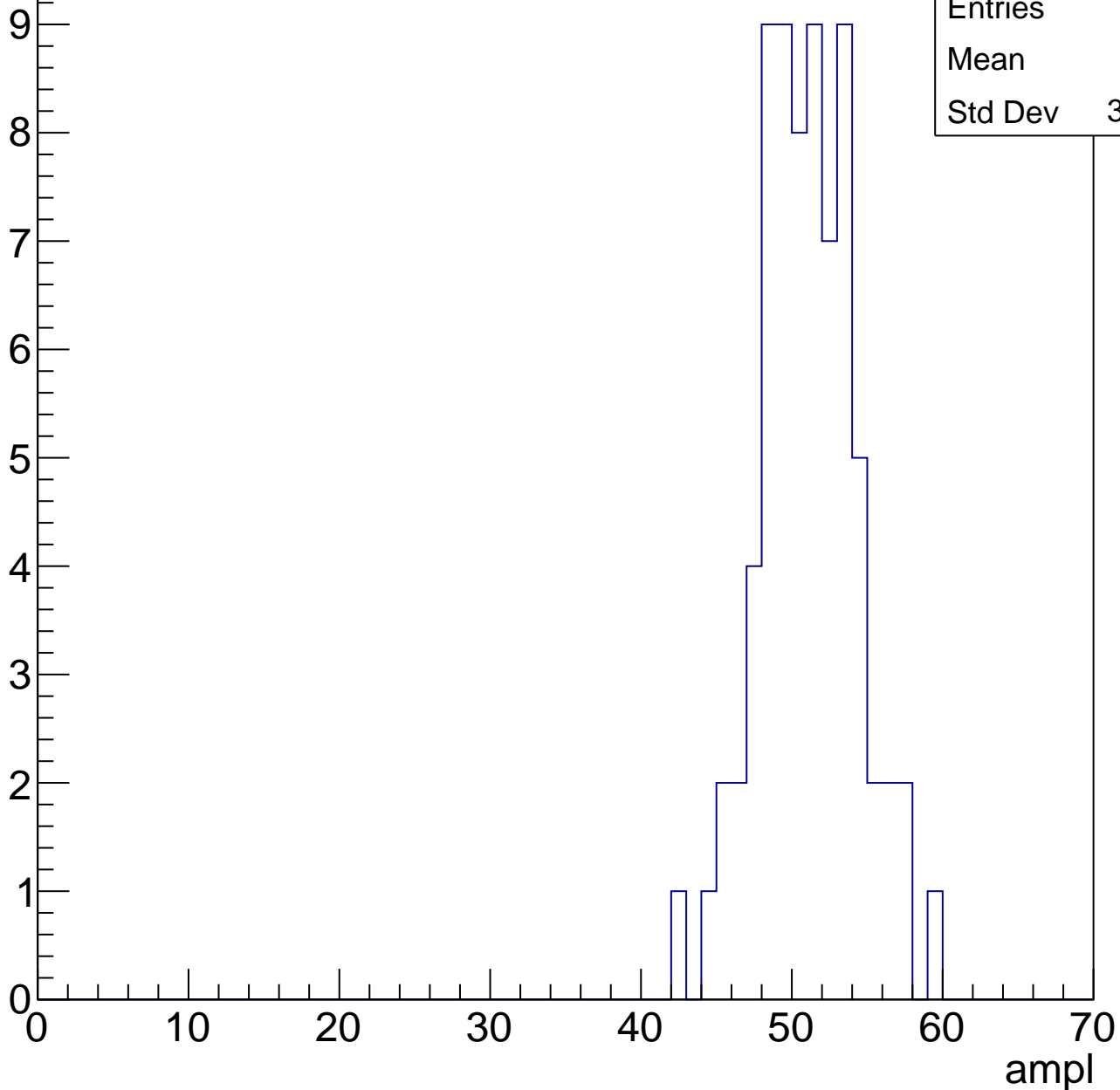
0

B1L003S, U16-ch71, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	50.6
Std Dev	3.174

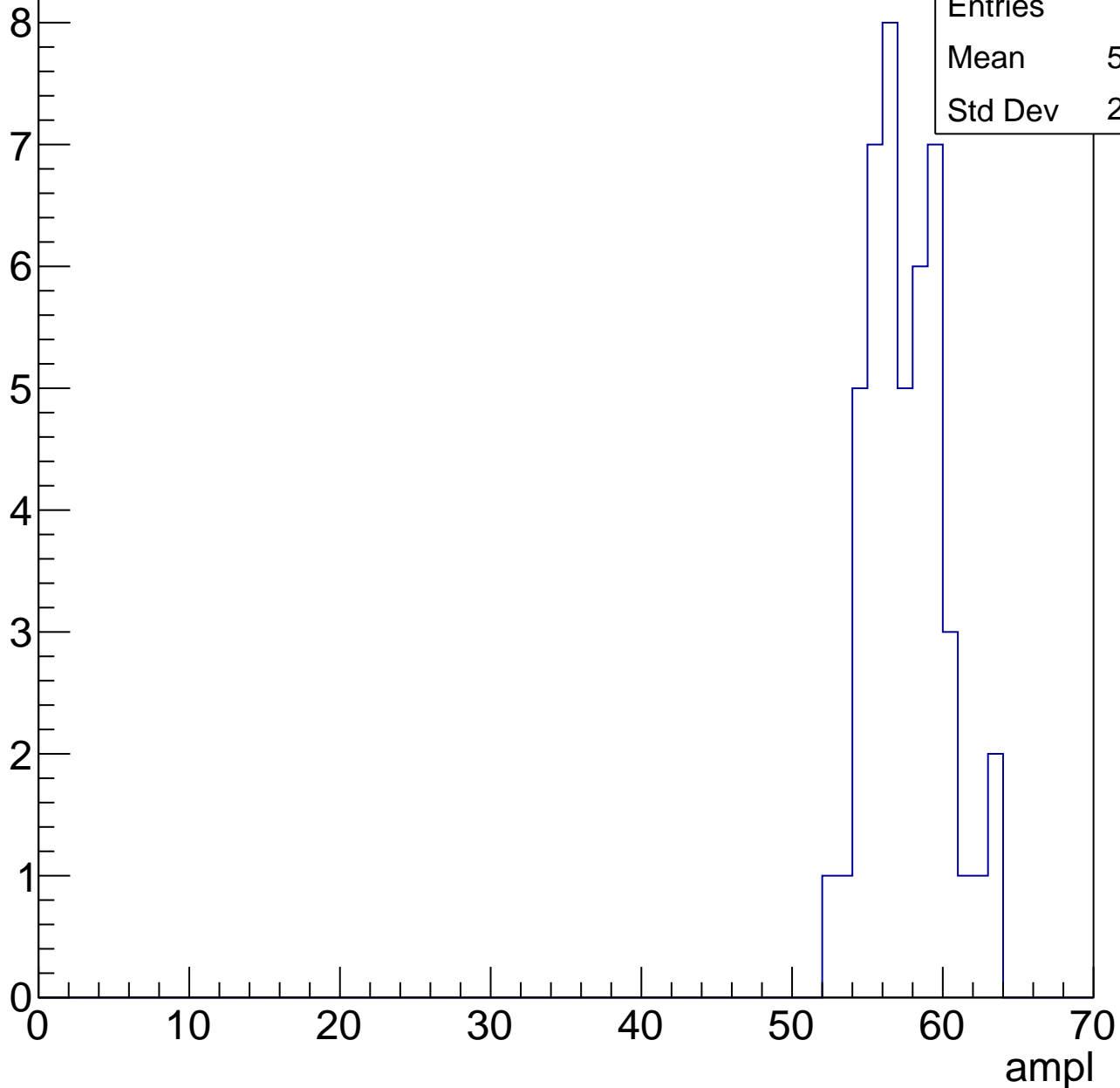


B1L003S, U16-ch71, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	57.09
Std Dev	2.508

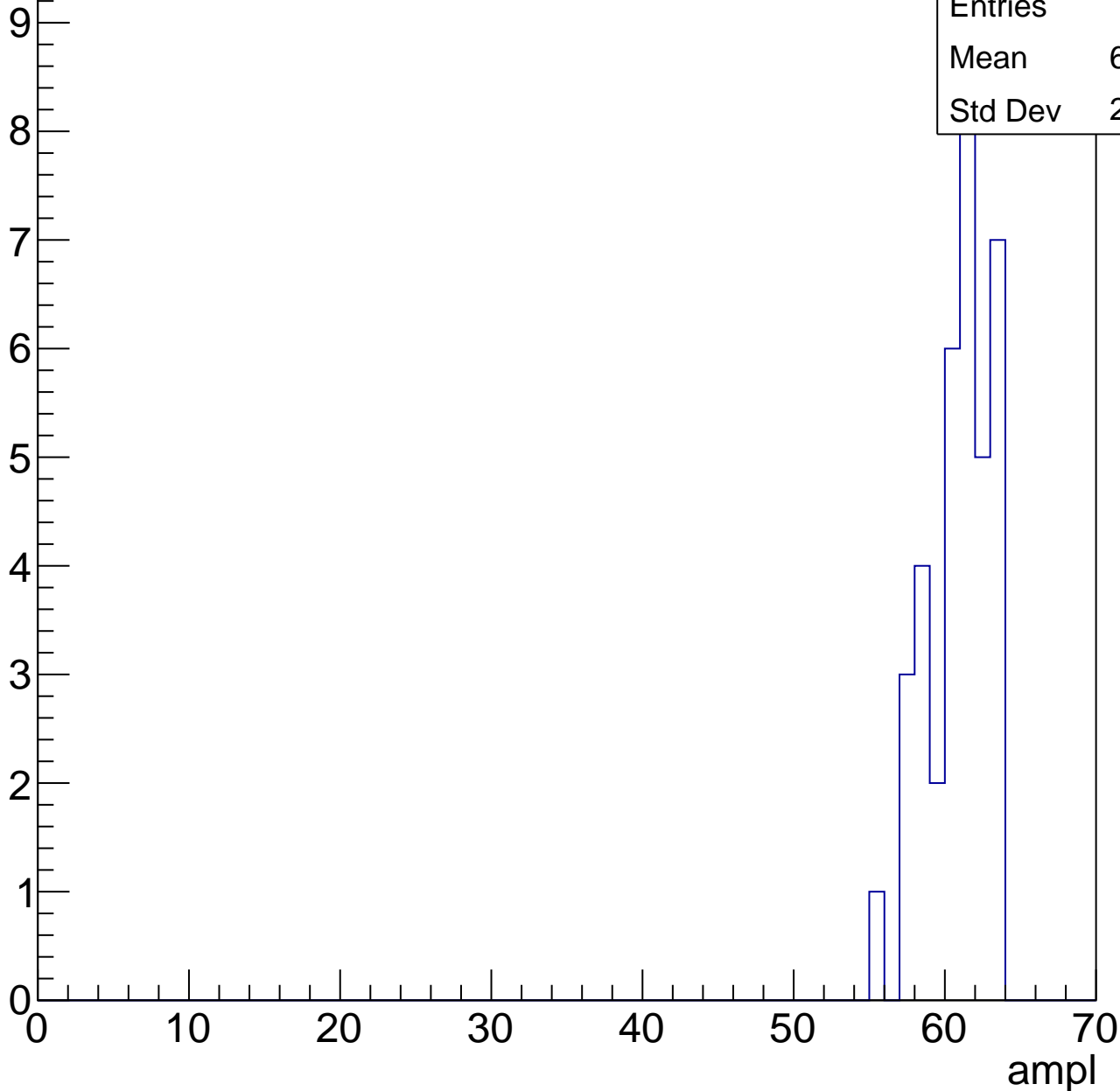


B1L003S, U16-ch71, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

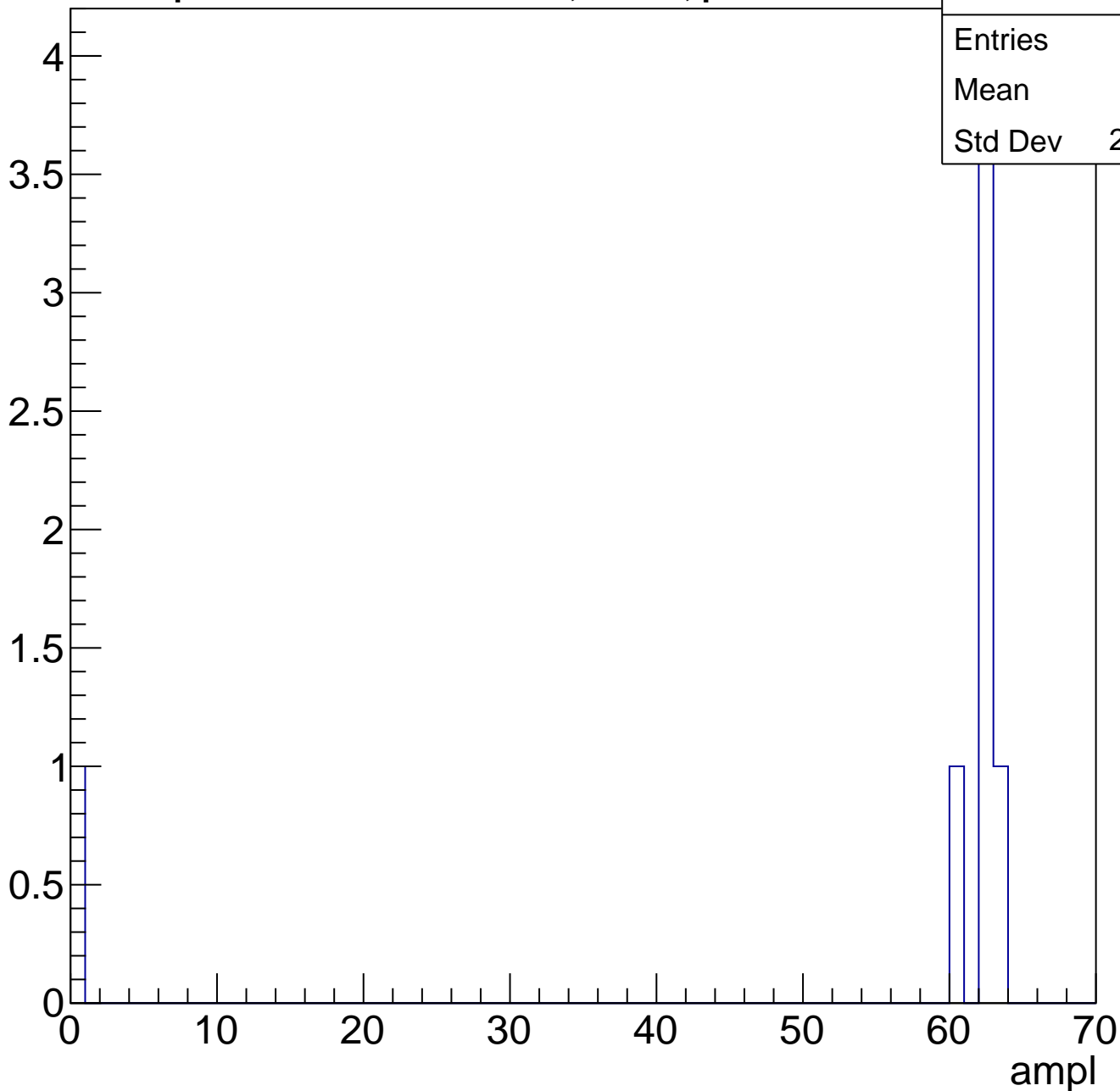
Entries	37
Mean	60.43
Std Dev	2.047



B1L003S, U16-ch71, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch71, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch72, adc0

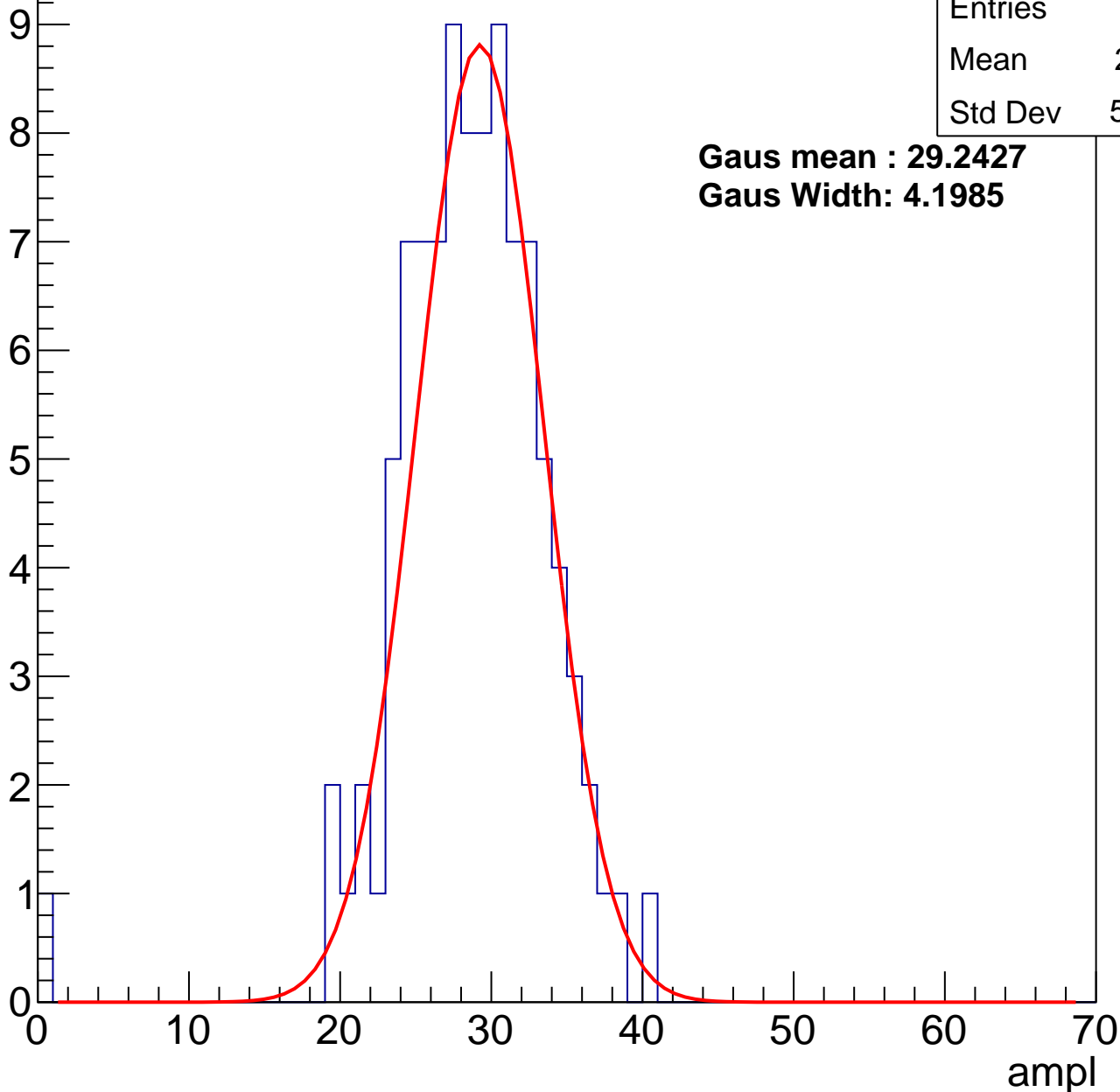
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	98
Mean	28.21
Std Dev	5.116

Gaus mean : 29.2427

Gaus Width: 4.1985



B1L003S, U16-ch72, adc1

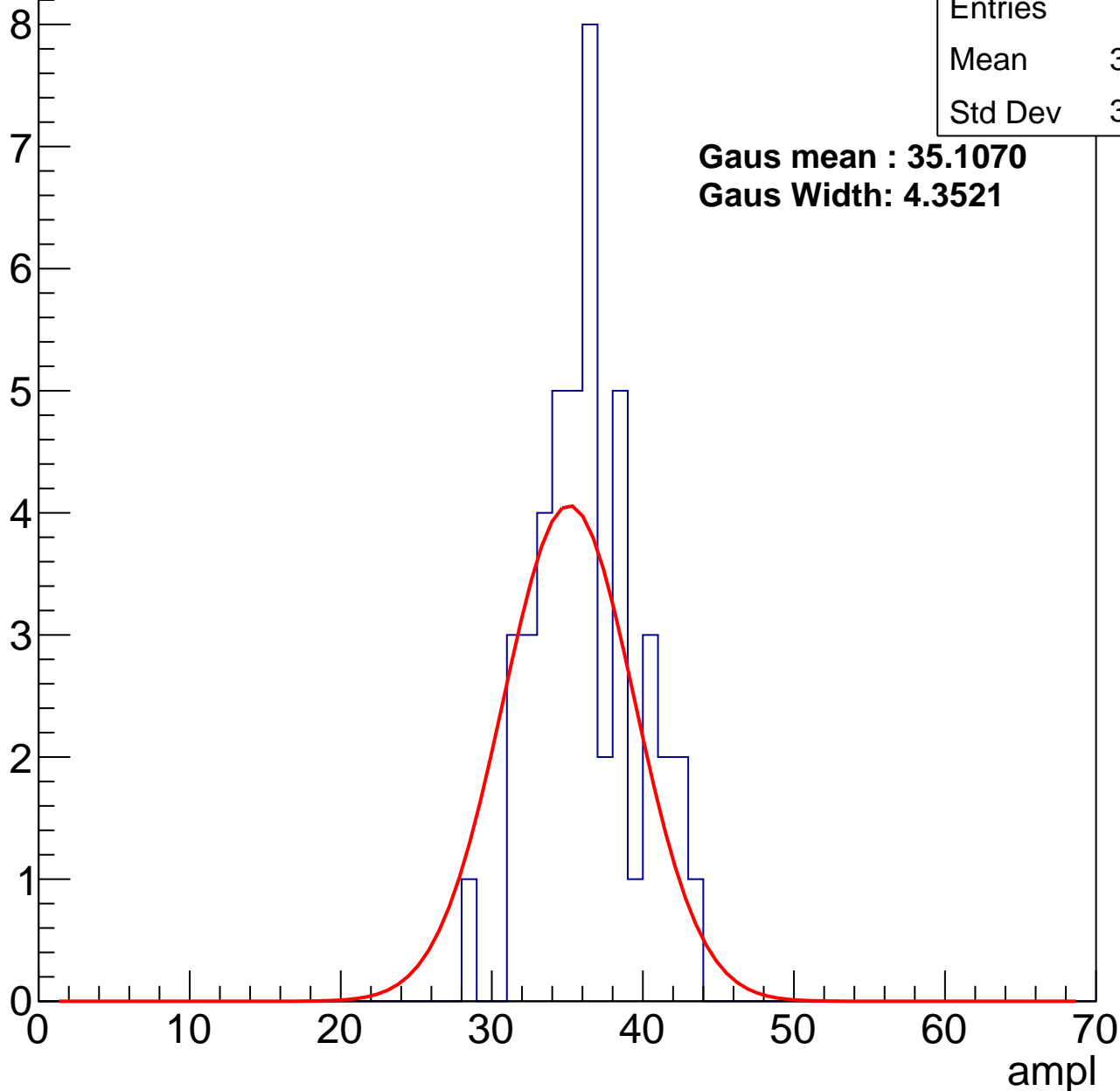
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	35.87
Std Dev	3.324

Gaus mean : 35.1070

Gaus Width: 4.3521



B1L003S, U16-ch72, adc2

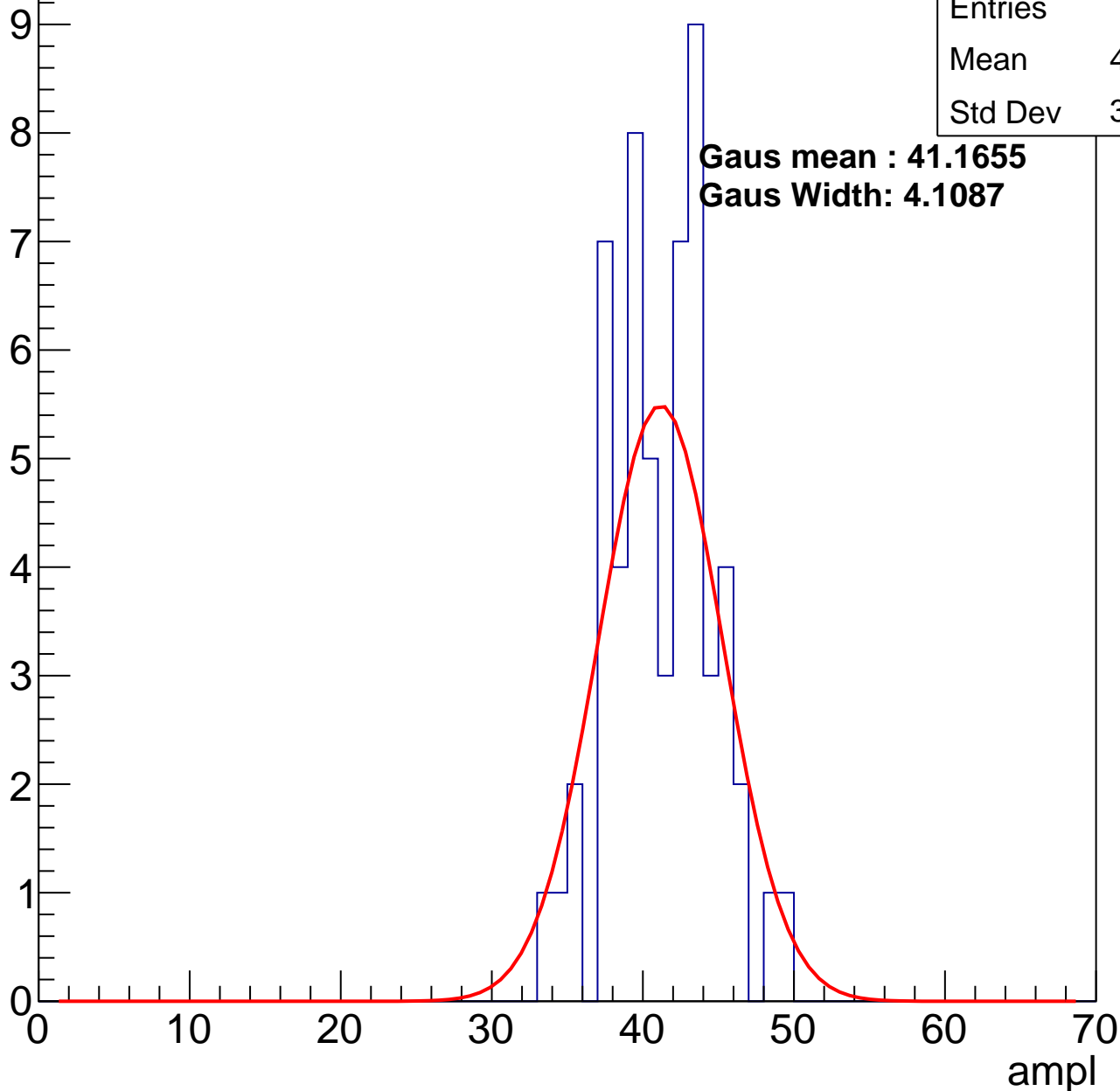
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	40.78
Std Dev	3.384

Gaus mean : 41.1655

Gaus Width: 4.1087

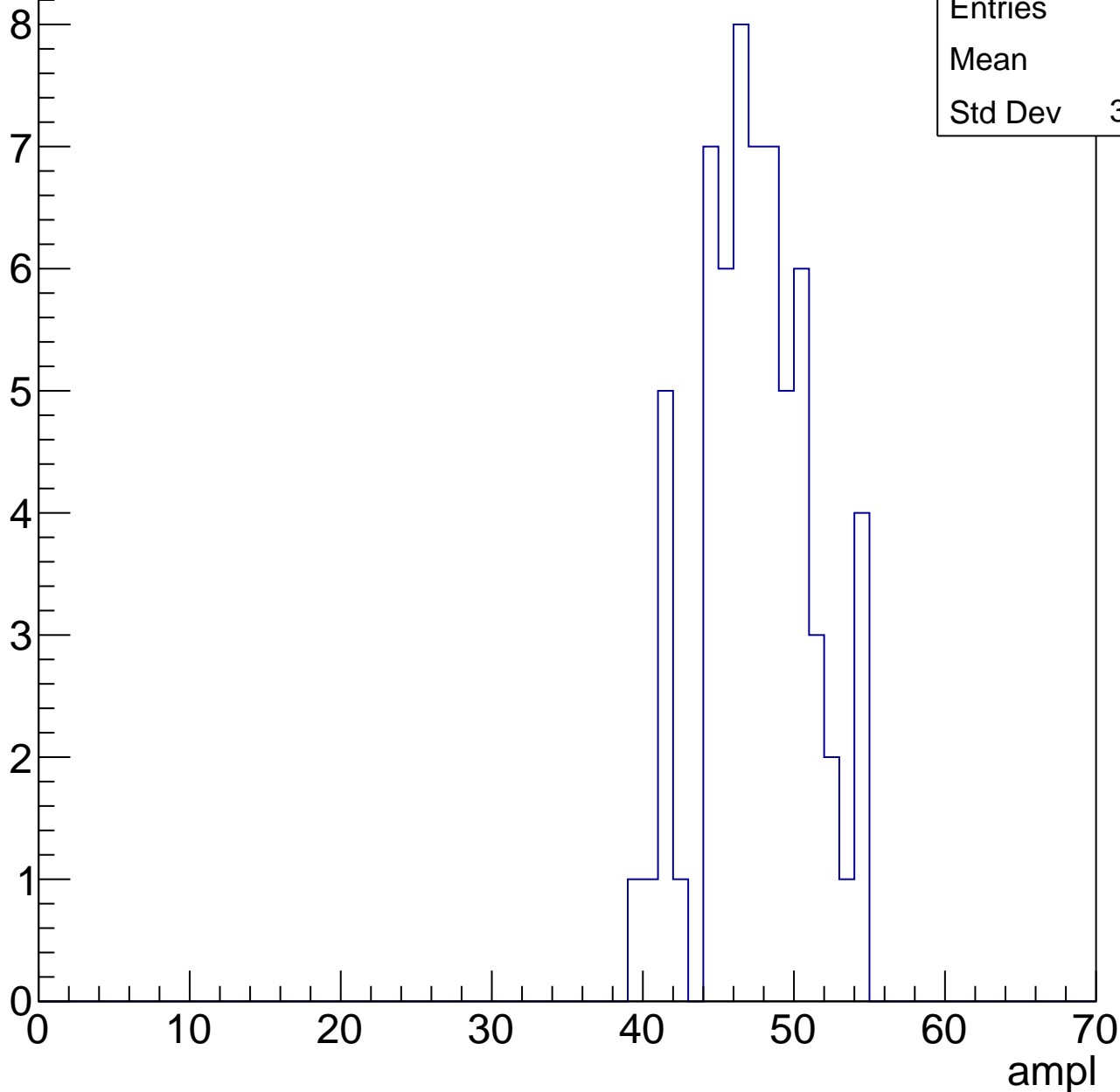


B1L003S, U16-ch72, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47
Std Dev	3.588

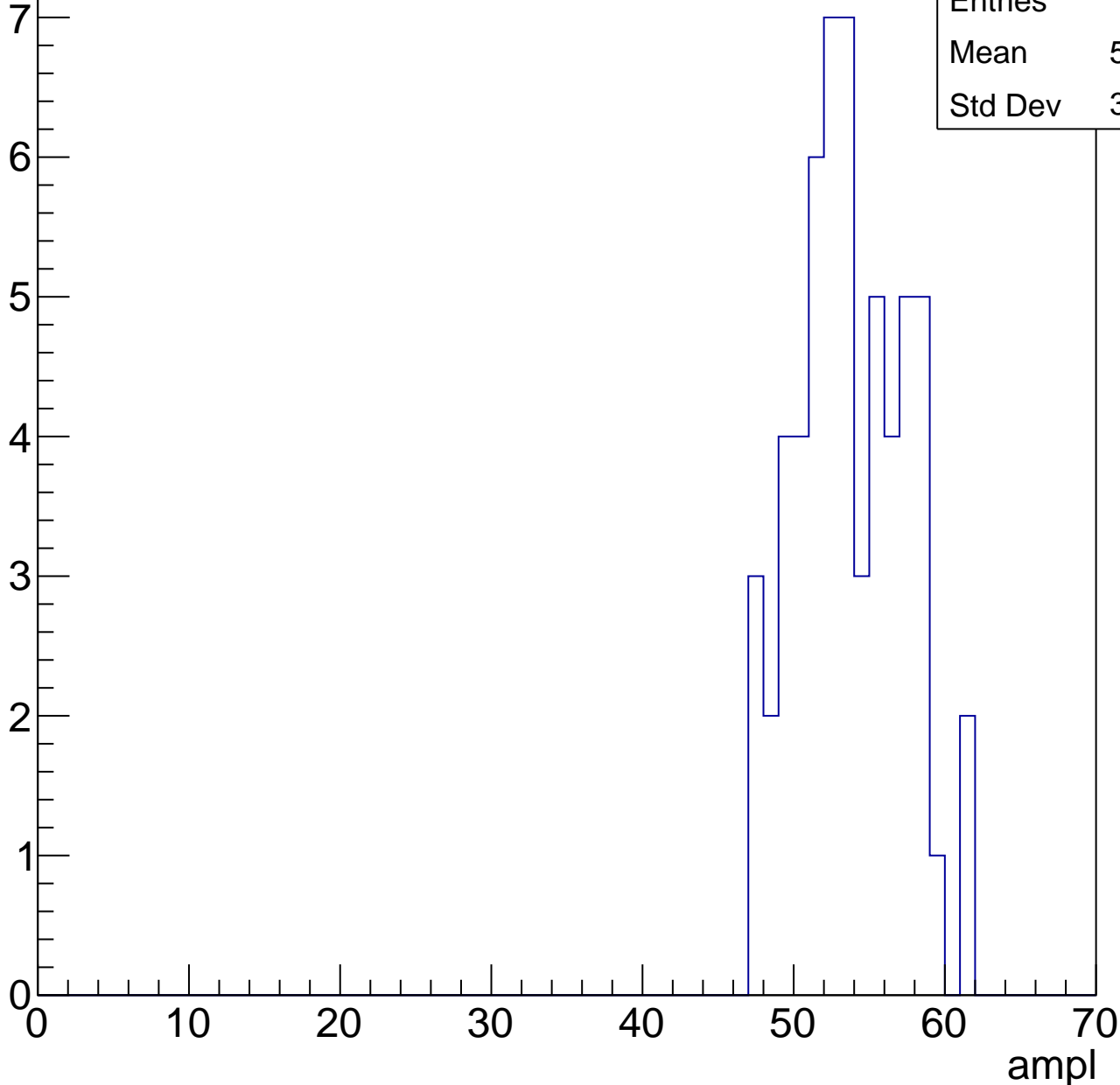


B1L003S, U16-ch72, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.29
Std Dev	3.504



B1L003S, U16-ch72, adc5

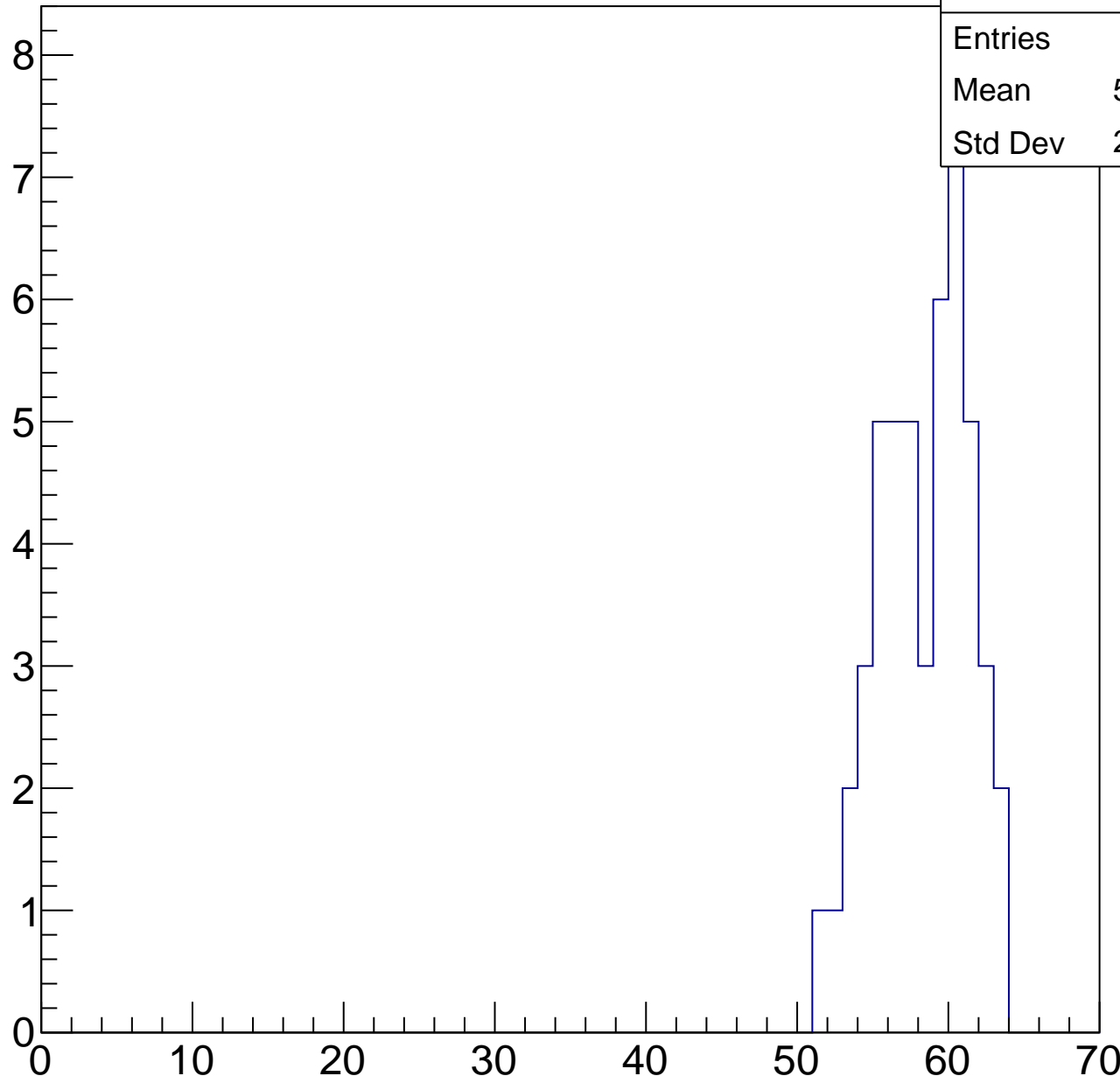
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	57.88
Std Dev	2.974

ampl

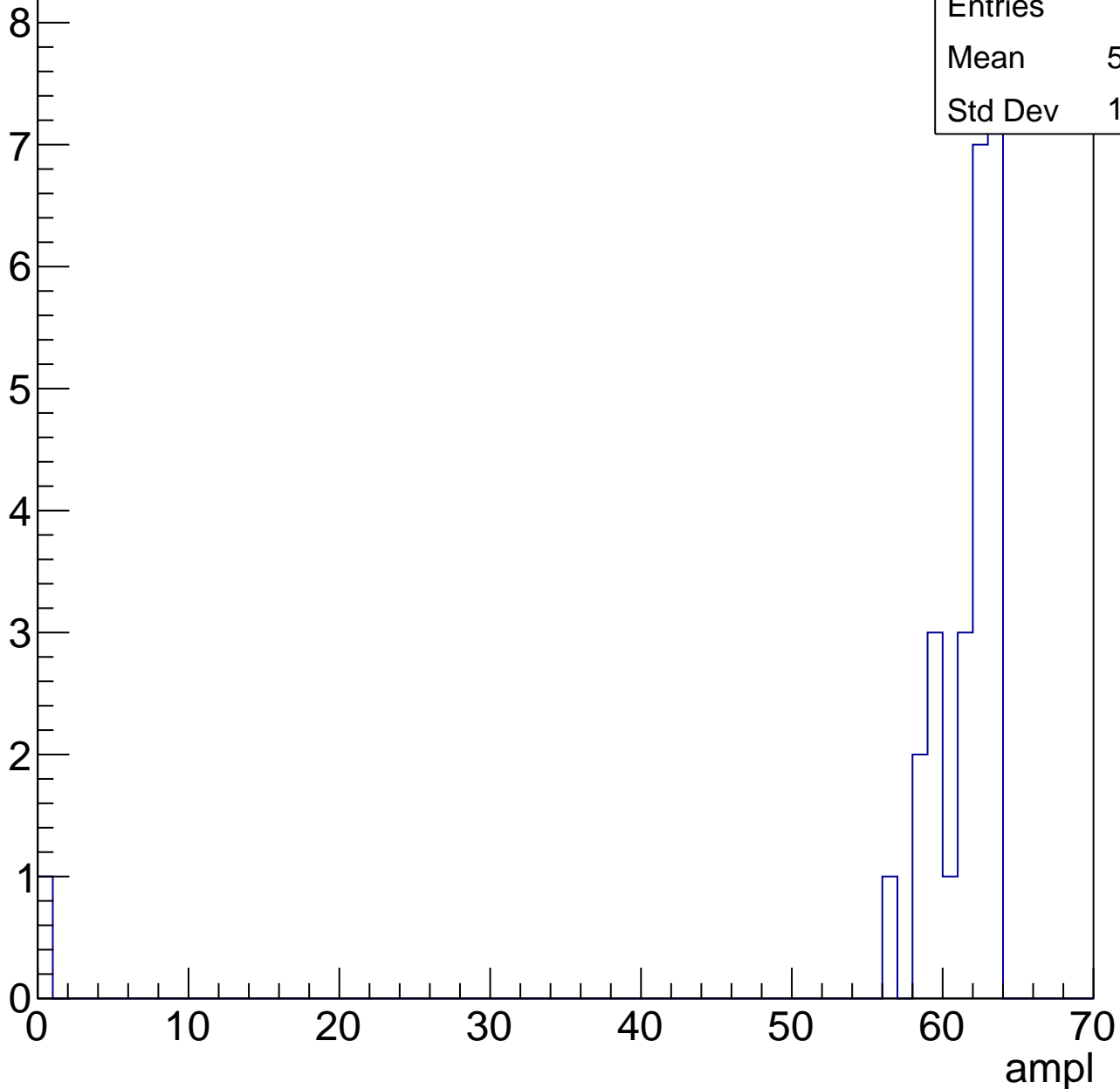


B1L003S, U16-ch72, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

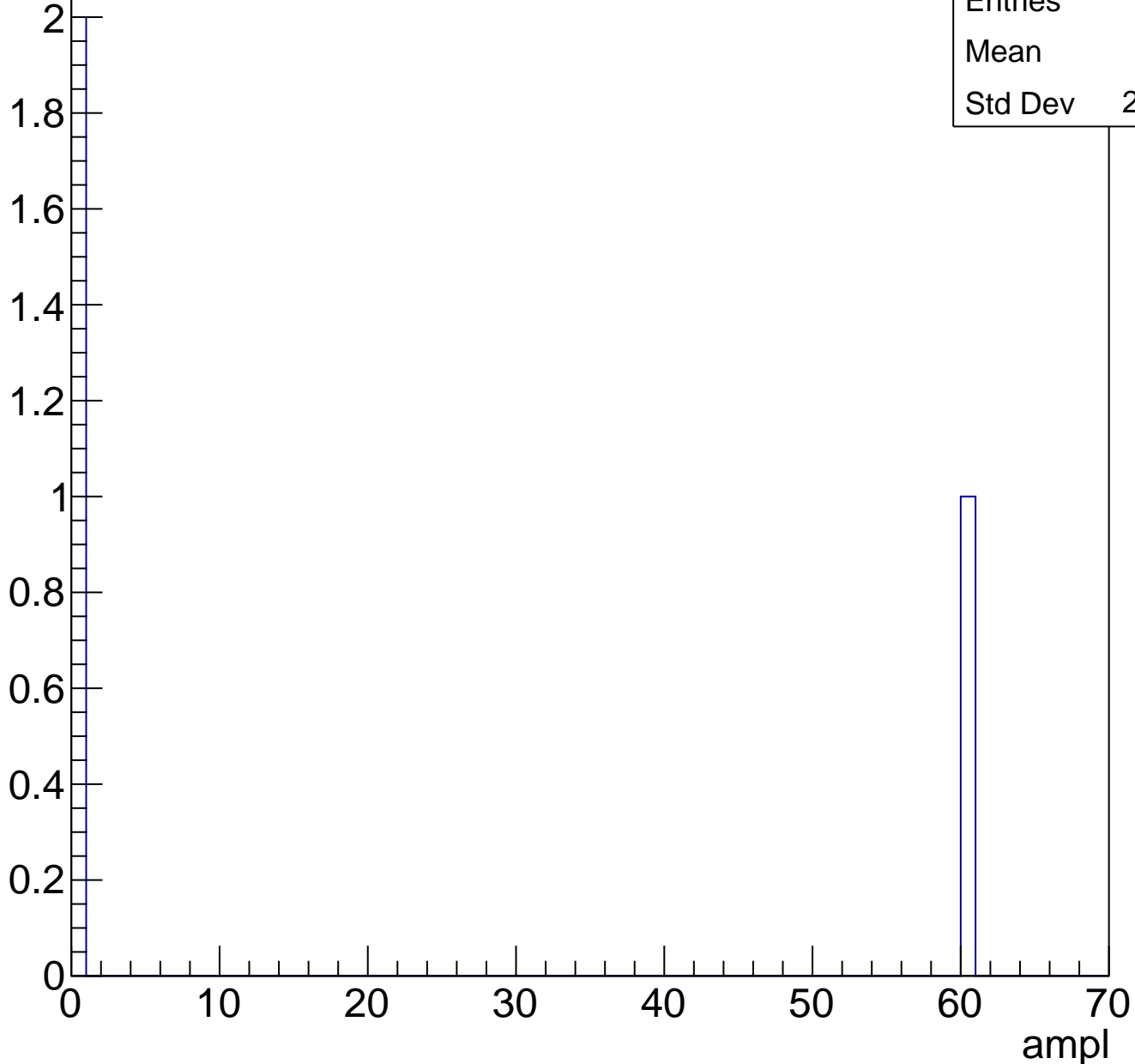
Entries	26
Mean	58.85
Std Dev	11.92



B1L003S, U16-ch72, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	20
Std Dev	28.28

B1L003S, U16-ch73, adc0

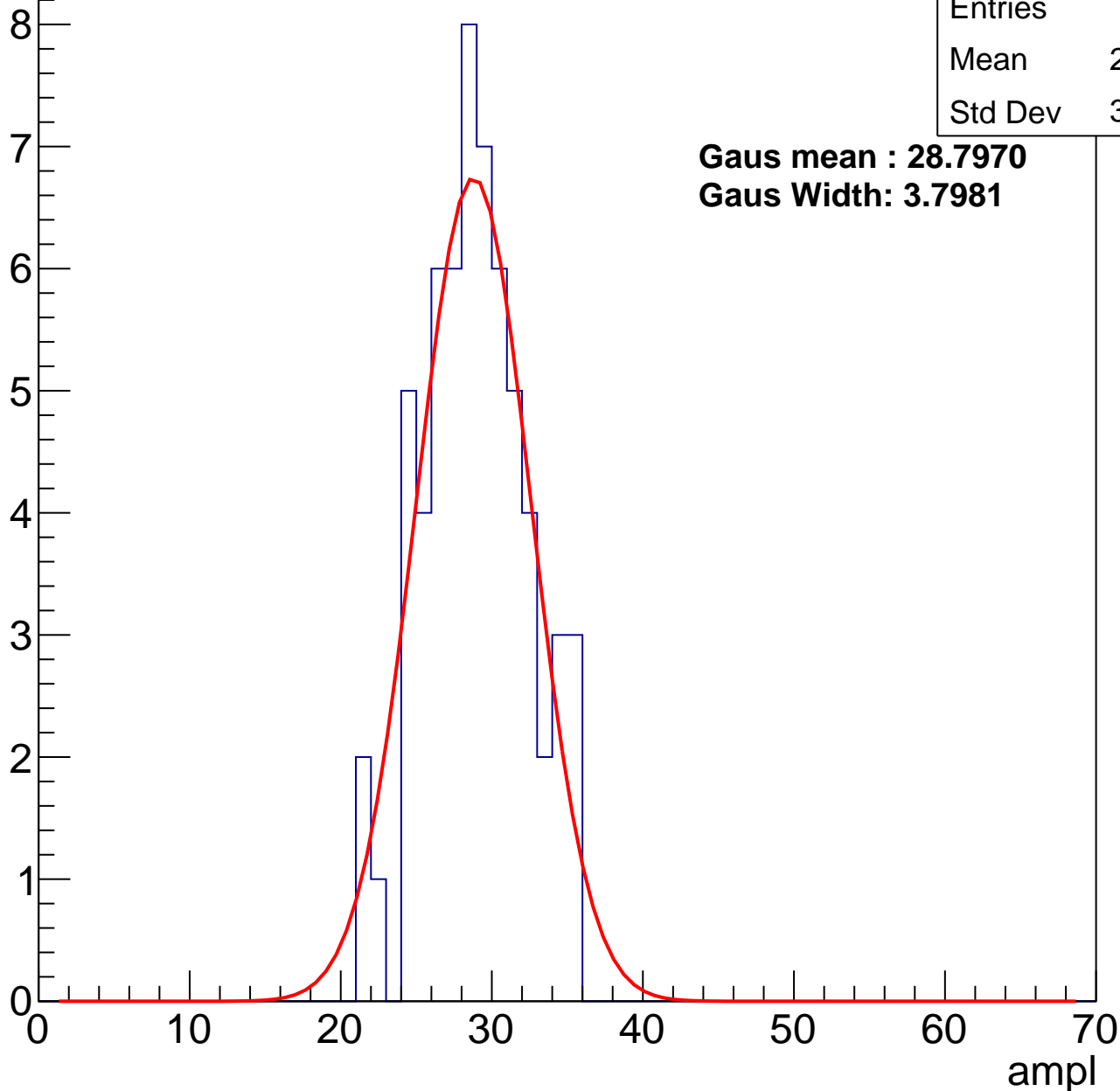
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	28.47
Std Dev	3.387

Gaus mean : 28.7970

Gaus Width: 3.7981



B1L003S, U16-ch73, adc1

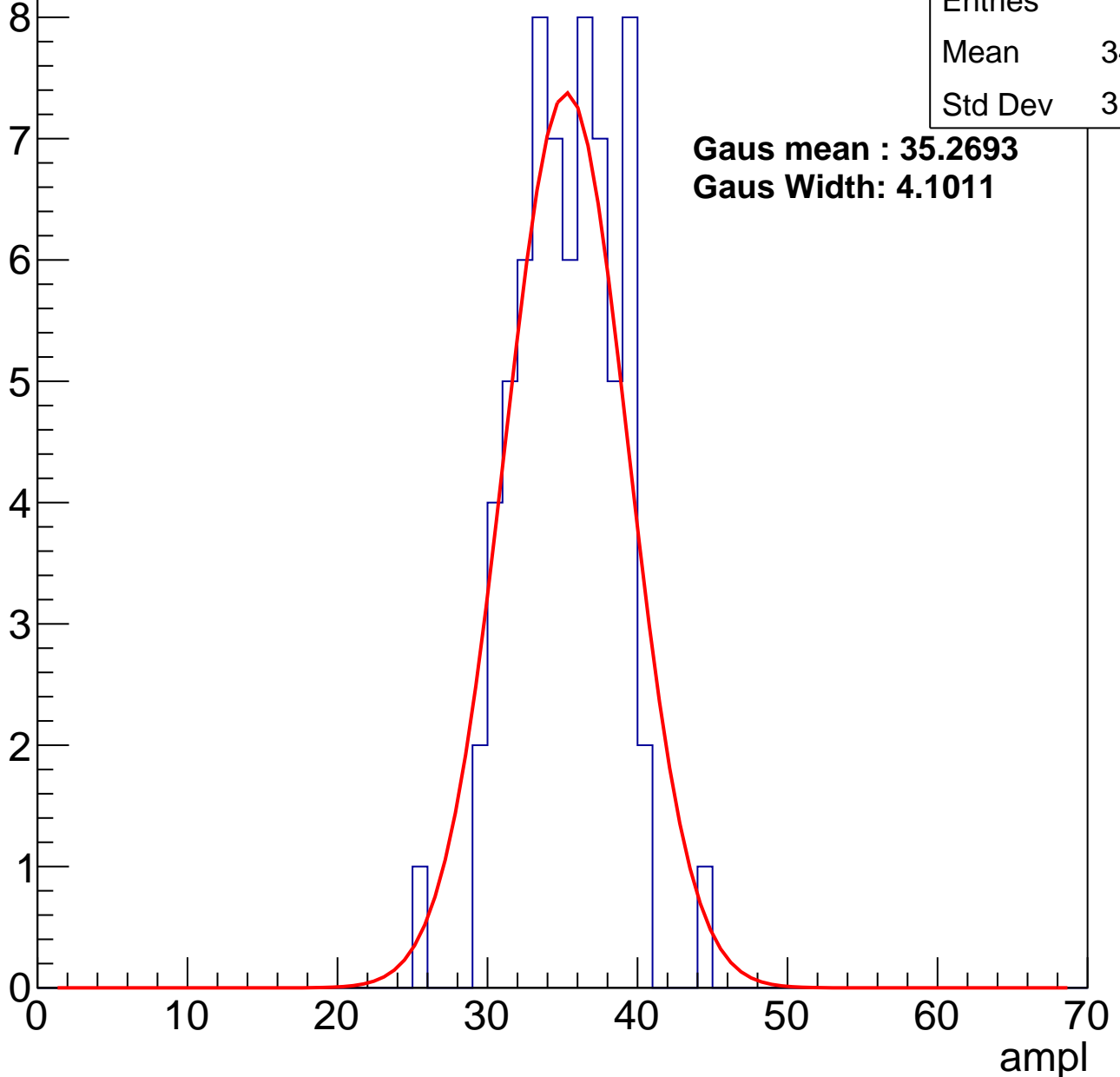
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	34.79
Std Dev	3.342

Gaus mean : 35.2693

Gaus Width: 4.1011



B1L003S, U16-ch73, adc2

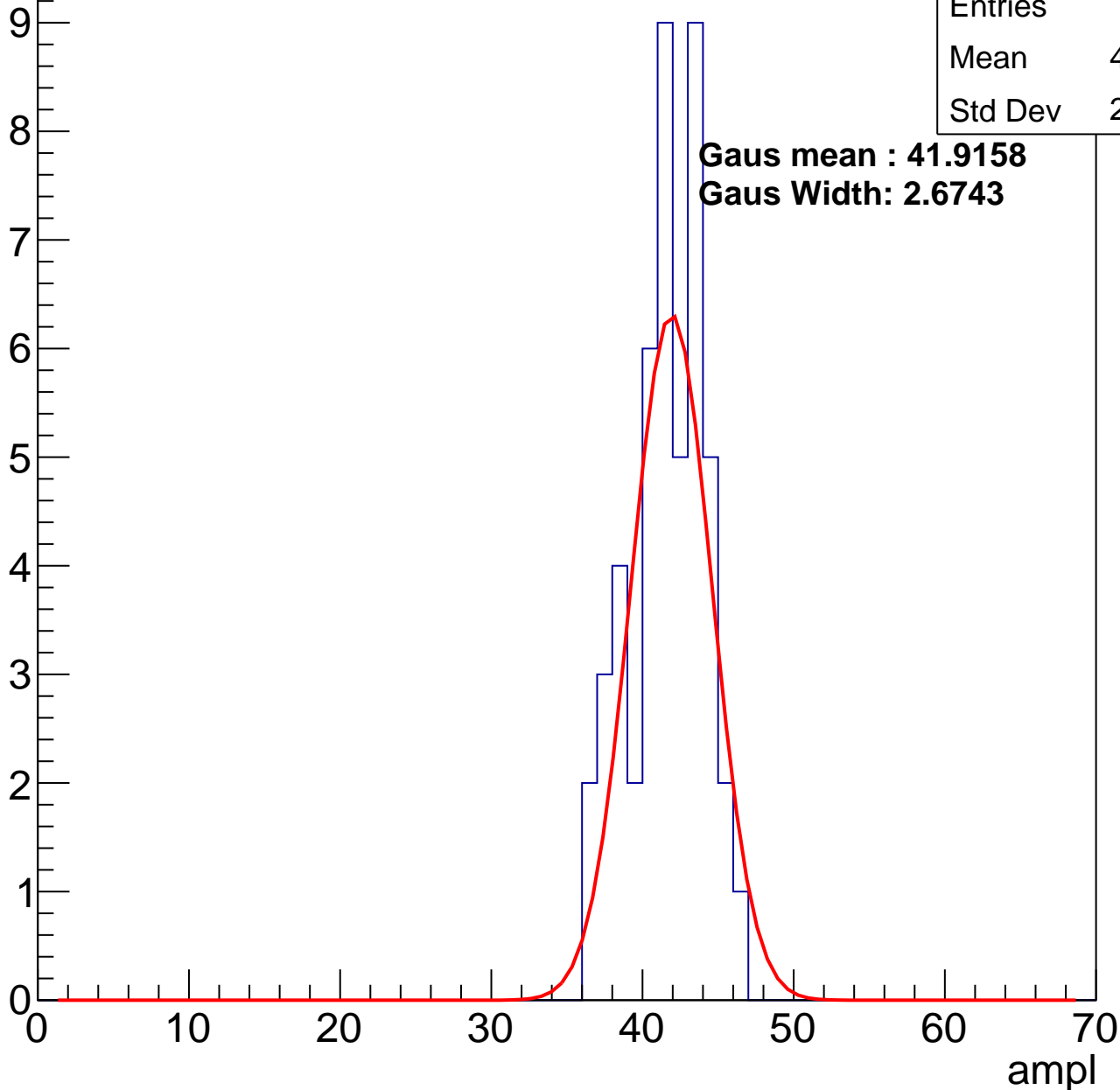
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	41.15
Std Dev	2.458

Gaus mean : 41.9158

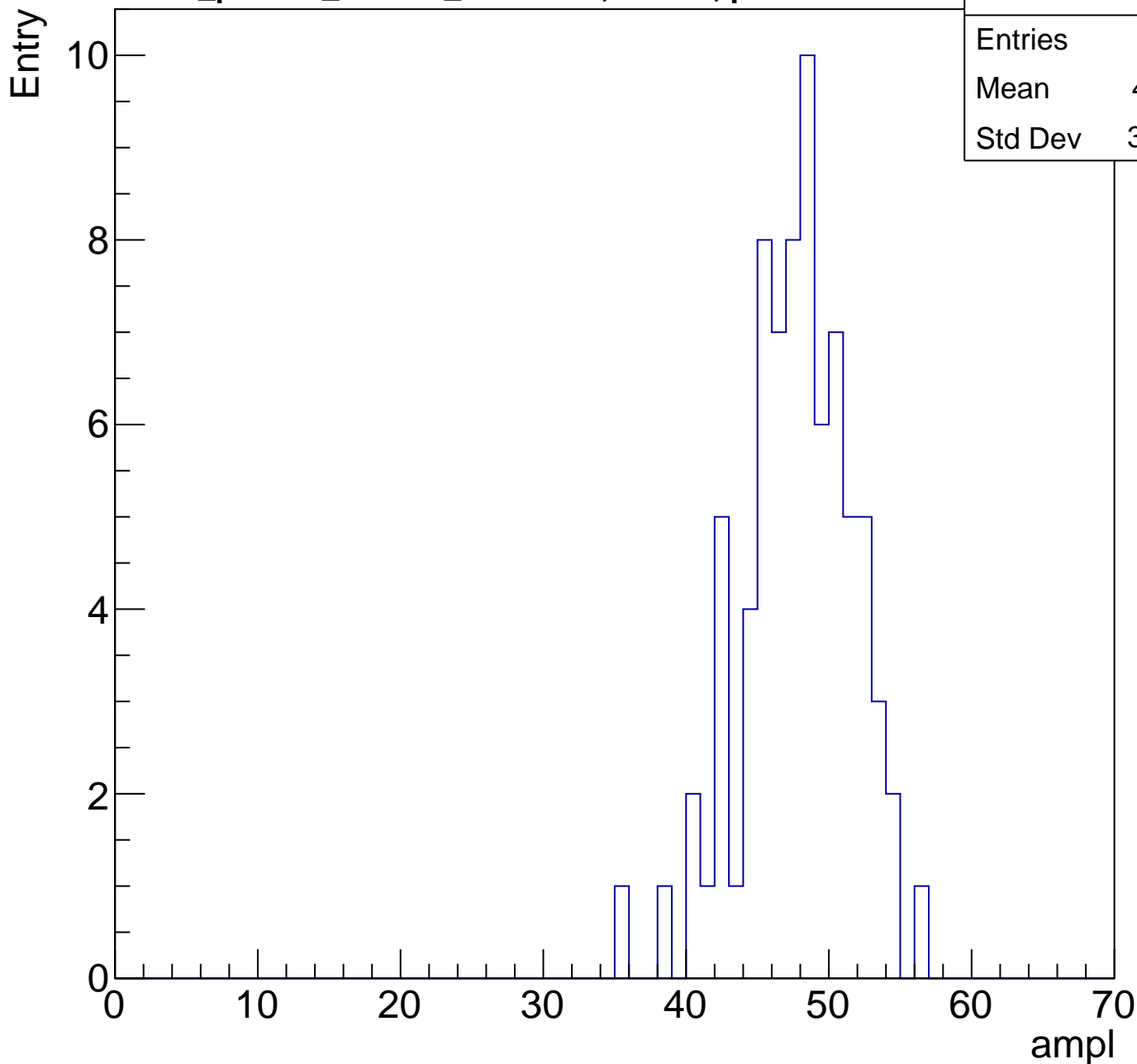
Gaus Width: 2.6743



B1L003S, U16-ch73, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	47.31
Std Dev	3.876

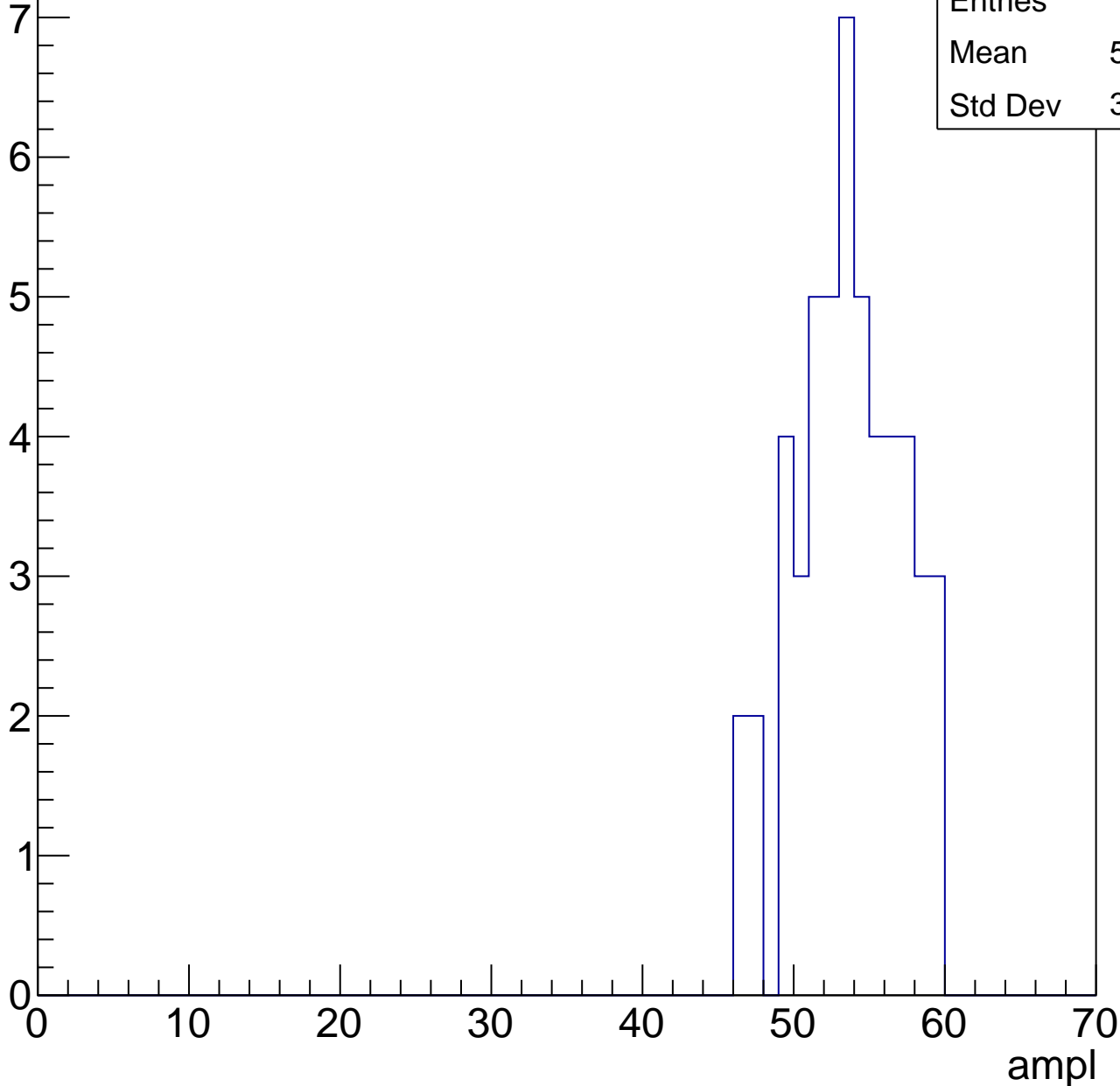


B1L003S, U16-ch73, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	53.16
Std Dev	3.398

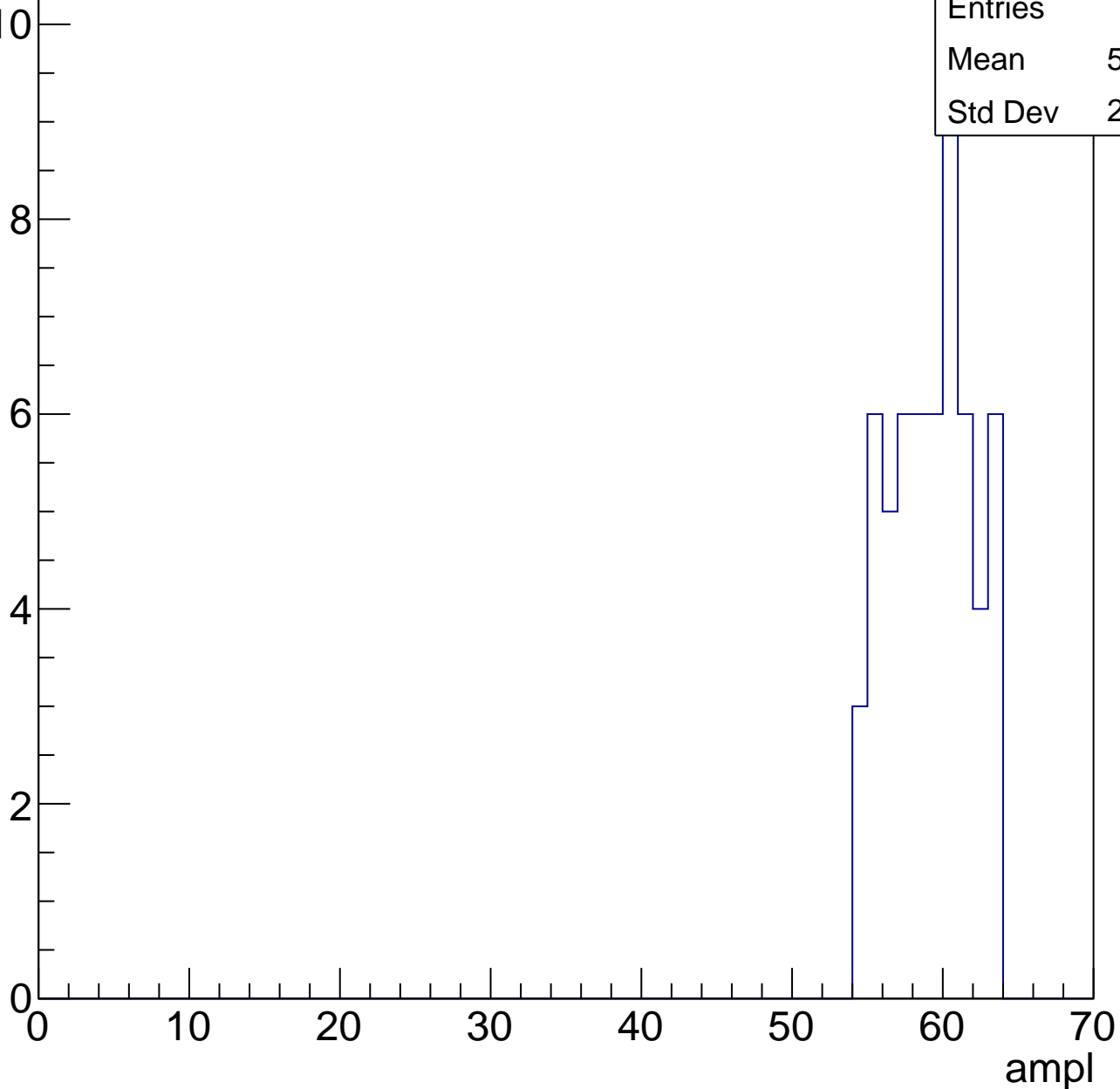


B1L003S, U16-ch73, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.76
Std Dev	2.654

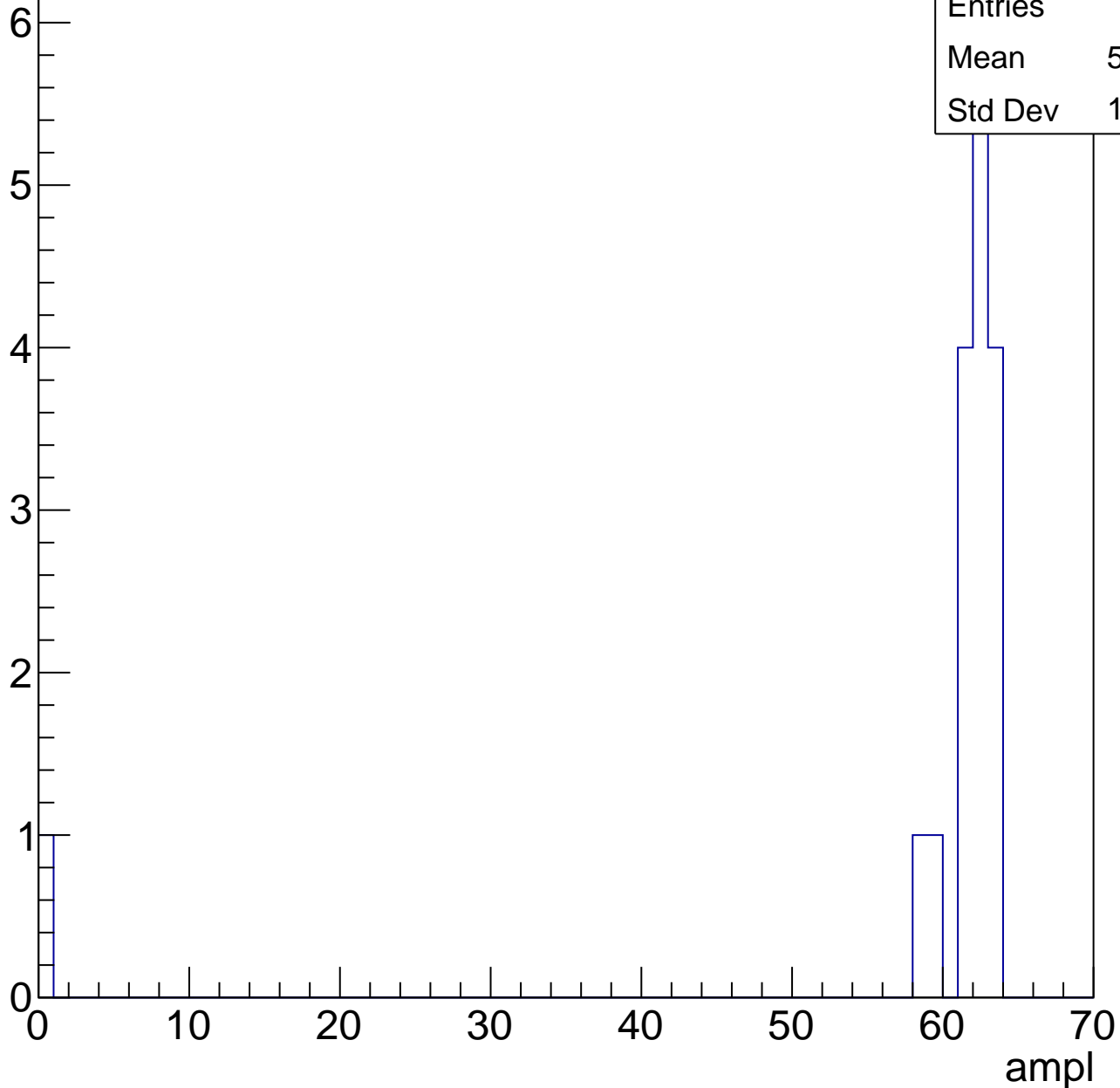


B1L003S, U16-ch73, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	57.94
Std Dev	14.55



B1L003S, U16-ch73, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch74, adc0

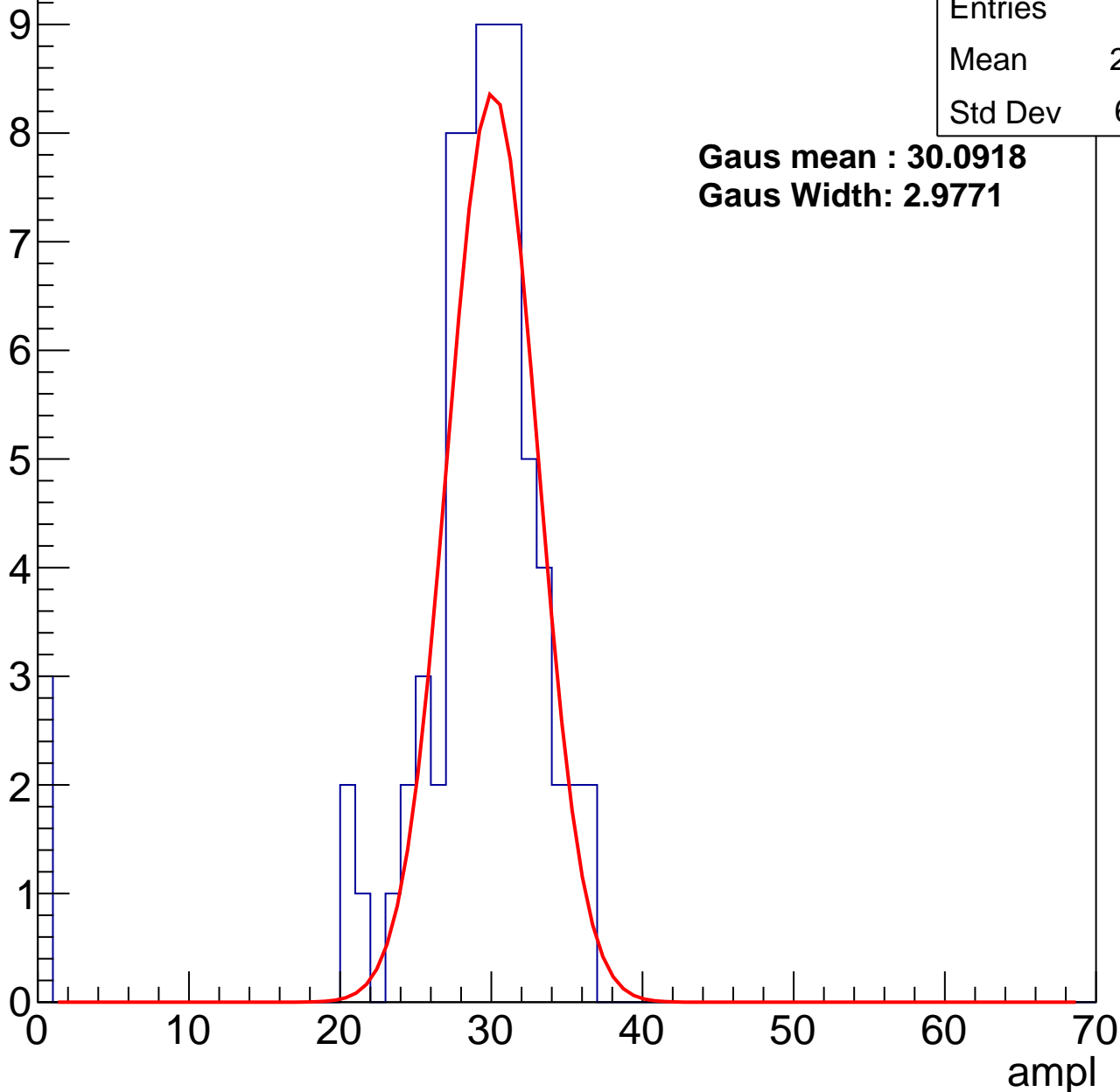
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	27.93
Std Dev	6.701

Gaus mean : 30.0918

Gaus Width: 2.9771



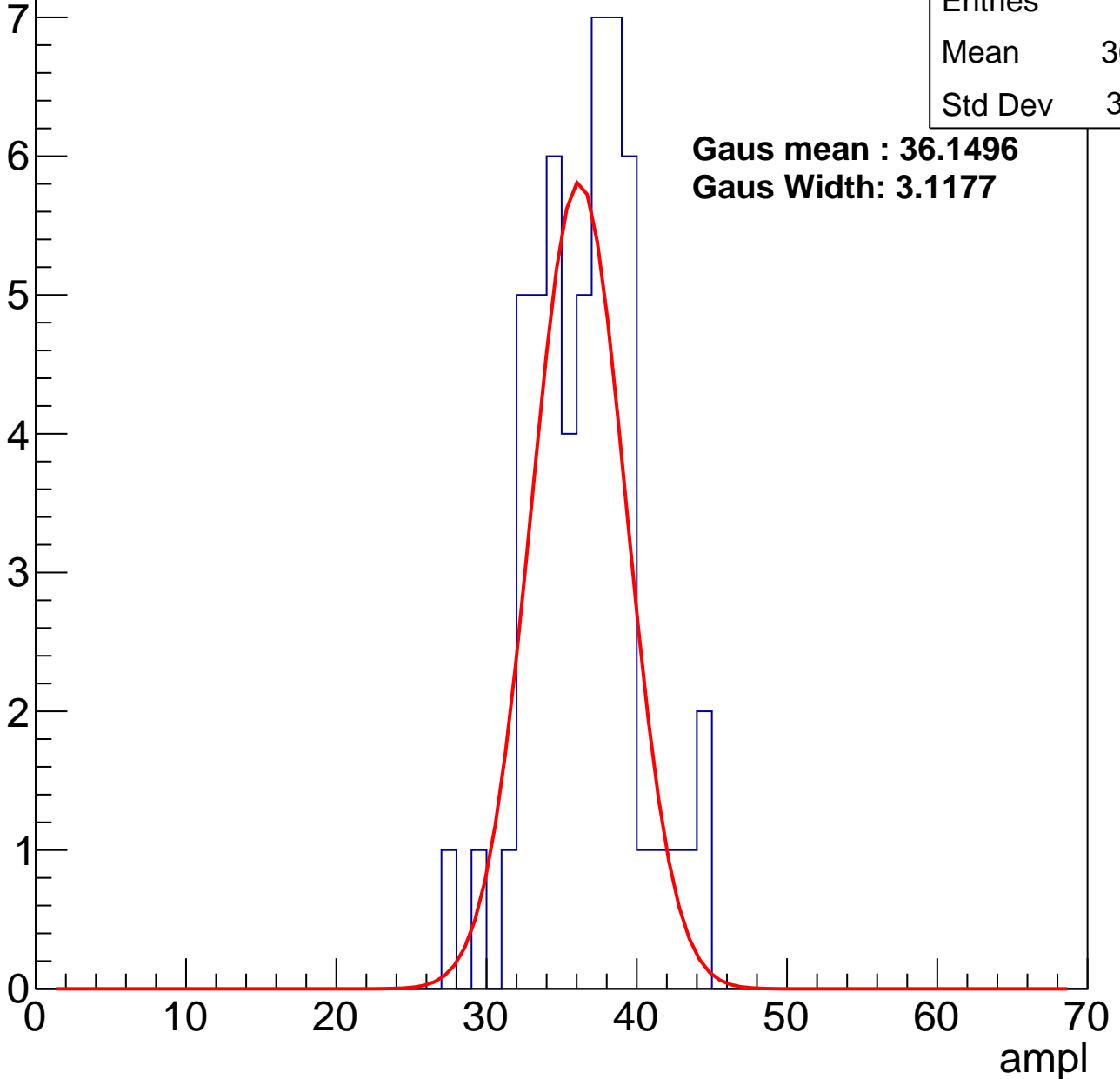
B1L003S, U16-ch74, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.09
Std Dev	3.471

Gaus mean : 36.1496
Gaus Width: 3.1177



B1L003S, U16-ch74, adc2

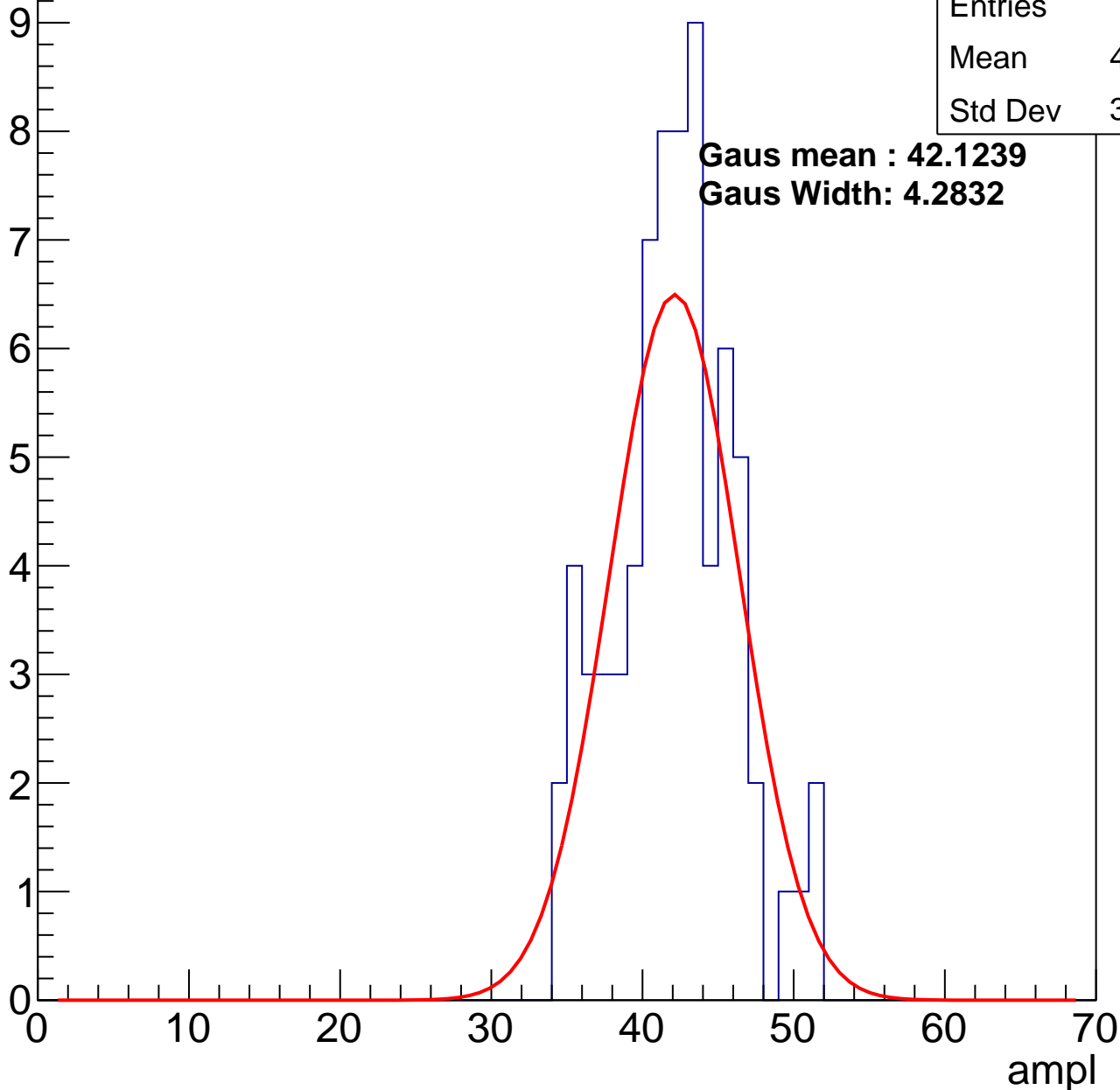
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	41.65
Std Dev	3.913

Gaus mean : 42.1239

Gaus Width: 4.2832

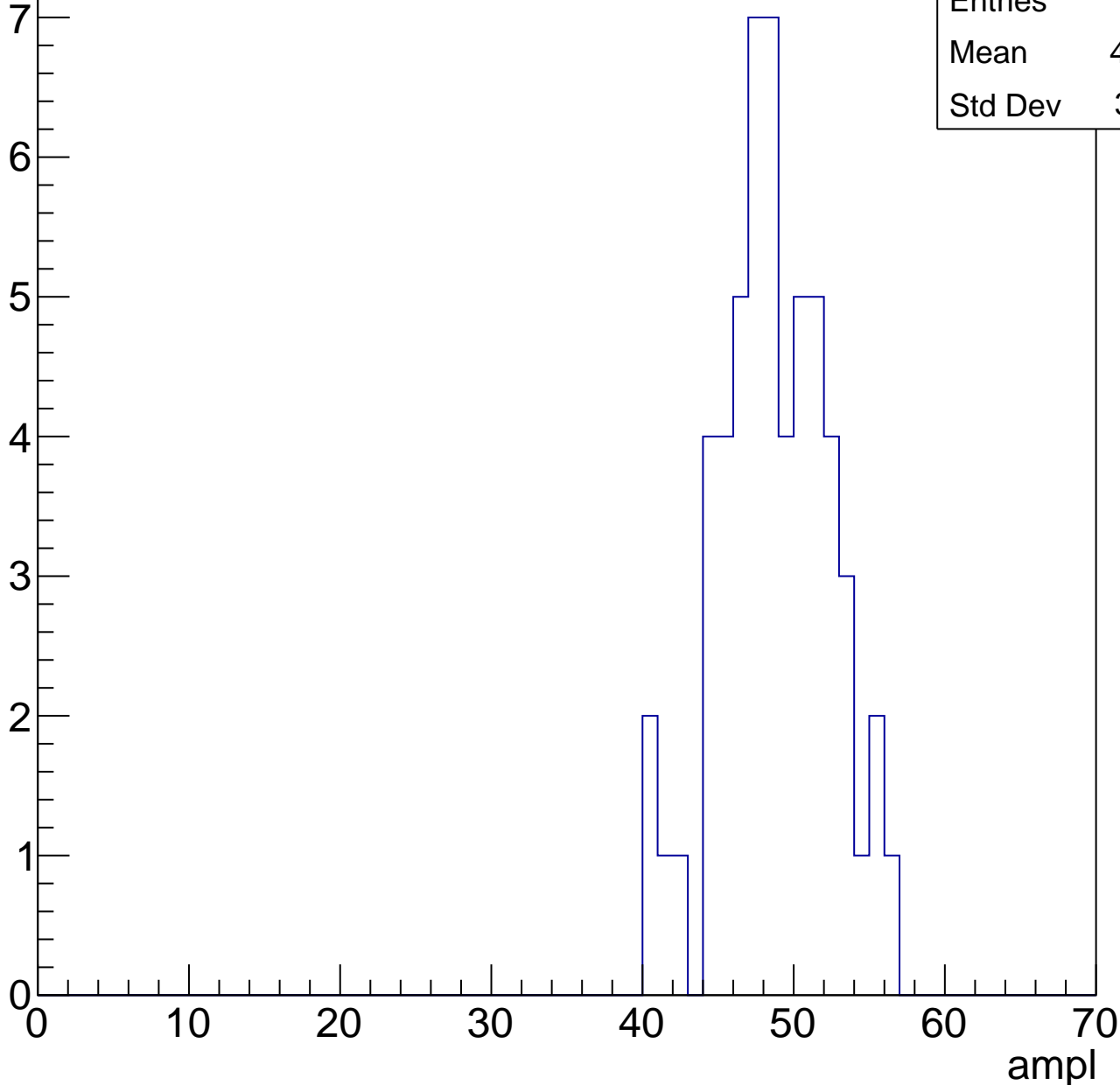


B1L003S, U16-ch74, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	48.25
Std Dev	3.641

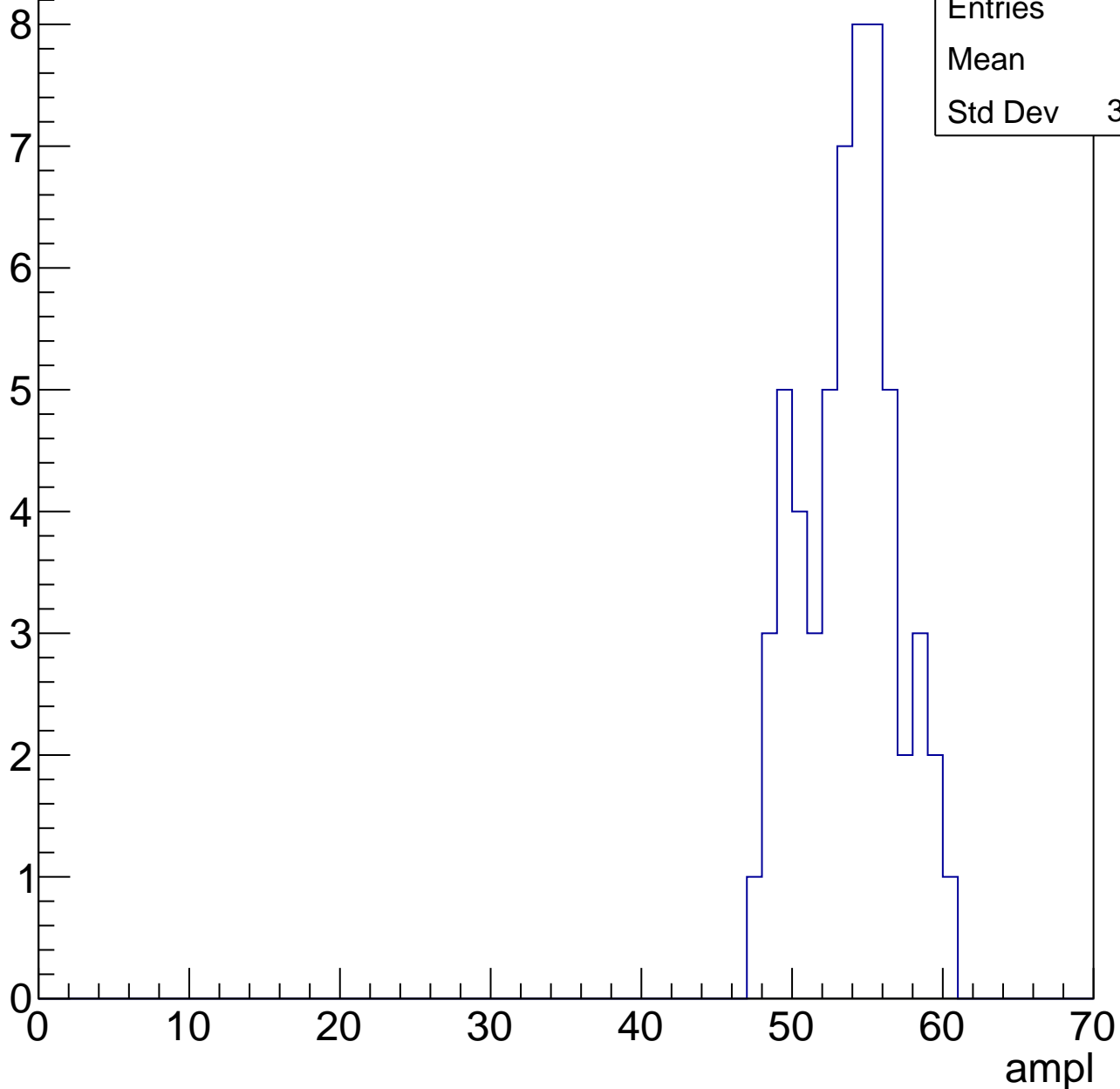


B1L003S, U16-ch74, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.3
Std Dev	3.112



B1L003S, U16-ch74, adc5

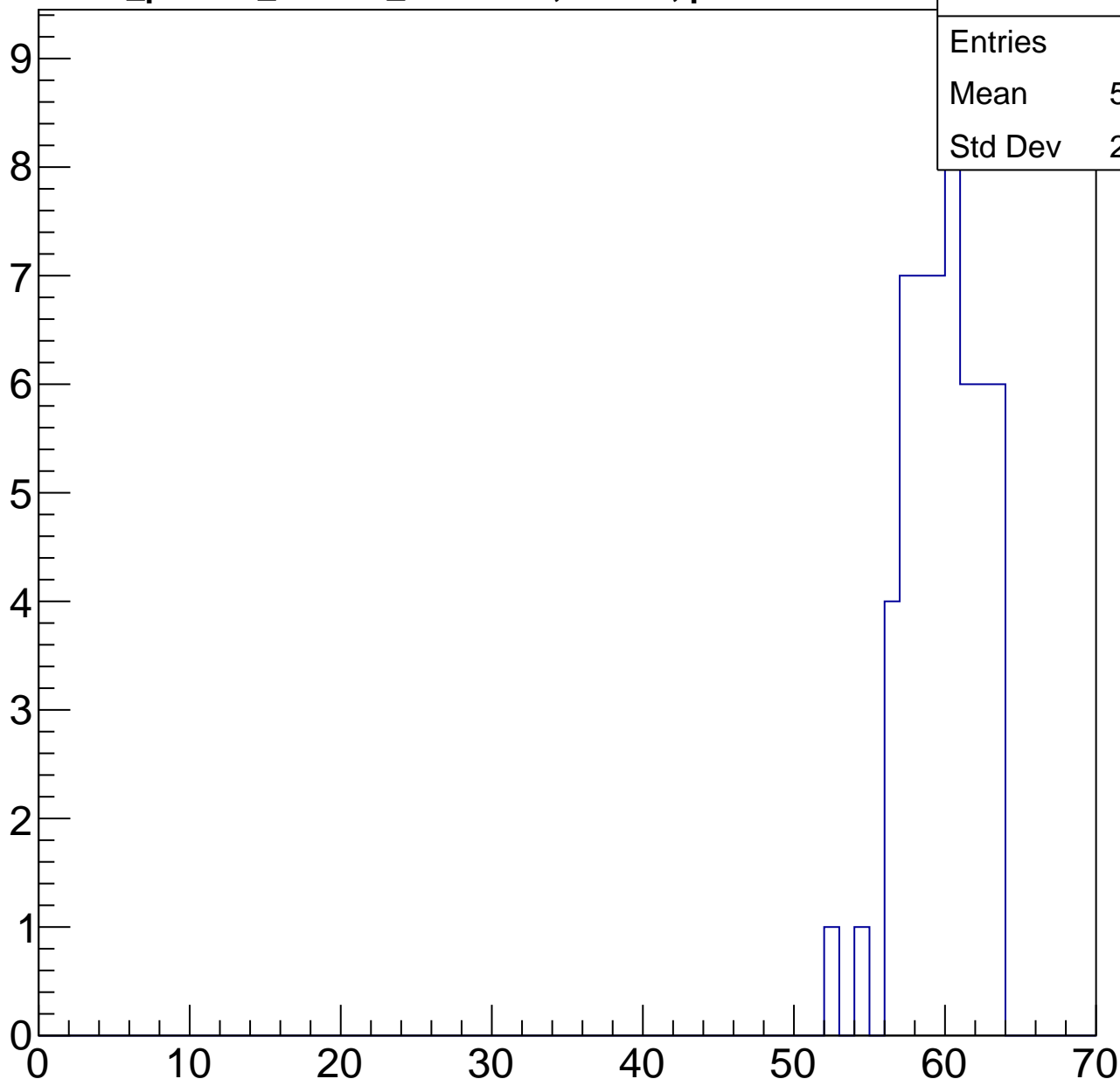
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	59.33
Std Dev	2.442

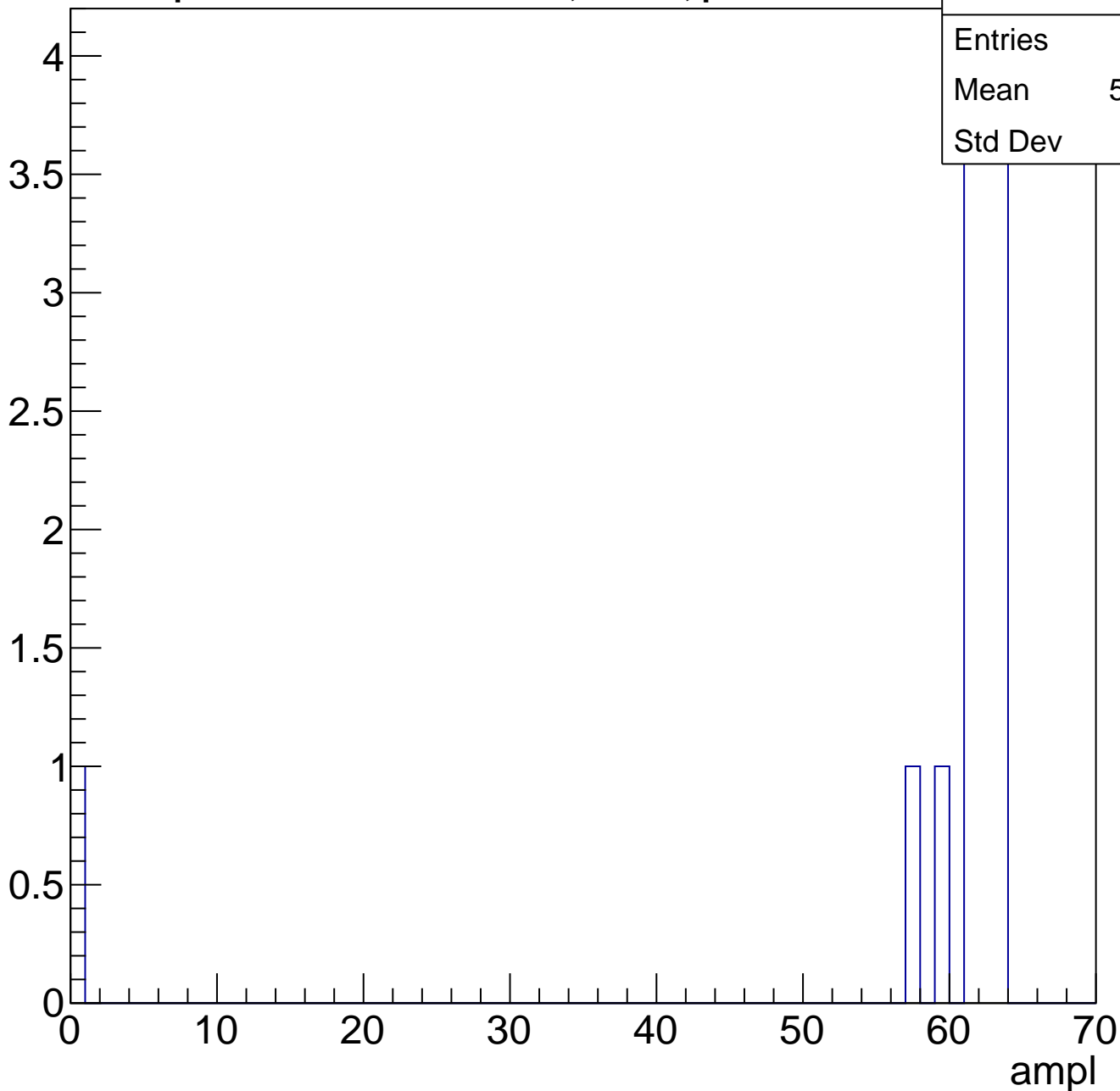
ampl



B1L003S, U16-ch74, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch74, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch75, adc0

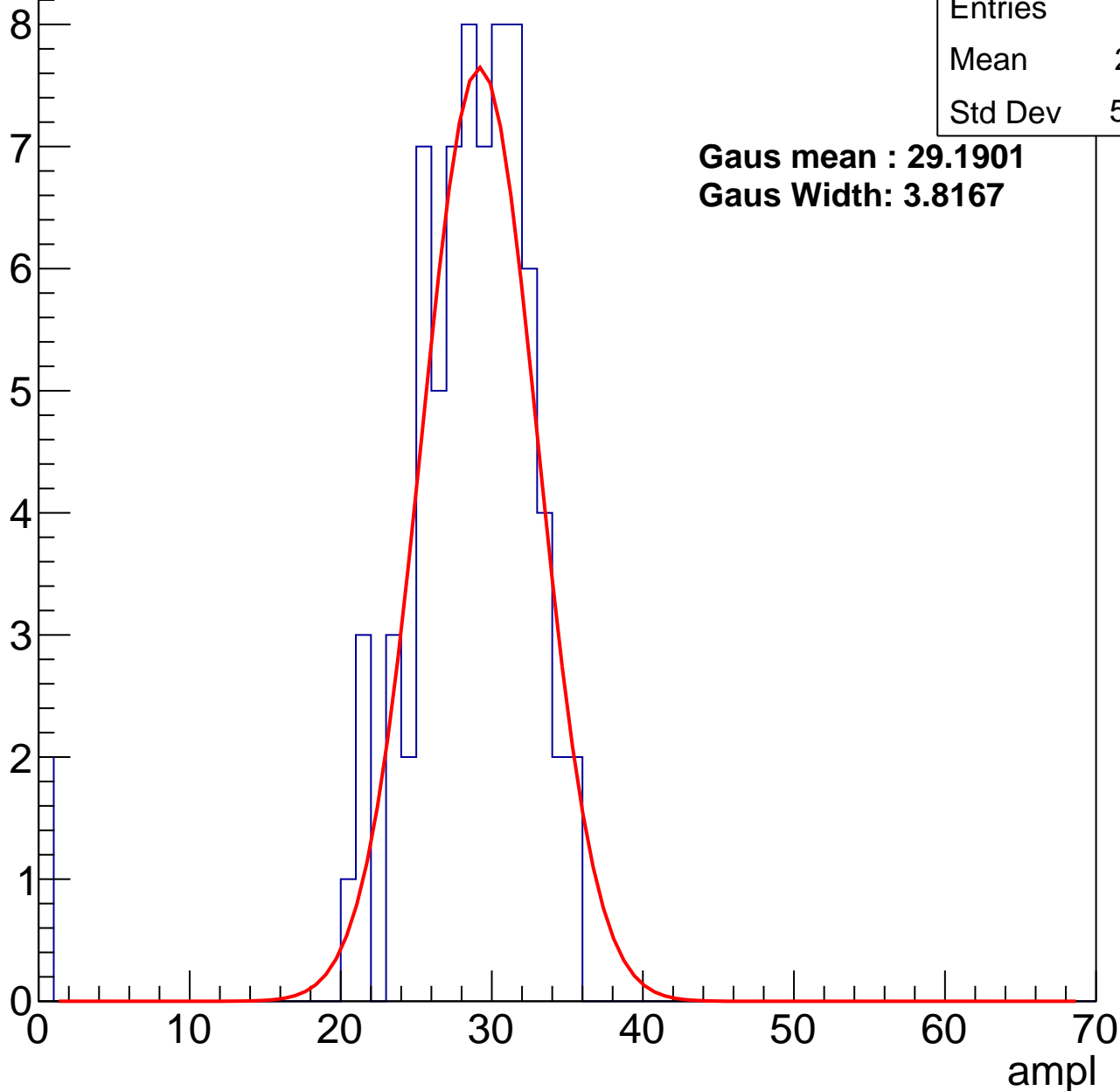
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	27.61
Std Dev	5.706

Gaus mean : 29.1901

Gaus Width: 3.8167



B1L003S, U16-ch75, adc1

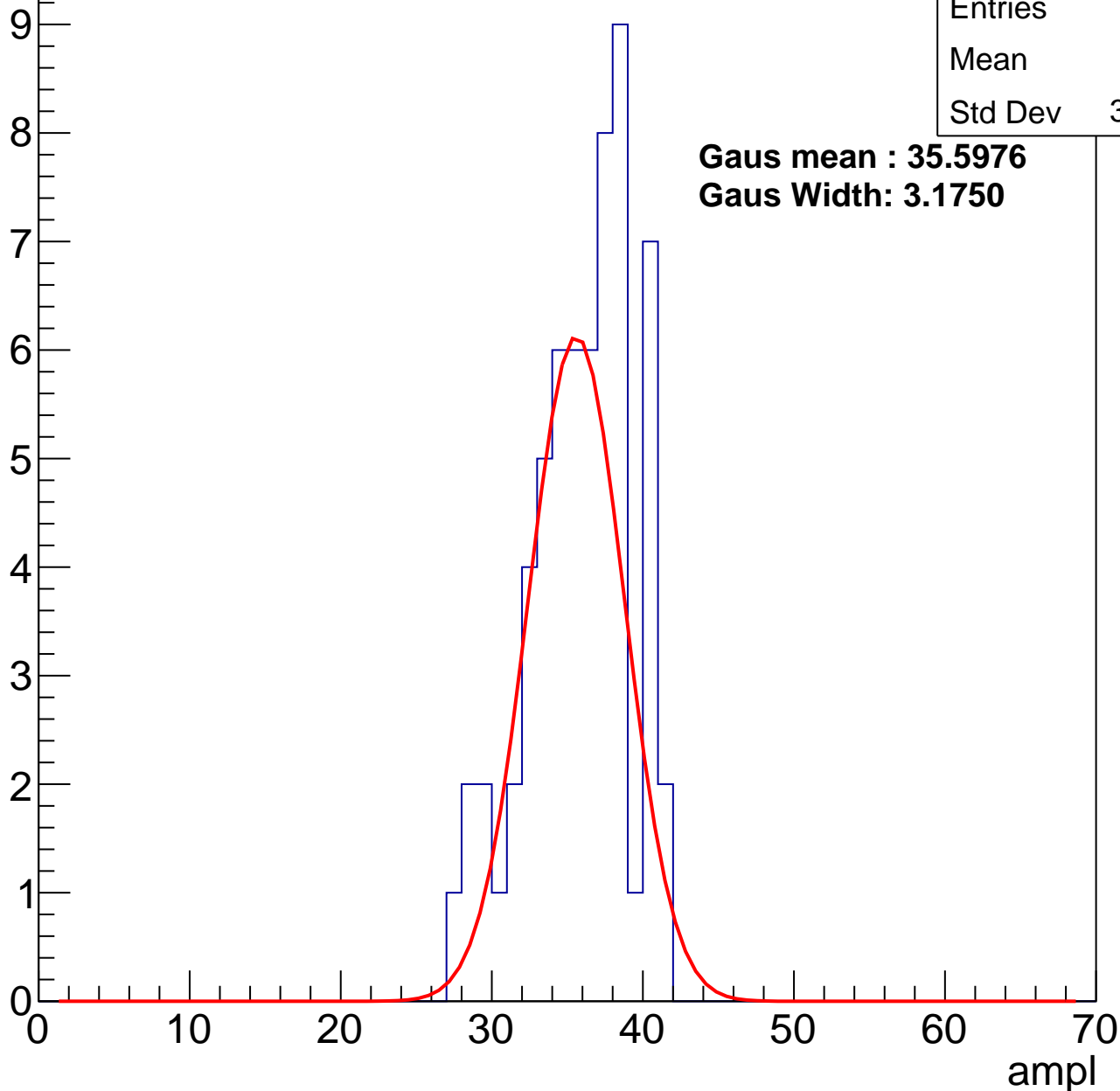
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.4
Std Dev	3.419

Gaus mean : 35.5976

Gaus Width: 3.1750



B1L003S, U16-ch75, adc2

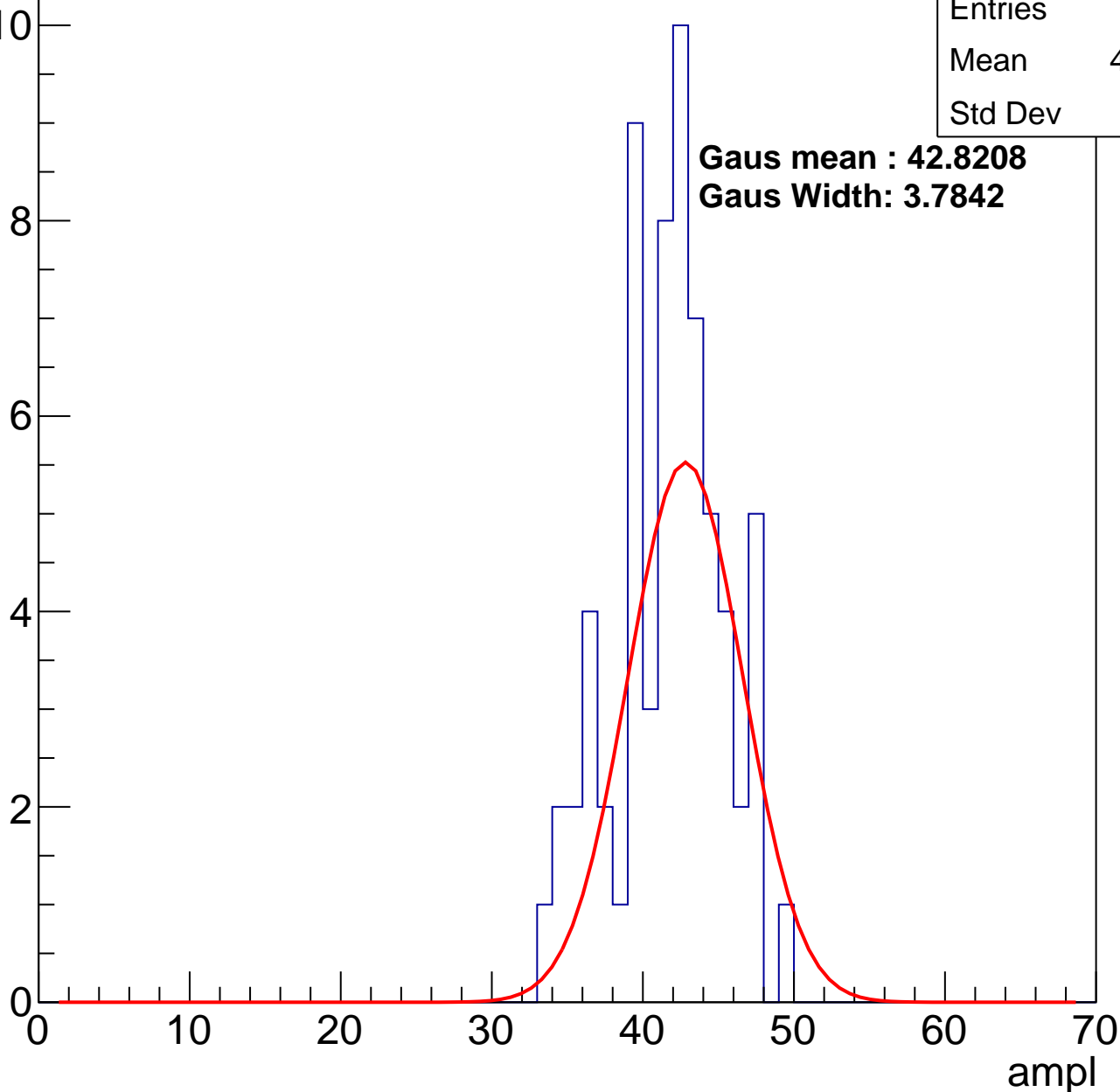
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.26
Std Dev	3.59

Gaus mean : 42.8208

Gaus Width: 3.7842

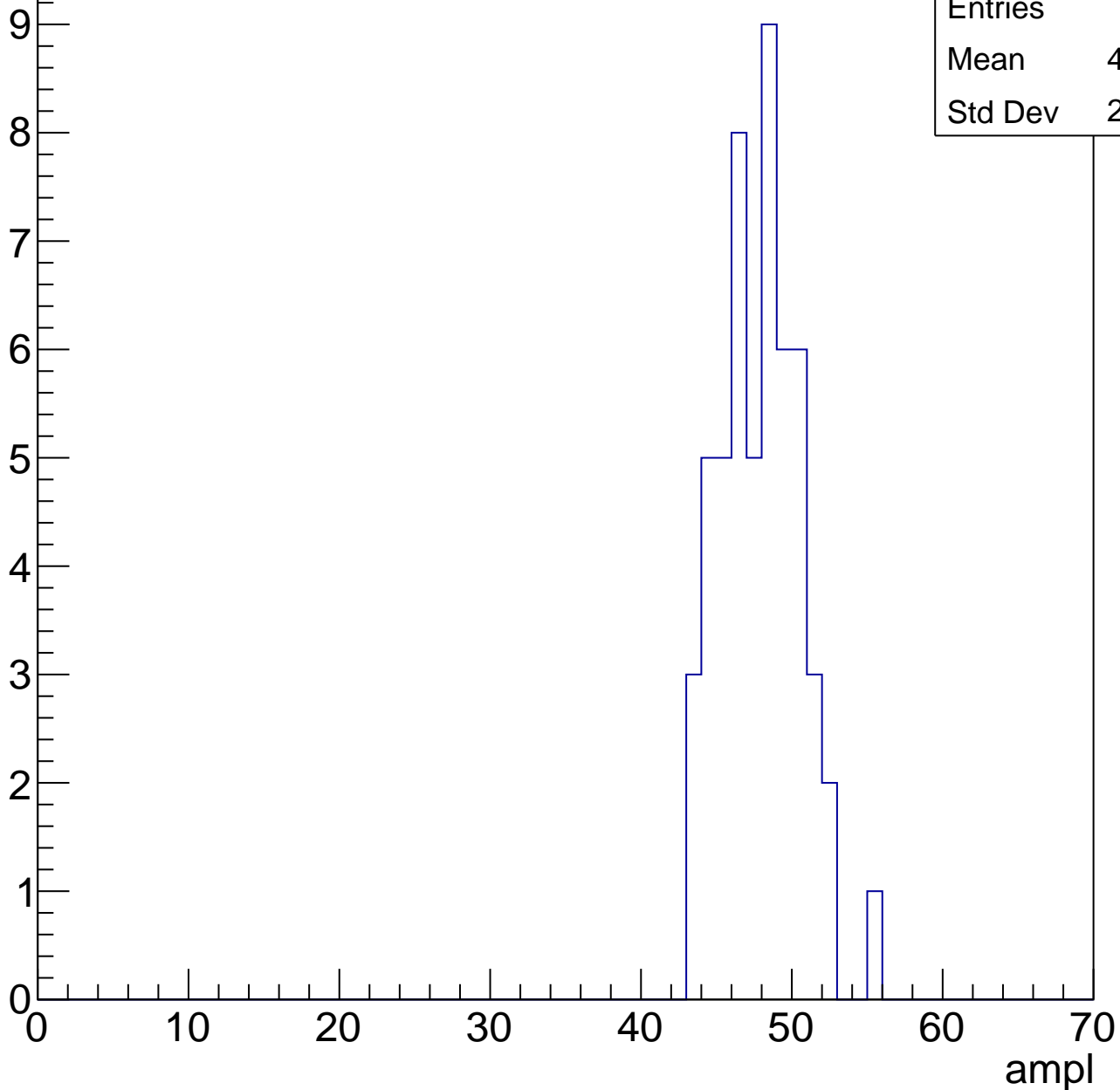


B1L003S, U16-ch75, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	47.45
Std Dev	2.603



B1L003S, U16-ch75, adc4

calib_packv5_042523_0143.root, FC#13, port D2

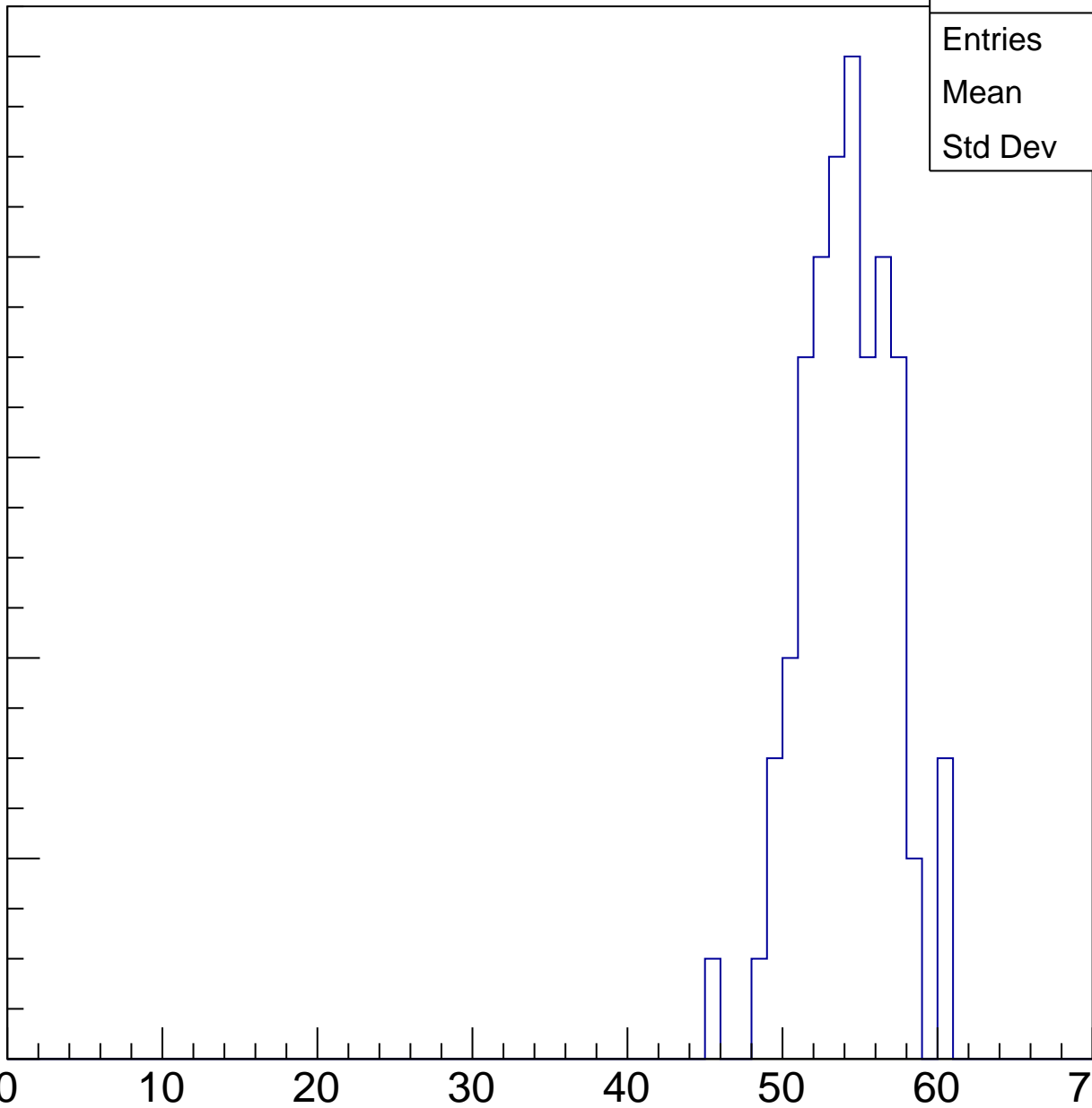
Entries	70
Mean	53.69
Std Dev	2.916

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

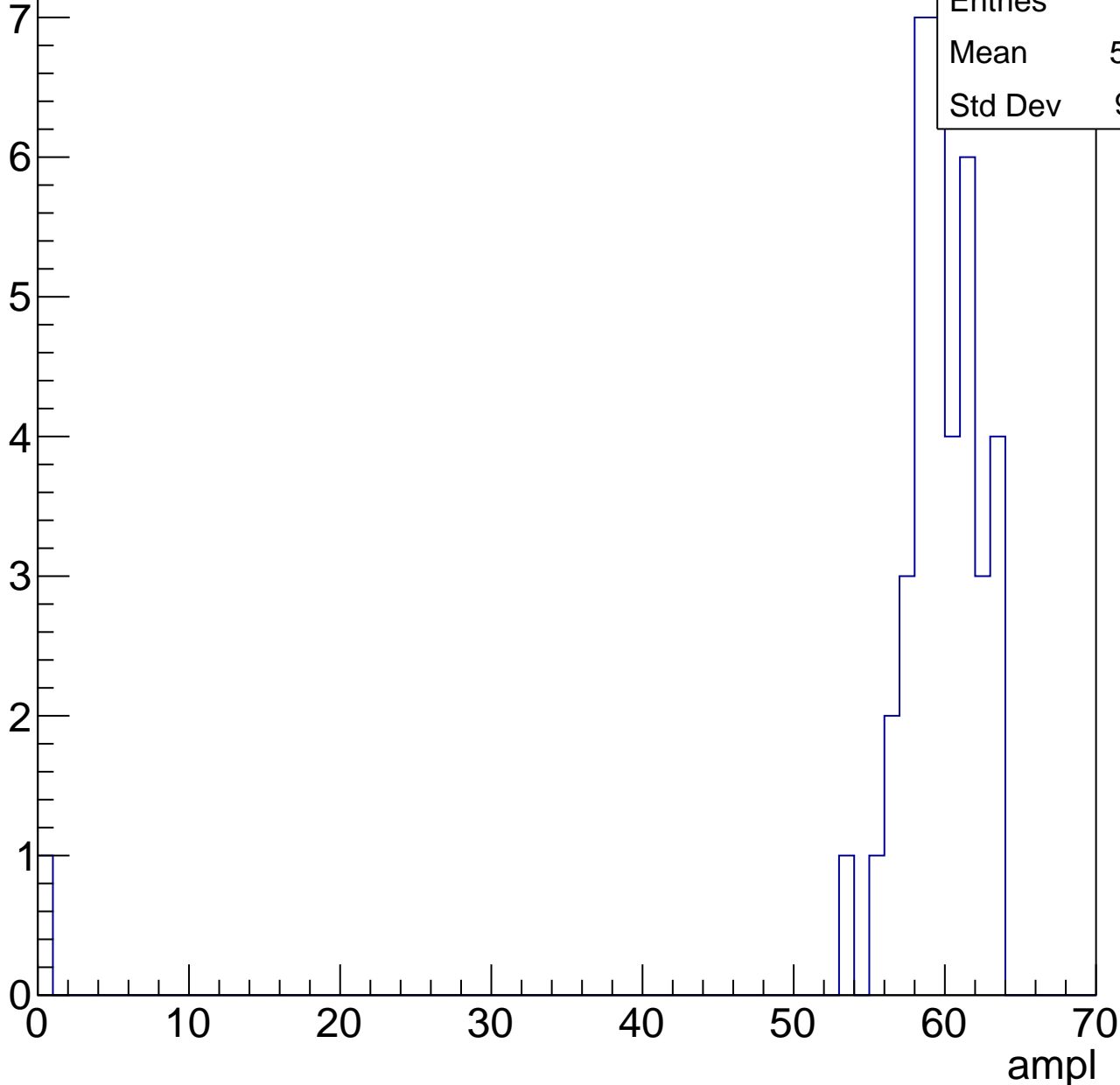


B1L003S, U16-ch75, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	57.79
Std Dev	9.651

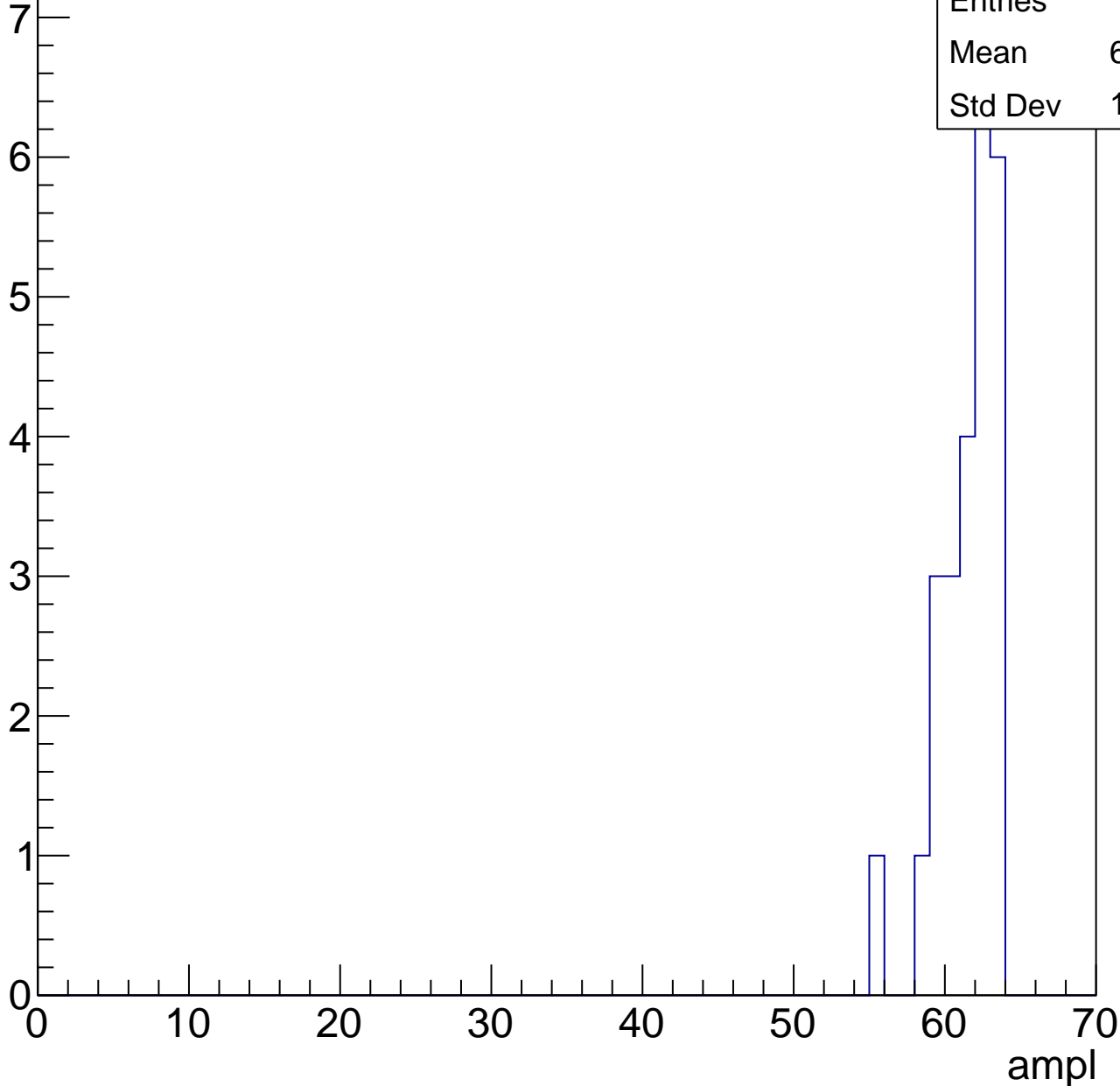


B1L003S, U16-ch75, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

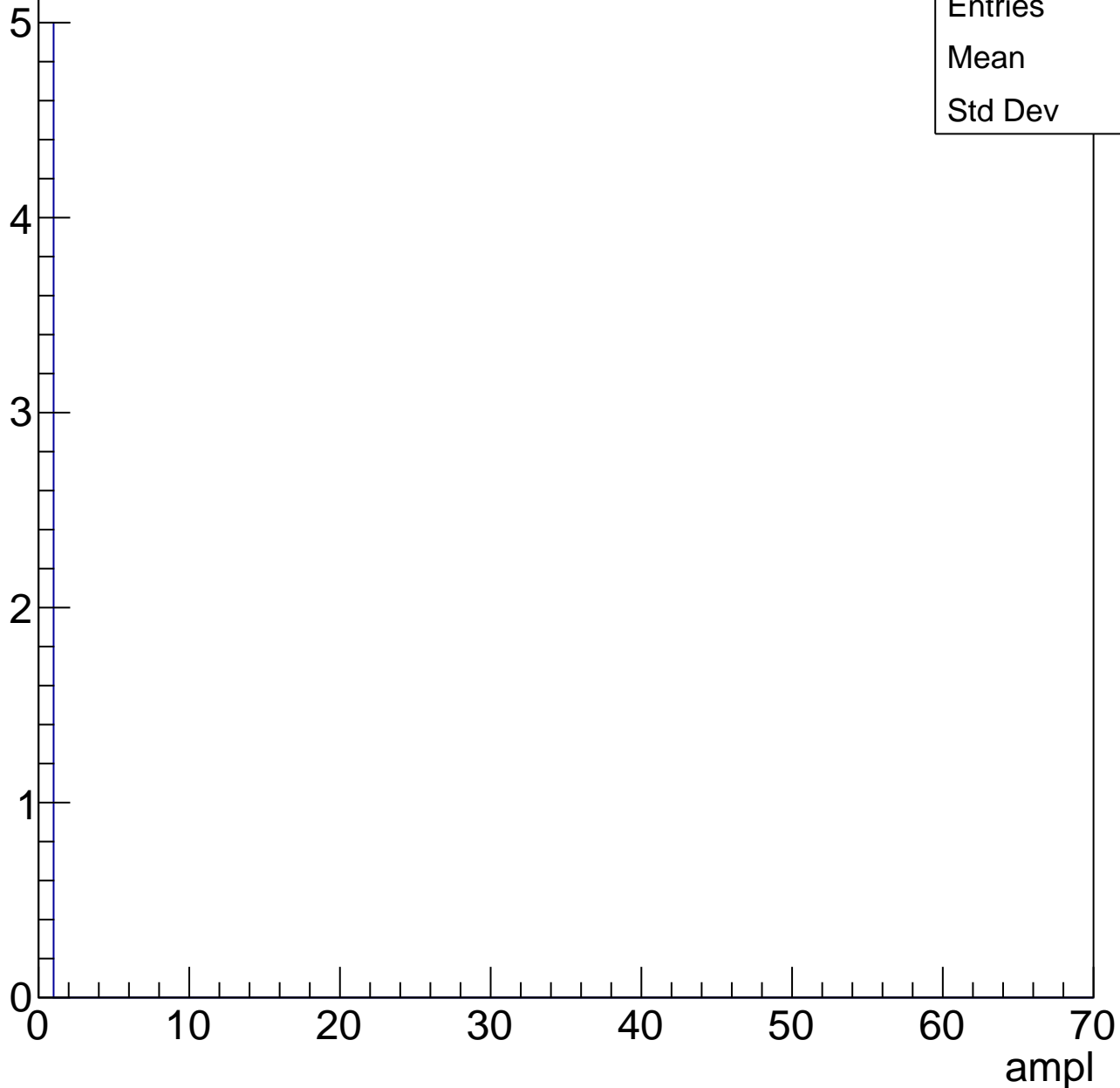
Entries	25
Mean	61.04
Std Dev	1.907



B1L003S, U16-ch75, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	0
Std Dev	0

B1L003S, U16-ch76, adc0

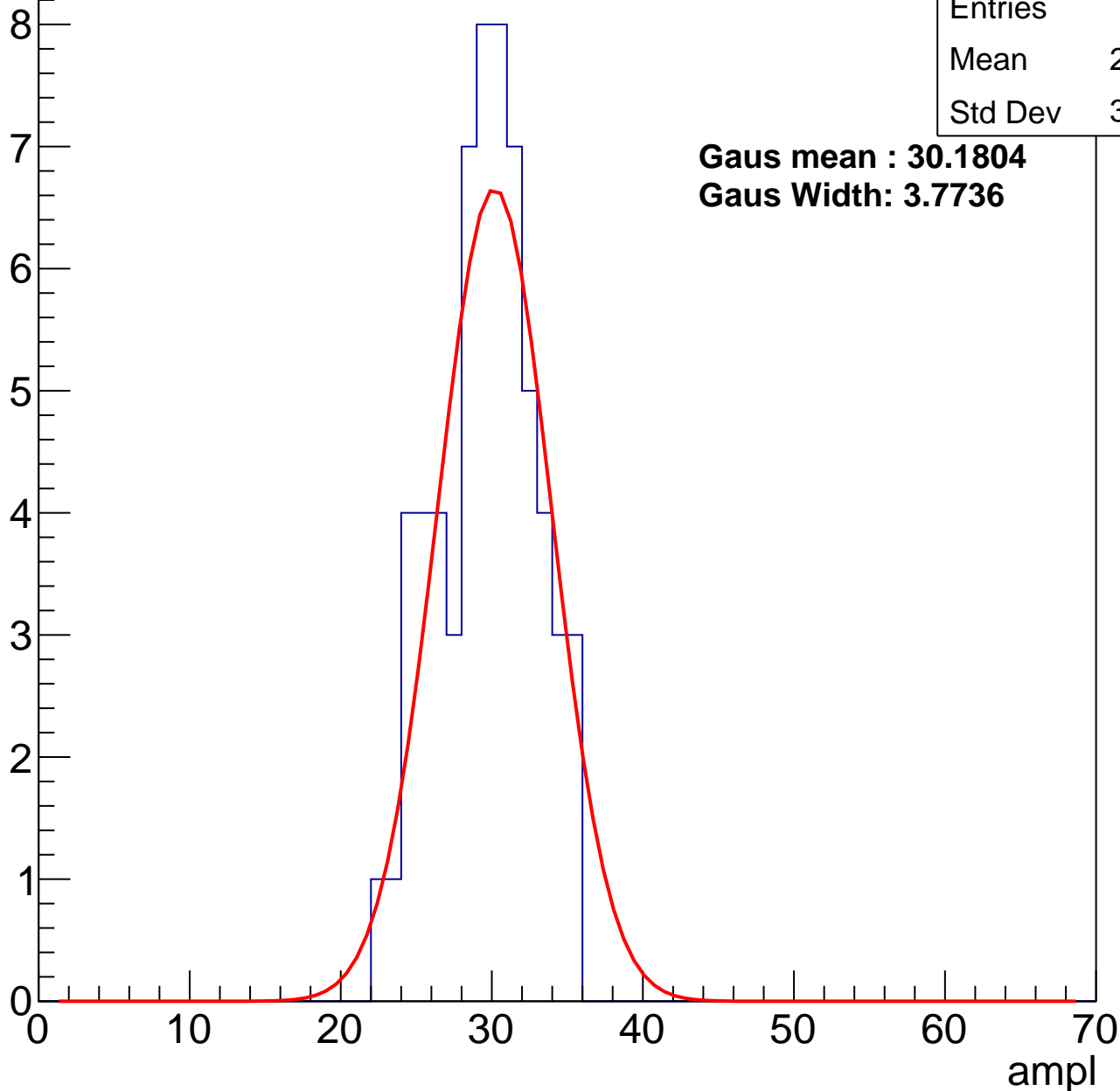
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.19
Std Dev	3.187

Gaus mean : 30.1804

Gaus Width: 3.7736



B1L003S, U16-ch76, adc1

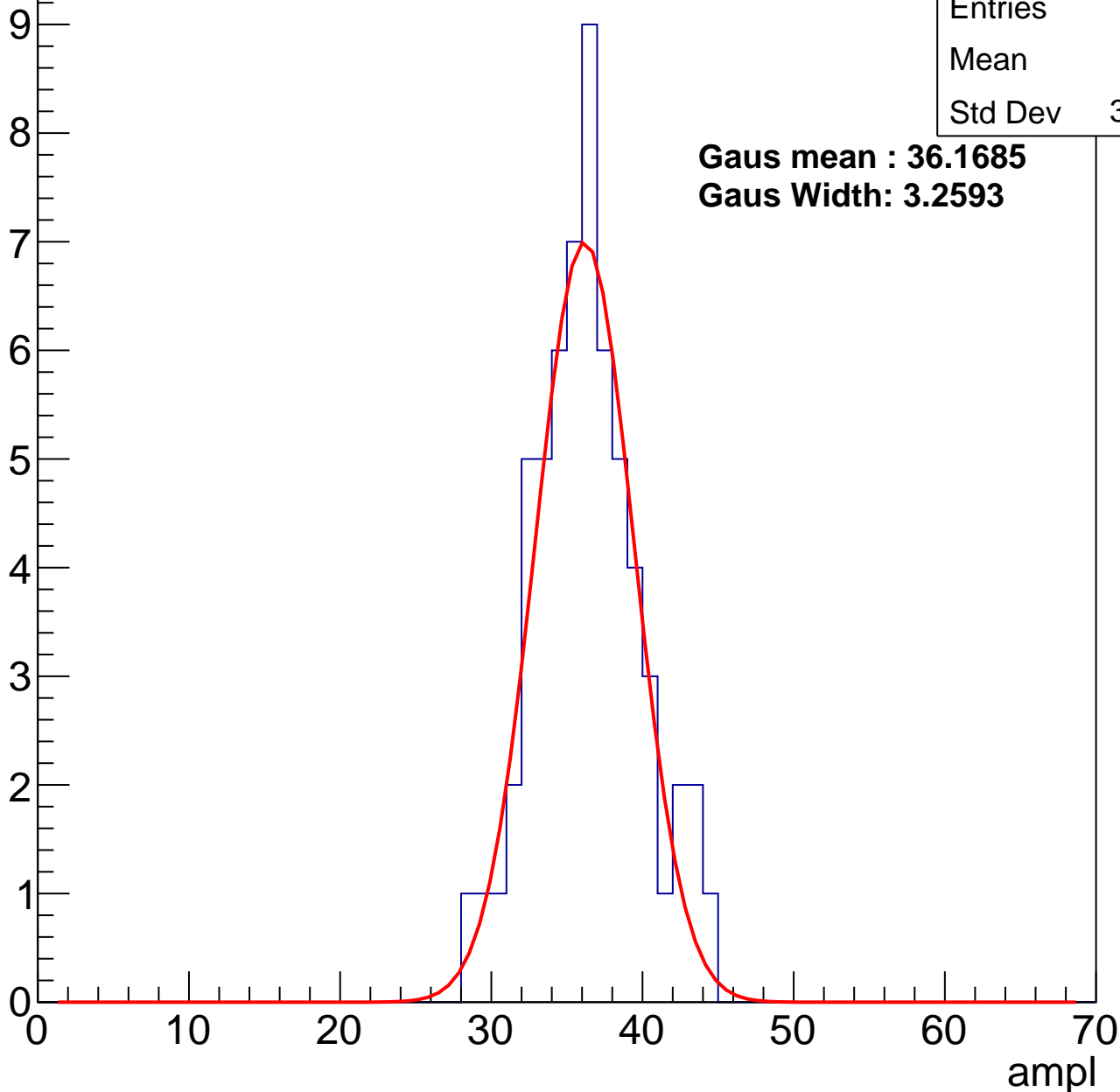
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.9
Std Dev	3.444

Gaus mean : 36.1685

Gaus Width: 3.2593



B1L003S, U16-ch76, adc2

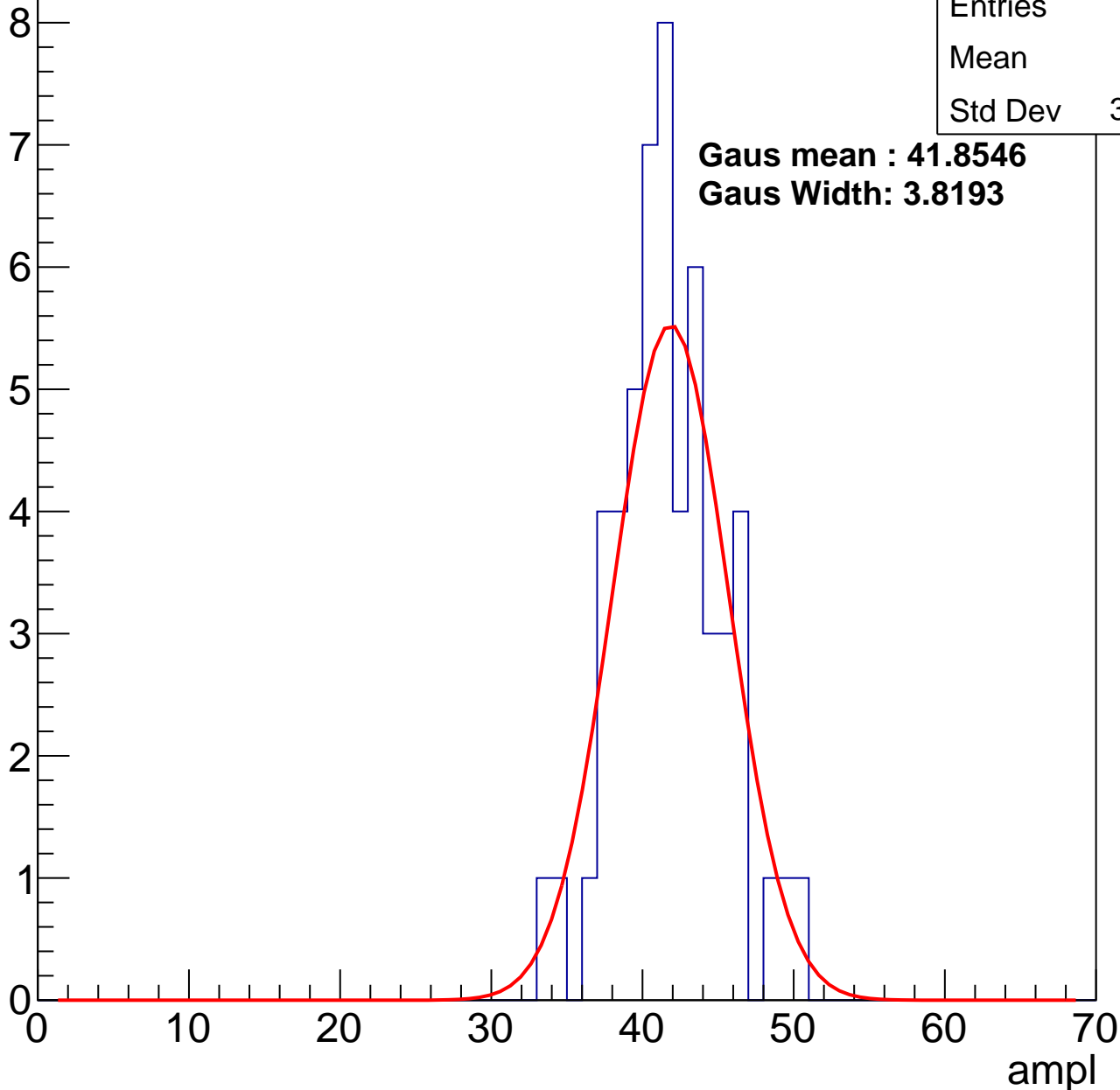
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.3
Std Dev	3.489

Gaus mean : 41.8546

Gaus Width: 3.8193

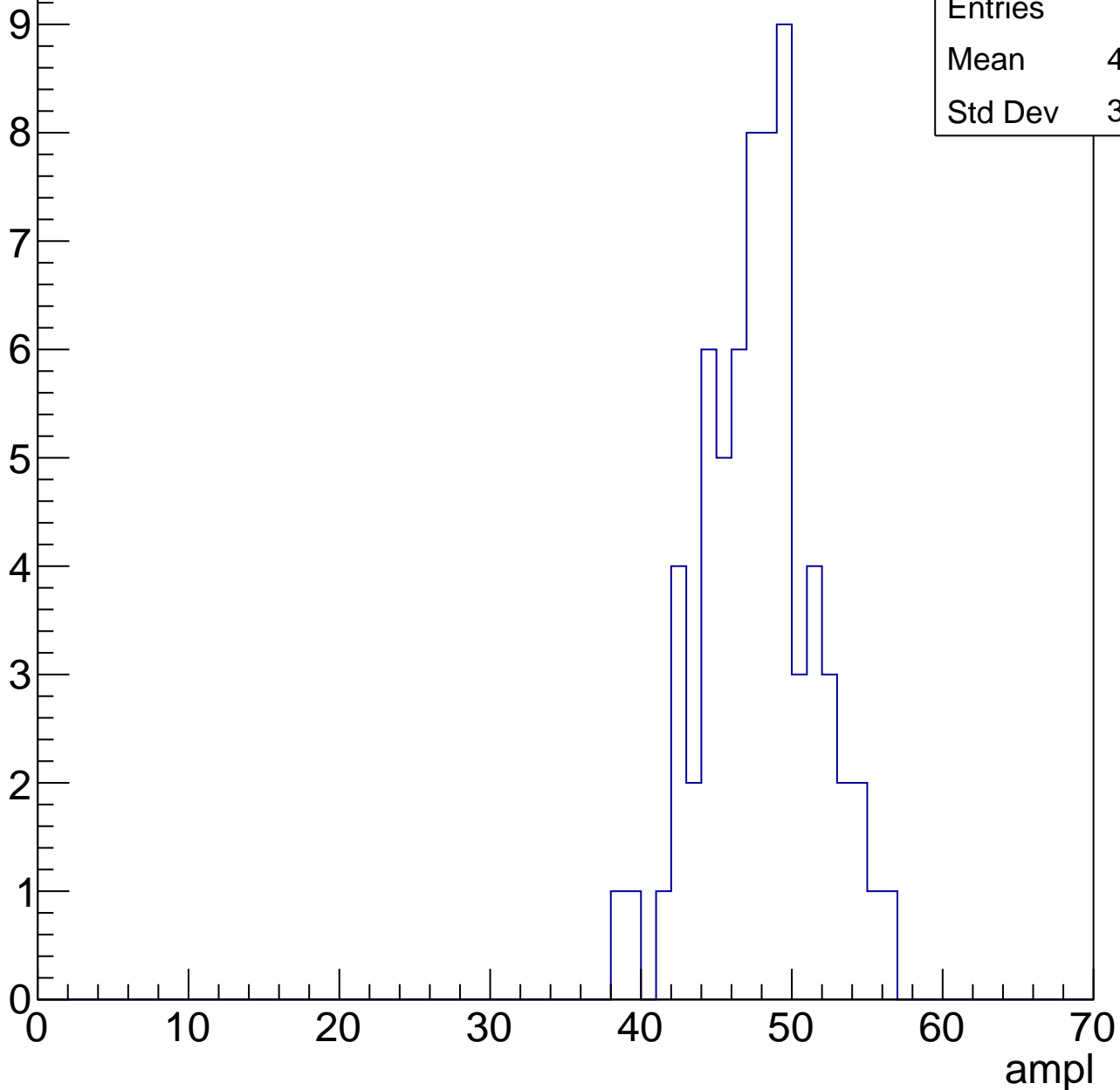


B1L003S, U16-ch76, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	47.36
Std Dev	3.684

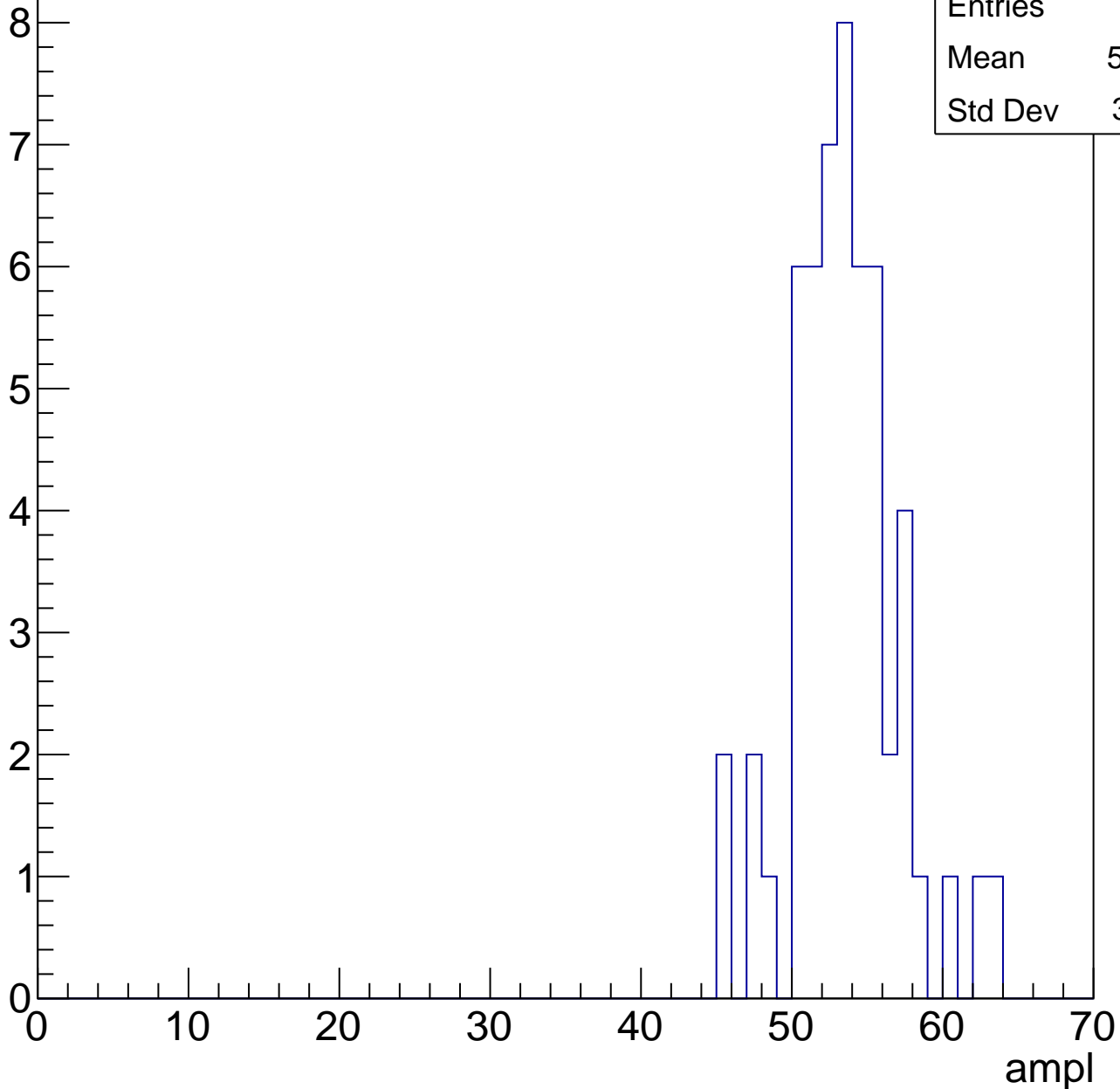


B1L003S, U16-ch76, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	53.02
Std Dev	3.541

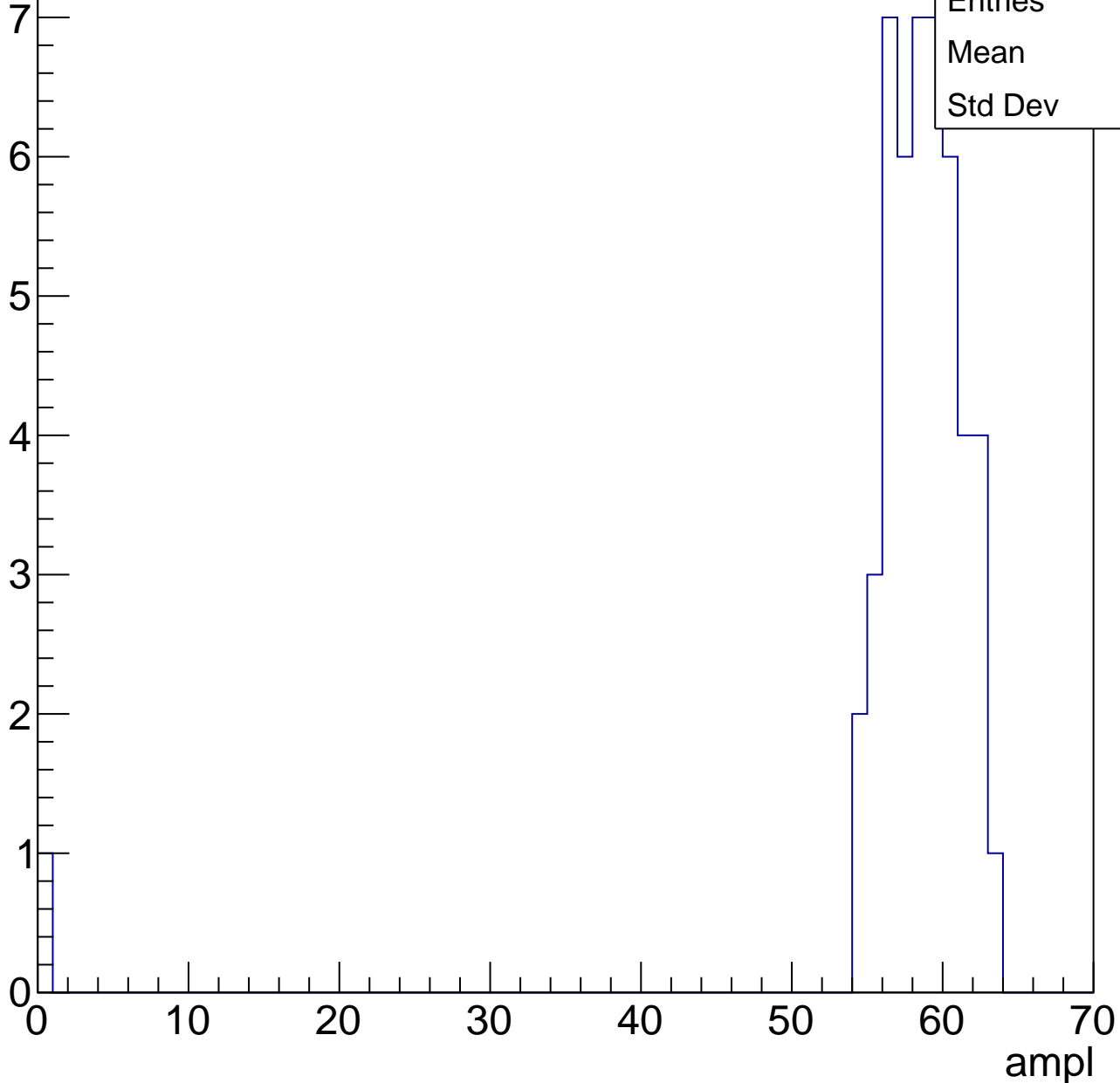


B1L003S, U16-ch76, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	57.1
Std Dev	8.63

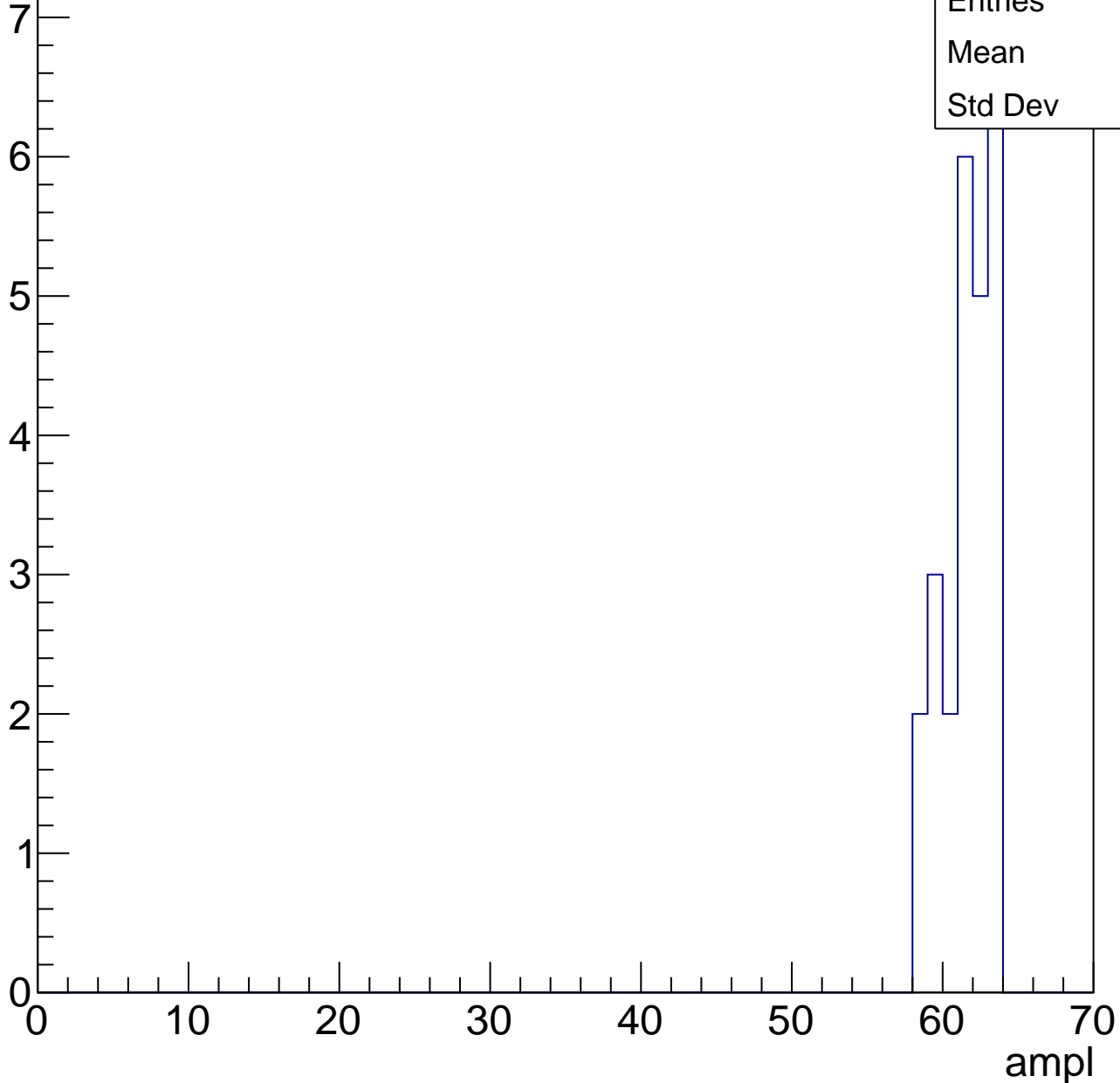


B1L003S, U16-ch76, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

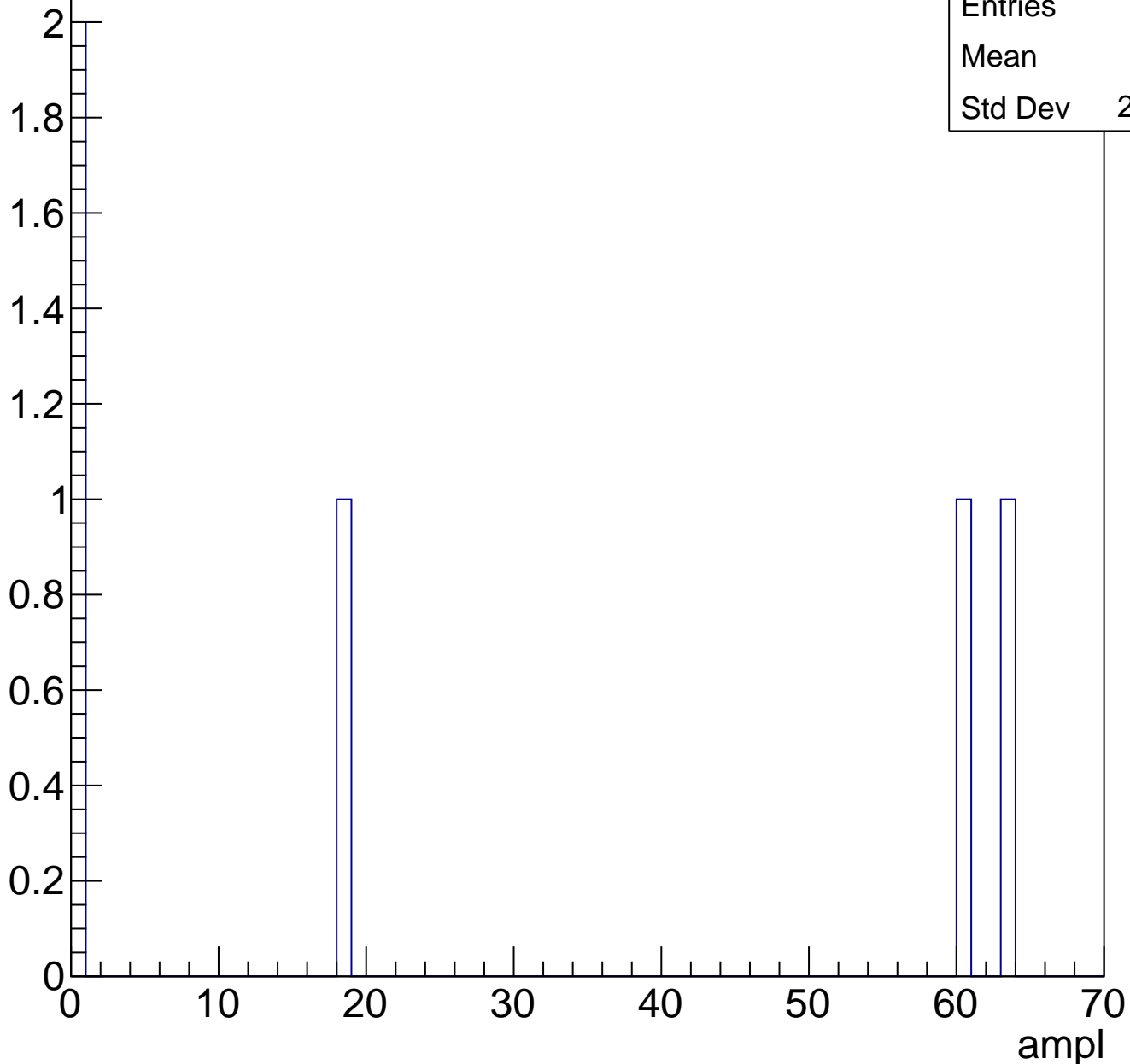
Entries	25
Mean	61.2
Std Dev	1.6



B1L003S, U16-ch76, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	28.2
Std Dev	27.99

B1L003S, U16-ch77, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	30.4
Std Dev	5.093

Gaus mean : 31.2772

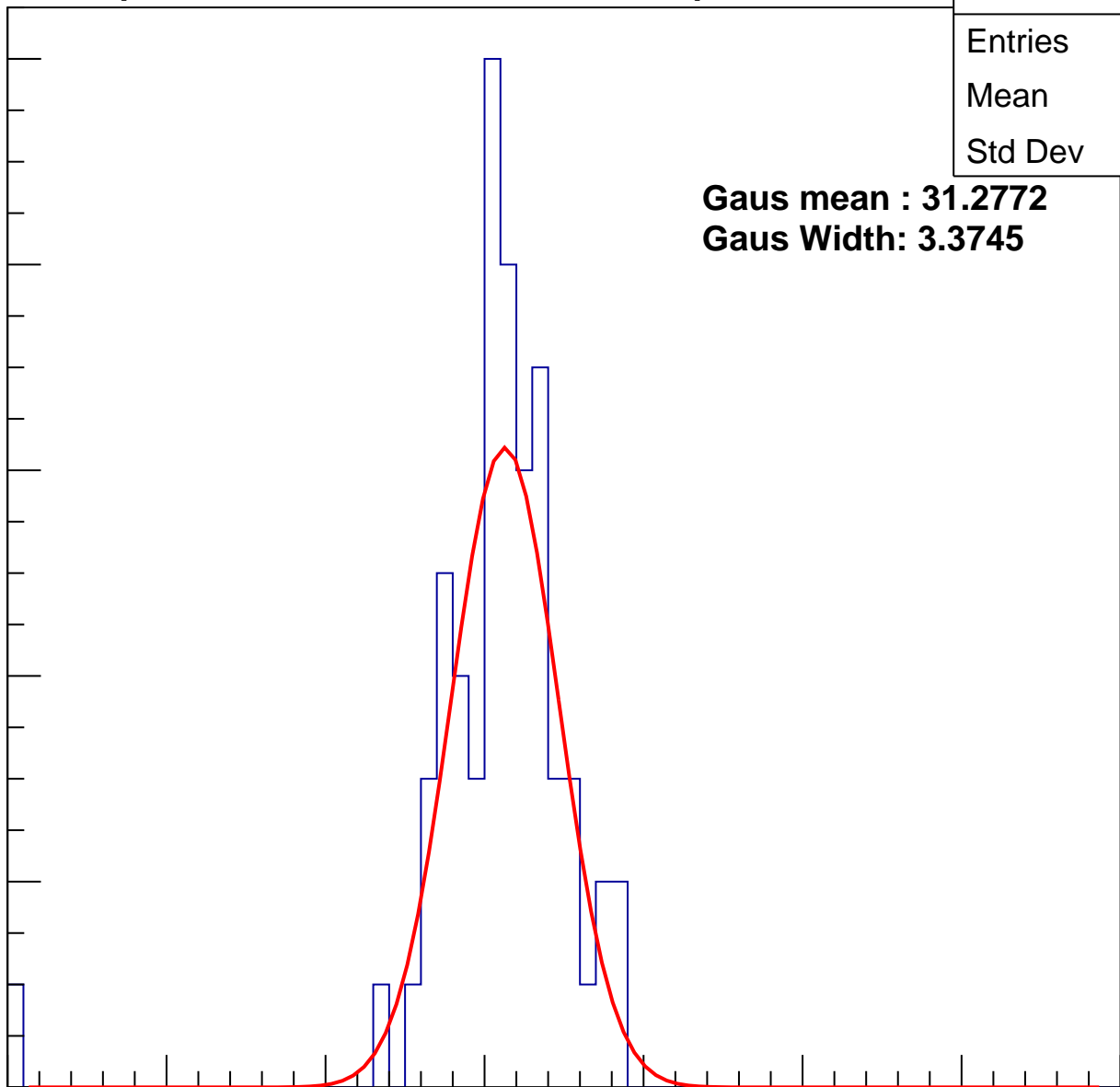
Gaus Width: 3.3745

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch77, adc1

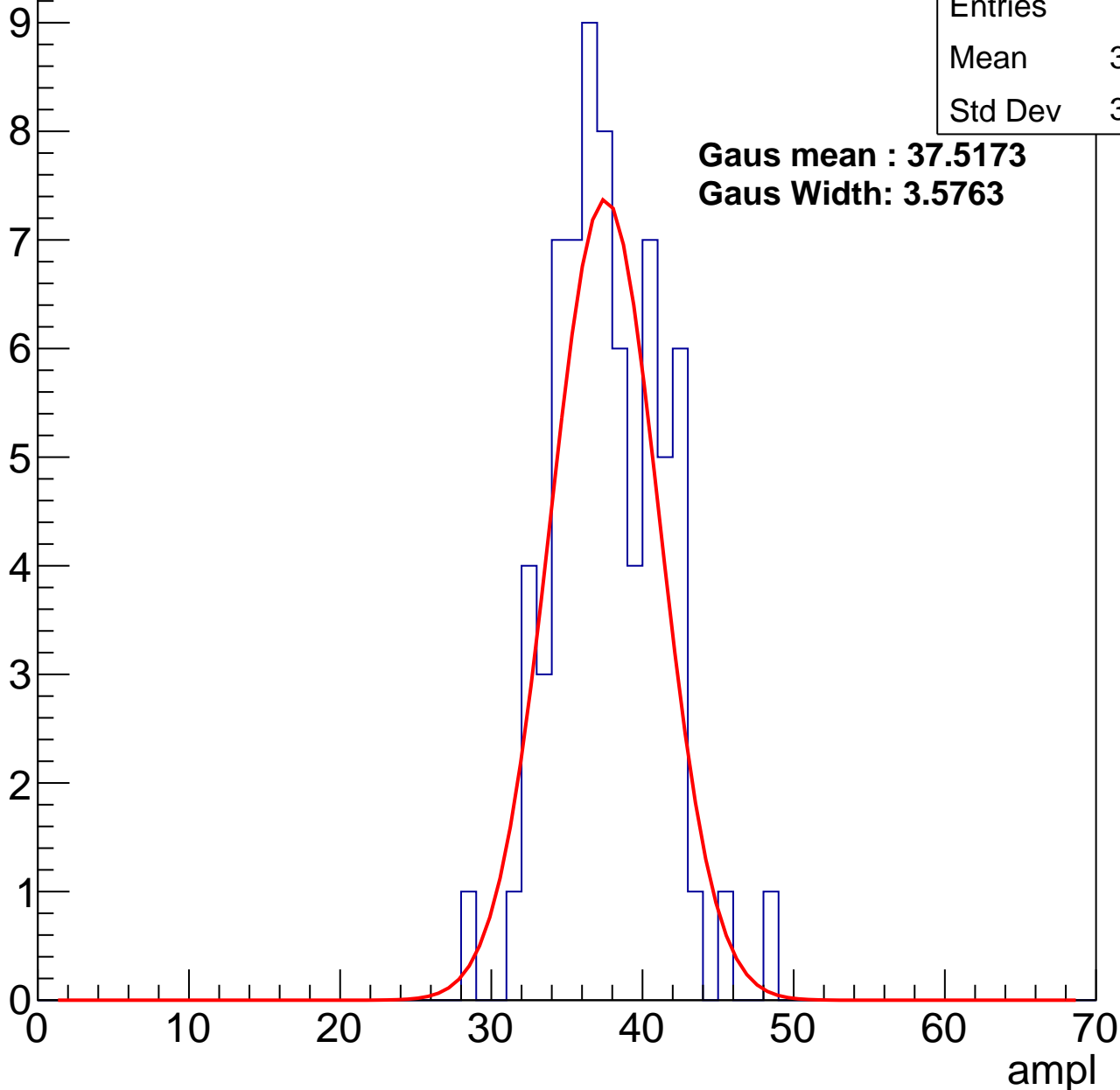
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	37.27
Std Dev	3.552

Gaus mean : 37.5173

Gaus Width: 3.5763



B1L003S, U16-ch77, adc2

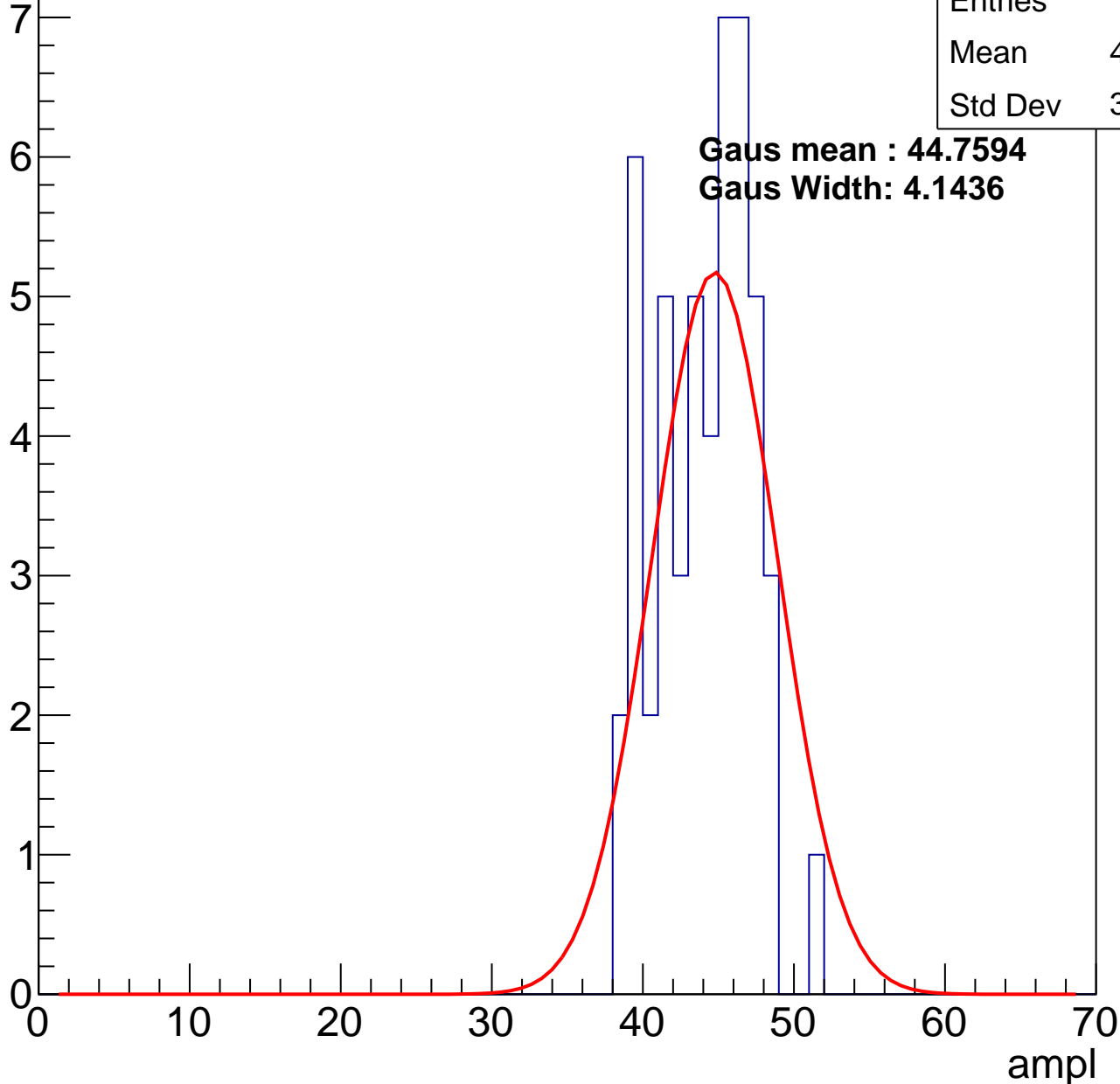
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.58
Std Dev	3.112

Gaus mean : 44.7594

Gaus Width: 4.1436

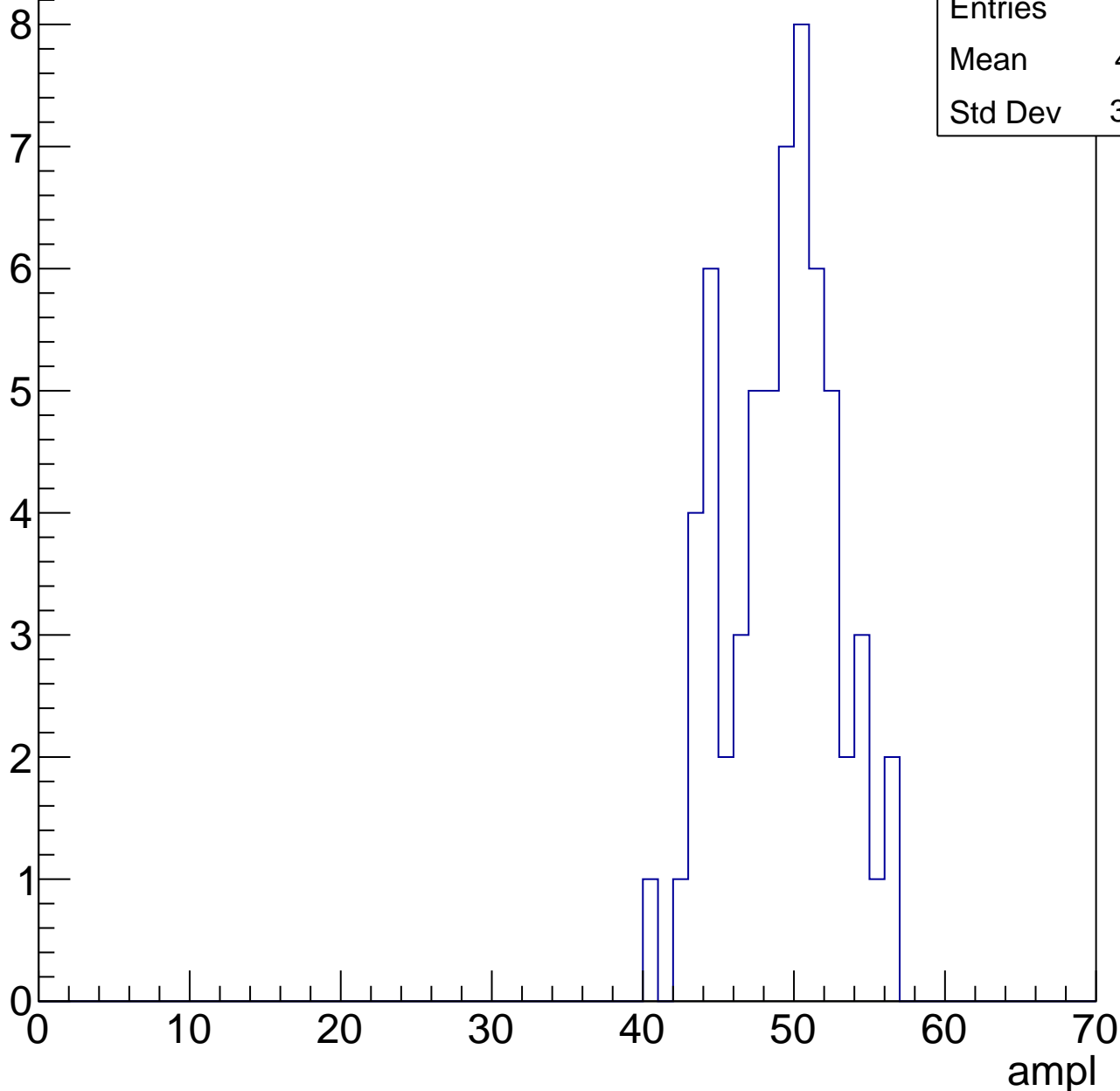


B1L003S, U16-ch77, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

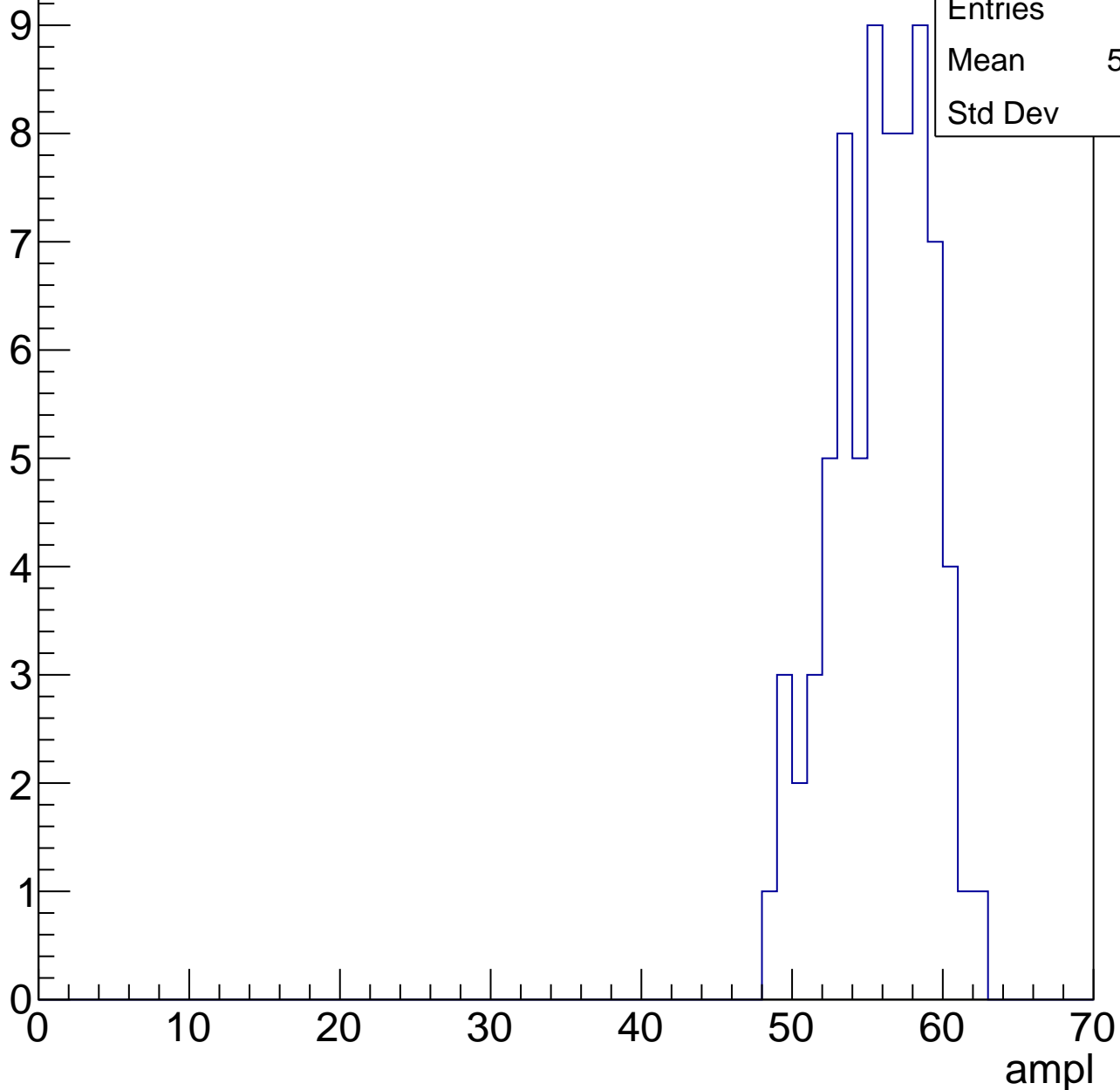
Entries	61
Mean	48.61
Std Dev	3.659



B1L003S, U16-ch77, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



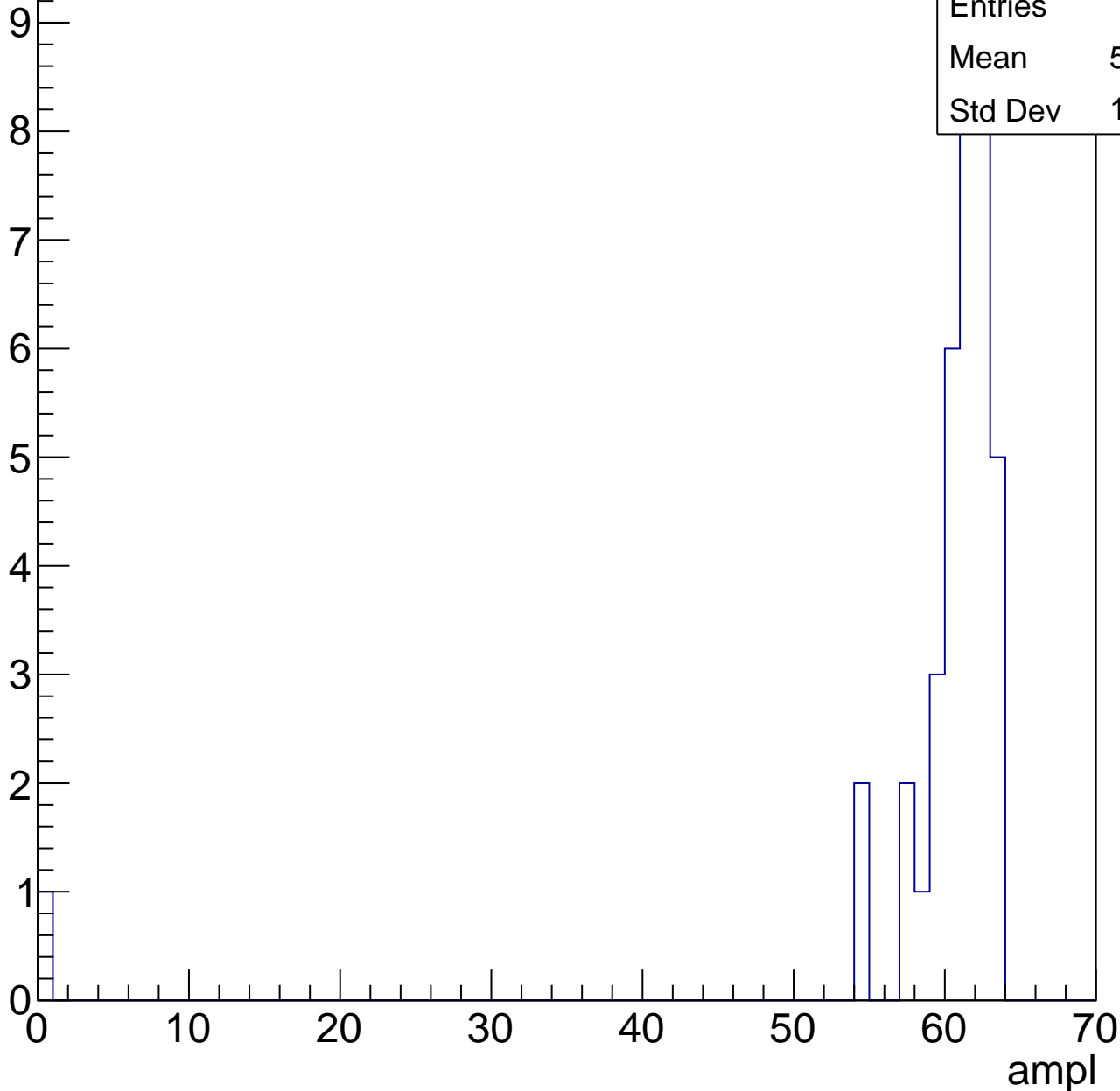
Entries	74
Mean	55.39
Std Dev	3.17

B1L003S, U16-ch77, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	58.86
Std Dev	10.05

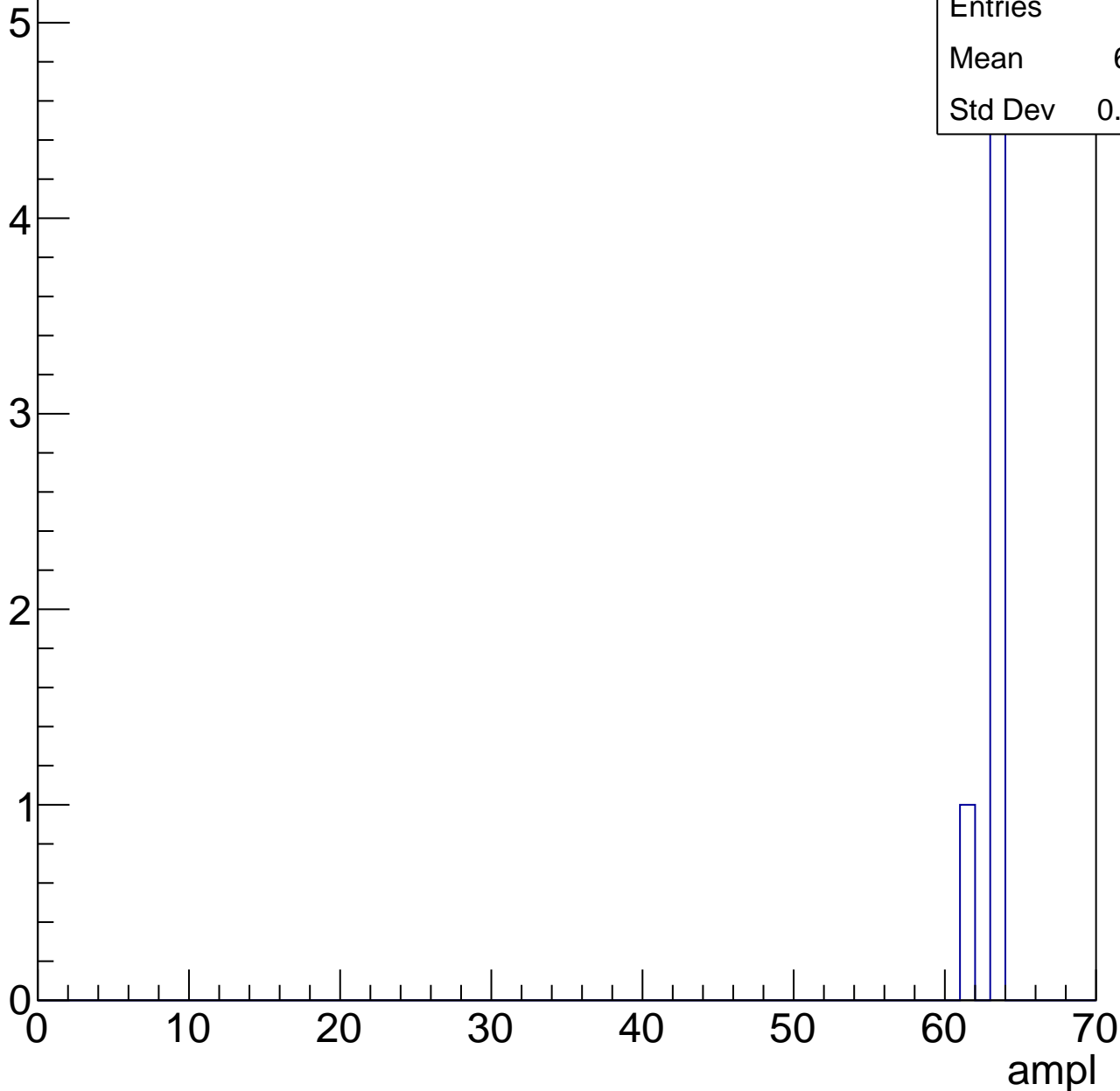


B1L003S, U16-ch77, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B1L003S, U16-ch77, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch78, adc0

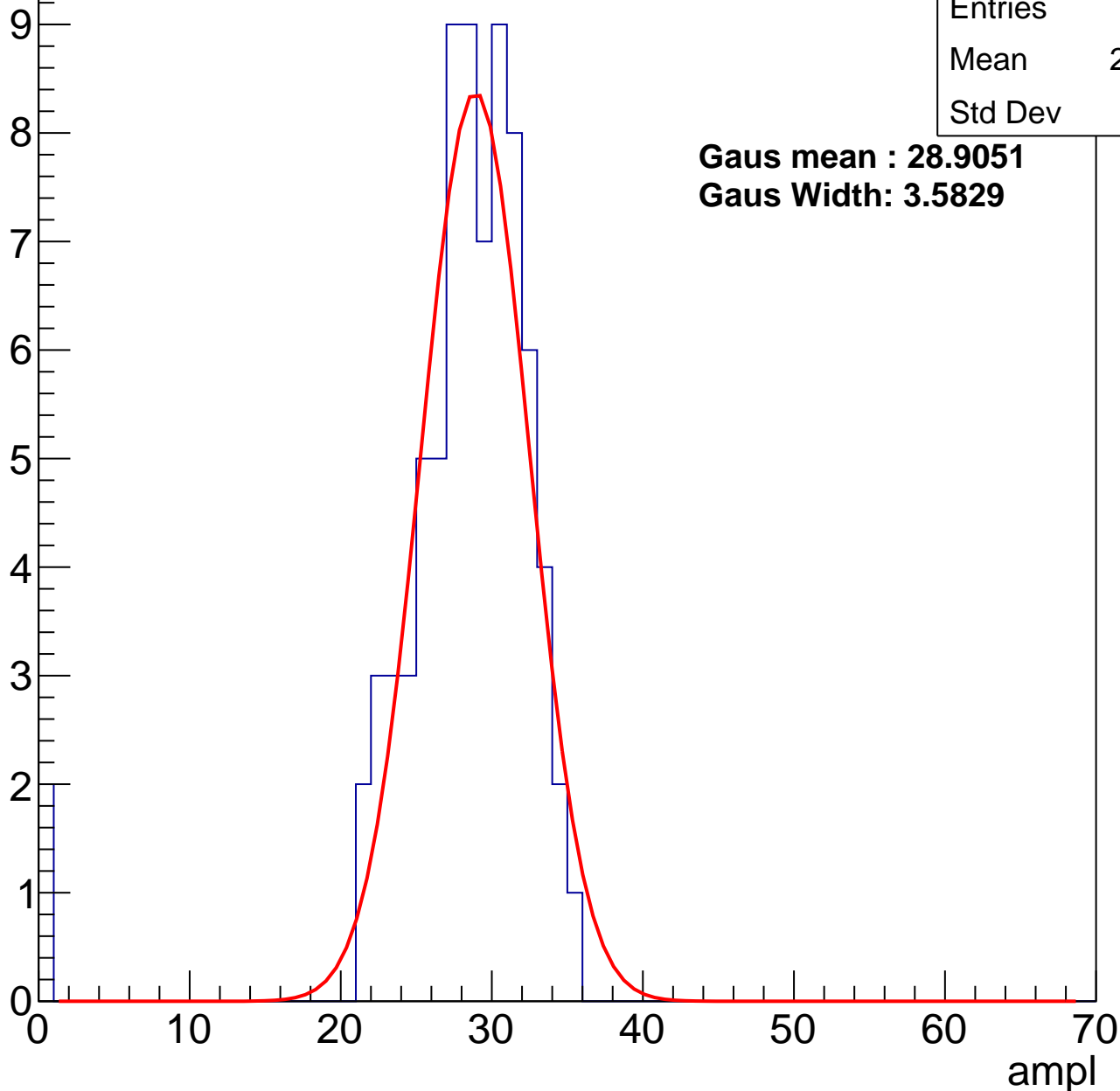
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	27.53
Std Dev	5.53

Gaus mean : 28.9051

Gaus Width: 3.5829



B1L003S, U16-ch78, adc1

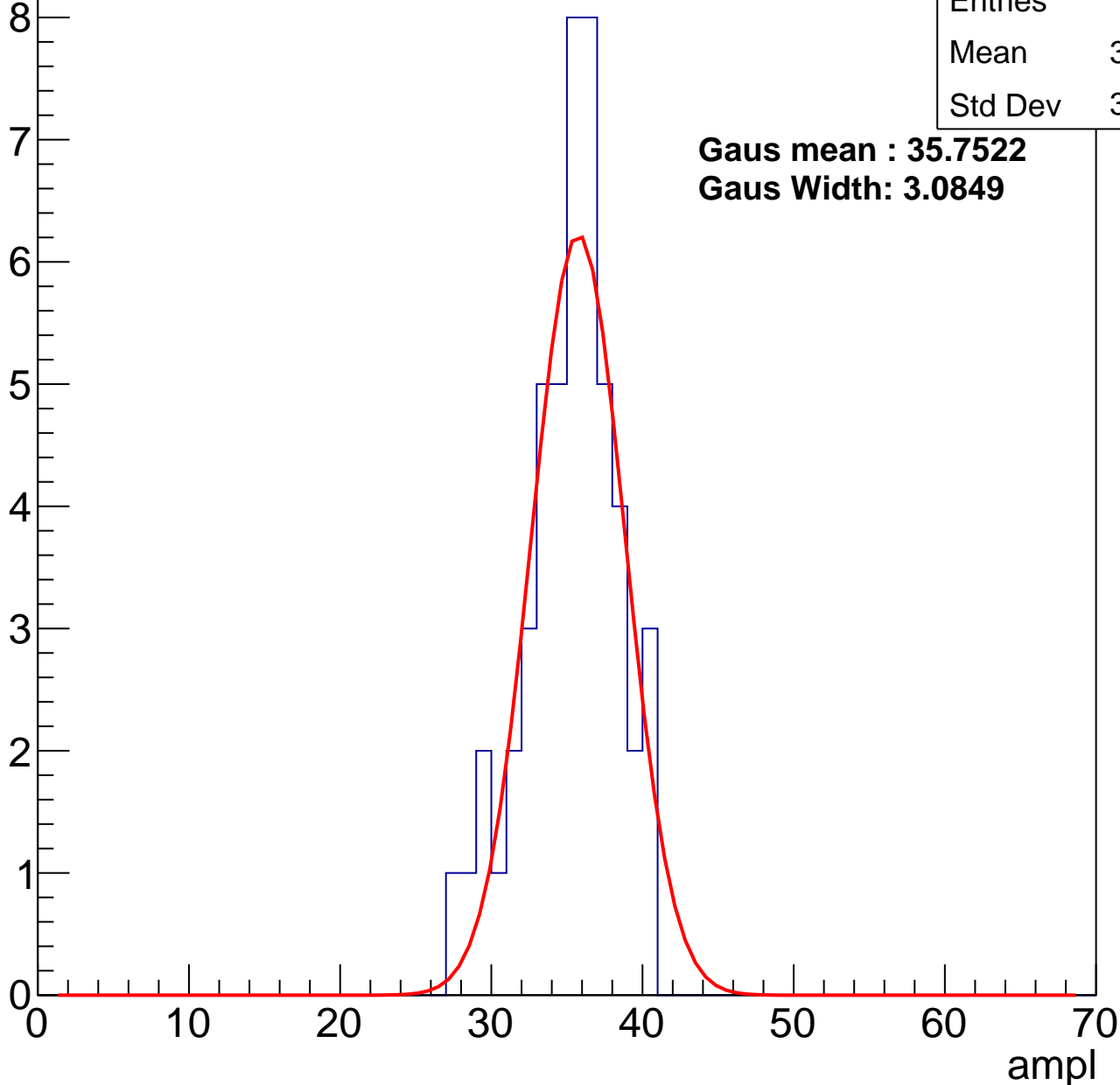
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	34.78
Std Dev	3.042

Gaus mean : 35.7522

Gaus Width: 3.0849



B1L003S, U16-ch78, adc2

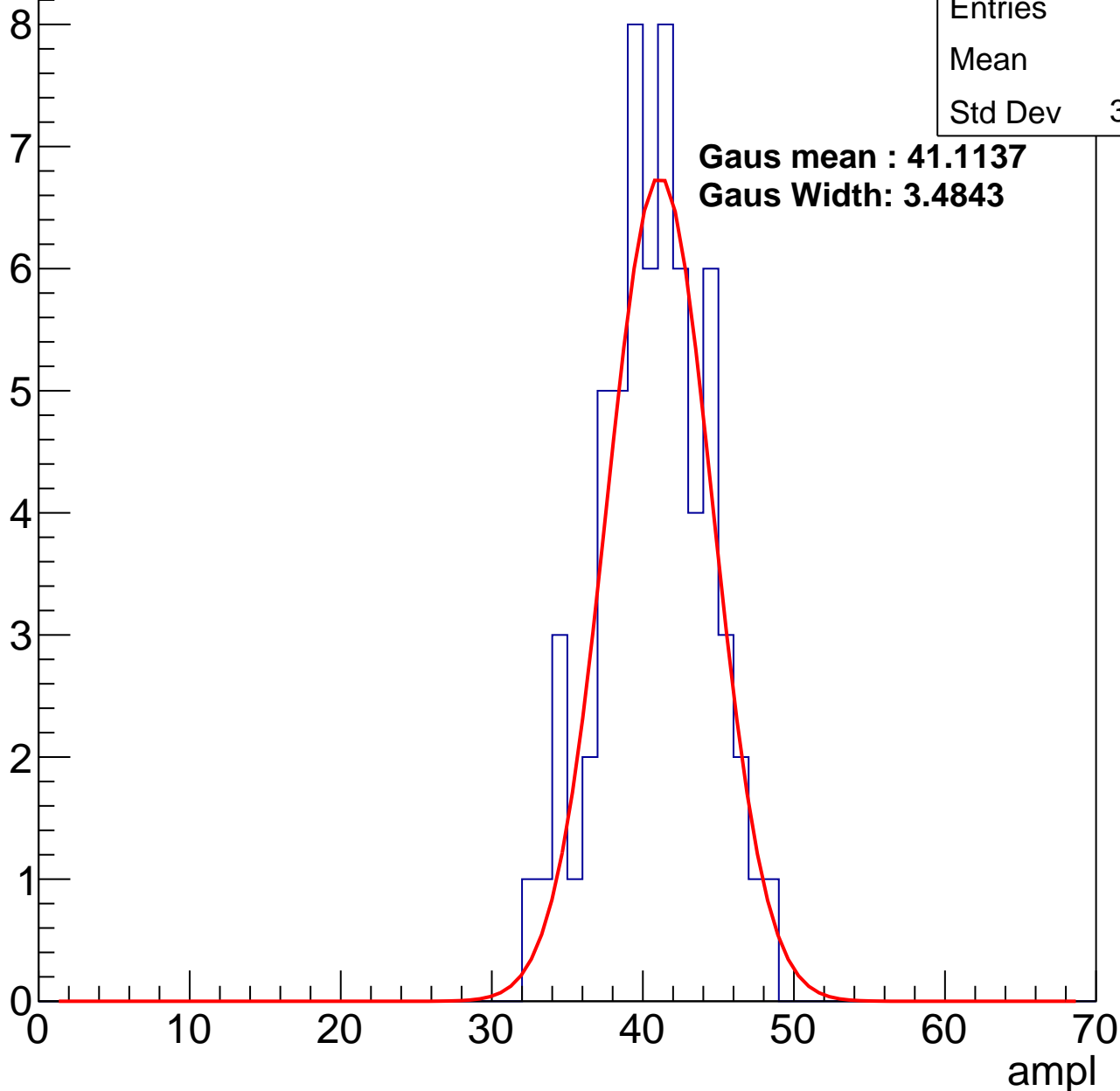
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	40.3
Std Dev	3.494

Gaus mean : 41.1137

Gaus Width: 3.4843

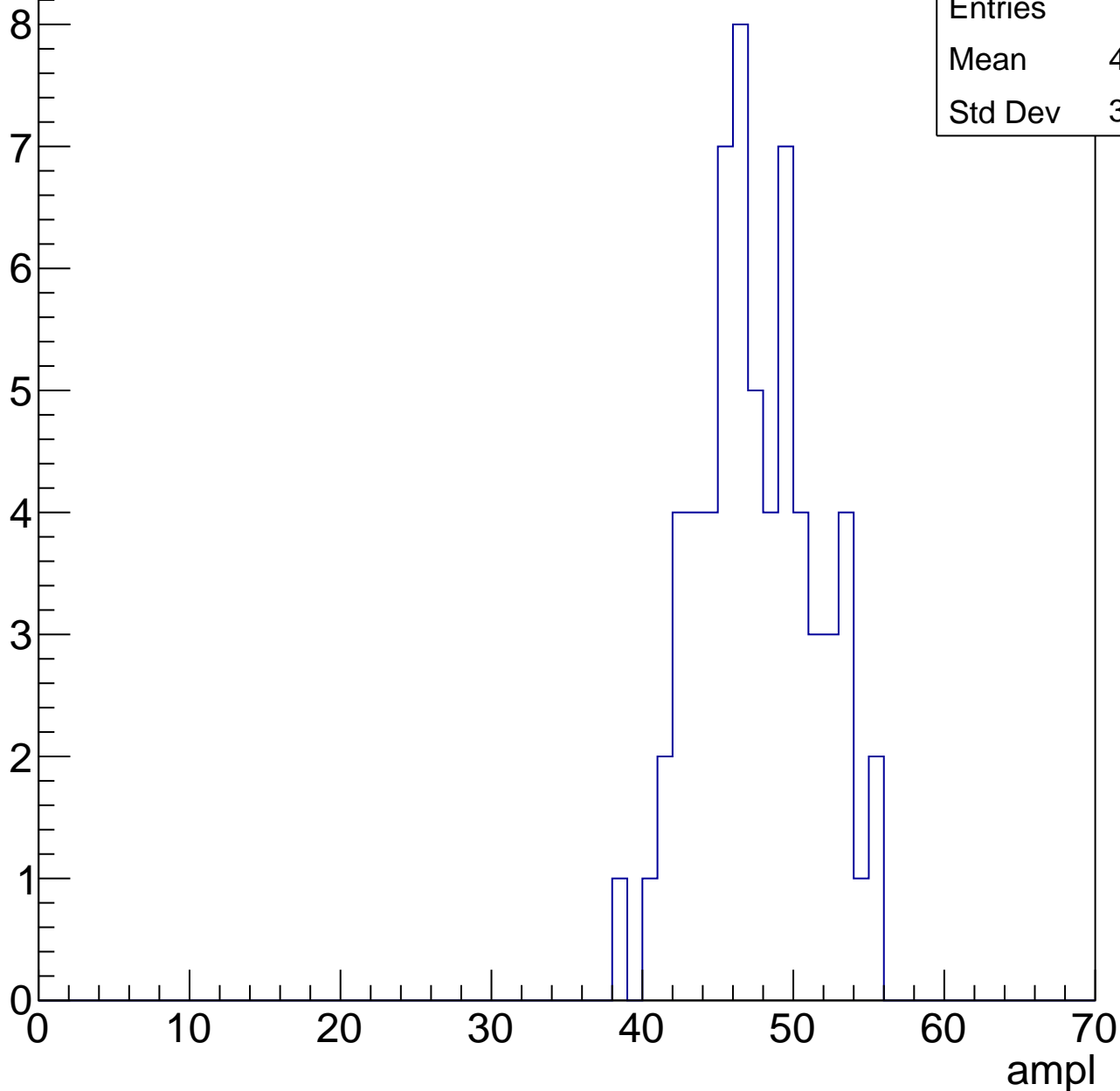


B1L003S, U16-ch78, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47.09
Std Dev	3.852

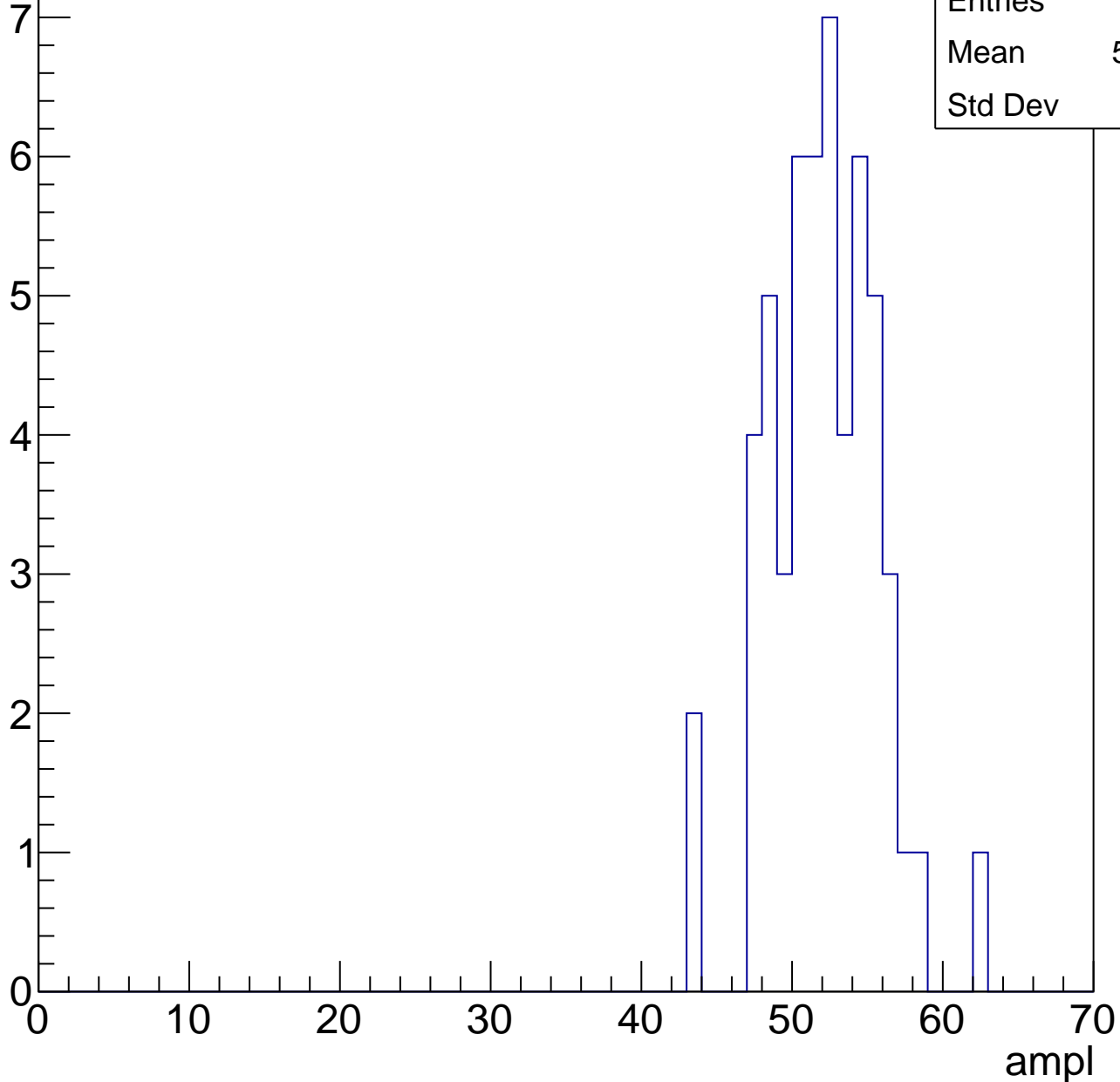


B1L003S, U16-ch78, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	51.61
Std Dev	3.53

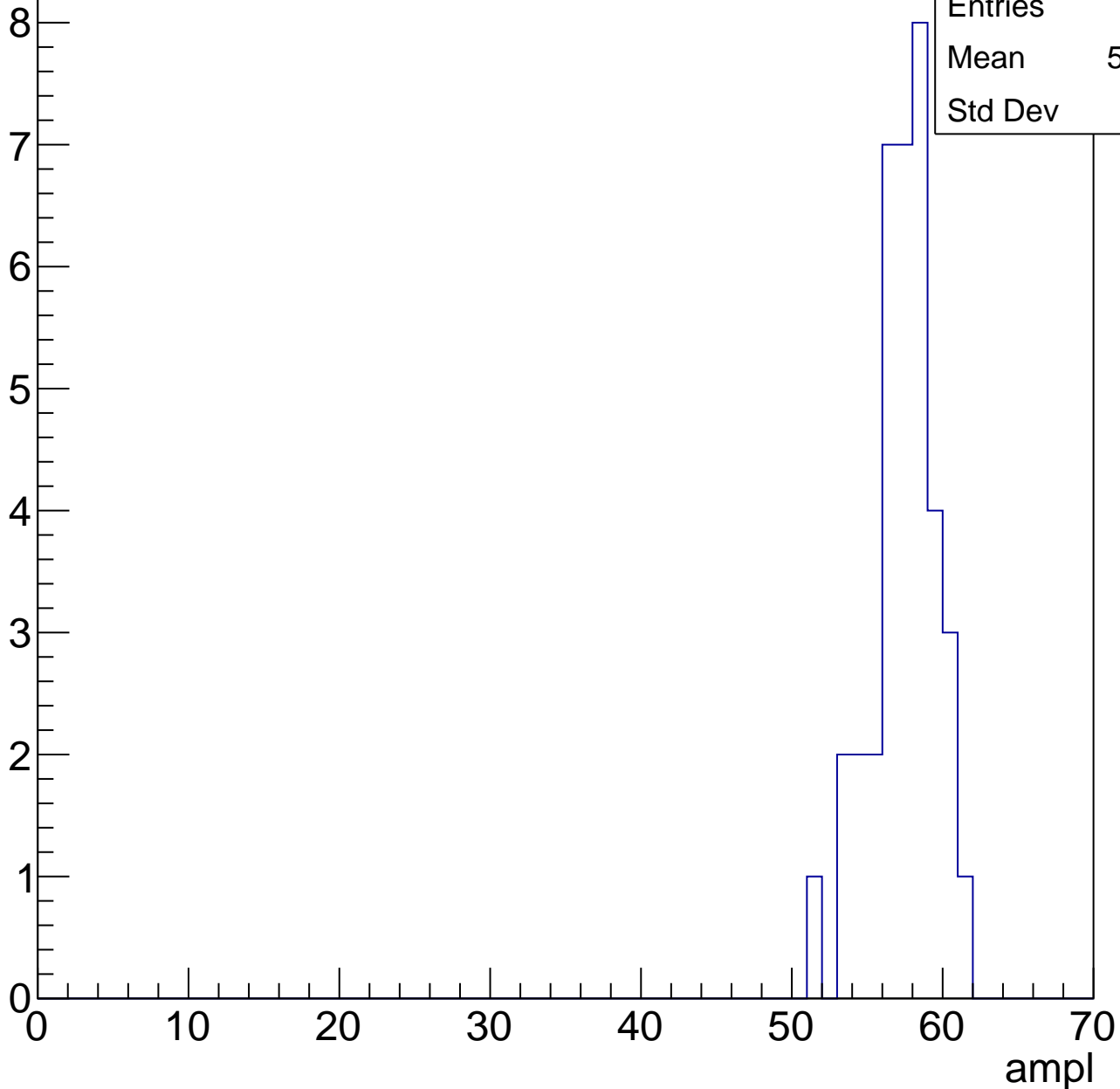


B1L003S, U16-ch78, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	56.95
Std Dev	2.13



B1L003S, U16-ch78, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	44
Mean	59.27
Std Dev	9.287

Entry

10

8

6

4

2

0

0

10

20

30

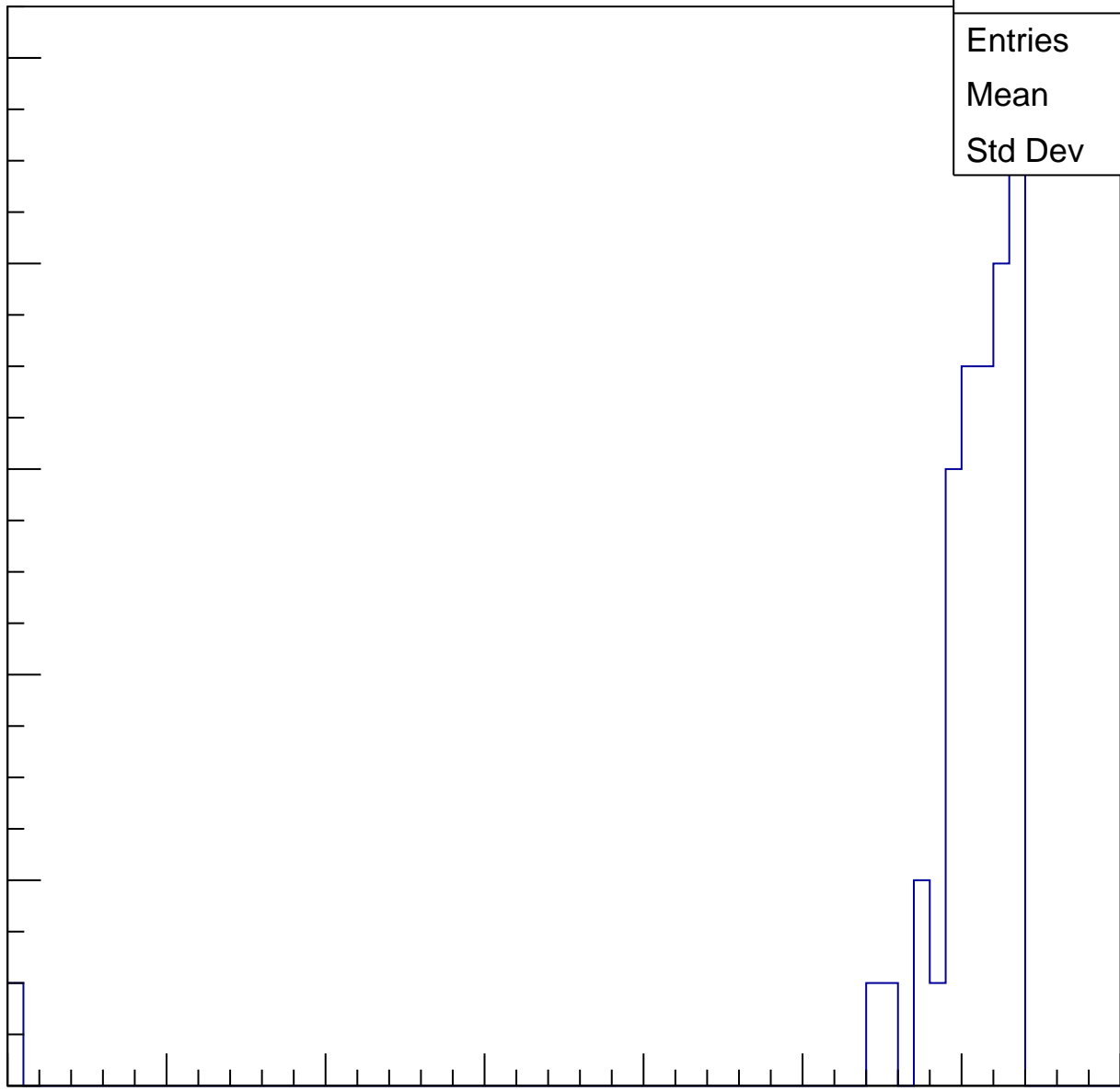
40

50

60

ampl

70



B1L003S, U16-ch78, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L003S, U16-ch79, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	28.83
Std Dev	3.648

Gaus mean : 29.4894

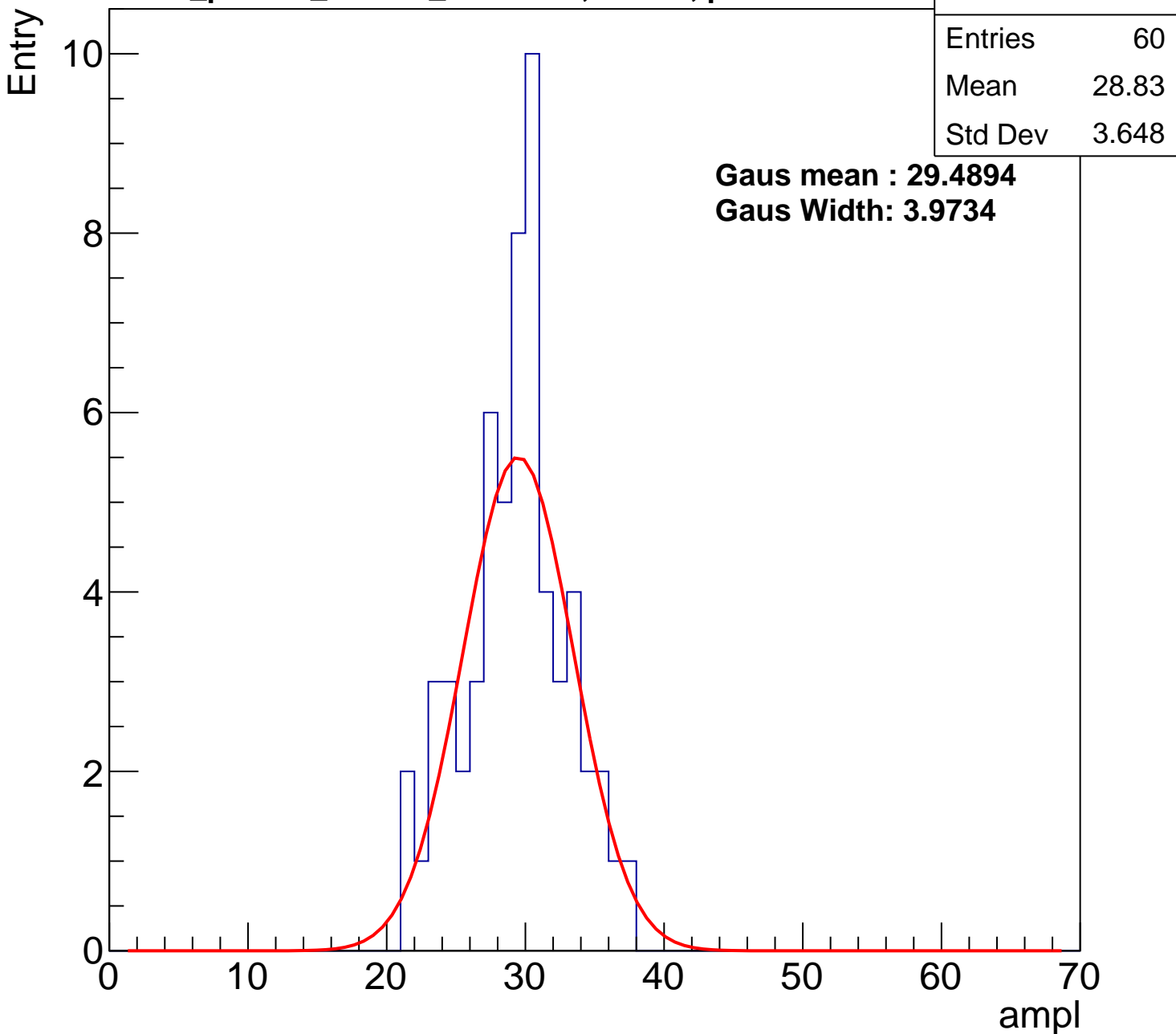
Gaus Width: 3.9734

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch79, adc1

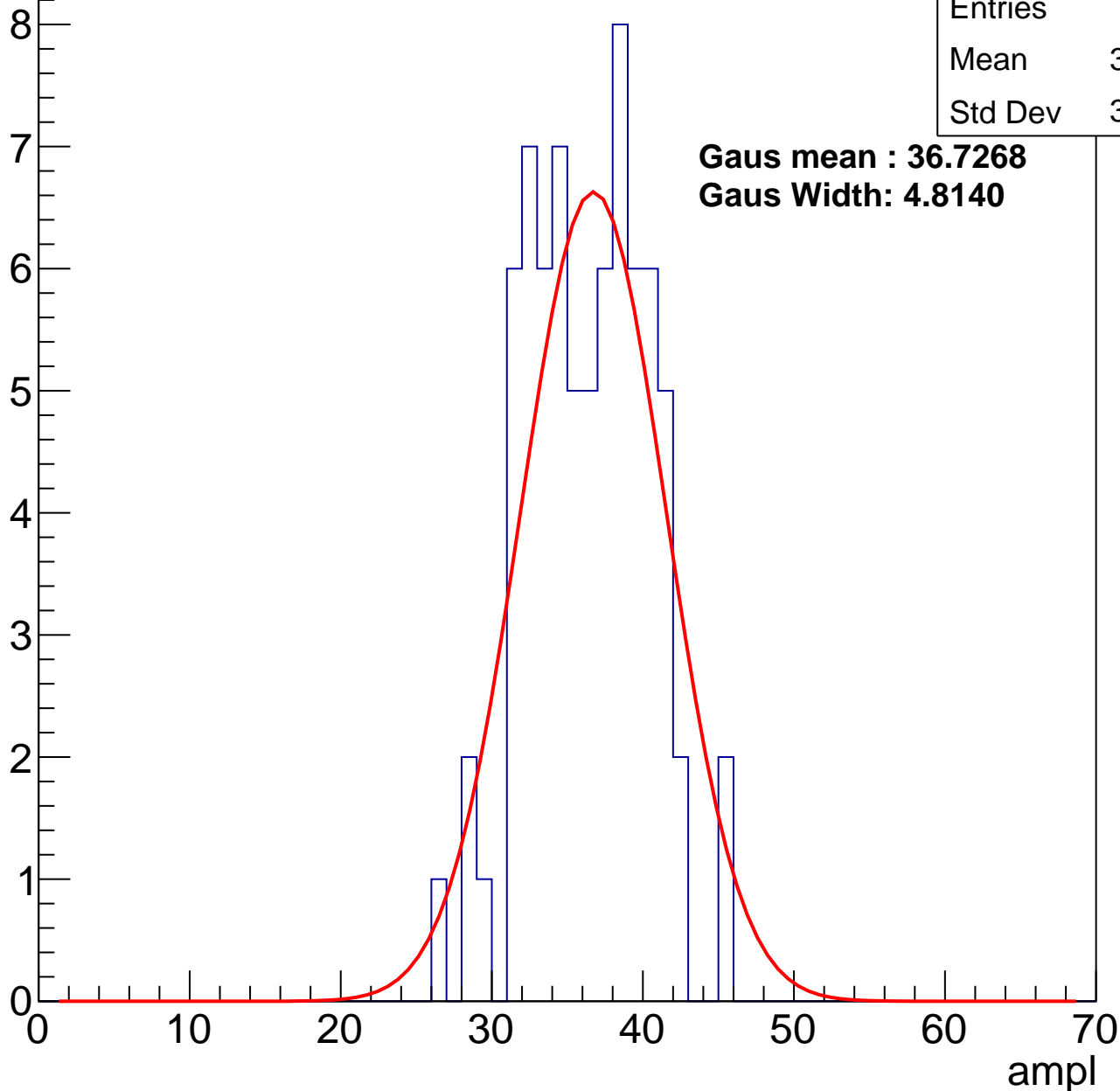
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	35.88
Std Dev	3.953

Gaus mean : 36.7268

Gaus Width: 4.8140



B1L003S, U16-ch79, adc2

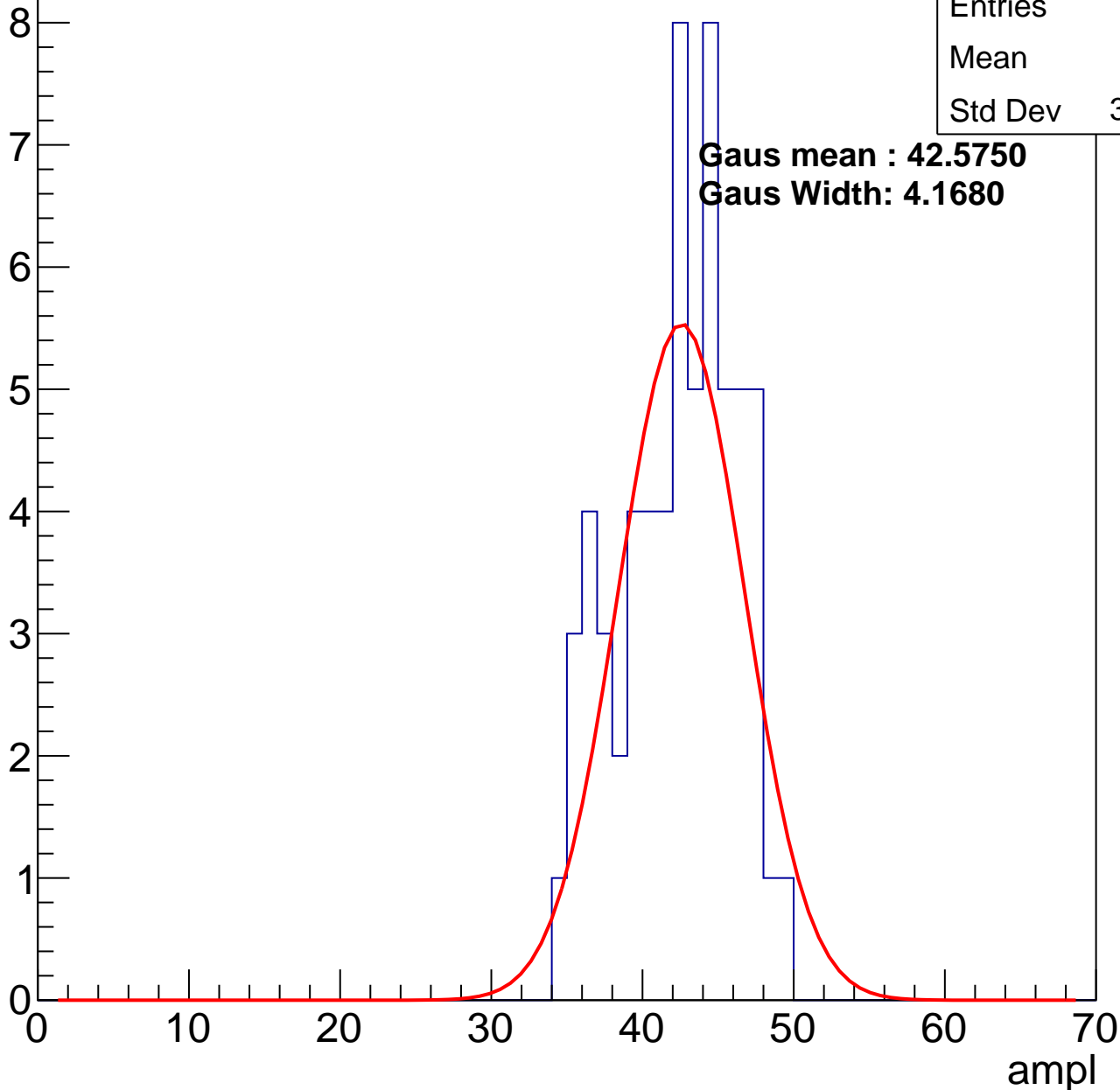
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.9
Std Dev	3.757

Gaus mean : 42.5750

Gaus Width: 4.1680

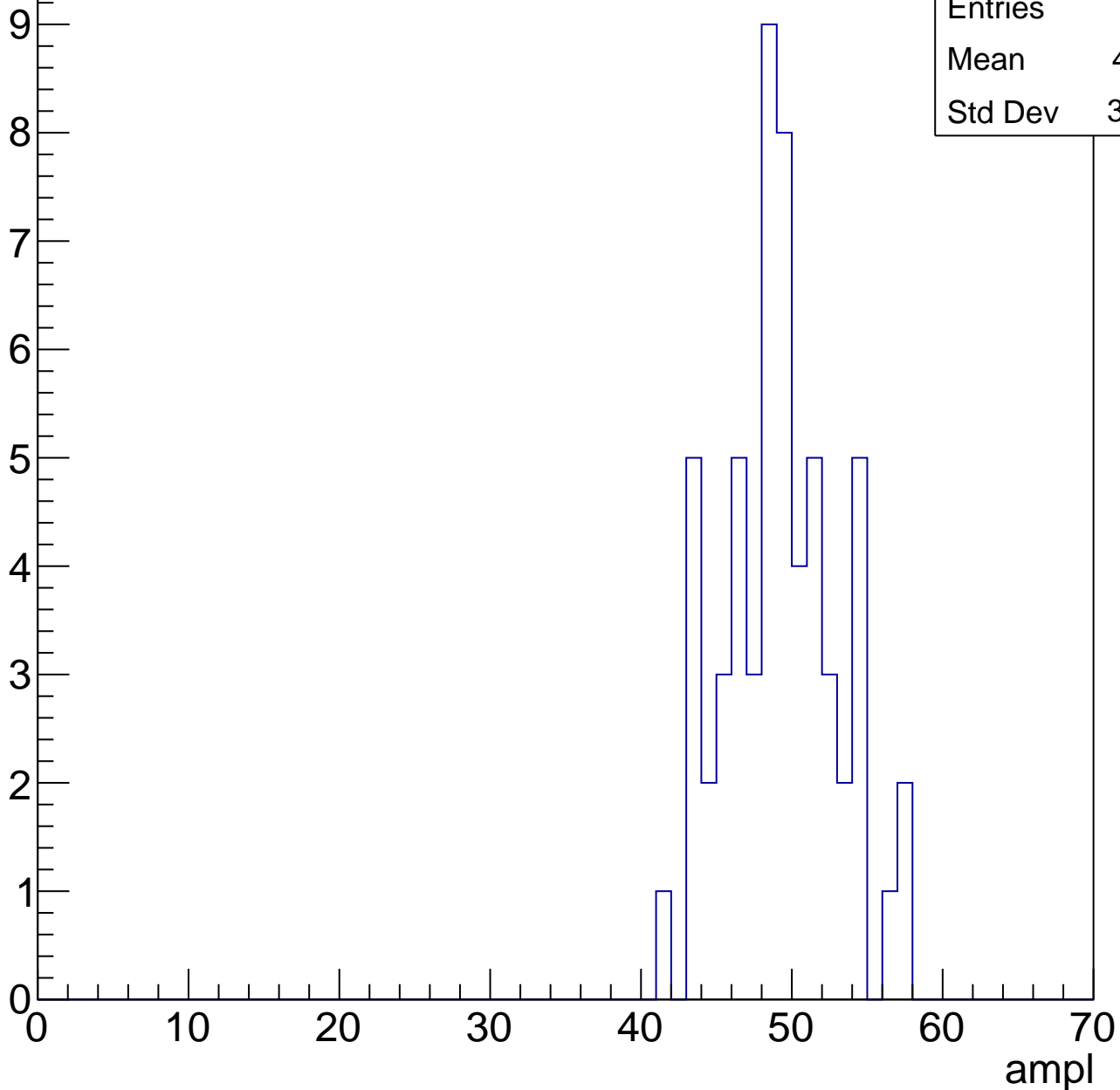


B1L003S, U16-ch79, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	48.81
Std Dev	3.693

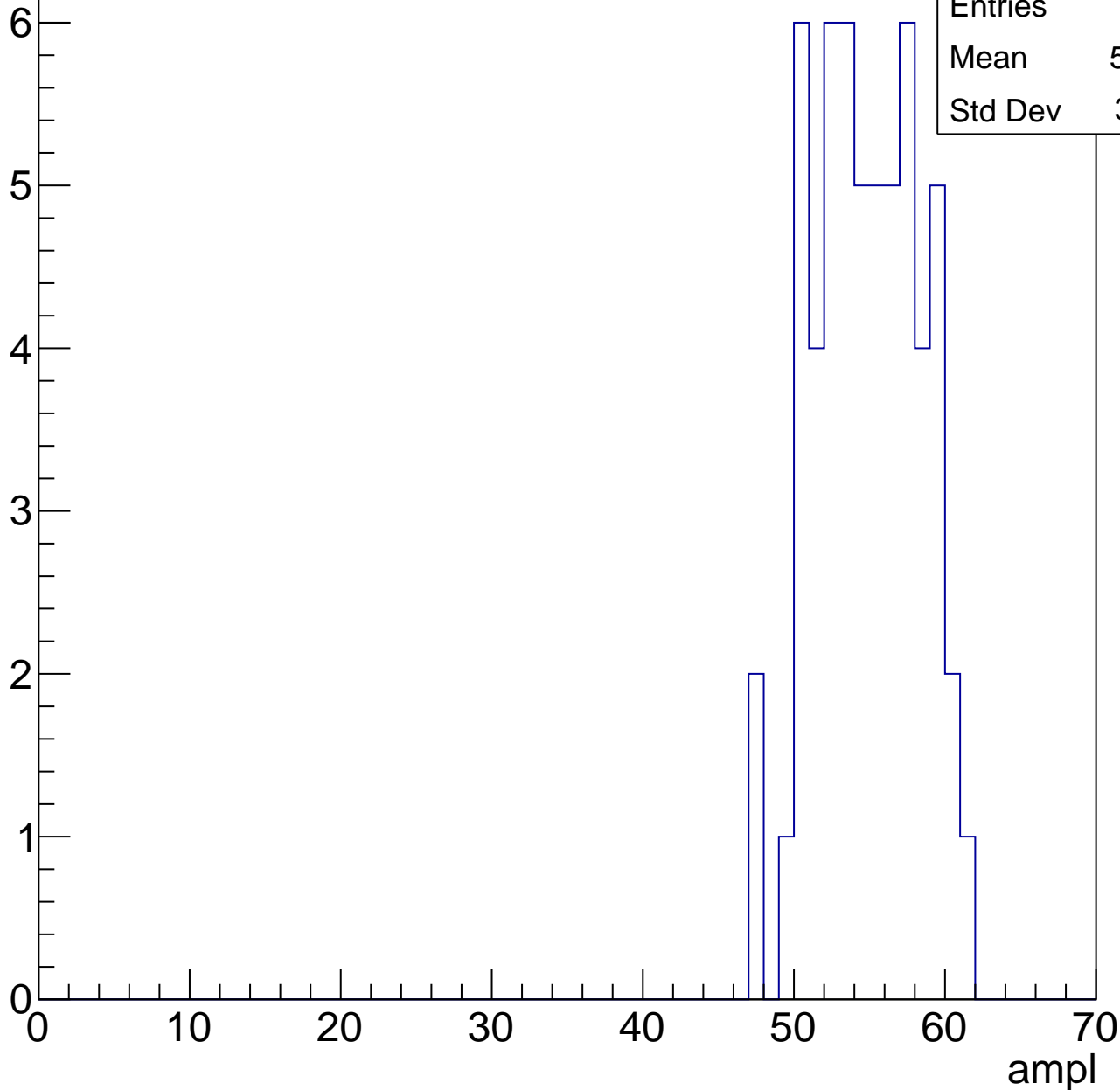


B1L003S, U16-ch79, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.34
Std Dev	3.391

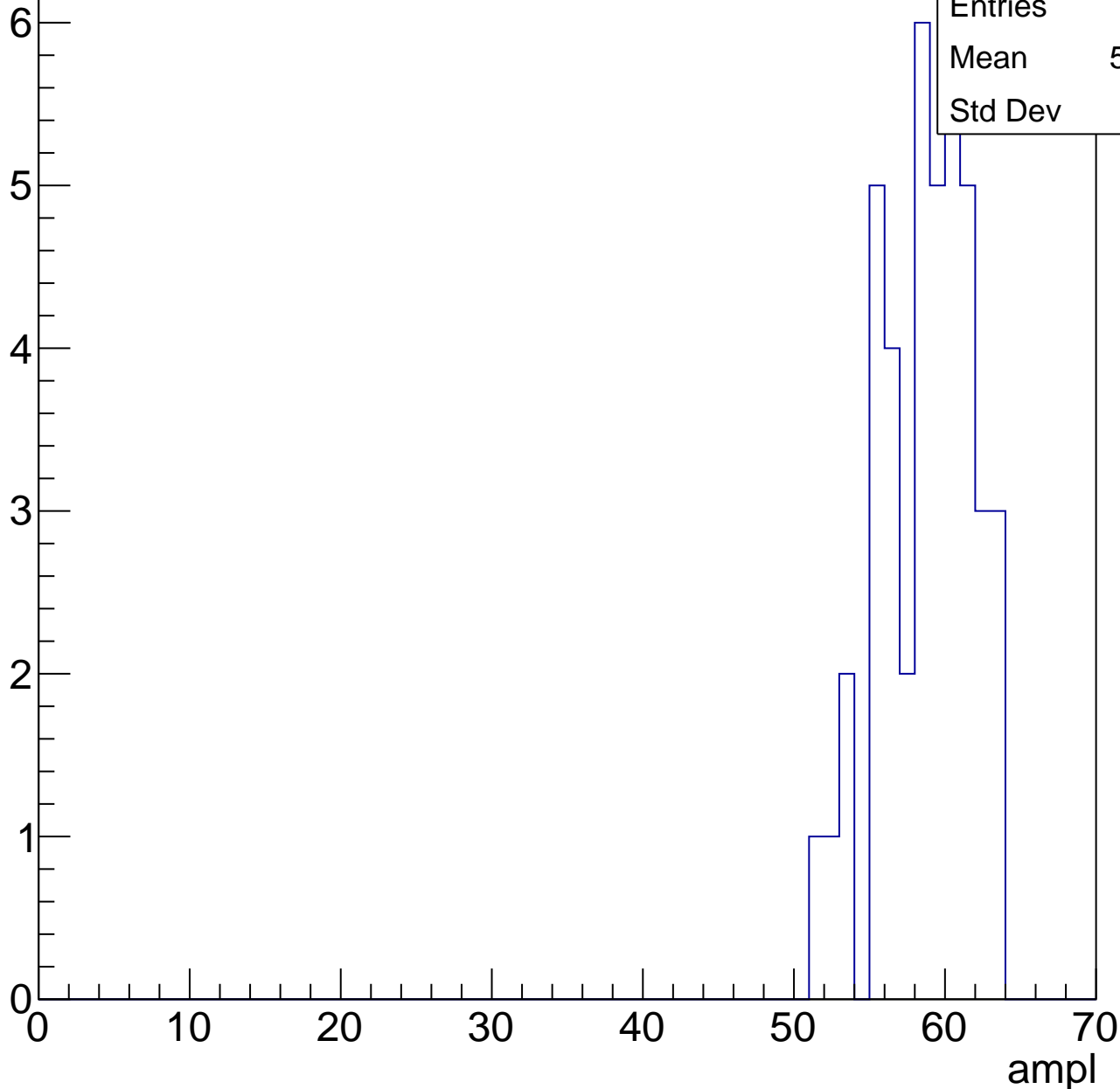


B1L003S, U16-ch79, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	58.26
Std Dev	3.02

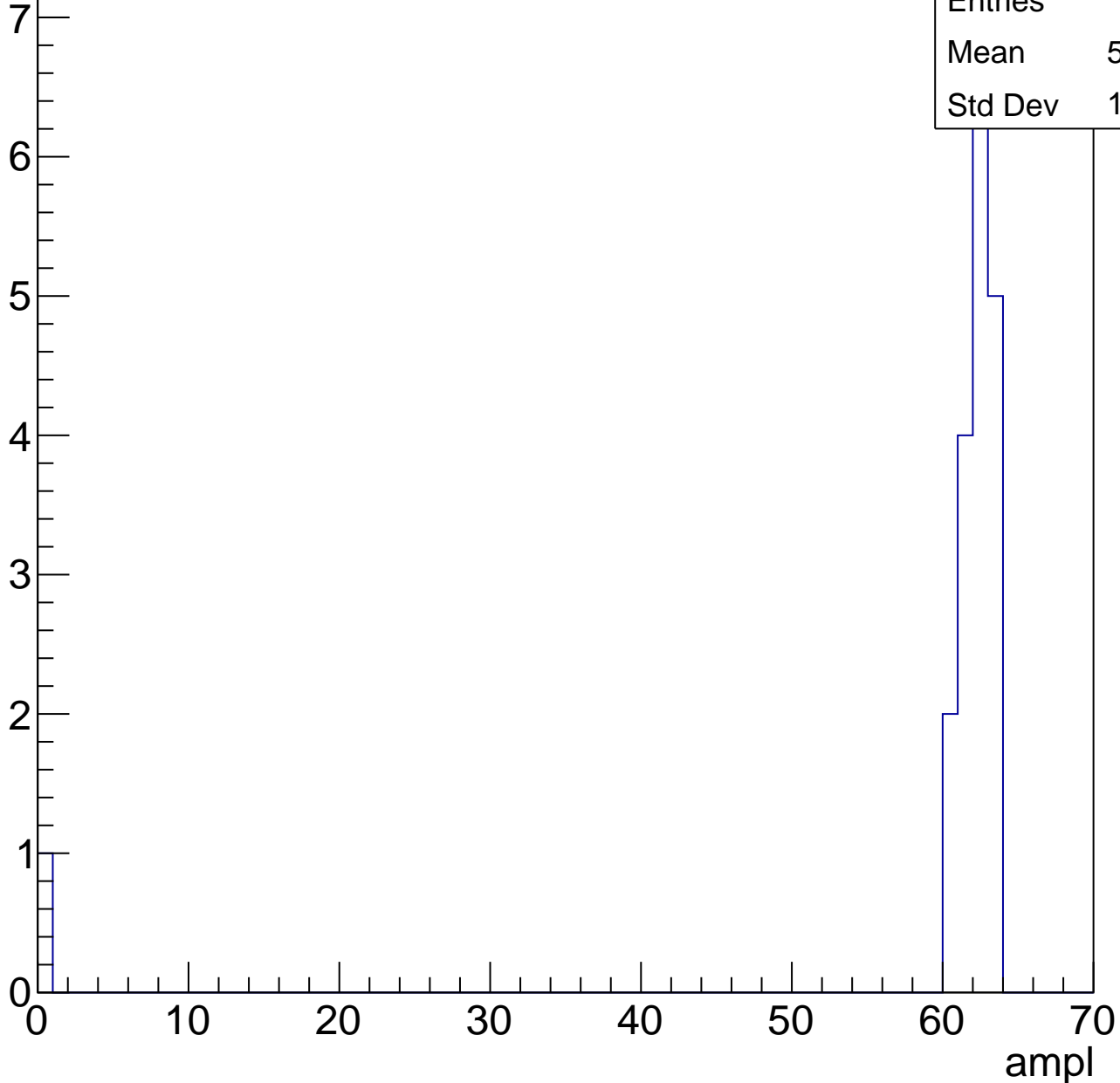


B1L003S, U16-ch79, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

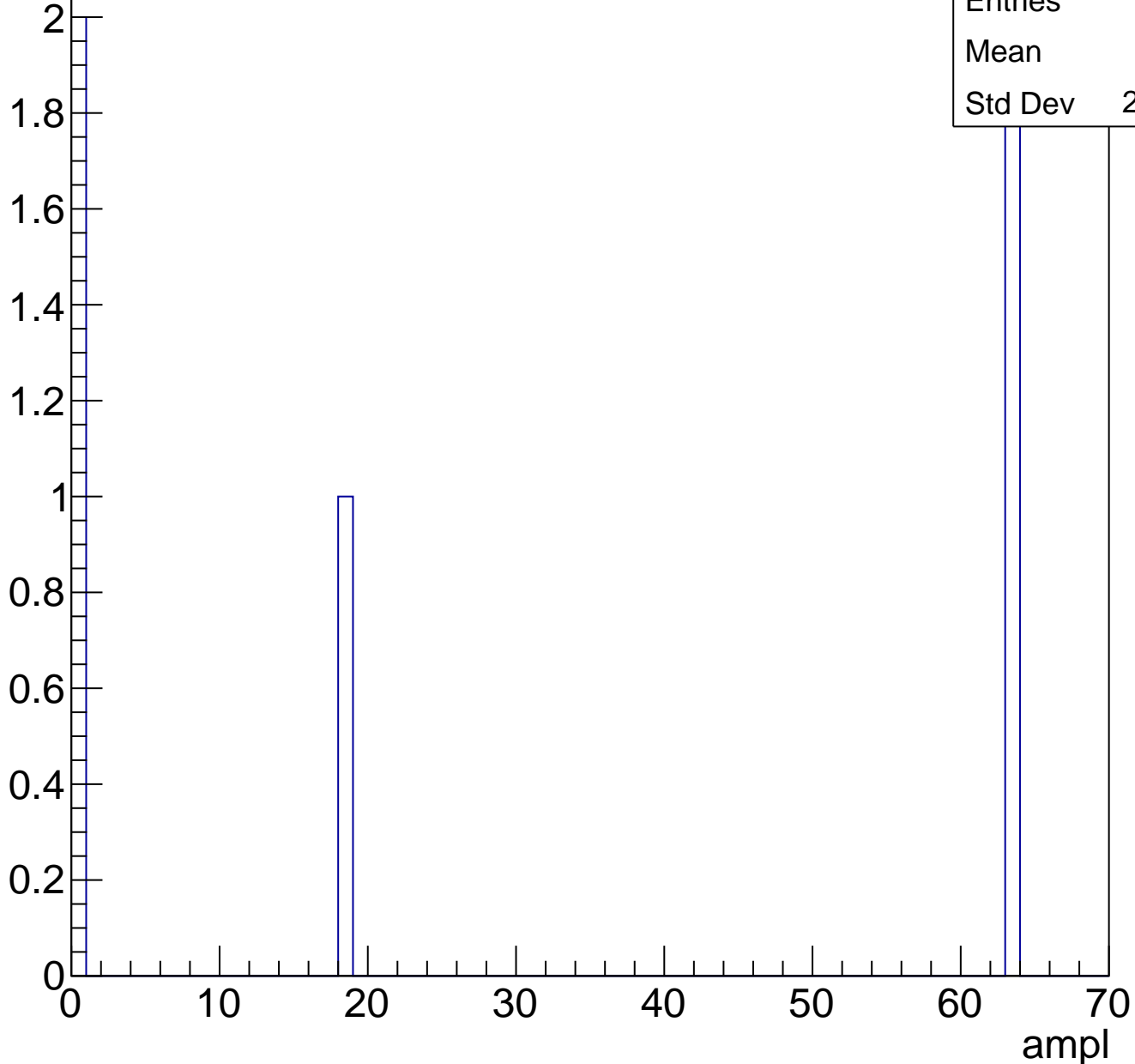
Entries	19
Mean	58.58
Std Dev	13.84



B1L003S, U16-ch79, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	28.8
Std Dev	28.69

B1L003S, U16-ch80, adc0

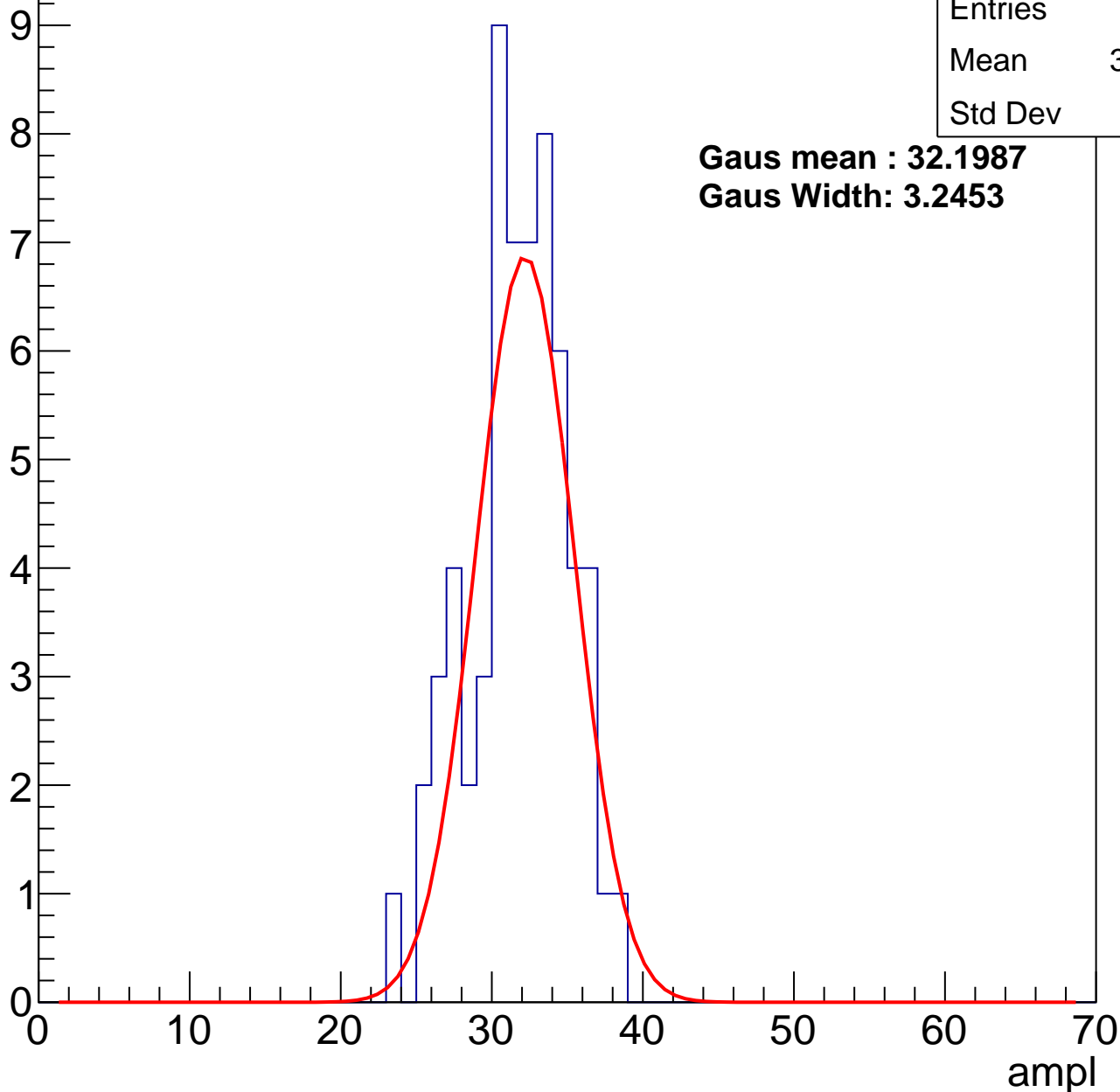
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	31.29
Std Dev	3.24

Gaus mean : 32.1987

Gaus Width: 3.2453



B1L003S, U16-ch80, adc1

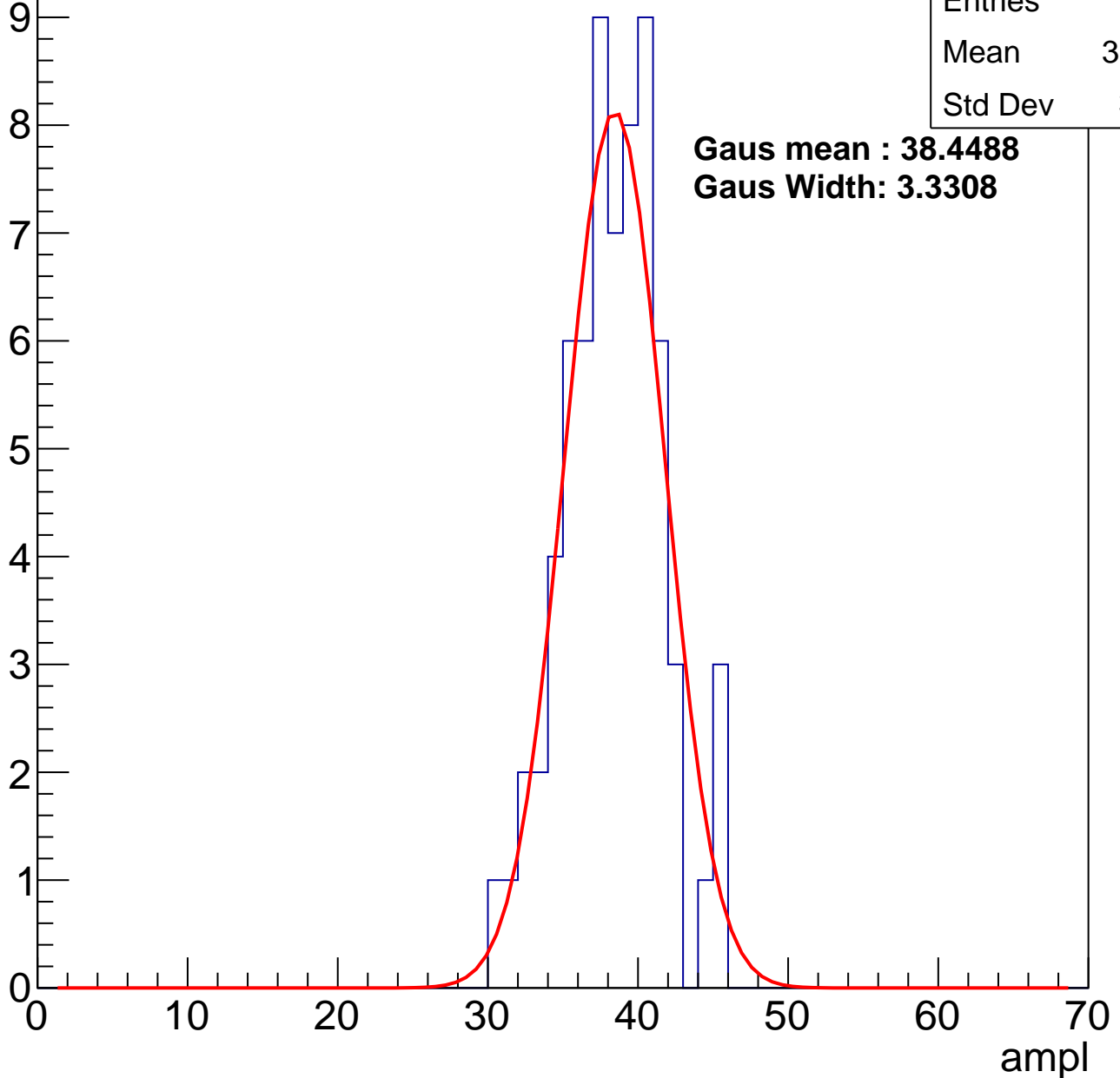
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.87
Std Dev	3.24

Gaus mean : 38.4488

Gaus Width: 3.3308



B1L003S, U16-ch80, adc2

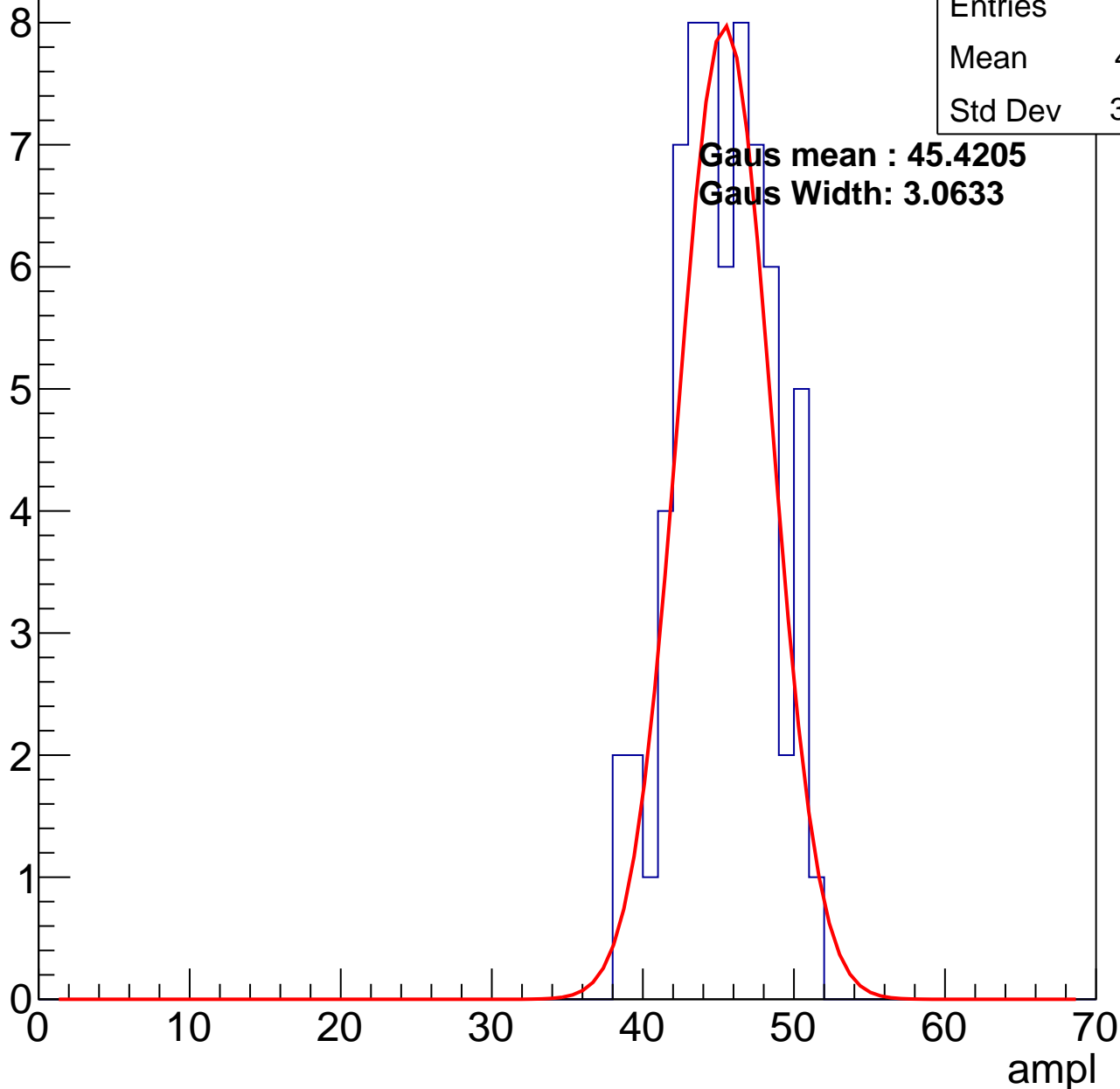
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	44.81
Std Dev	3.097

Gaus mean : 45.4205

Gaus Width: 3.0633

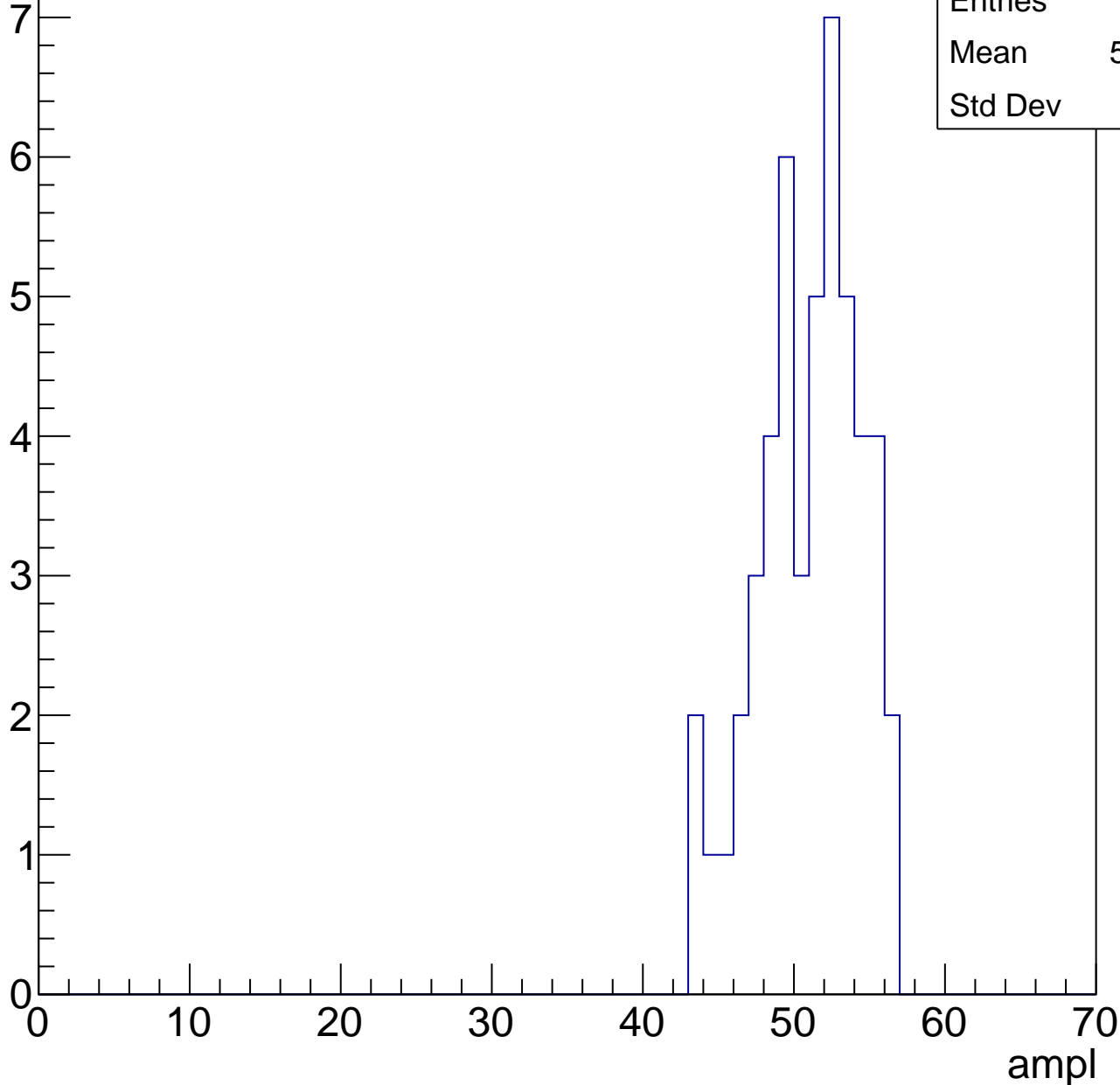


B1L003S, U16-ch80, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	50.53
Std Dev	3.32

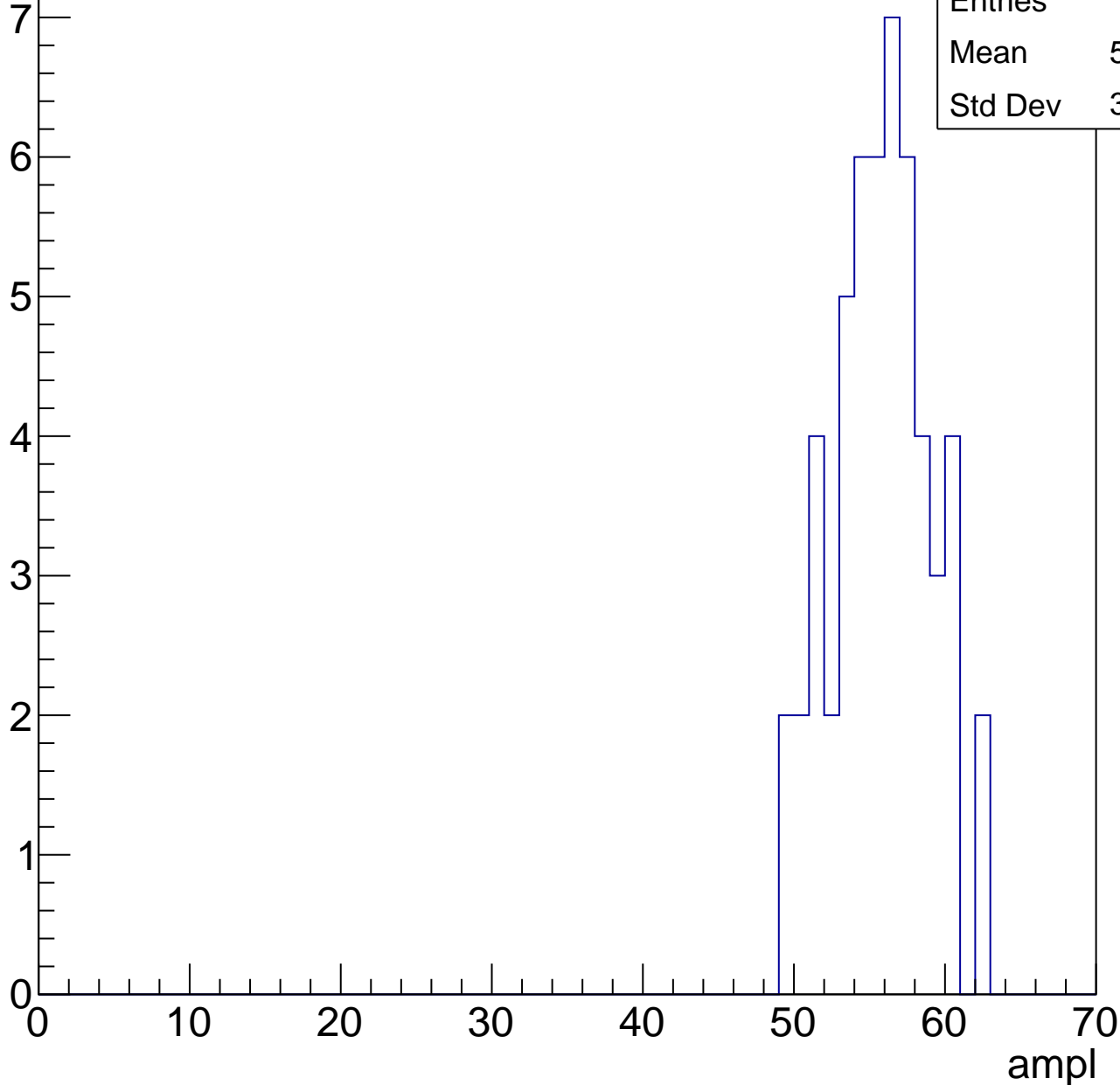


B1L003S, U16-ch80, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.32
Std Dev	3.185

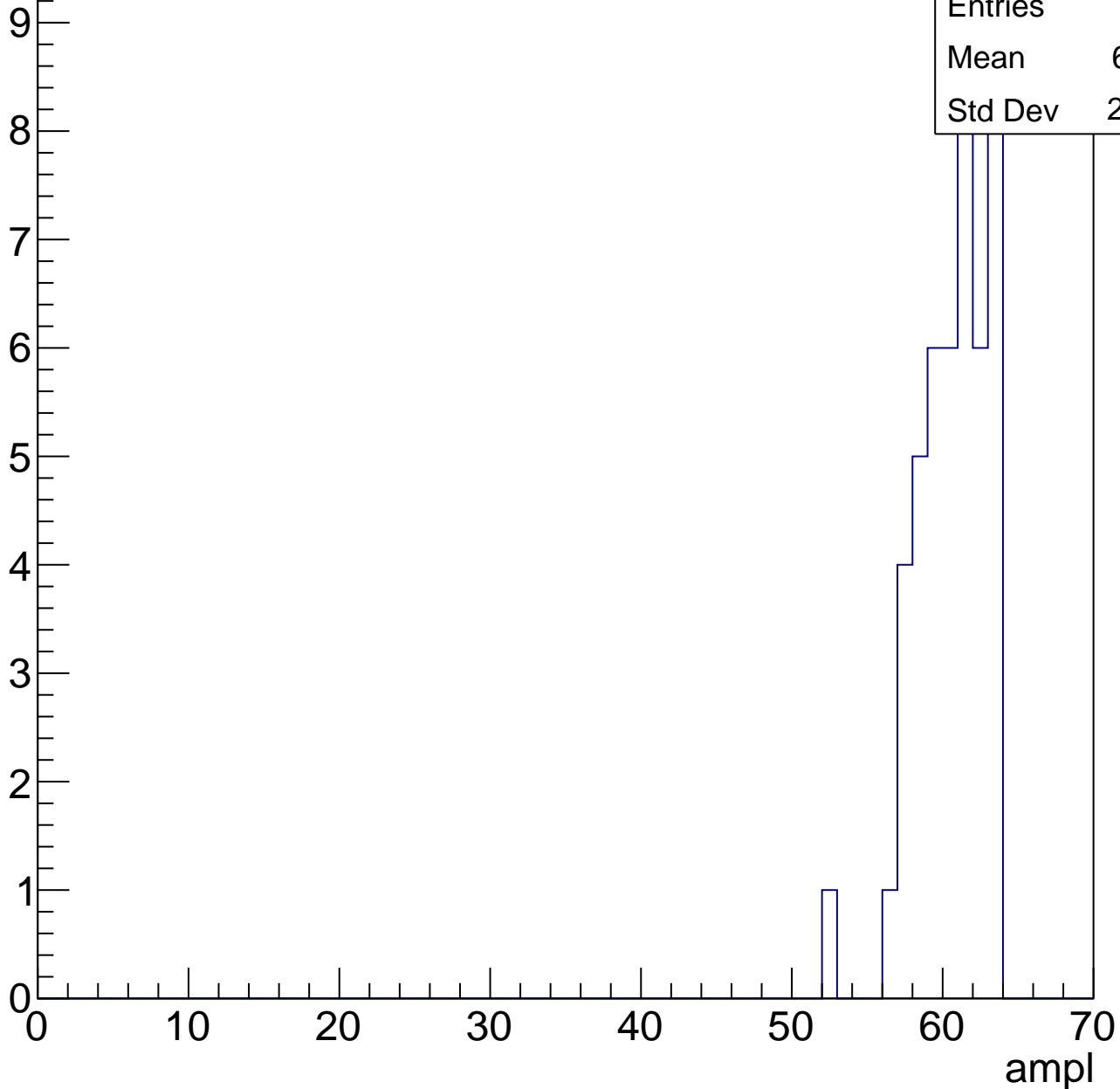


B1L003S, U16-ch80, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	60.11
Std Dev	2.315



B1L003S, U16-ch80, adc6

calib_packv5_042523_0143.root, FC#13, port D2

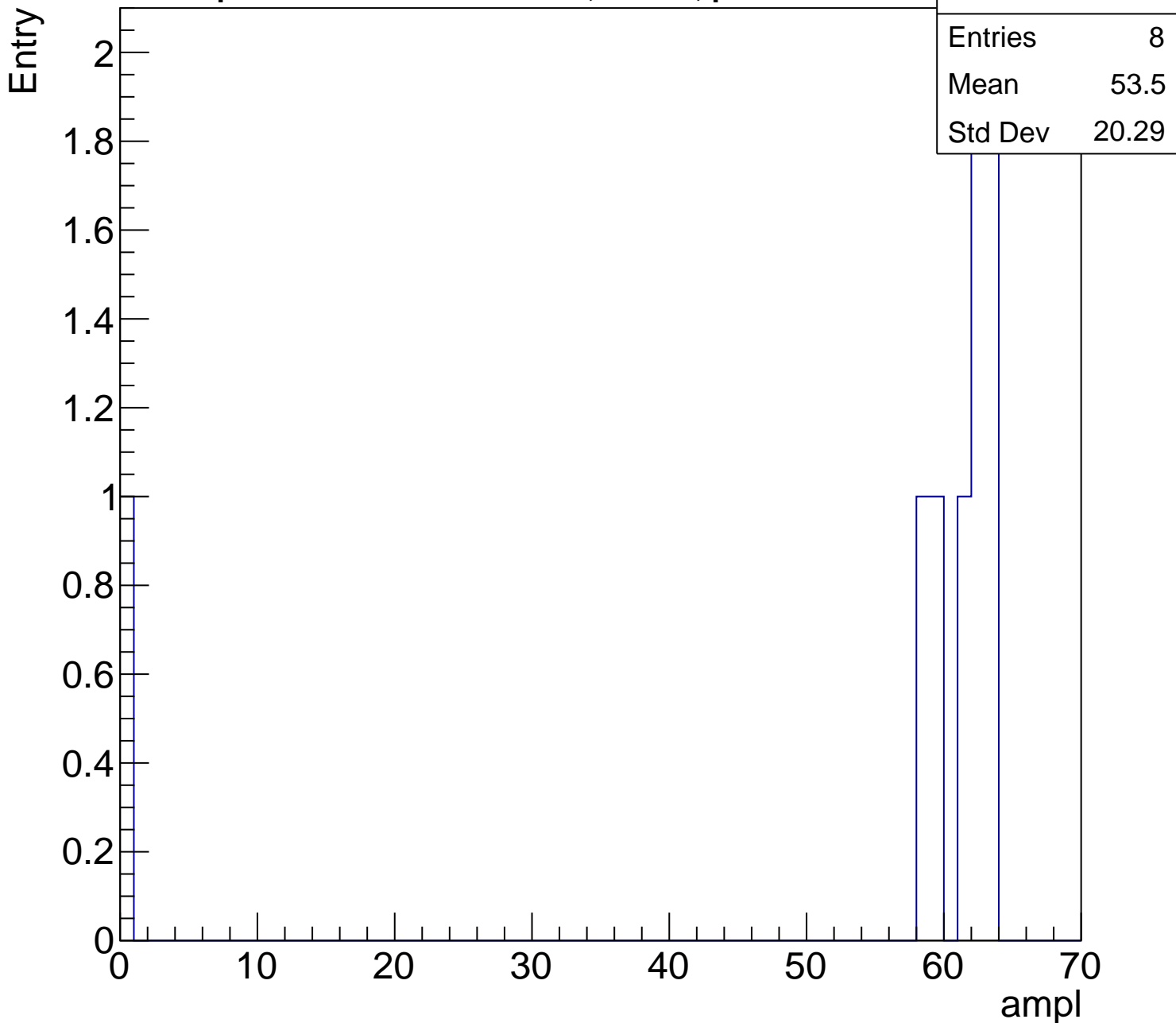
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.5
Std Dev	20.29

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch80, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L003S, U16-ch81, adc0

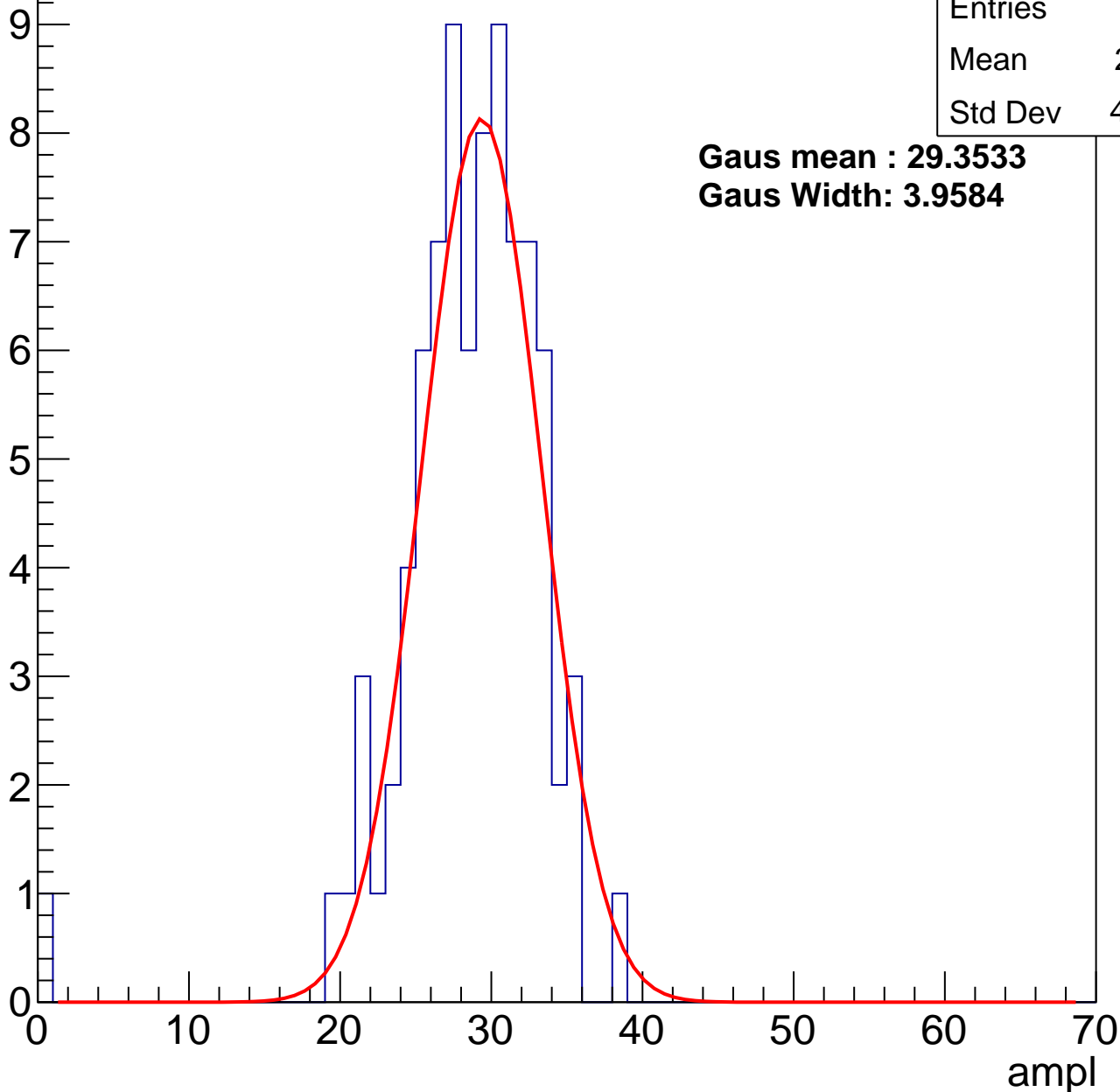
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	28.11
Std Dev	4.884

Gaus mean : 29.3533

Gaus Width: 3.9584



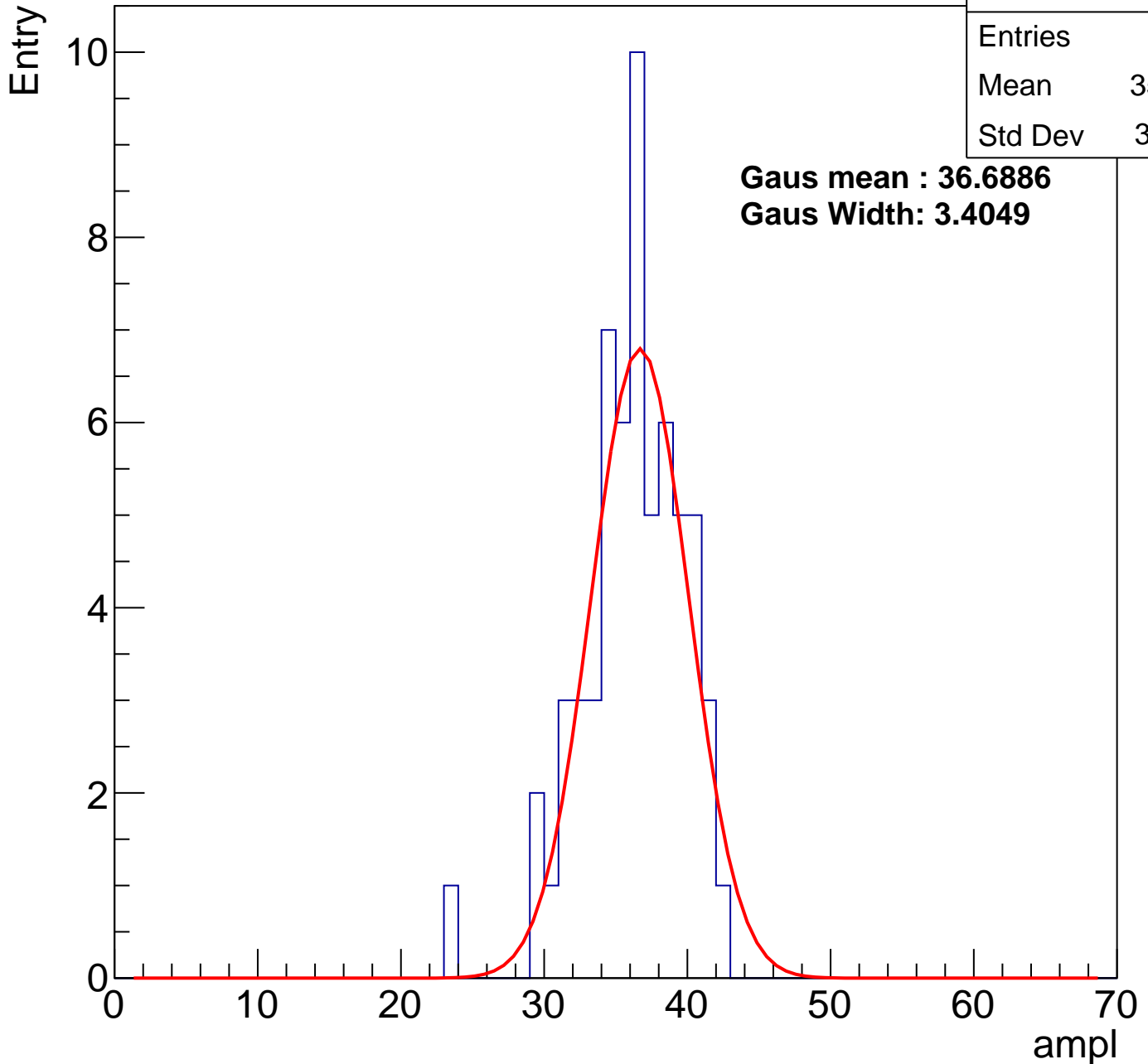
B1L003S, U16-ch81, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	35.74
Std Dev	3.501

Gaus mean : 36.6886

Gaus Width: 3.4049



B1L003S, U16-ch81, adc2

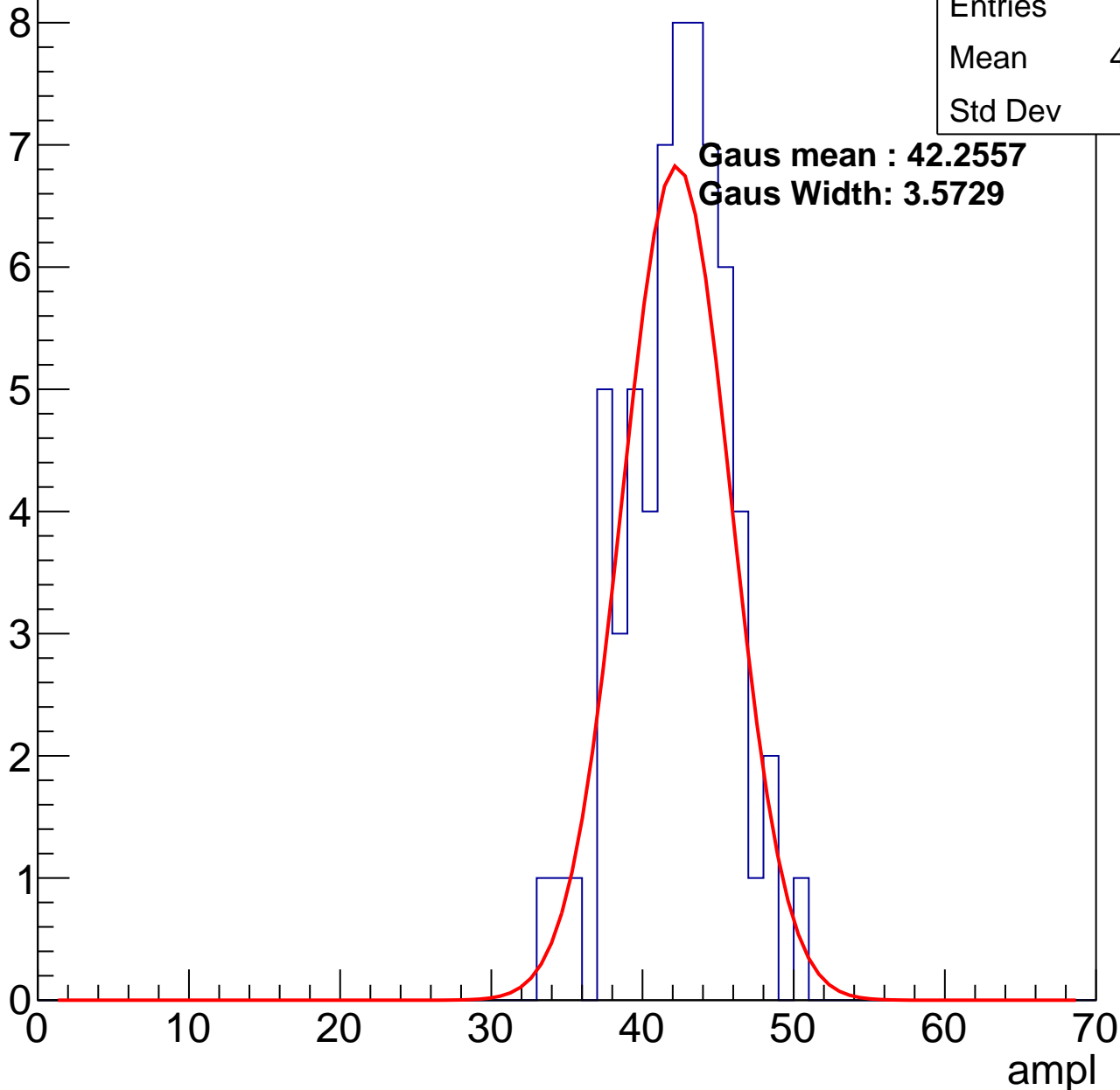
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.84
Std Dev	3.42

Gaus mean : 42.2557

Gaus Width: 3.5729

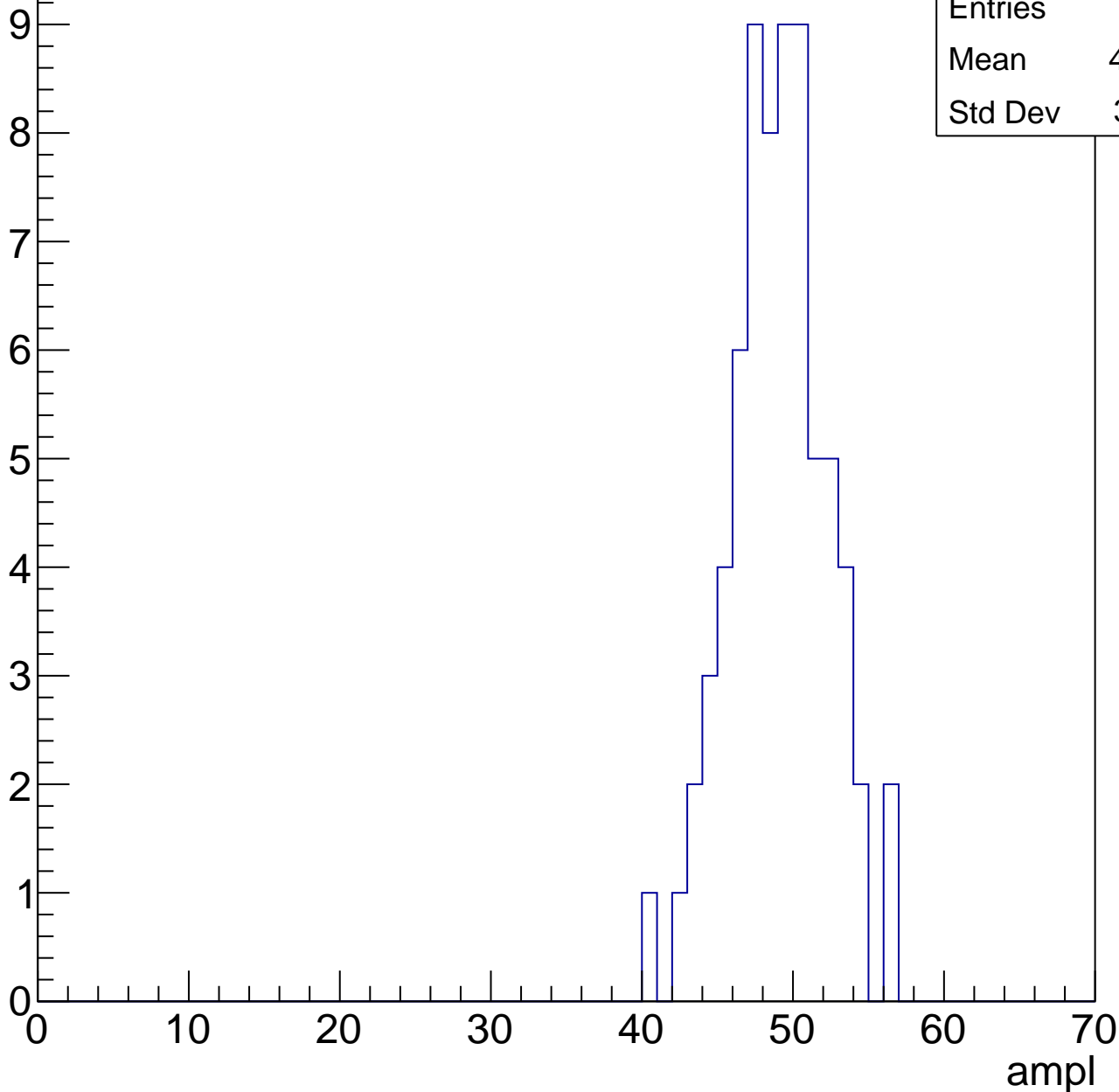


B1L003S, U16-ch81, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	48.59
Std Dev	3.191



B1L003S, U16-ch81, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	39
Mean	54.54
Std Dev	2.437

Entry

10

8

6

4

2

0

0

10

20

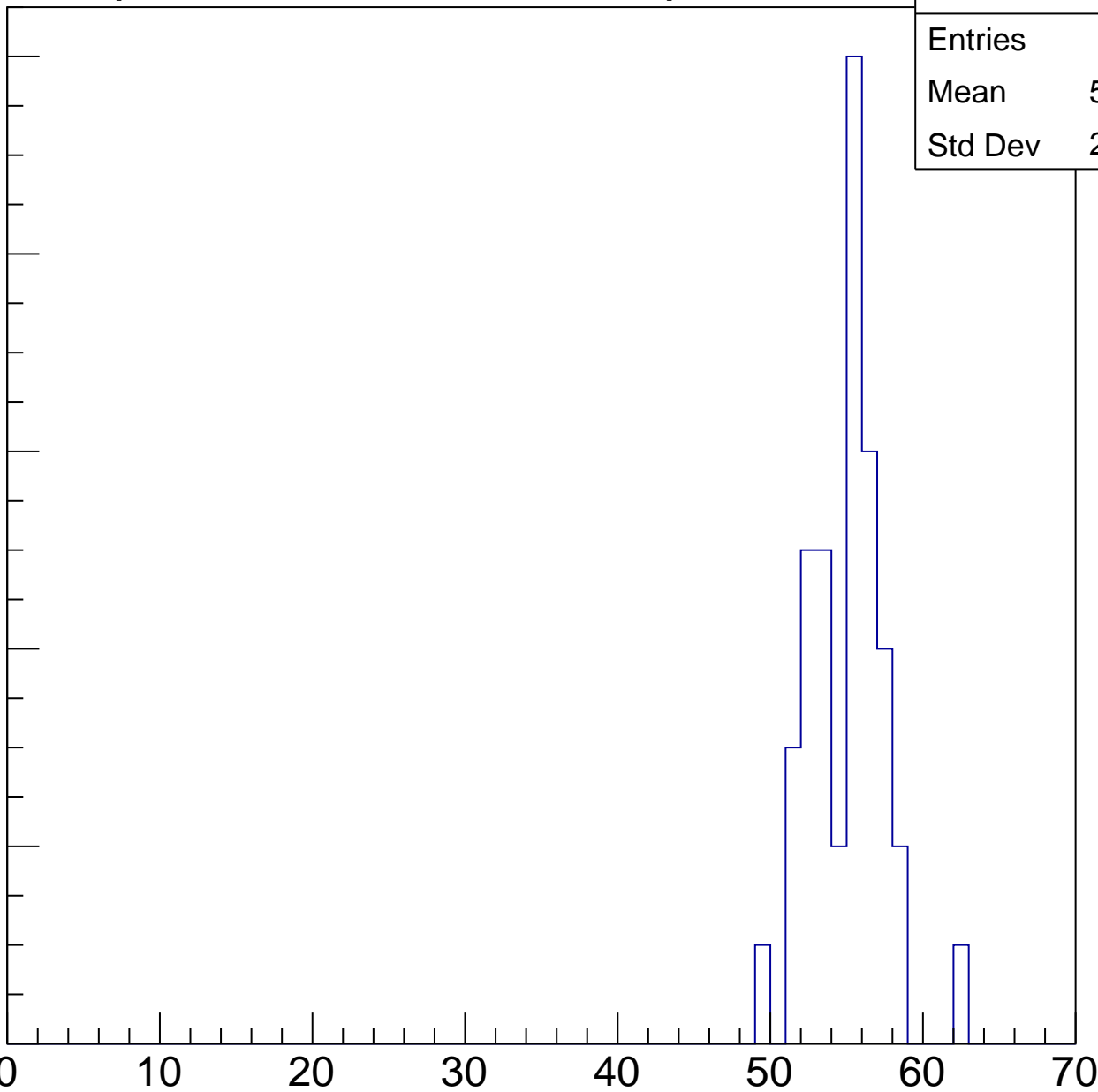
30

40

50

60

ampl

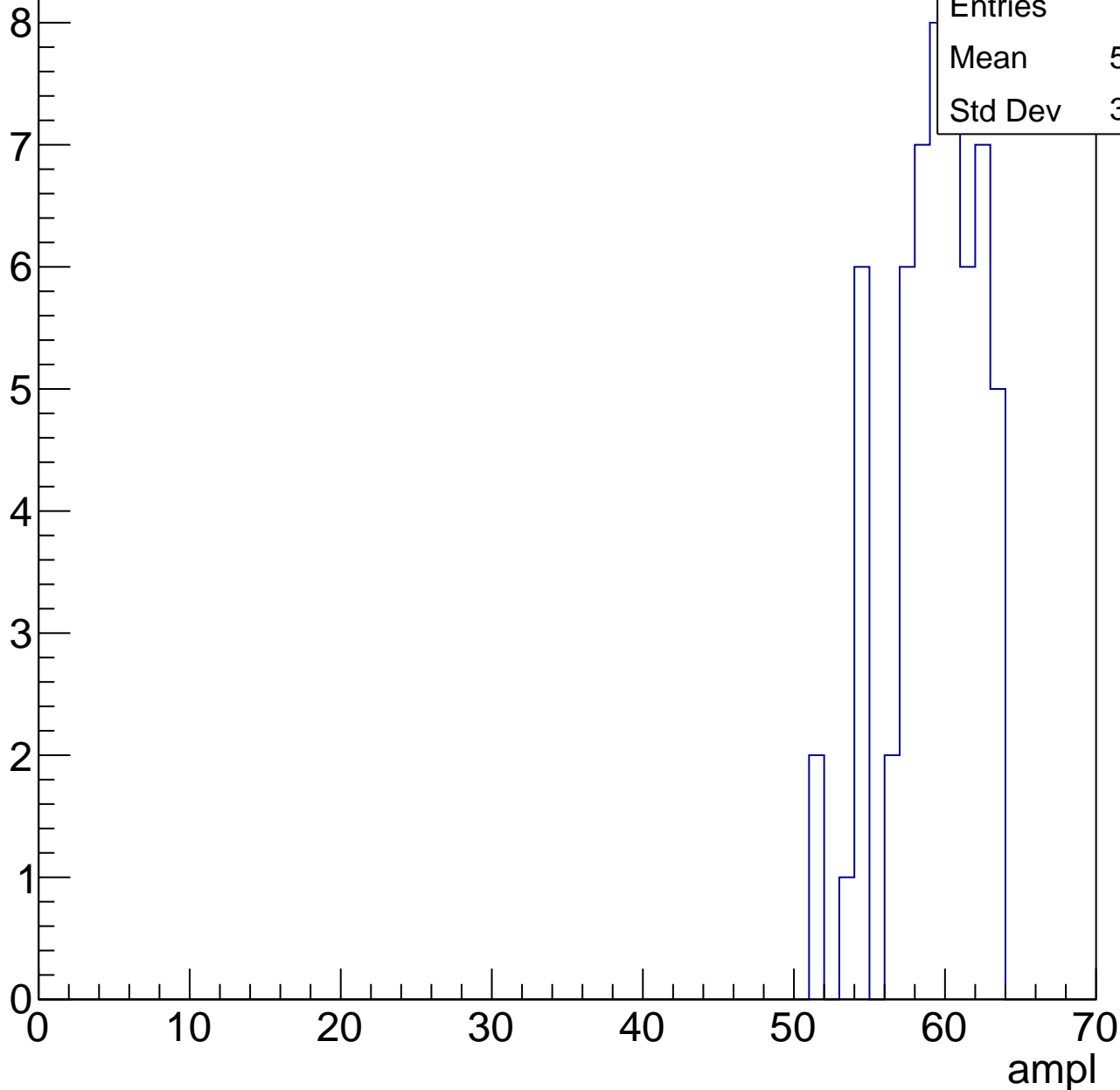


B1L003S, U16-ch81, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.72
Std Dev	3.033

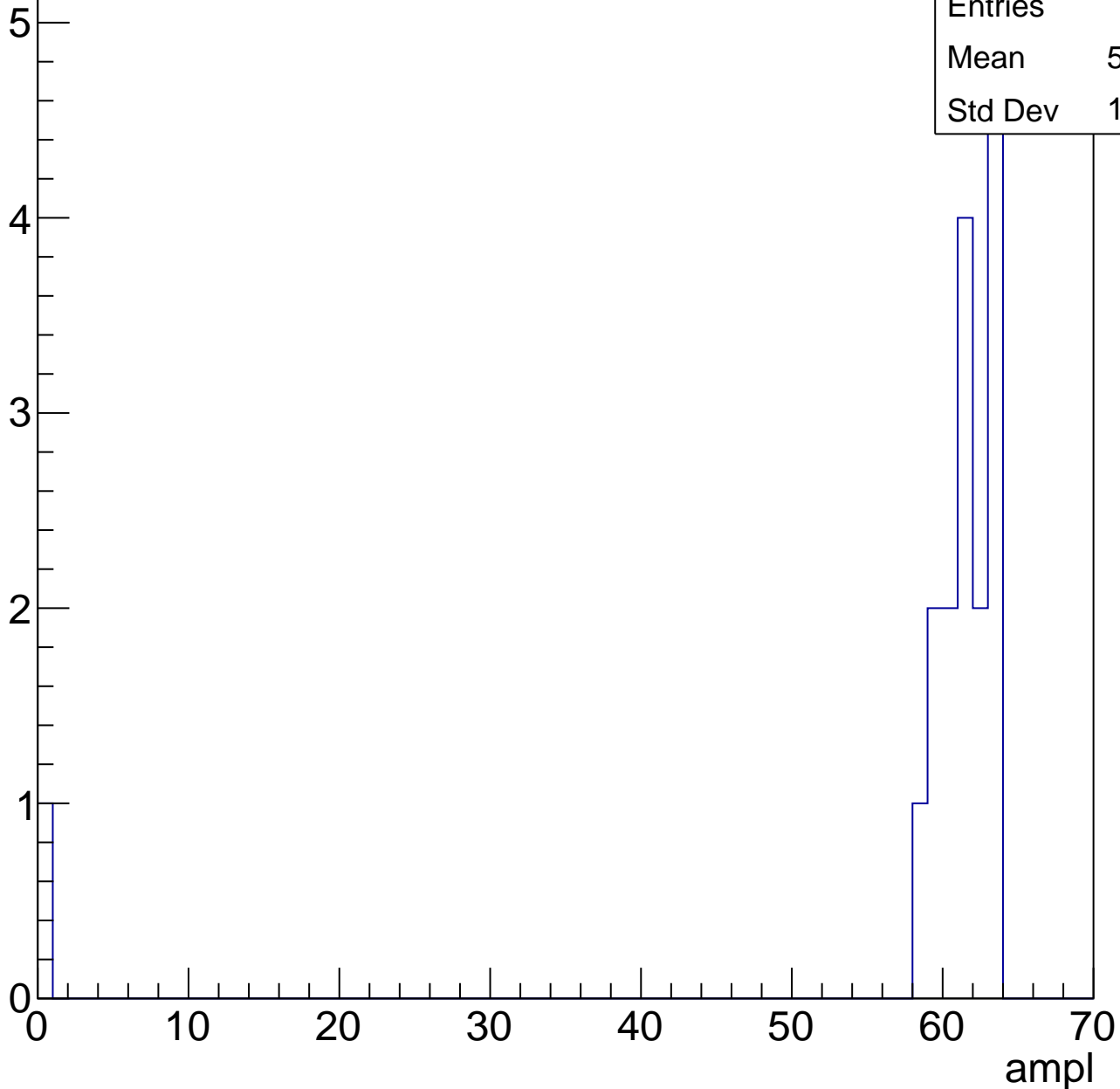


B1L003S, U16-ch81, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	57.59
Std Dev	14.48



B1L003S, U16-ch81, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch82, adc0

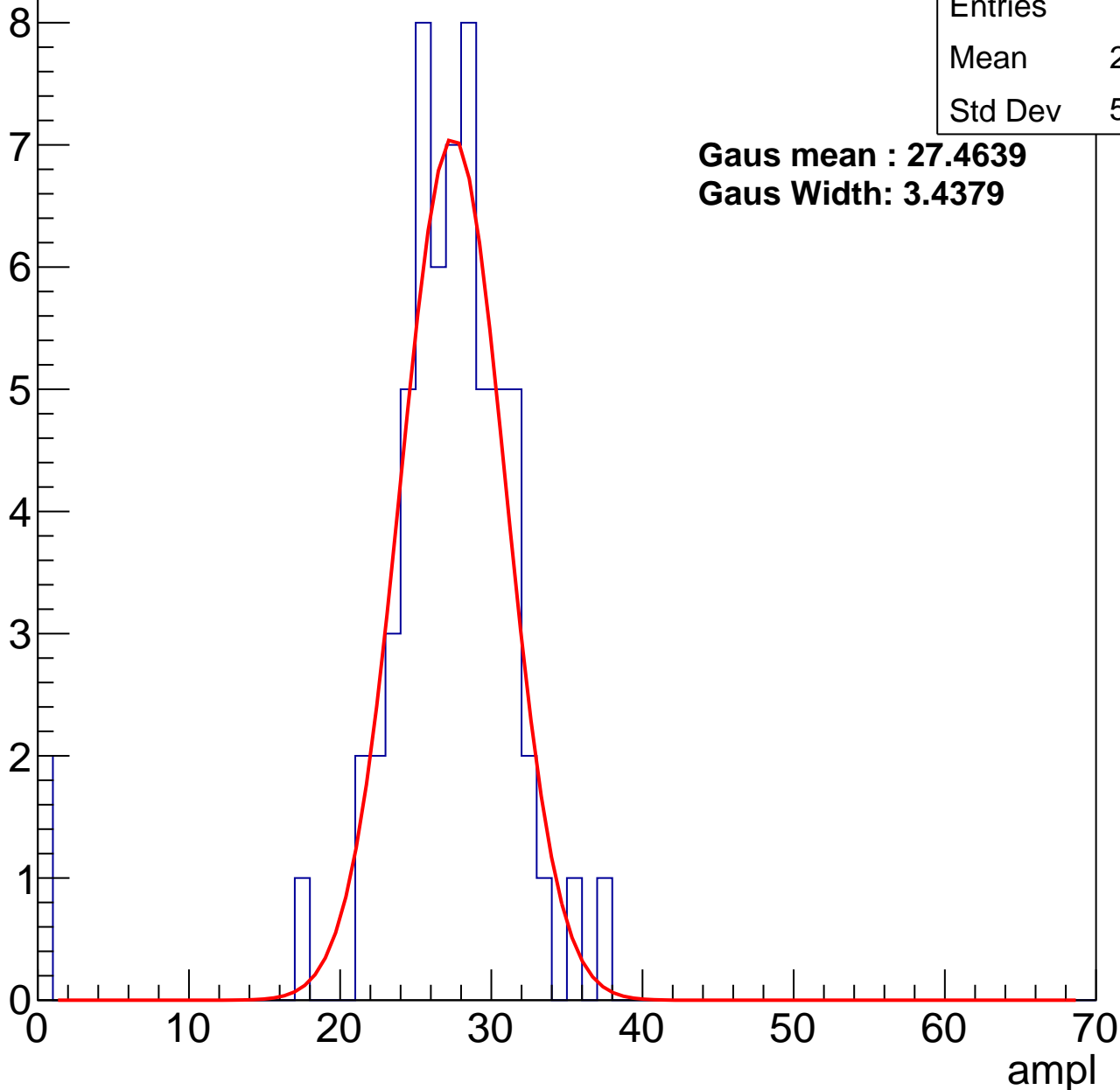
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	26.25
Std Dev	5.834

Gaus mean : 27.4639

Gaus Width: 3.4379



B1L003S, U16-ch82, adc1

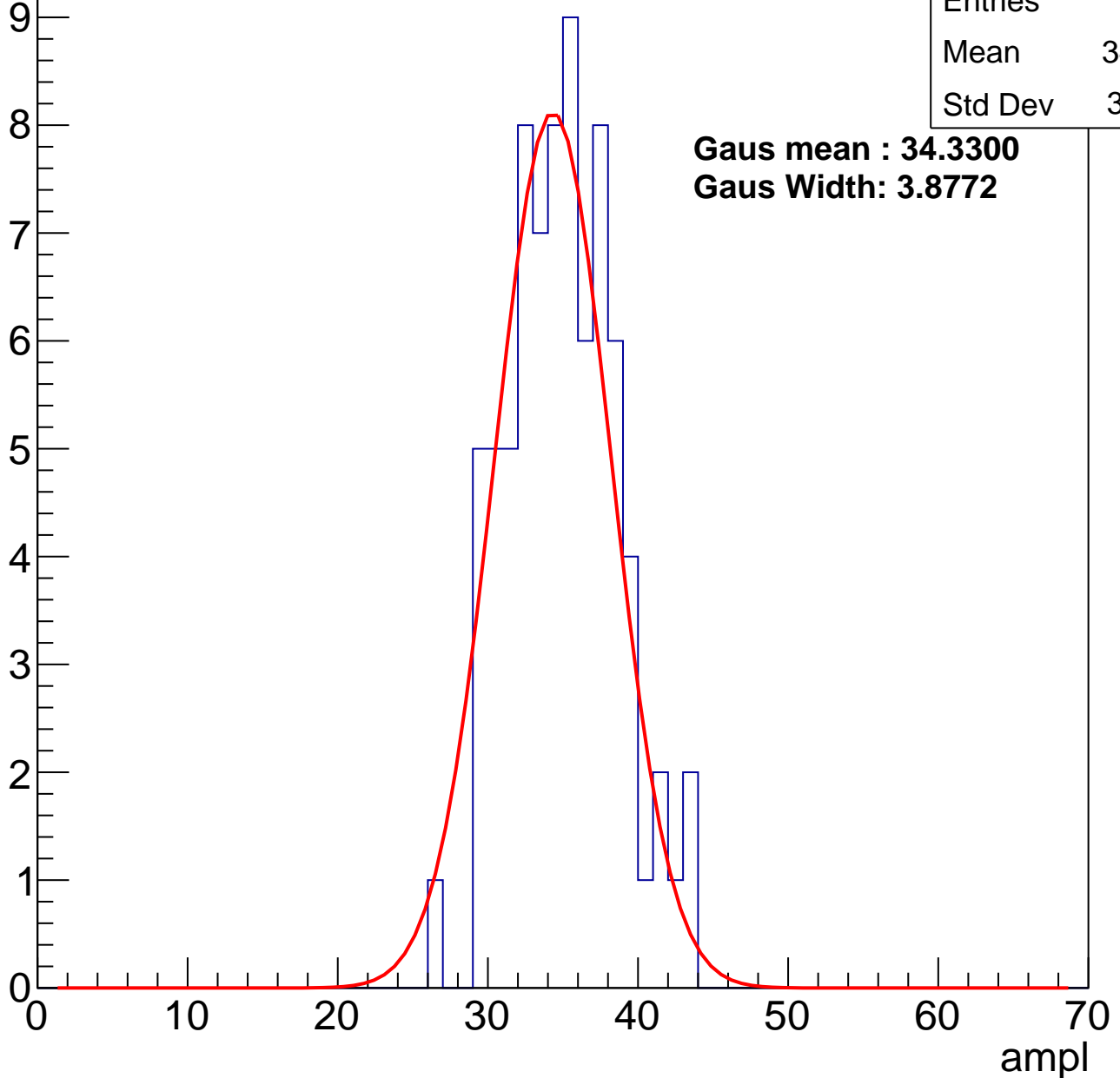
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	34.56
Std Dev	3.561

Gaus mean : 34.3300

Gaus Width: 3.8772



B1L003S, U16-ch82, adc2

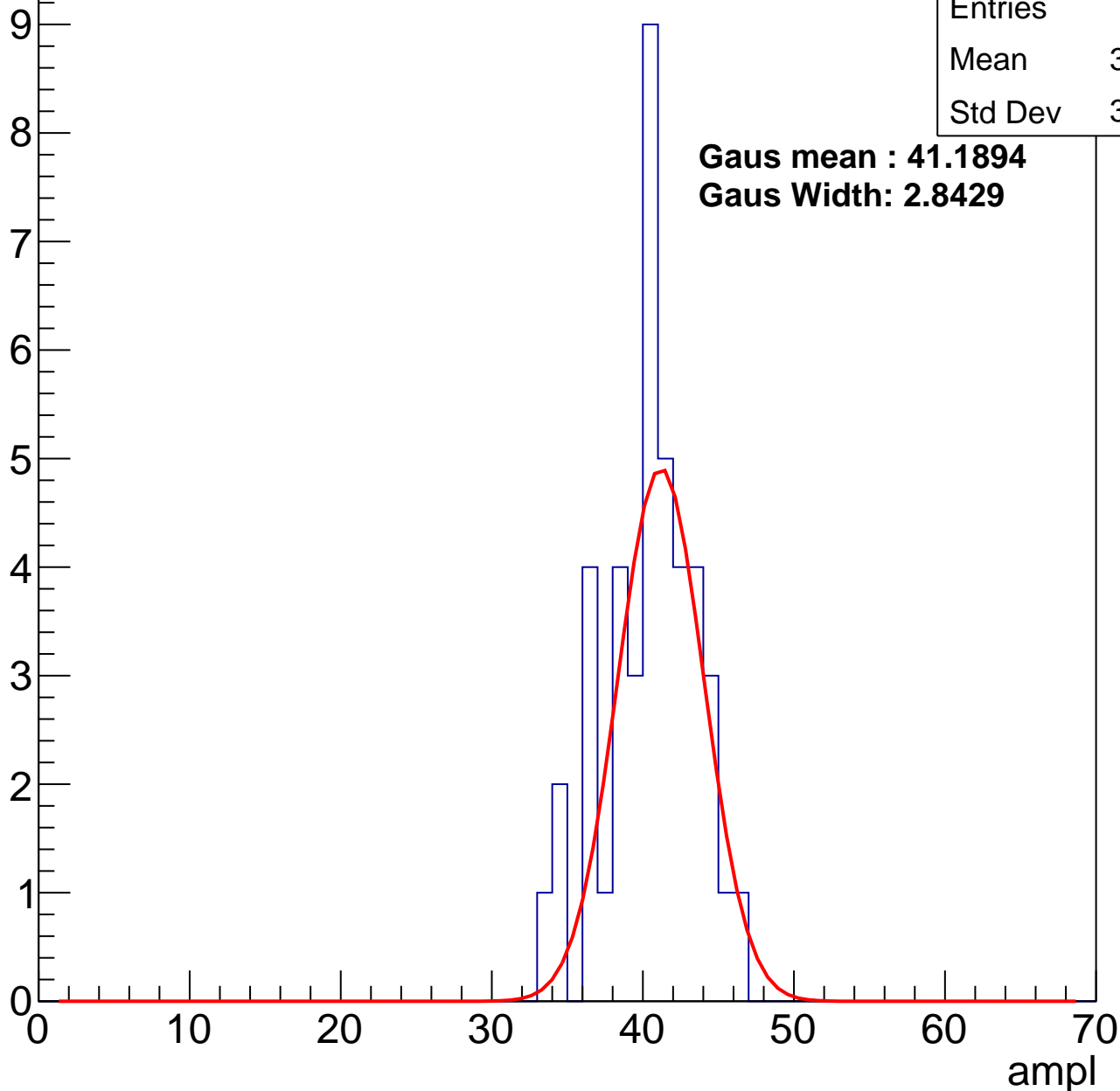
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	39.98
Std Dev	3.004

Gaus mean : 41.1894

Gaus Width: 2.8429

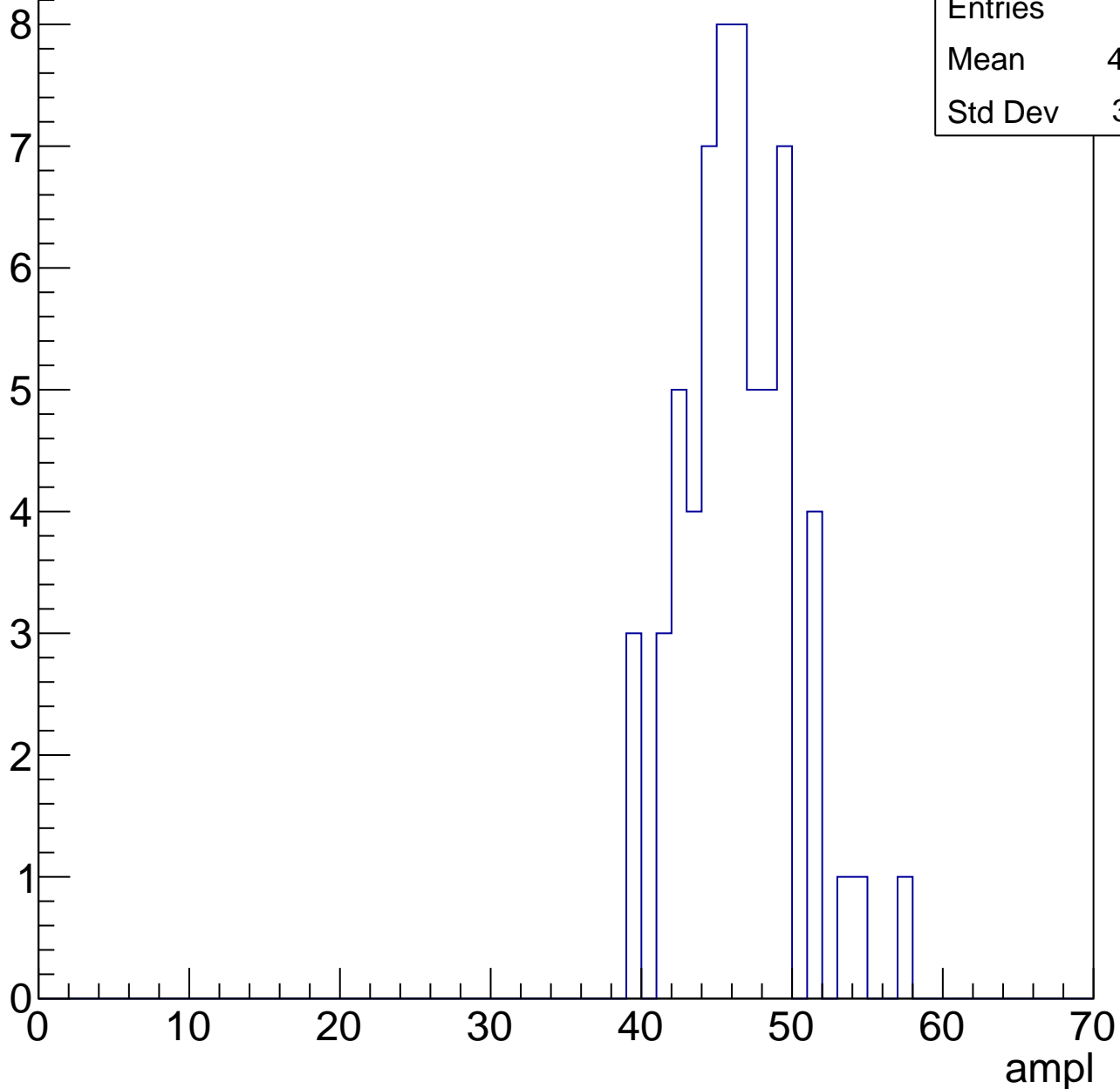


B1L003S, U16-ch82, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	45.87
Std Dev	3.581

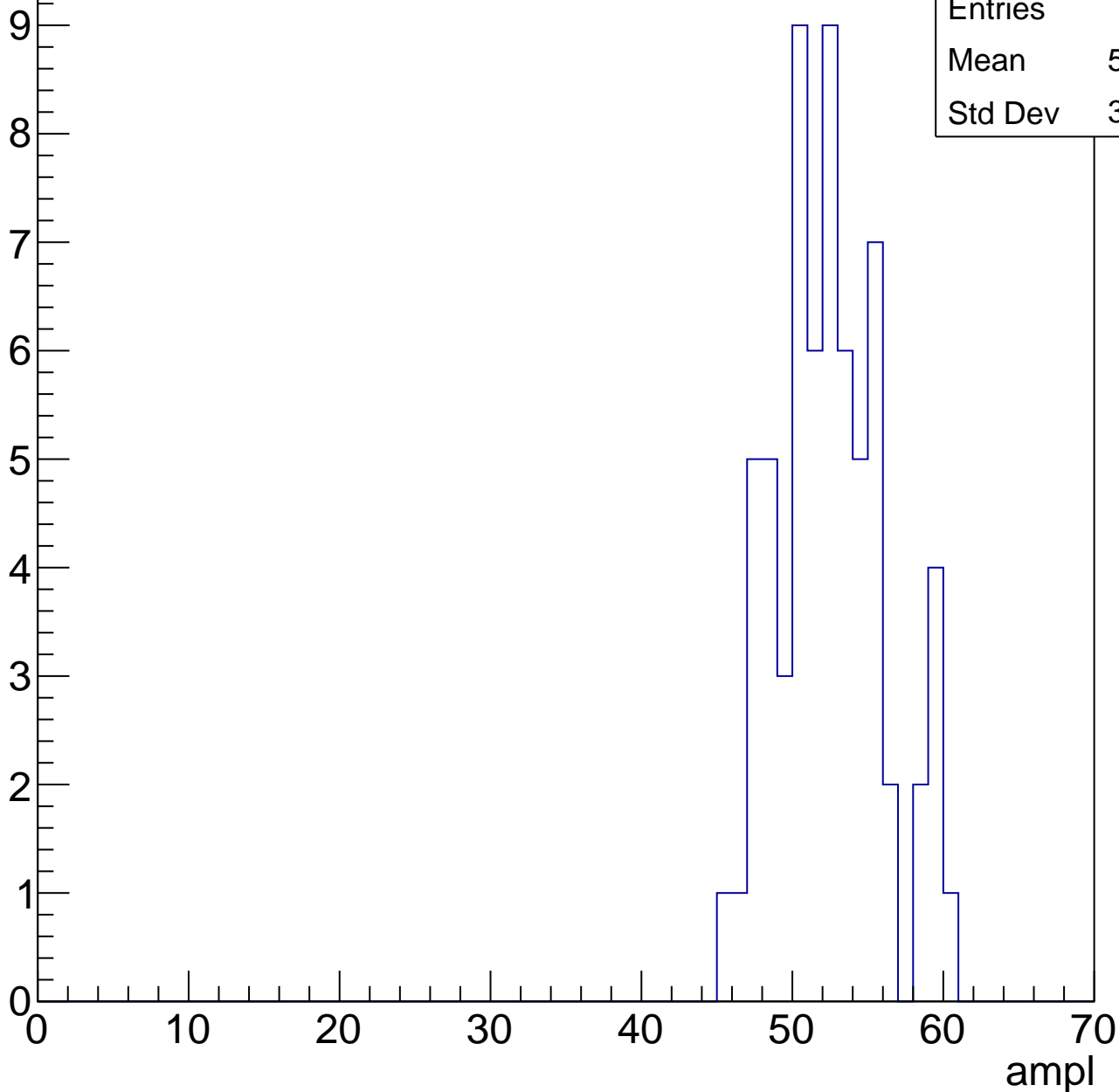


B1L003S, U16-ch82, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	52.03
Std Dev	3.507

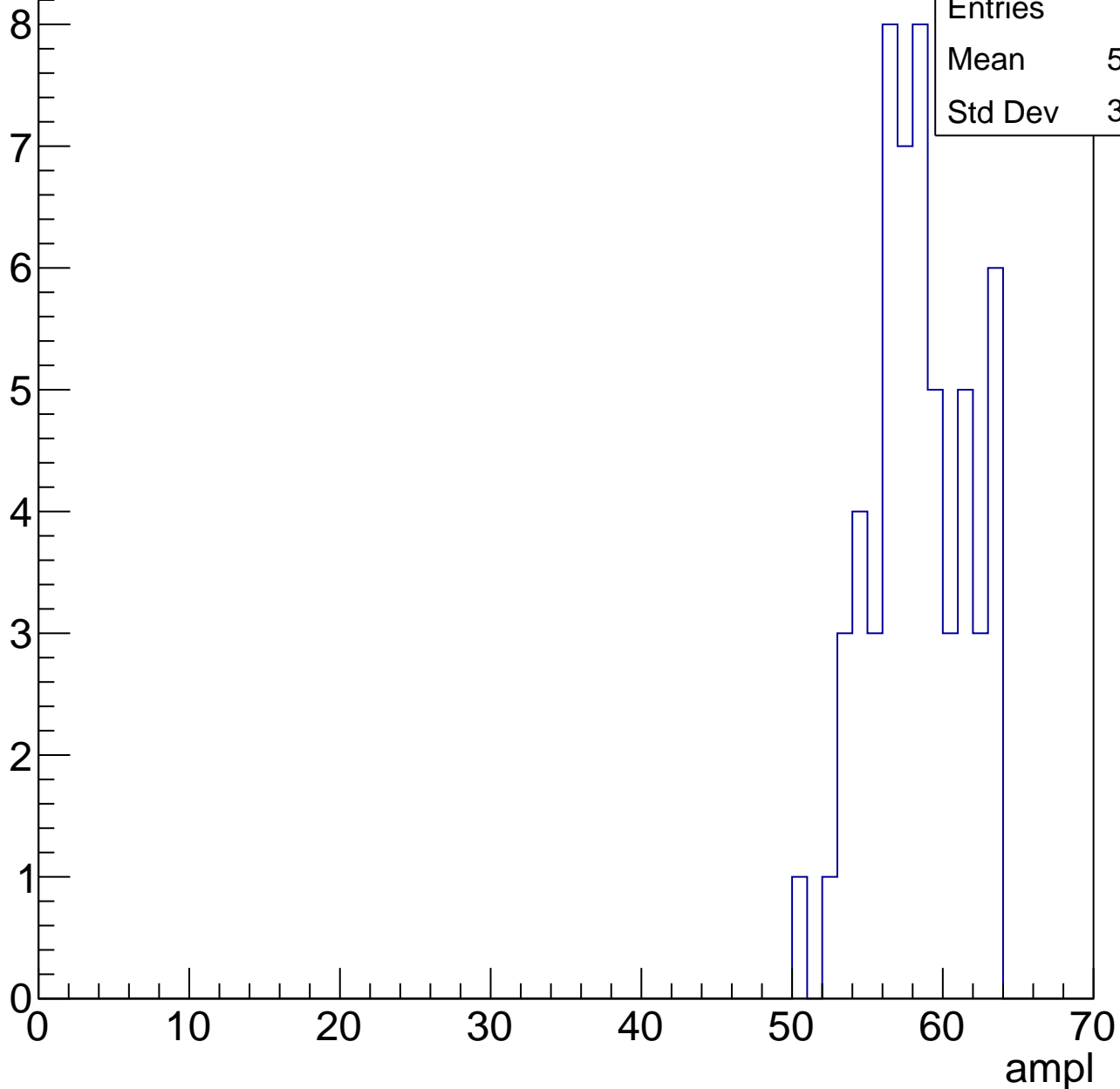


B1L003S, U16-ch82, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57.84
Std Dev	3.144

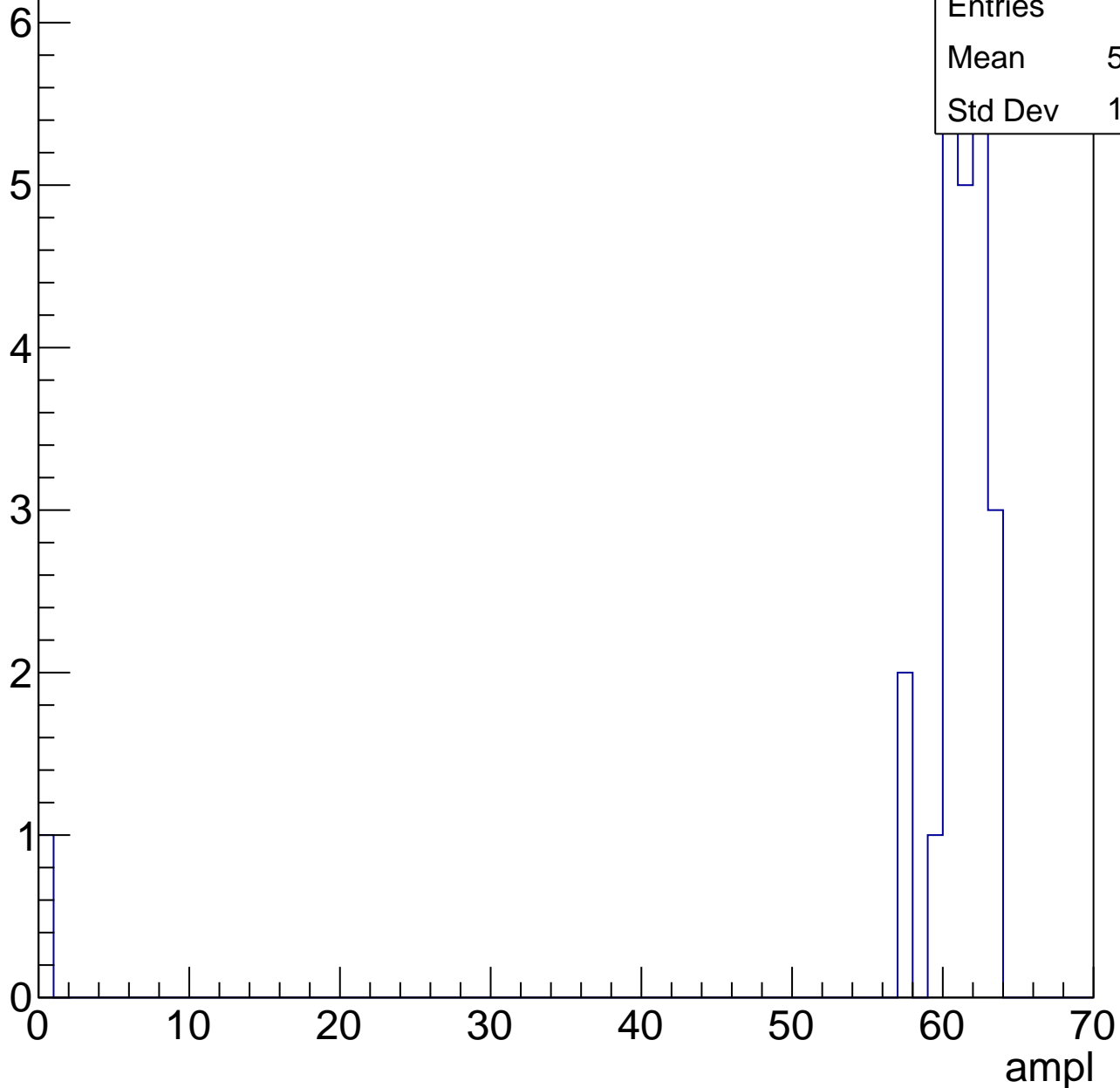


B1L003S, U16-ch82, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	24
Mean	58.29
Std Dev	12.26



B1L003S, U16-ch82, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch83, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	29.16
Std Dev	4.776

Gaus mean : 29.2866

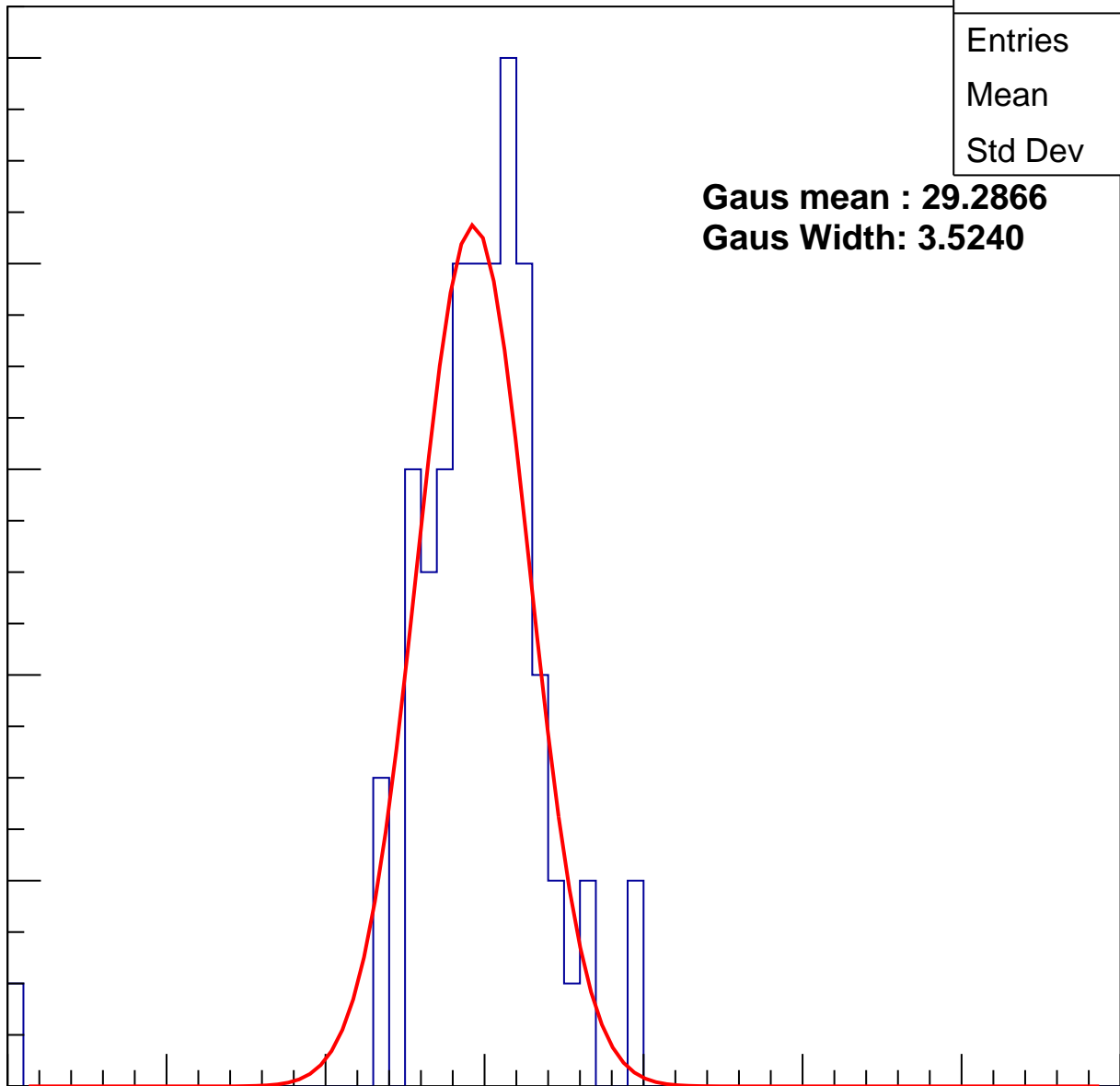
Gaus Width: 3.5240

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch83, adc1

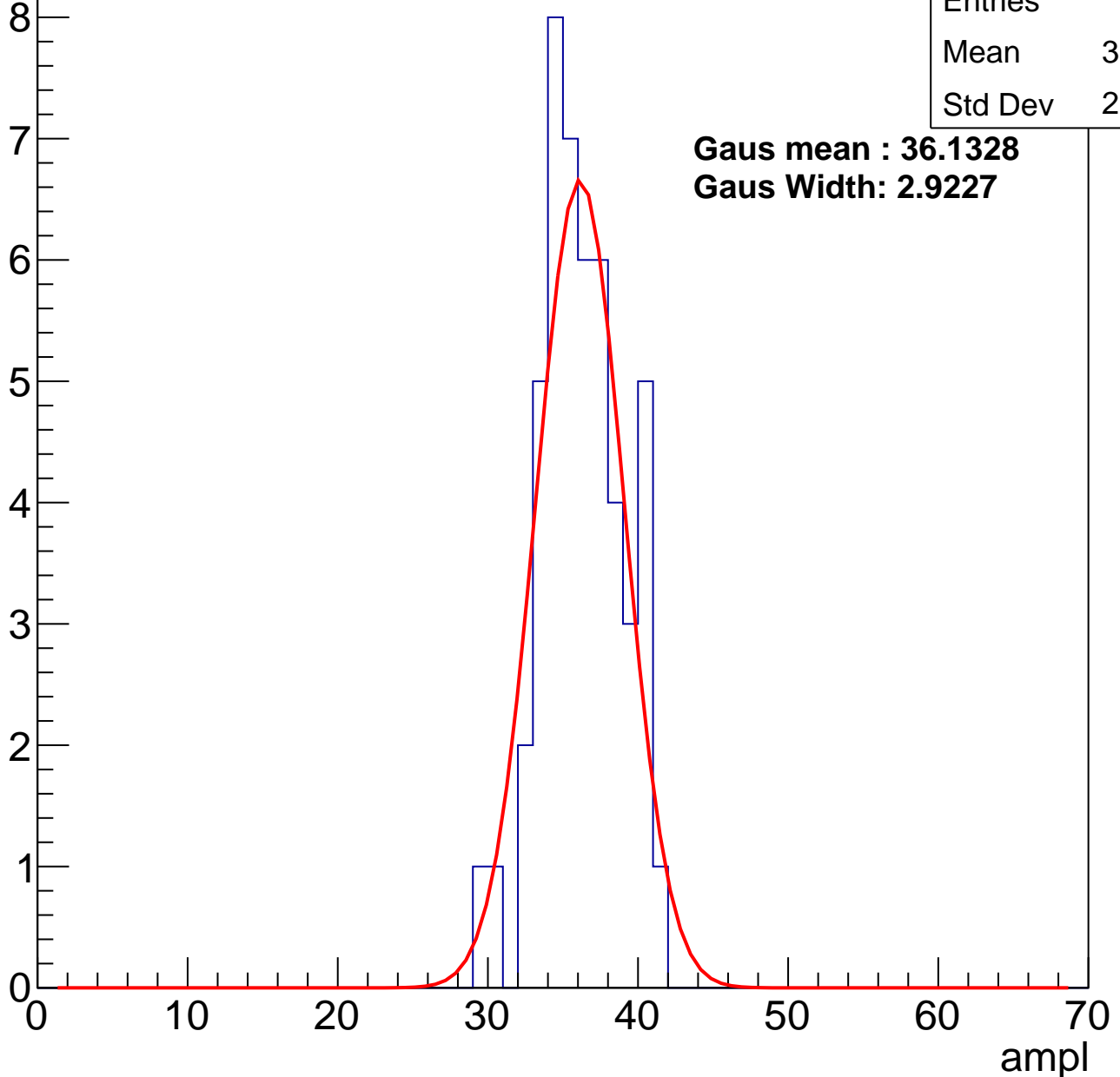
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	35.78
Std Dev	2.682

Gaus mean : 36.1328

Gaus Width: 2.9227



B1L003S, U16-ch83, adc2

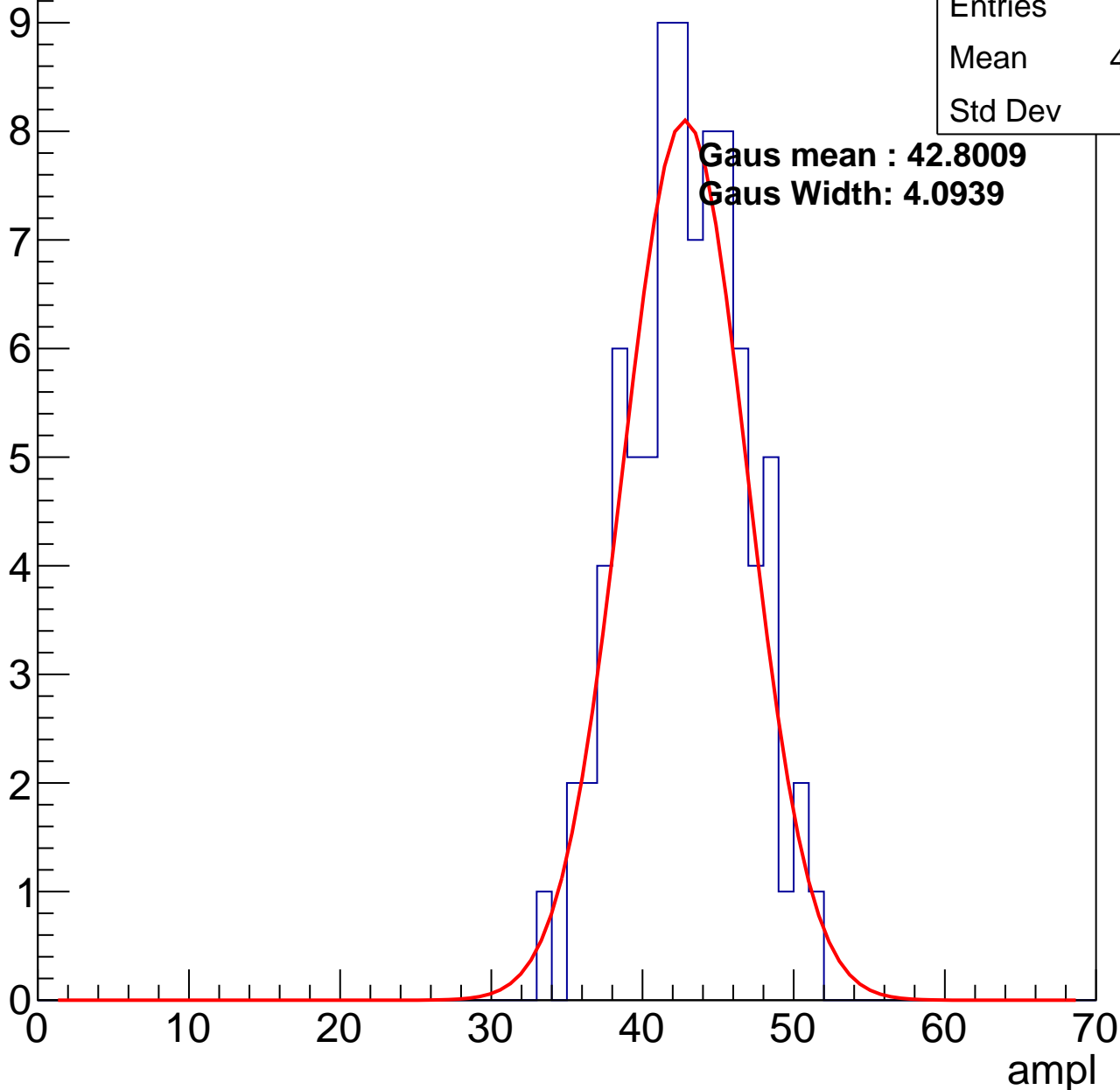
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	42.47
Std Dev	3.83

Gaus mean : 42.8009

Gaus Width: 4.0939

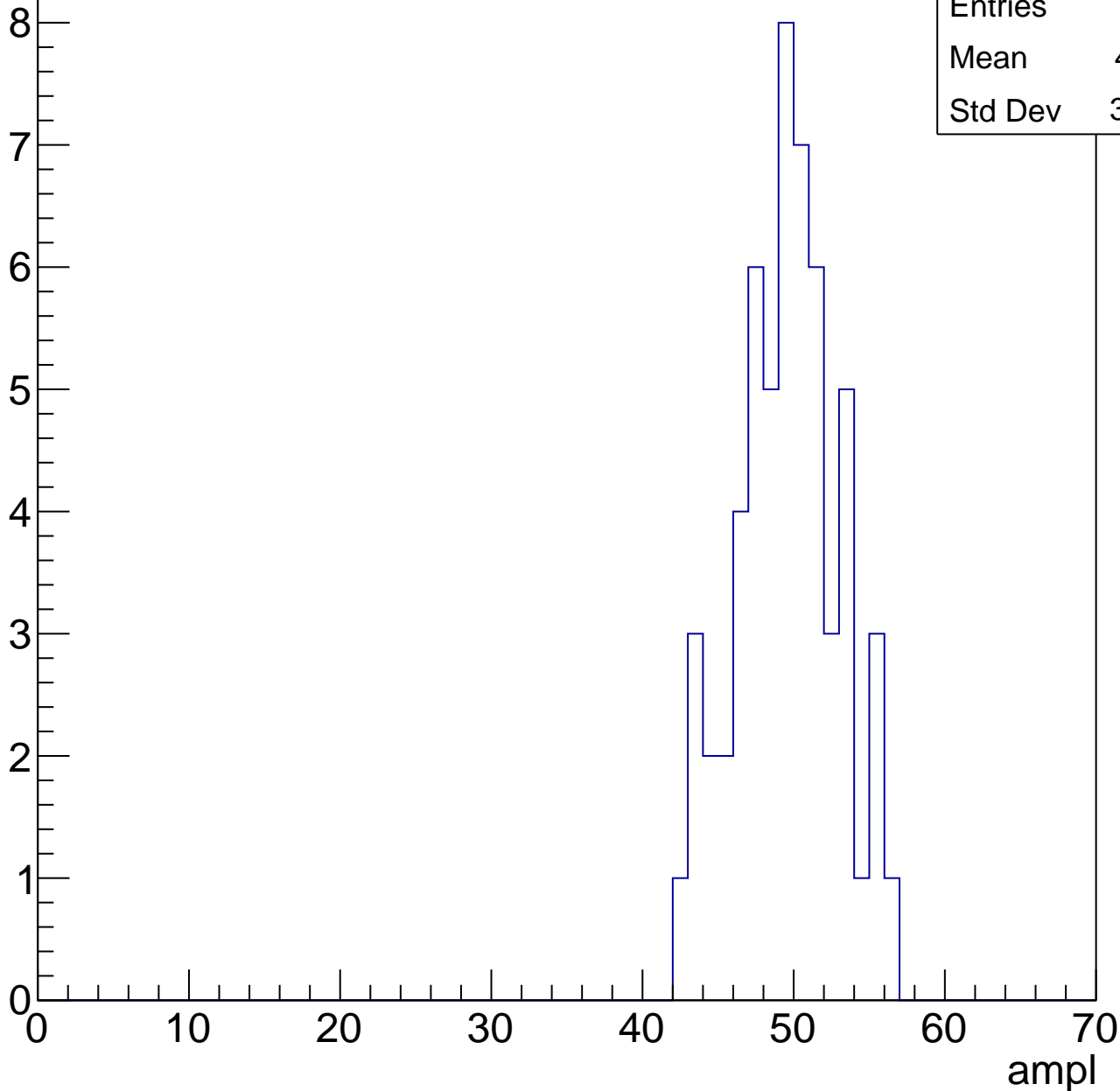


B1L003S, U16-ch83, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

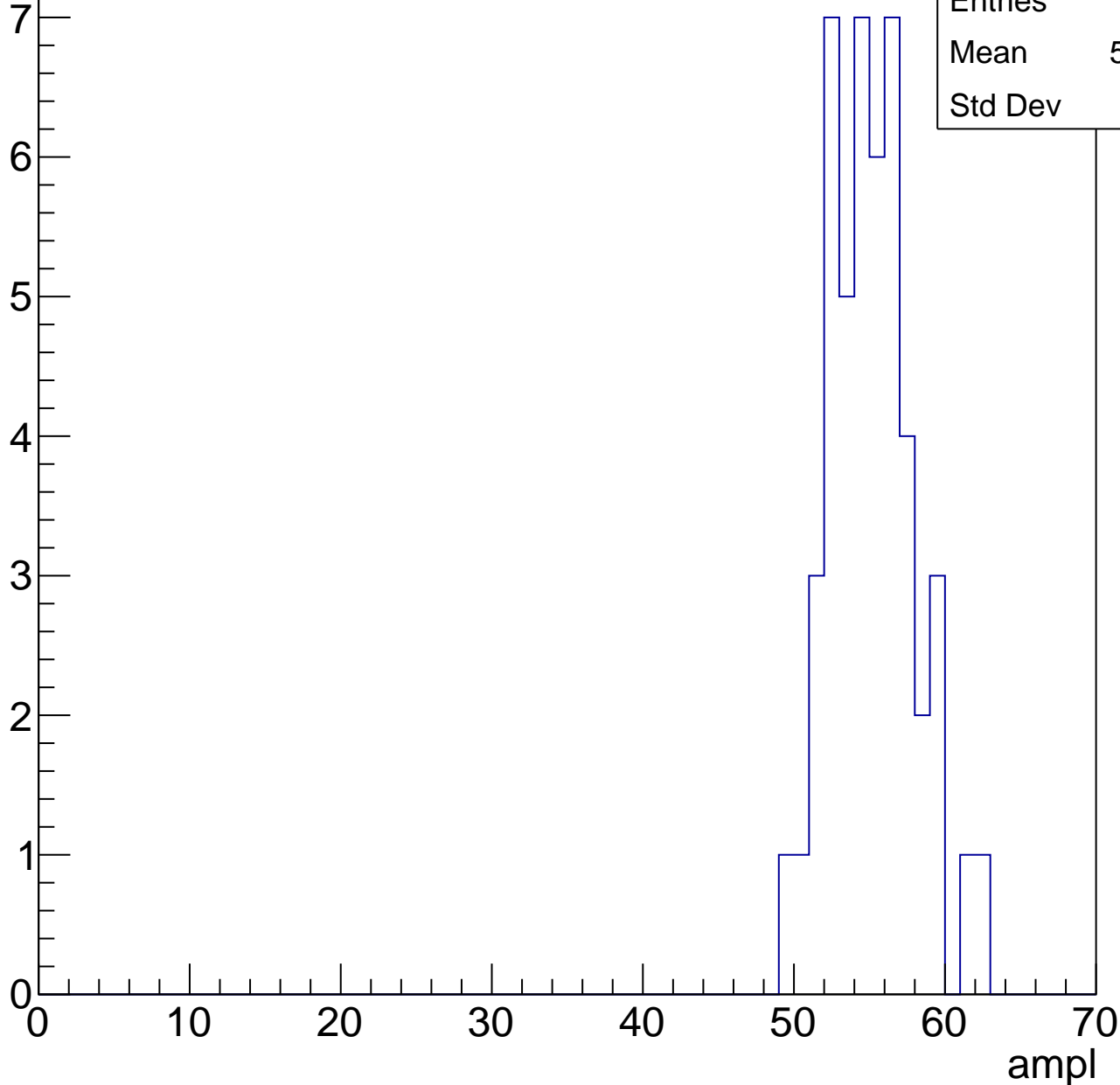
Entries	57
Mean	49.11
Std Dev	3.307



B1L003S, U16-ch83, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	48
Mean	54.69
Std Dev	2.77

B1L003S, U16-ch83, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	52
Mean	58.4
Std Dev	8.515

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

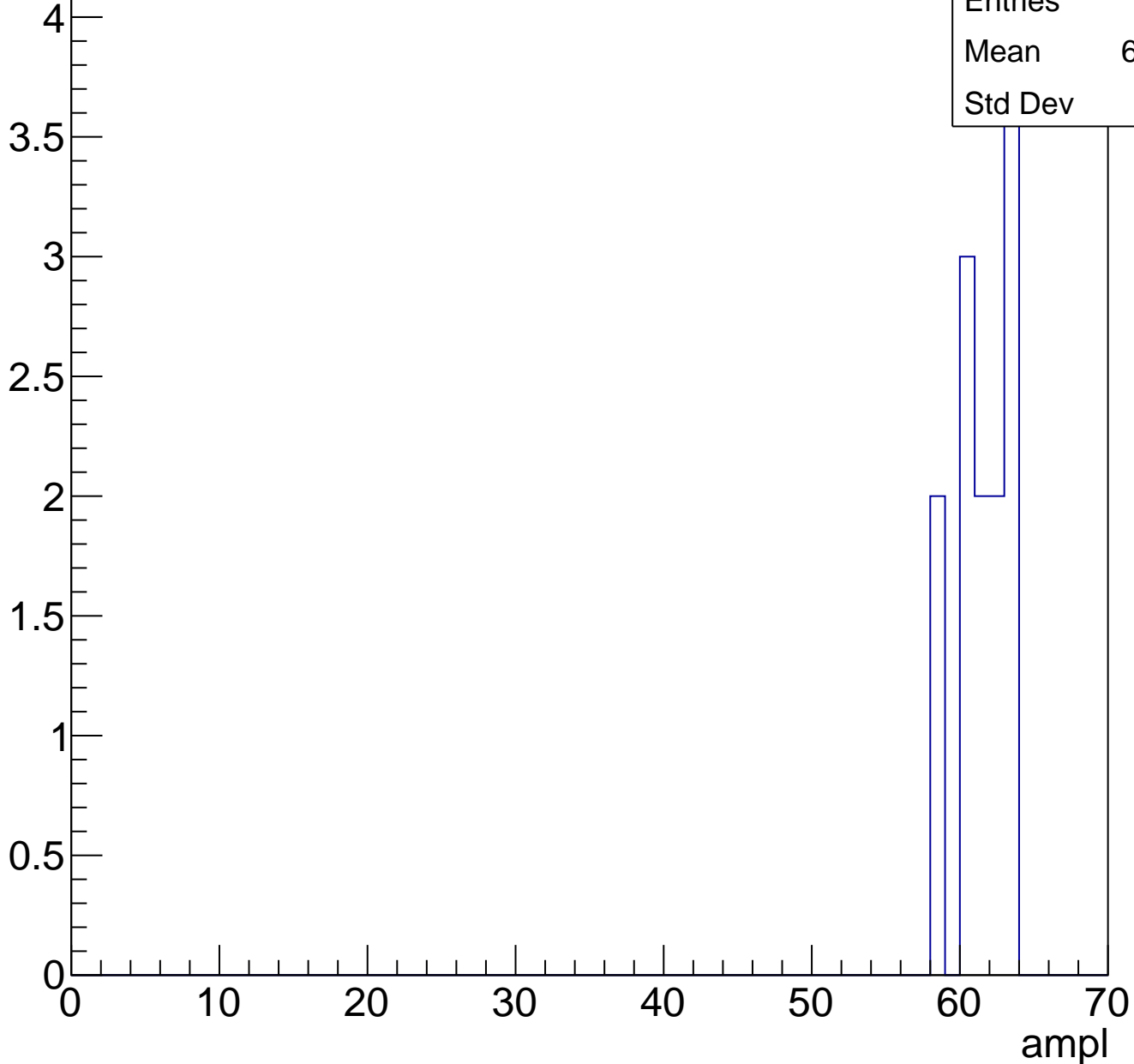
70

ampl

B1L003S, U16-ch83, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch83, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch84, adc0

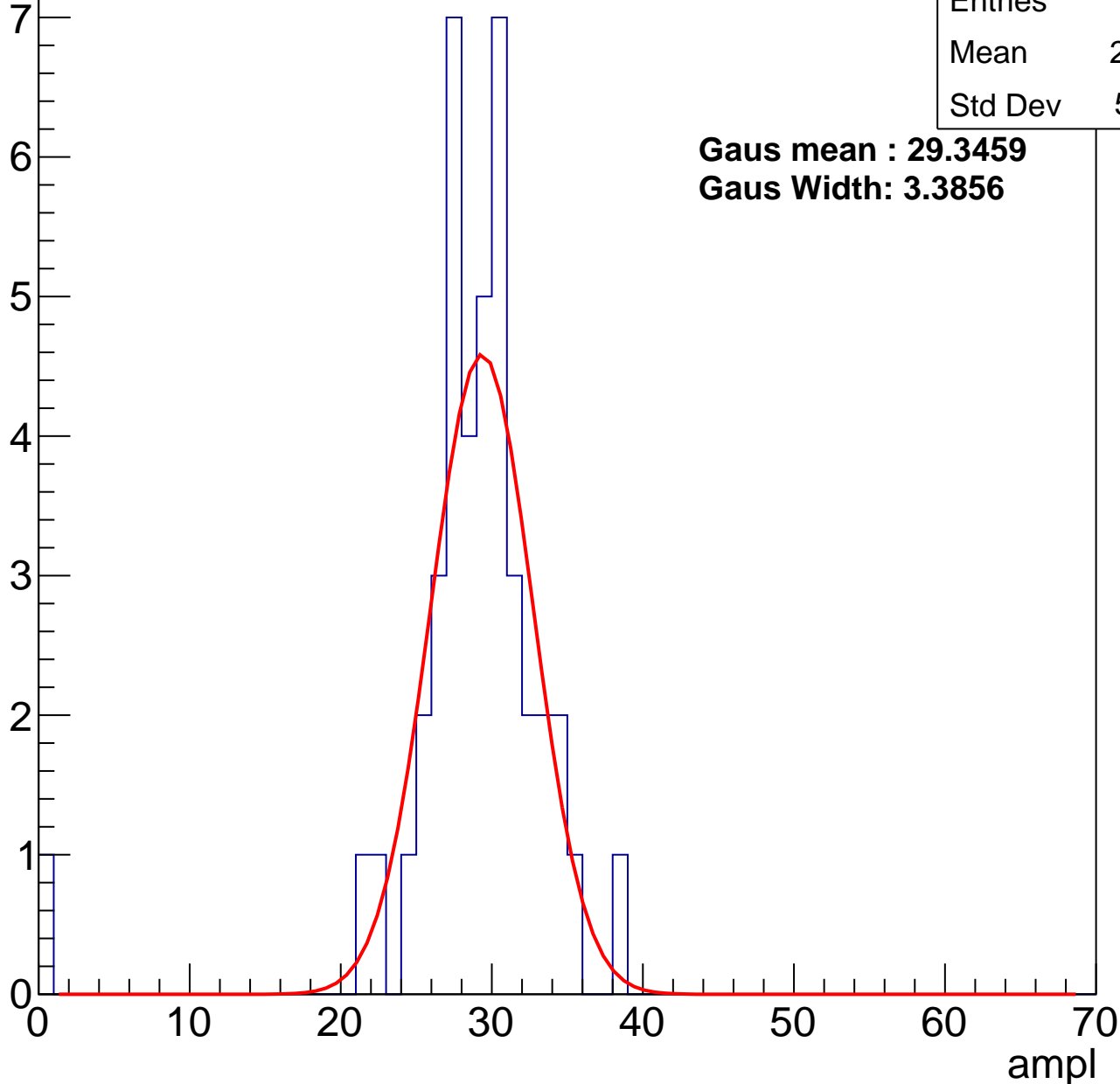
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	28.26
Std Dev	5.461

Gaus mean : 29.3459

Gaus Width: 3.3856



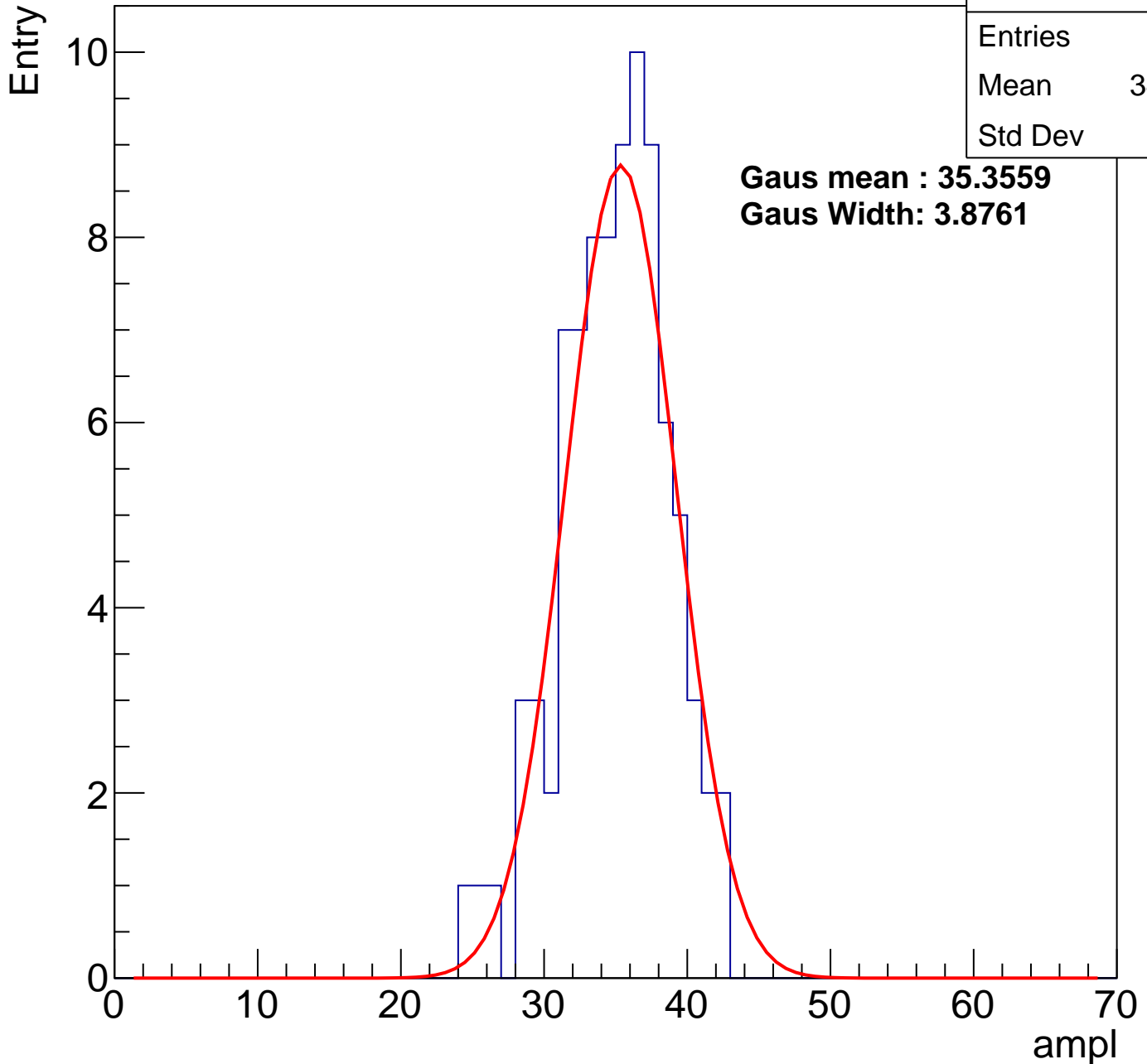
B1L003S, U16-ch84, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	34.48
Std Dev	3.75

Gaus mean : 35.3559

Gaus Width: 3.8761



B1L003S, U16-ch84, adc2

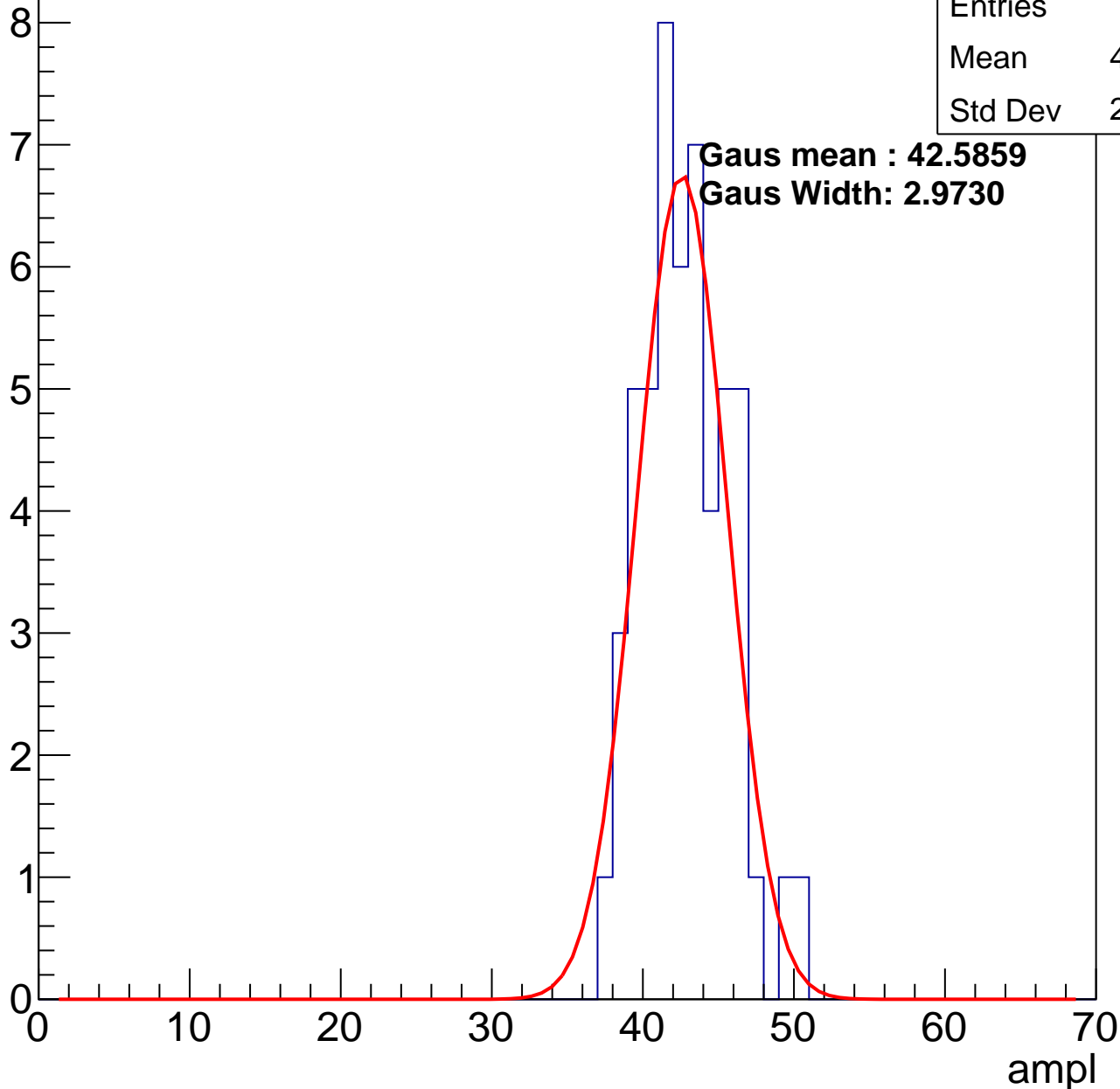
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.38
Std Dev	2.857

Gaus mean : 42.5859

Gaus Width: 2.9730

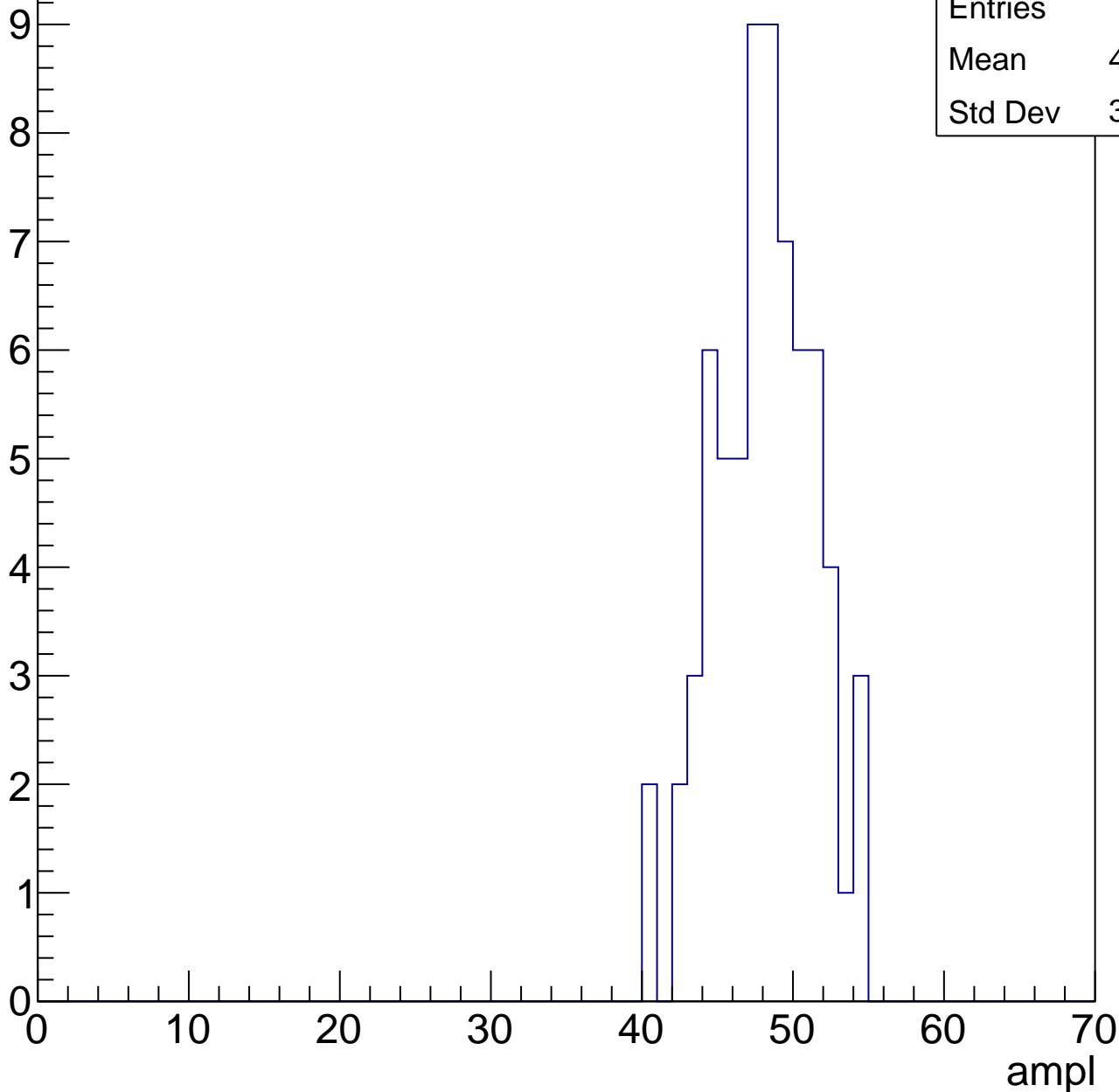


B1L003S, U16-ch84, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

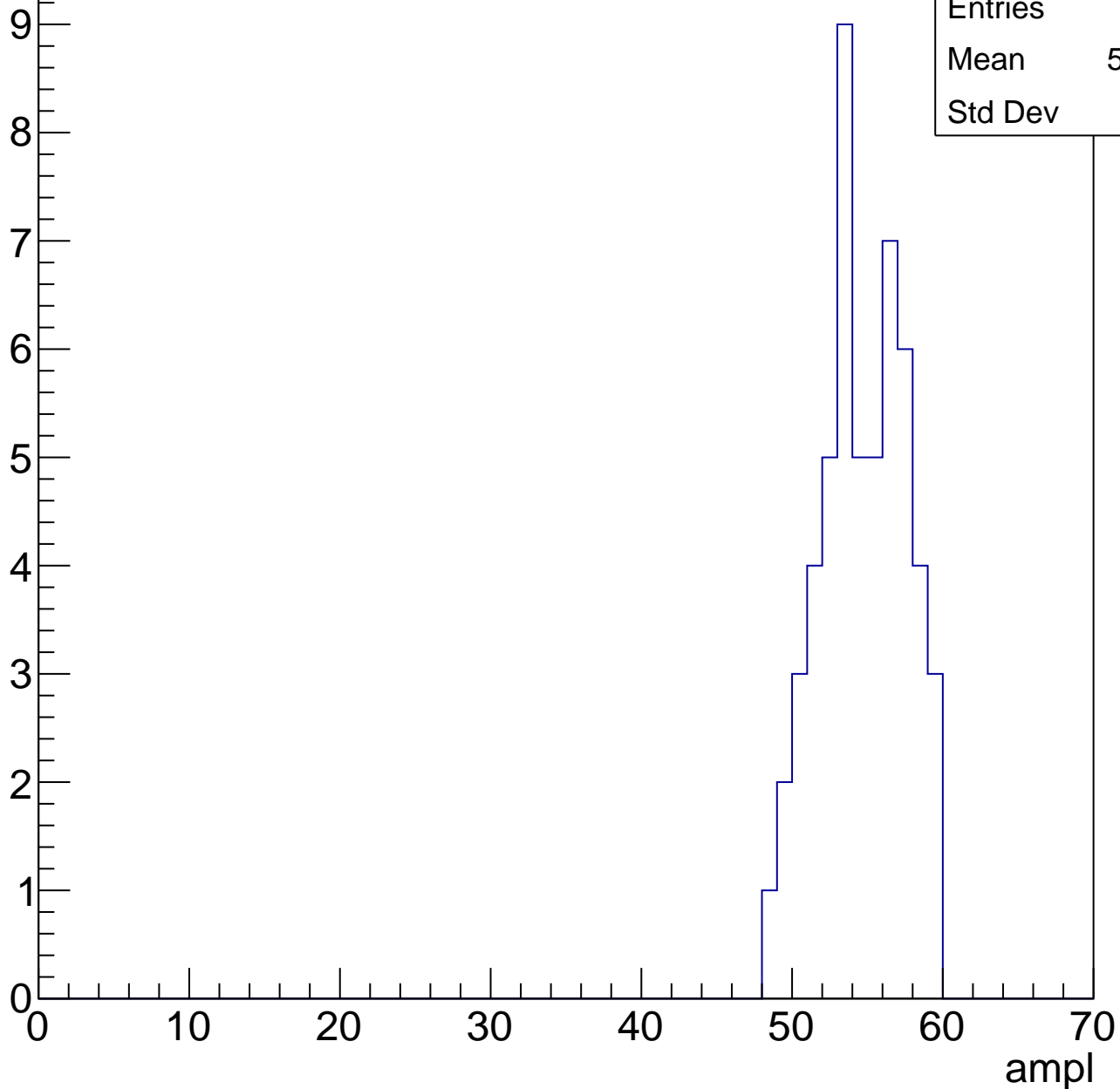
Entries	68
Mean	47.63
Std Dev	3.249



B1L003S, U16-ch84, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



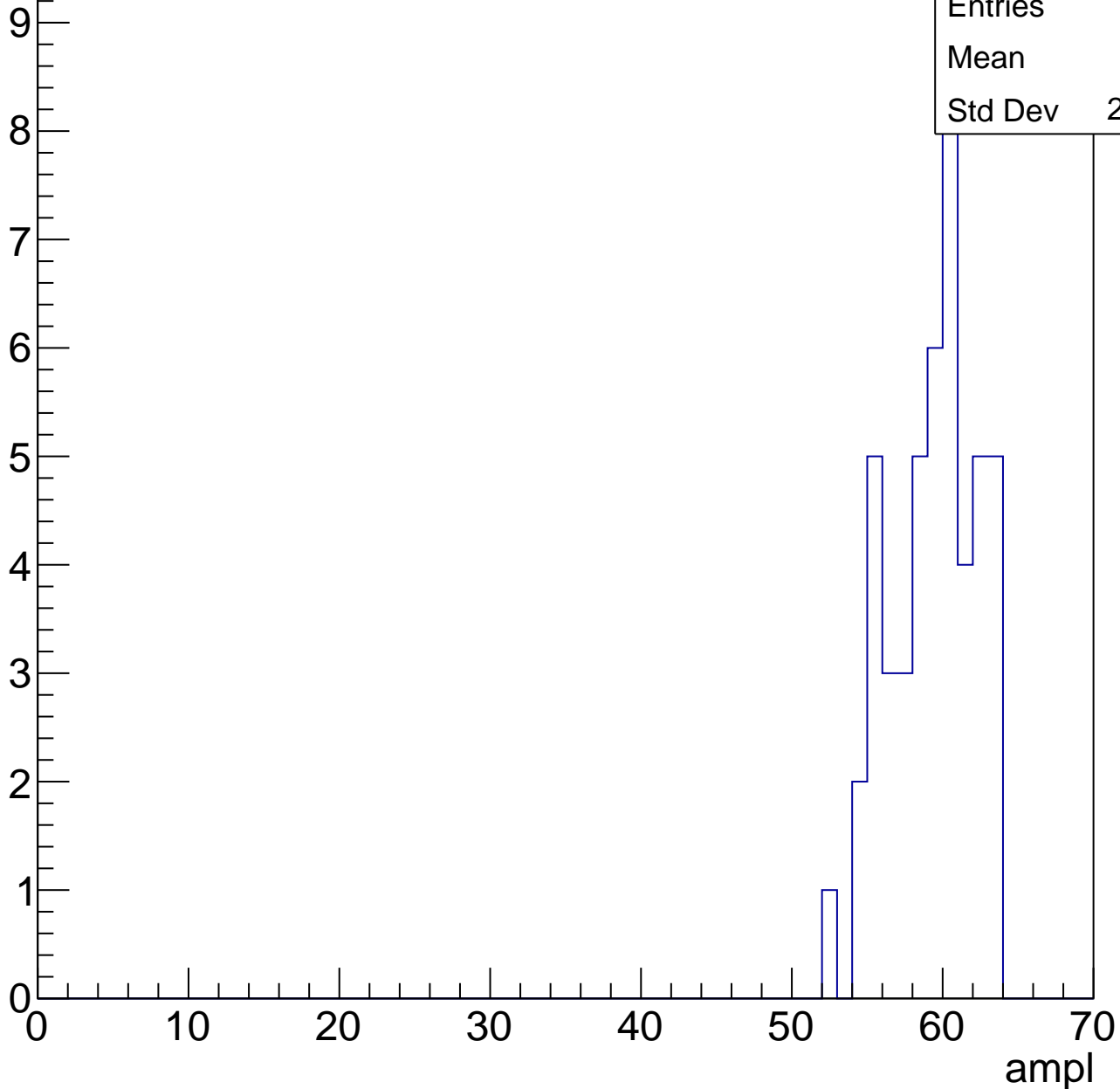
Entries	54
Mean	54.17
Std Dev	2.8

B1L003S, U16-ch84, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	58.9
Std Dev	2.786

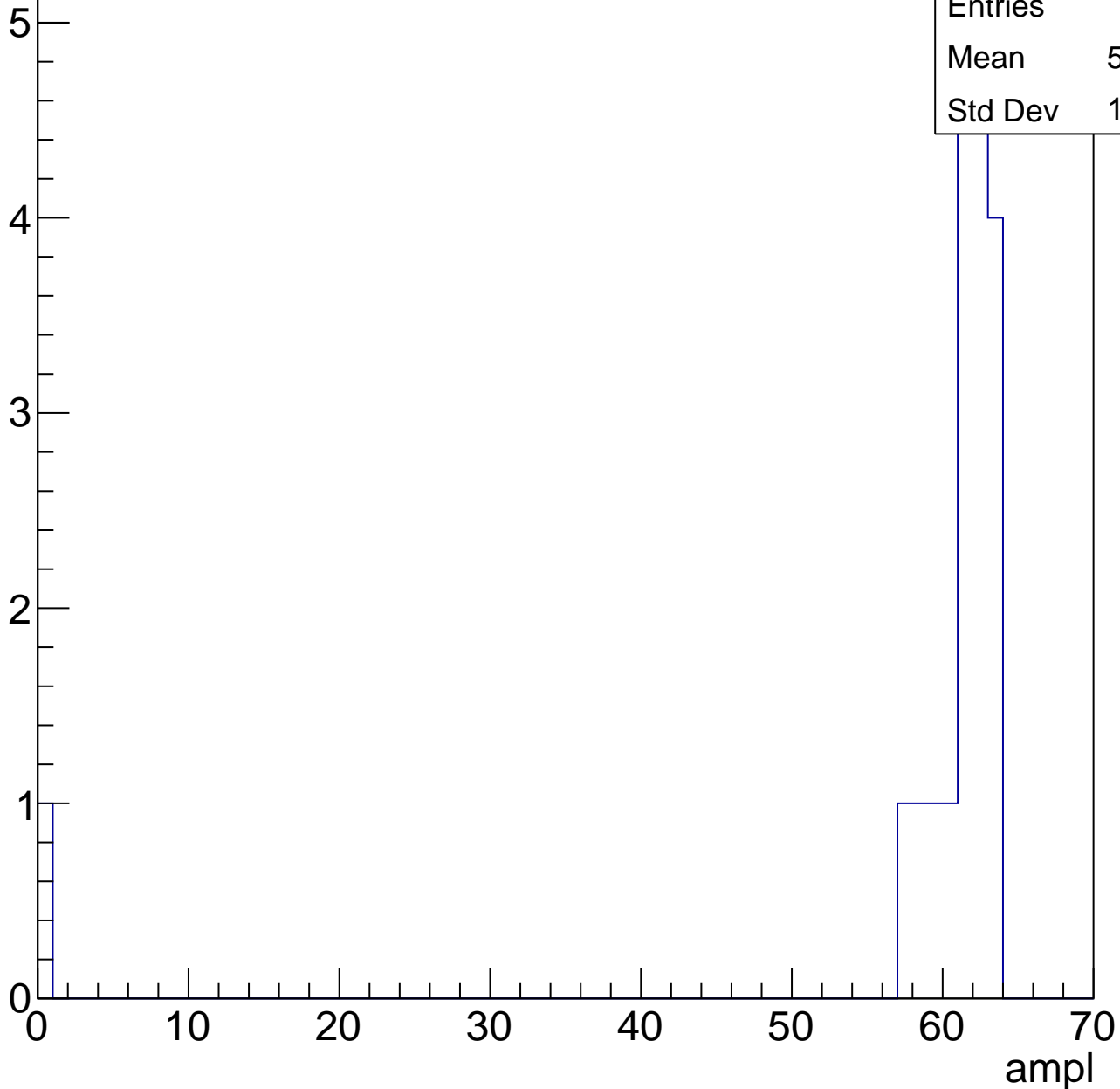


B1L003S, U16-ch84, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

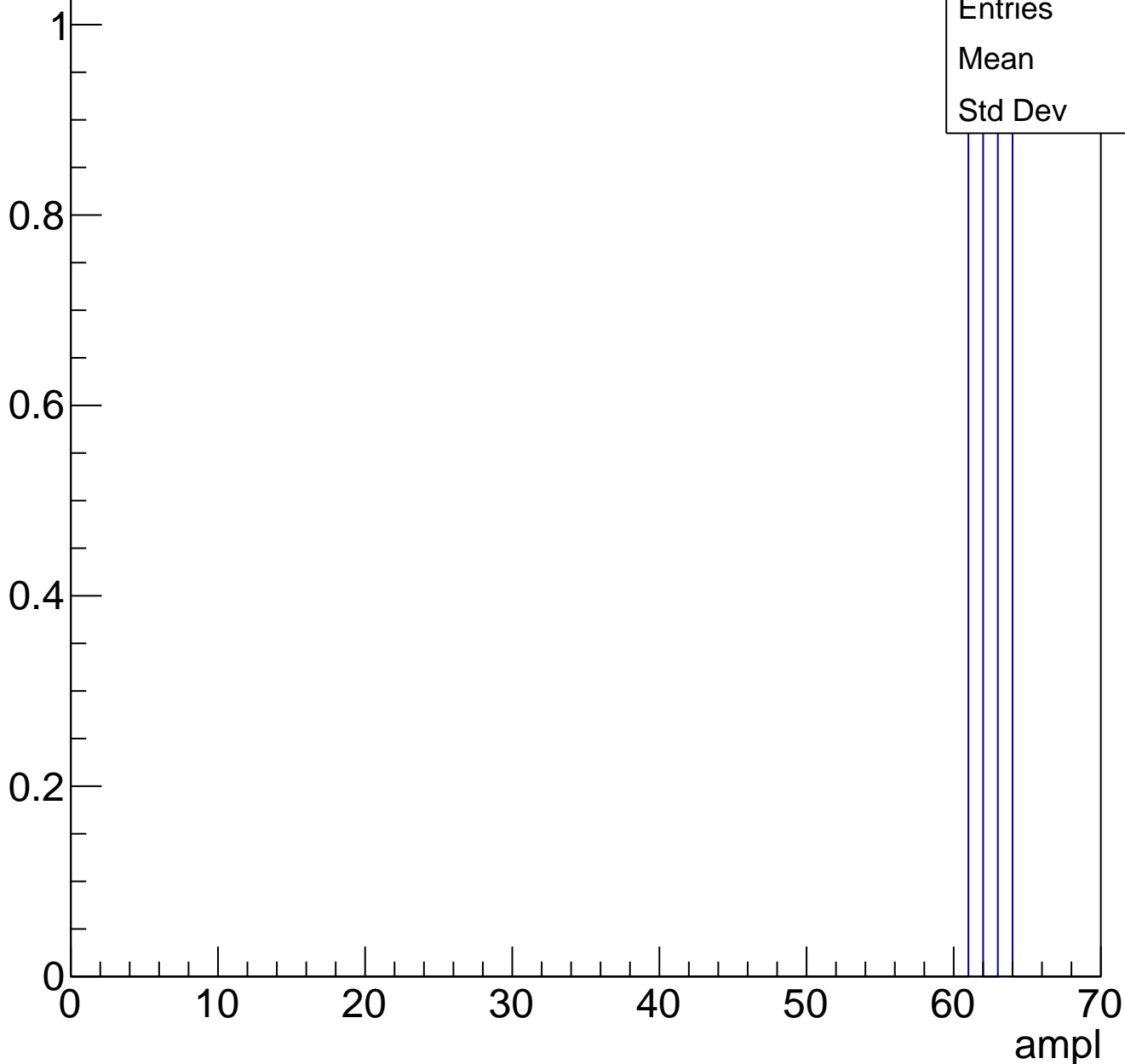
Entries	19
Mean	57.95
Std Dev	13.76



B1L003S, U16-ch84, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch85, adc0

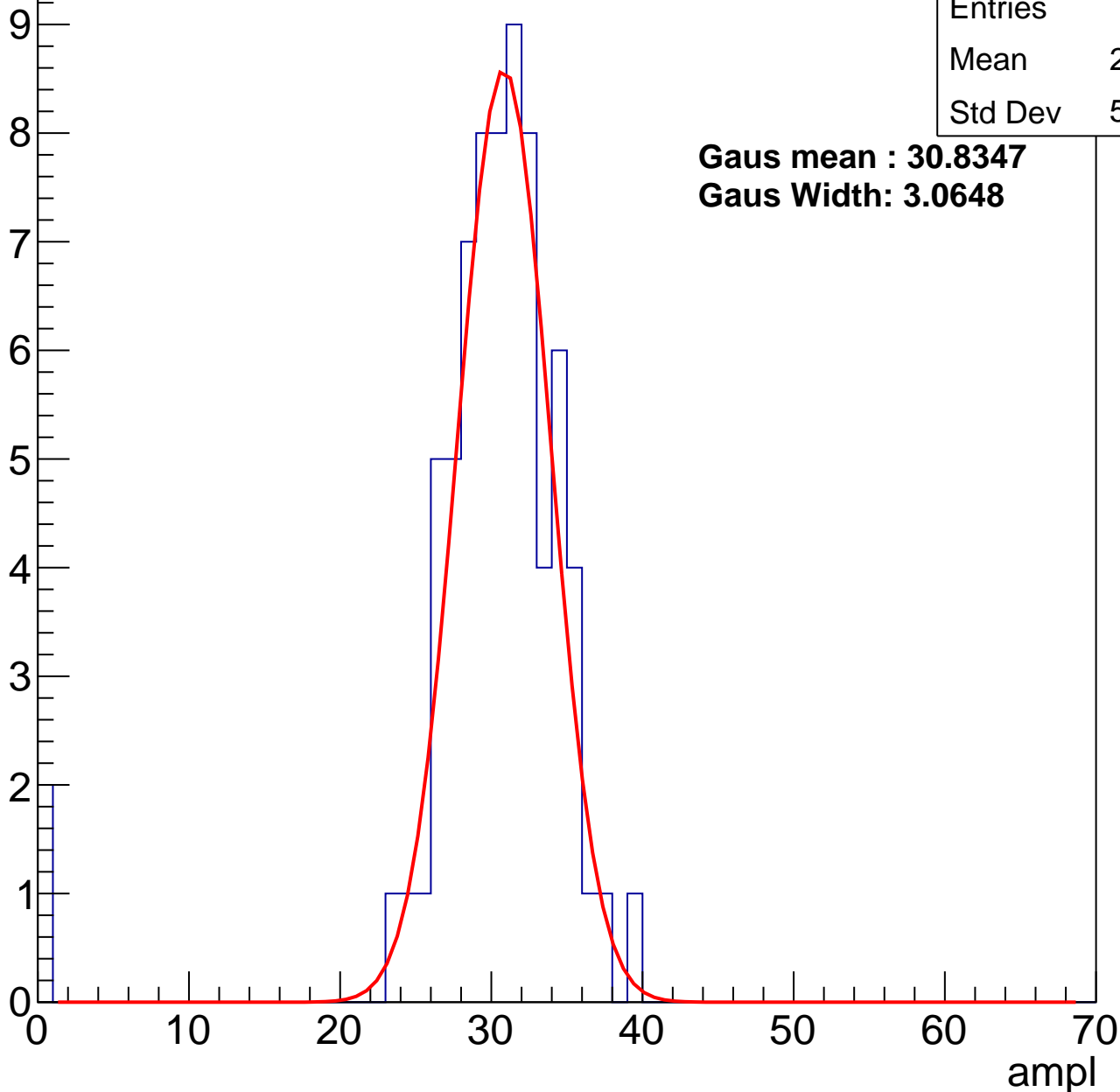
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.56
Std Dev	5.888

Gaus mean : 30.8347

Gaus Width: 3.0648



B1L003S, U16-ch85, adc1

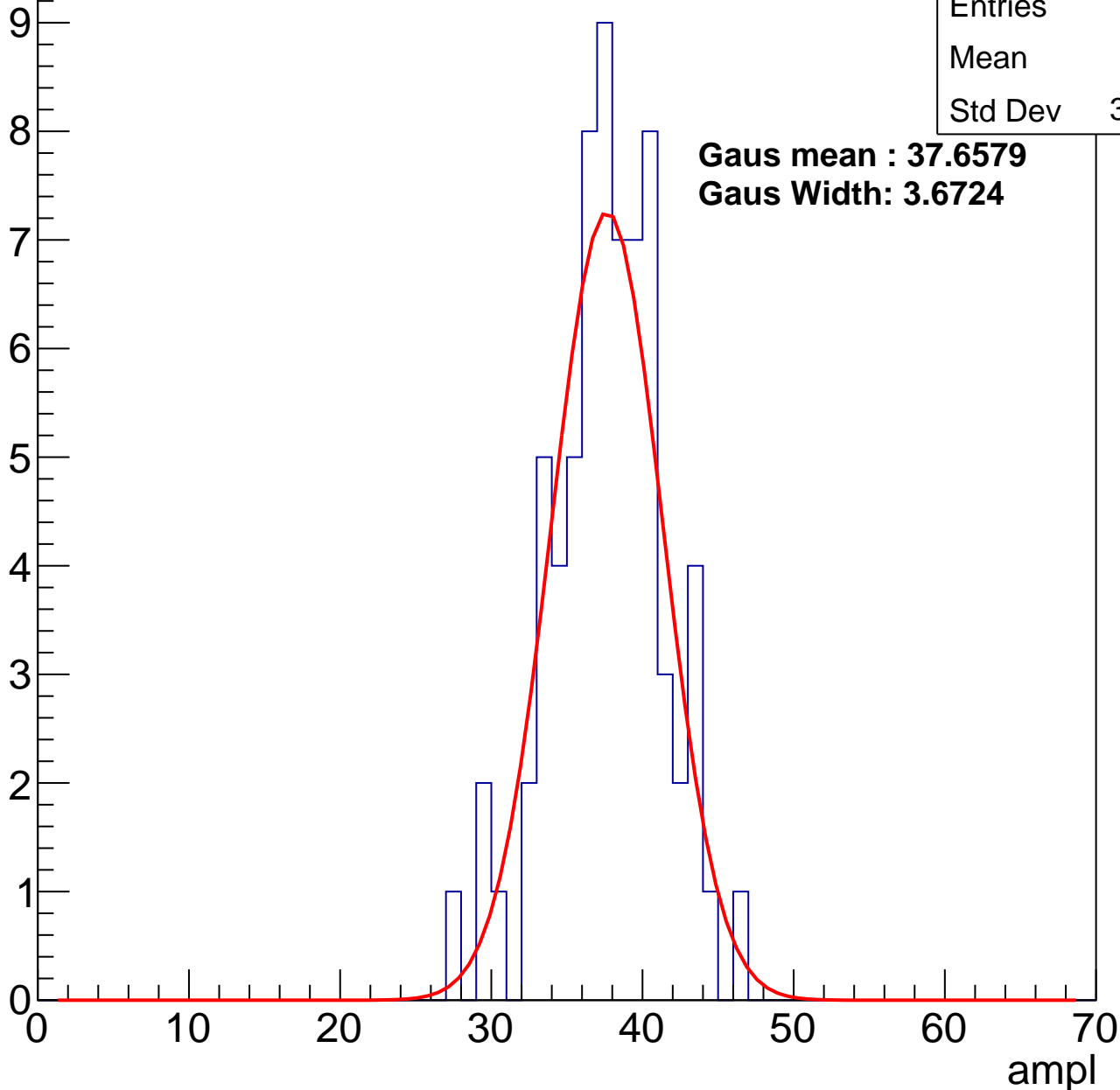
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.2
Std Dev	3.679

Gaus mean : 37.6579

Gaus Width: 3.6724



B1L003S, U16-ch85, adc2

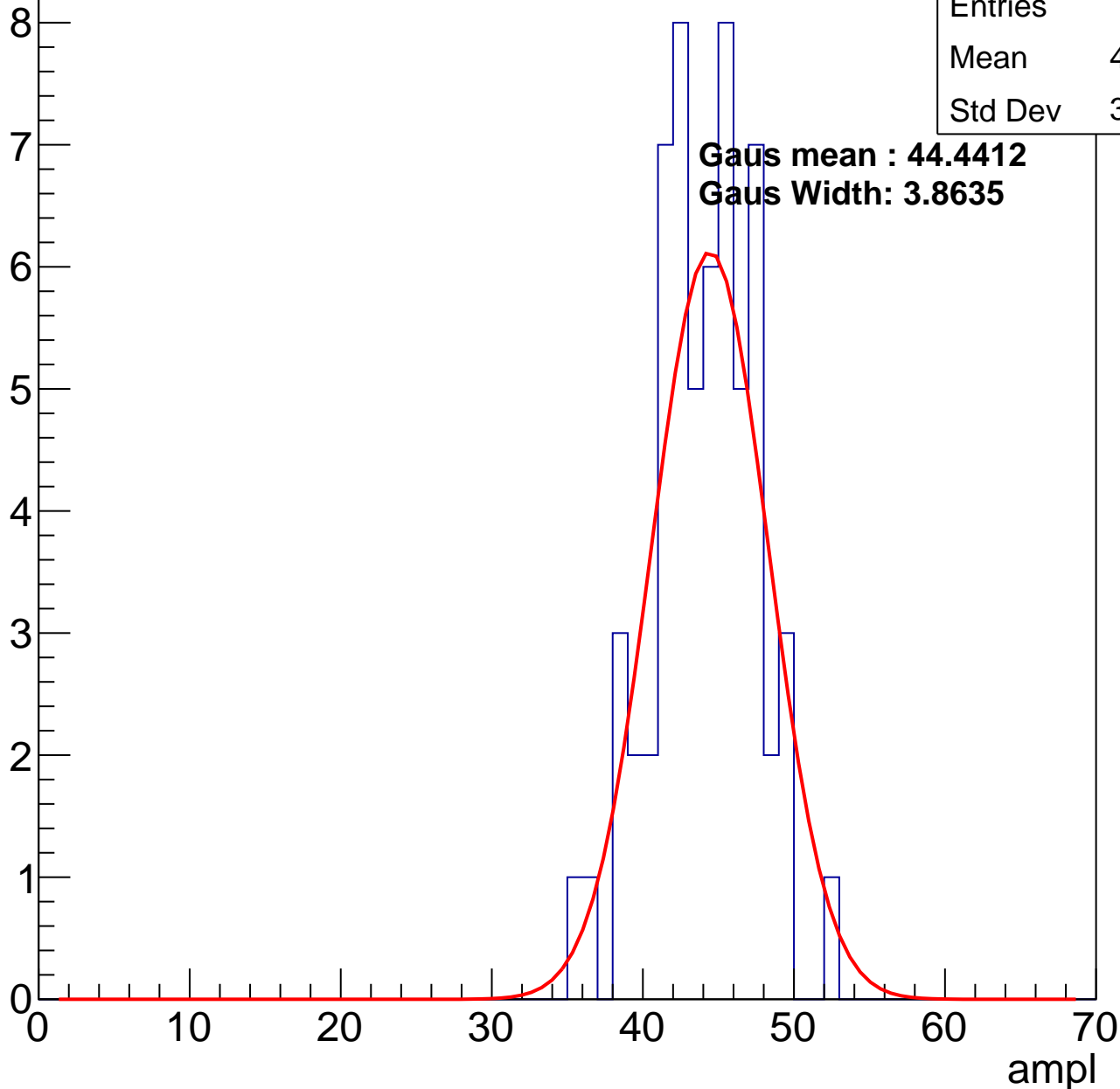
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.59
Std Dev	3.355

Gaus mean : 44.4412

Gaus Width: 3.8635



B1L003S, U16-ch85, adc3

calib_packv5_042523_0143.root, FC#13, port D2

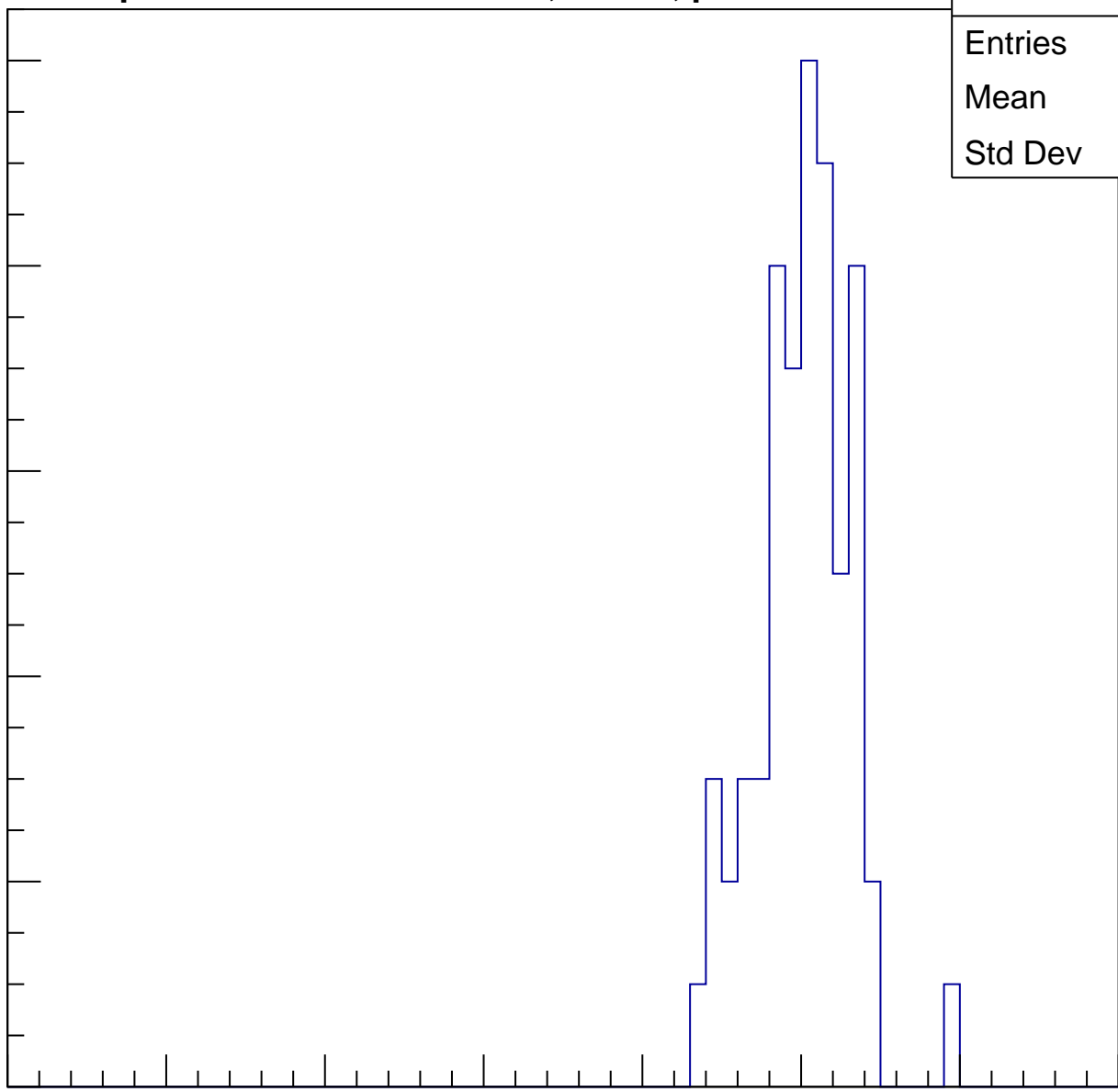
Entries	62
Mean	49.69
Std Dev	2.921

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

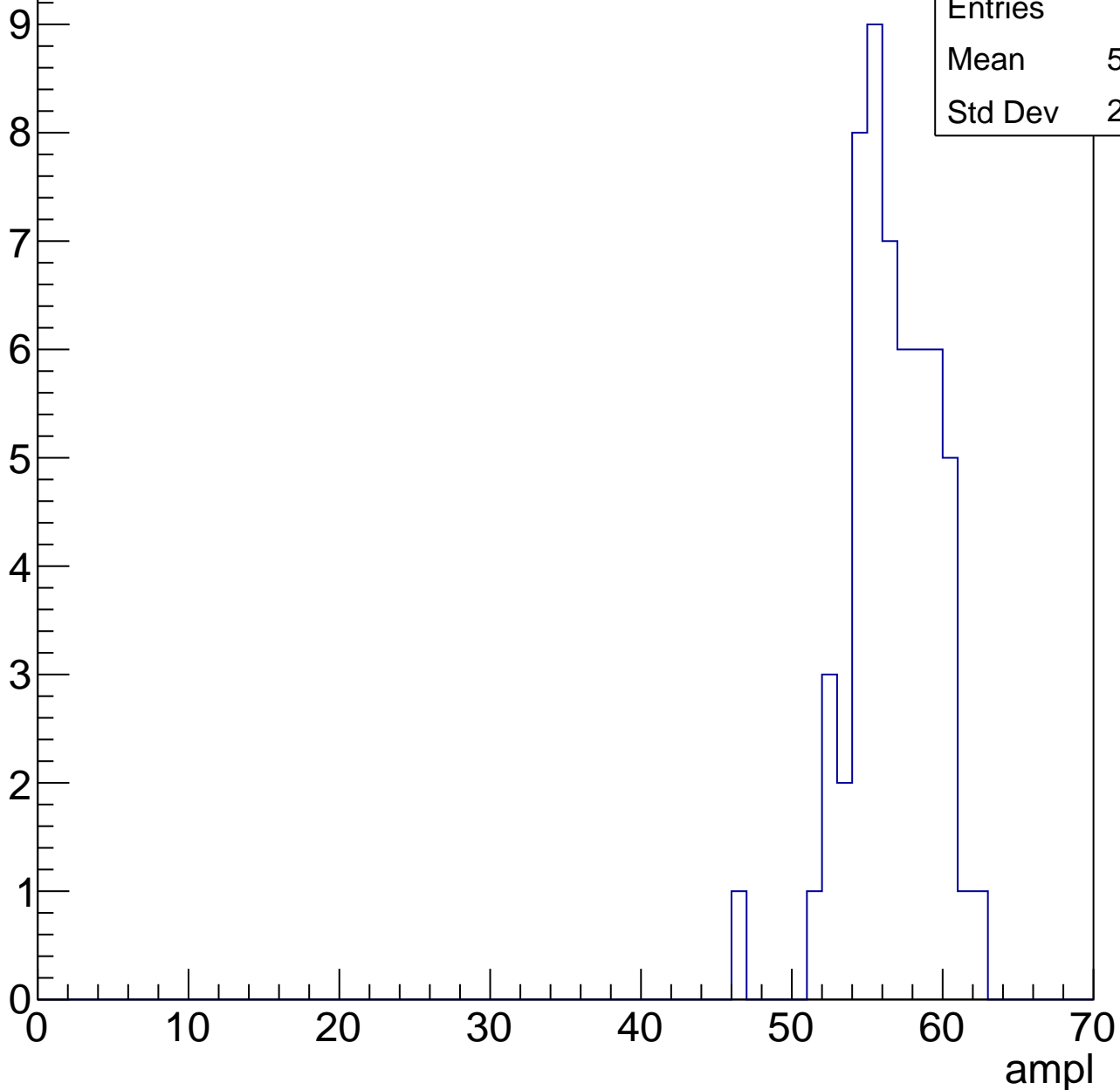


B1L003S, U16-ch85, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	56.16
Std Dev	2.852

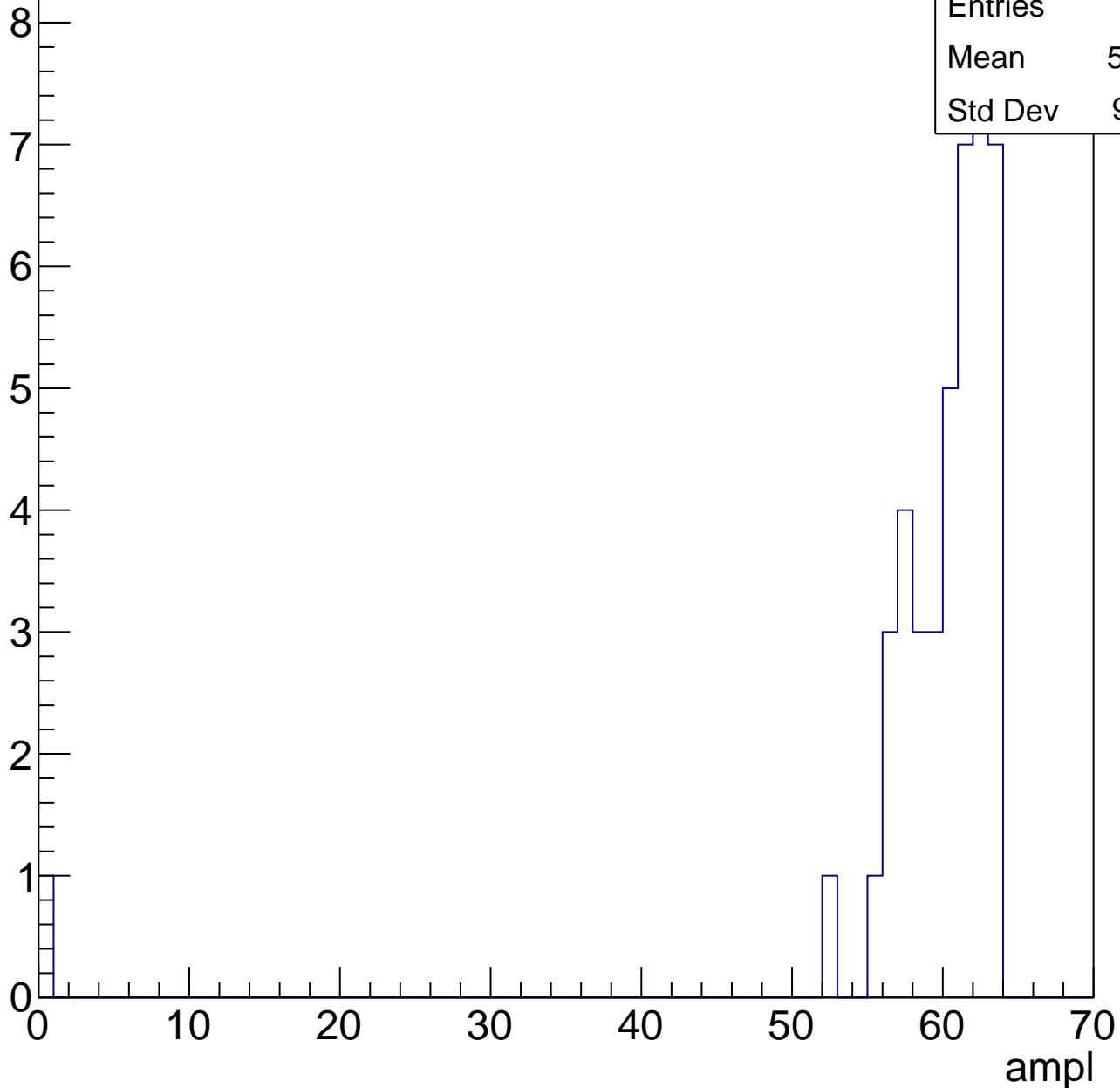


B1L003S, U16-ch85, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

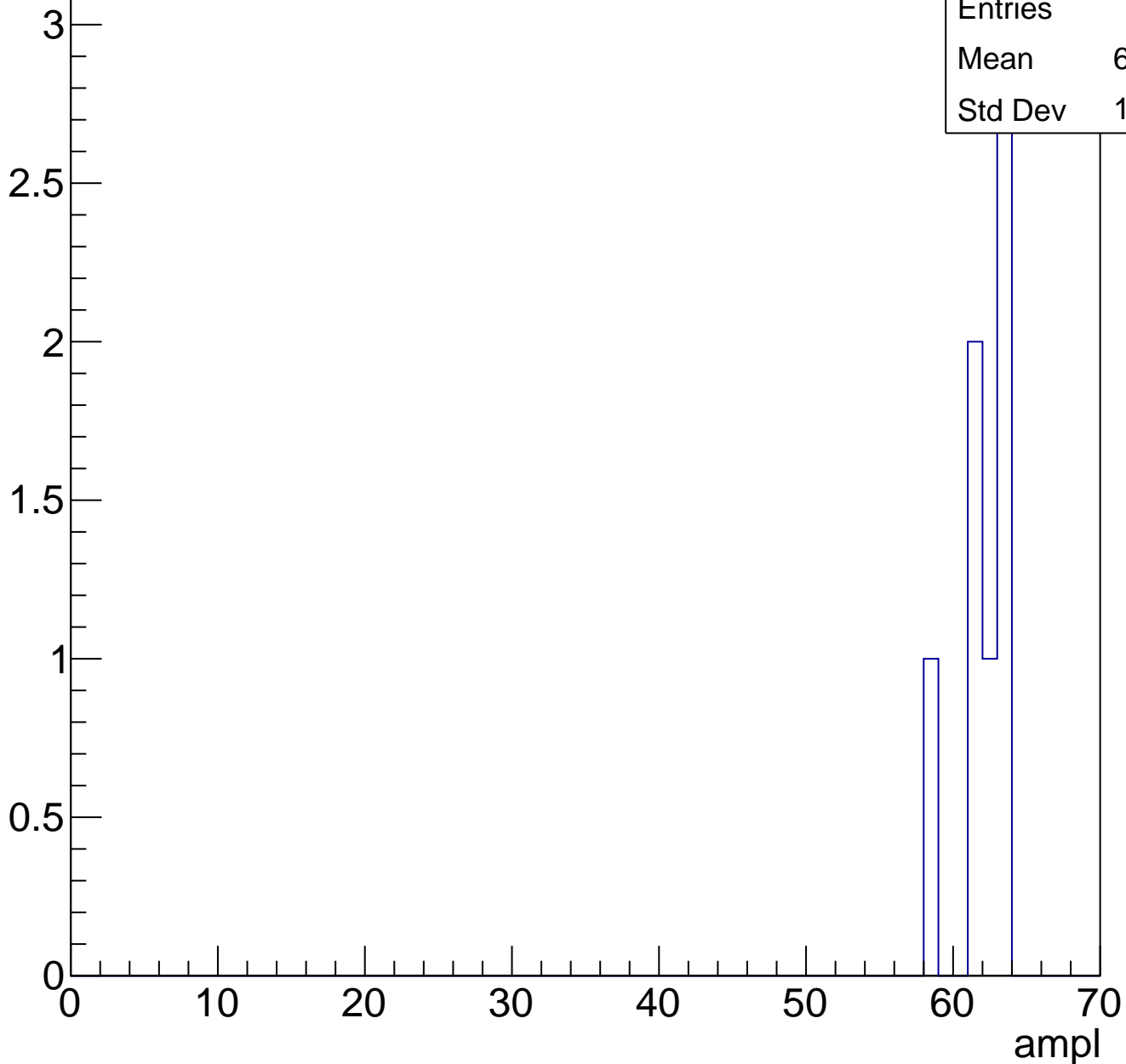
Entries	43
Mean	58.56
Std Dev	9.401



B1L003S, U16-ch85, adc6

calib_packv5_042523_0143.root, FC#13, port D2

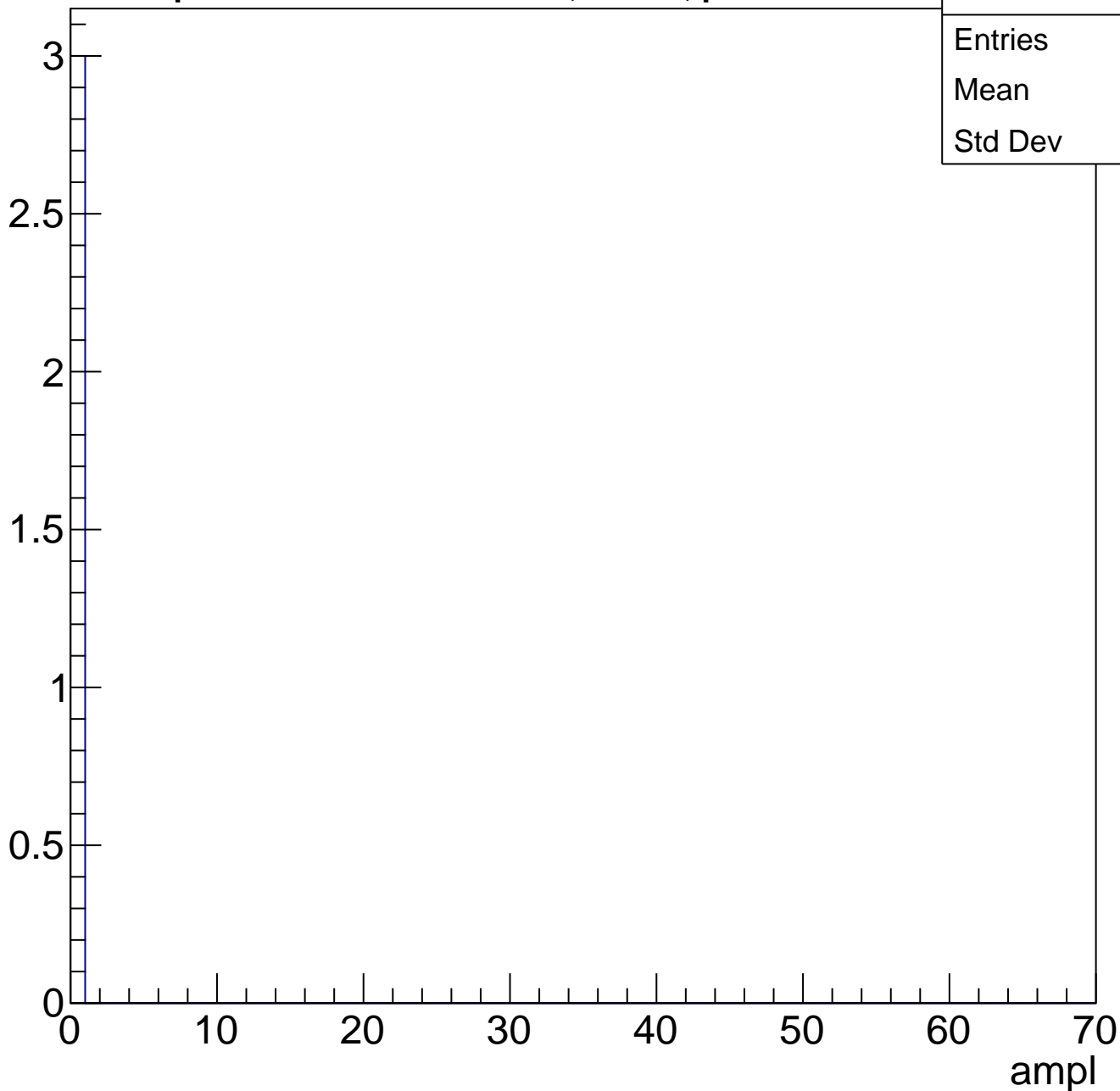
Entry



B1L003S, U16-ch85, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U16-ch86, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	30.12
Std Dev	2.698

Gaus mean : 30.4269

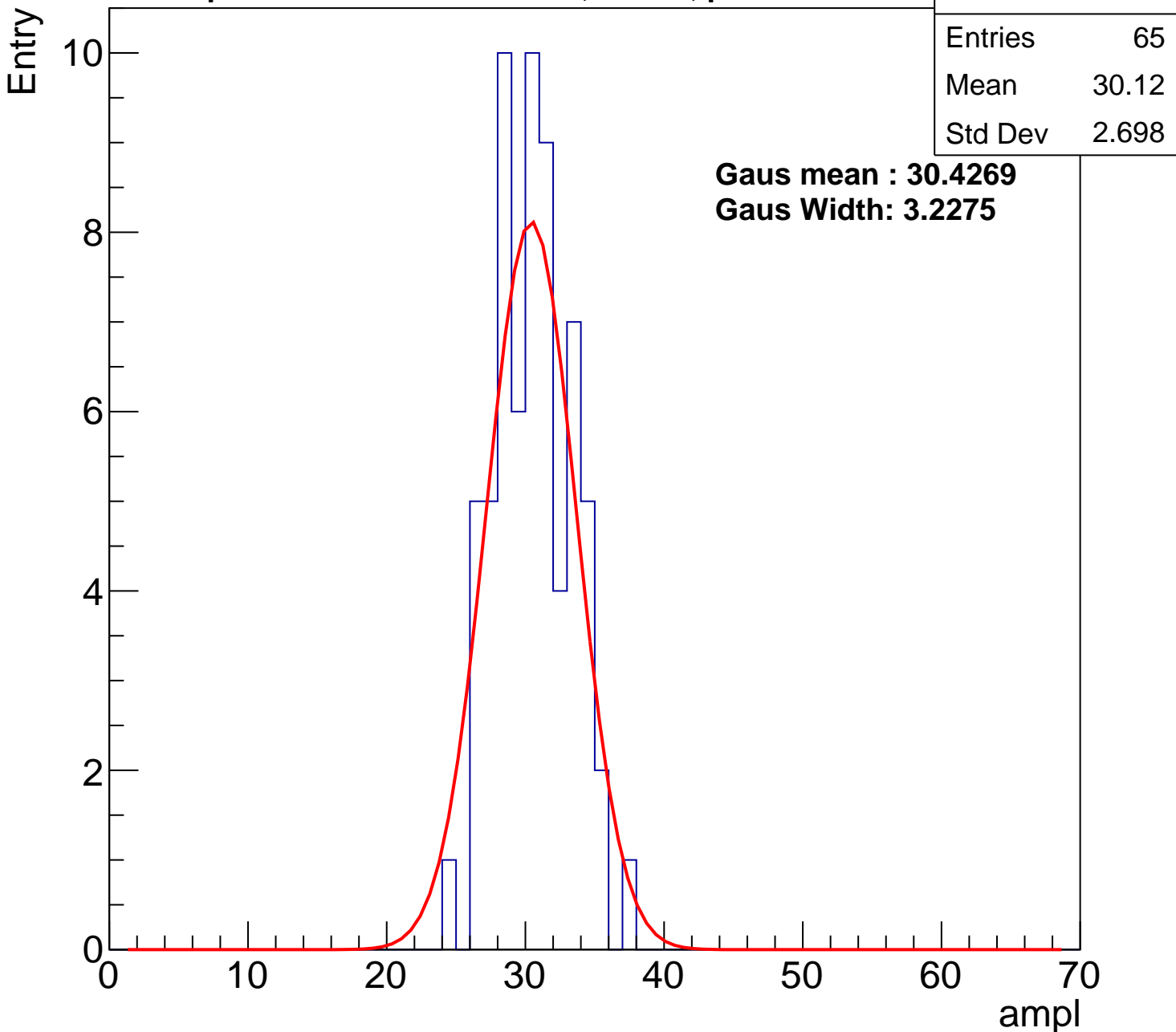
Gaus Width: 3.2275

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch86, adc1

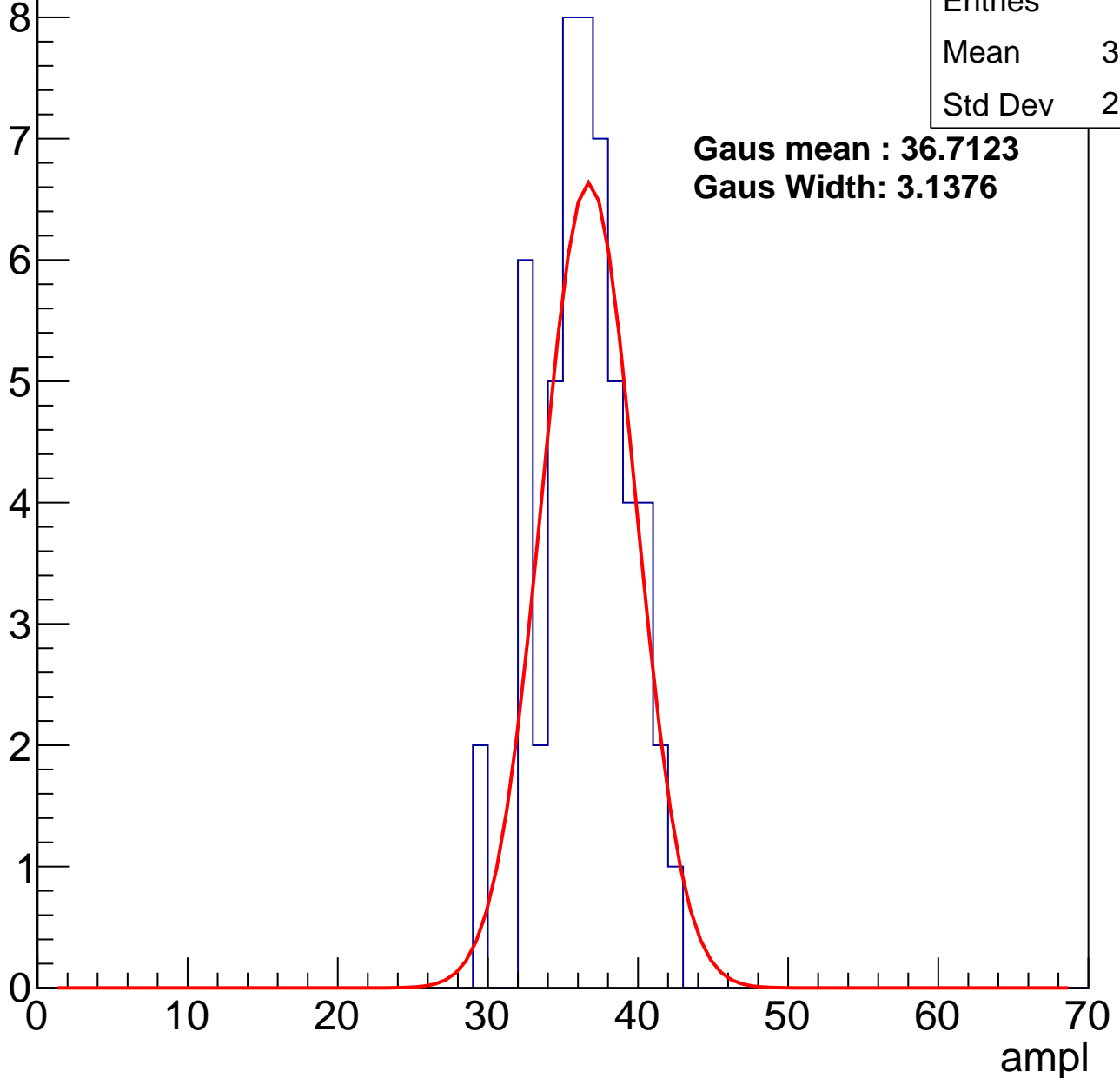
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	35.98
Std Dev	2.896

Gaus mean : 36.7123

Gaus Width: 3.1376



B1L003S, U16-ch86, adc2

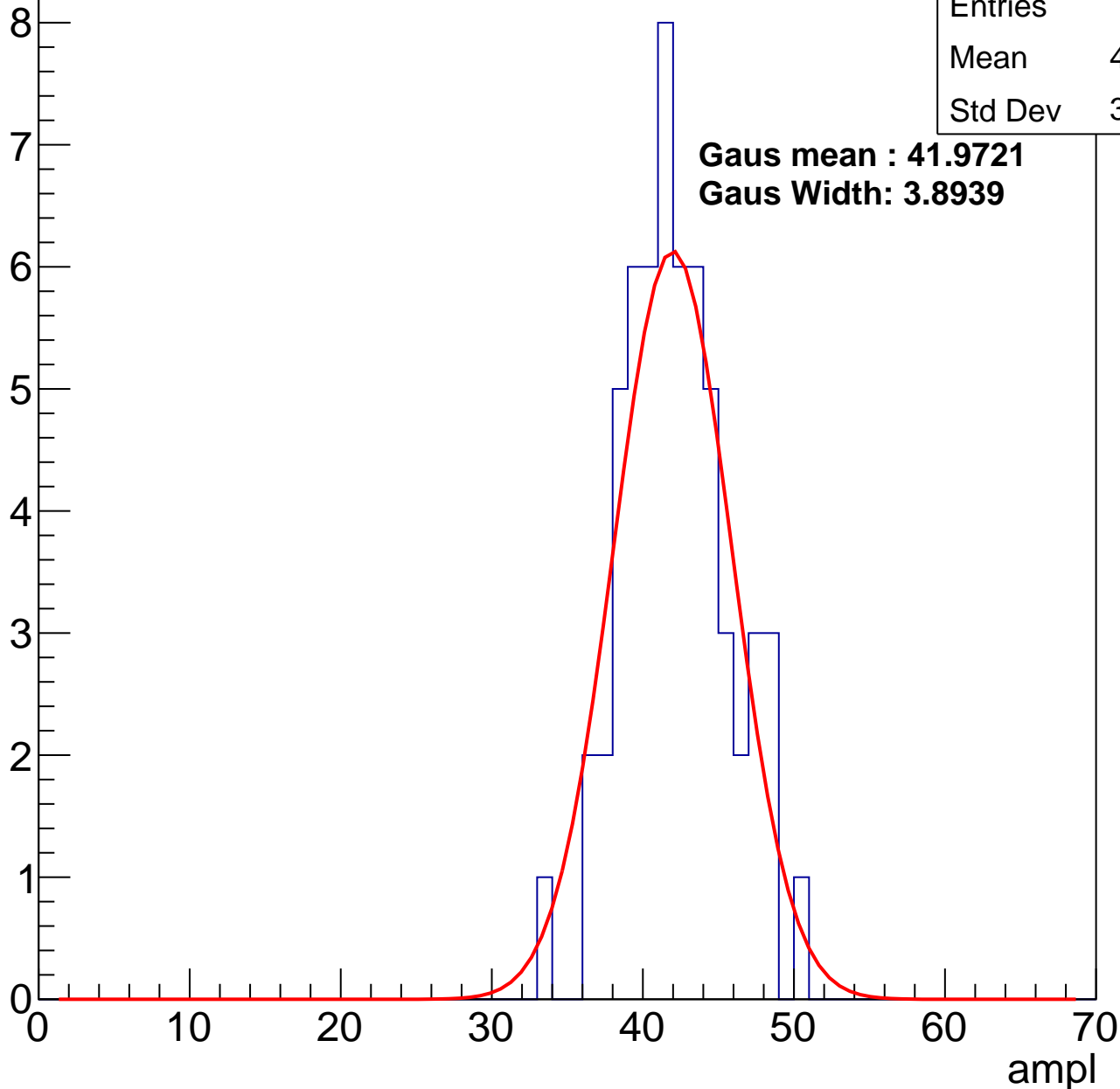
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.75
Std Dev	3.438

Gaus mean : 41.9721

Gaus Width: 3.8939

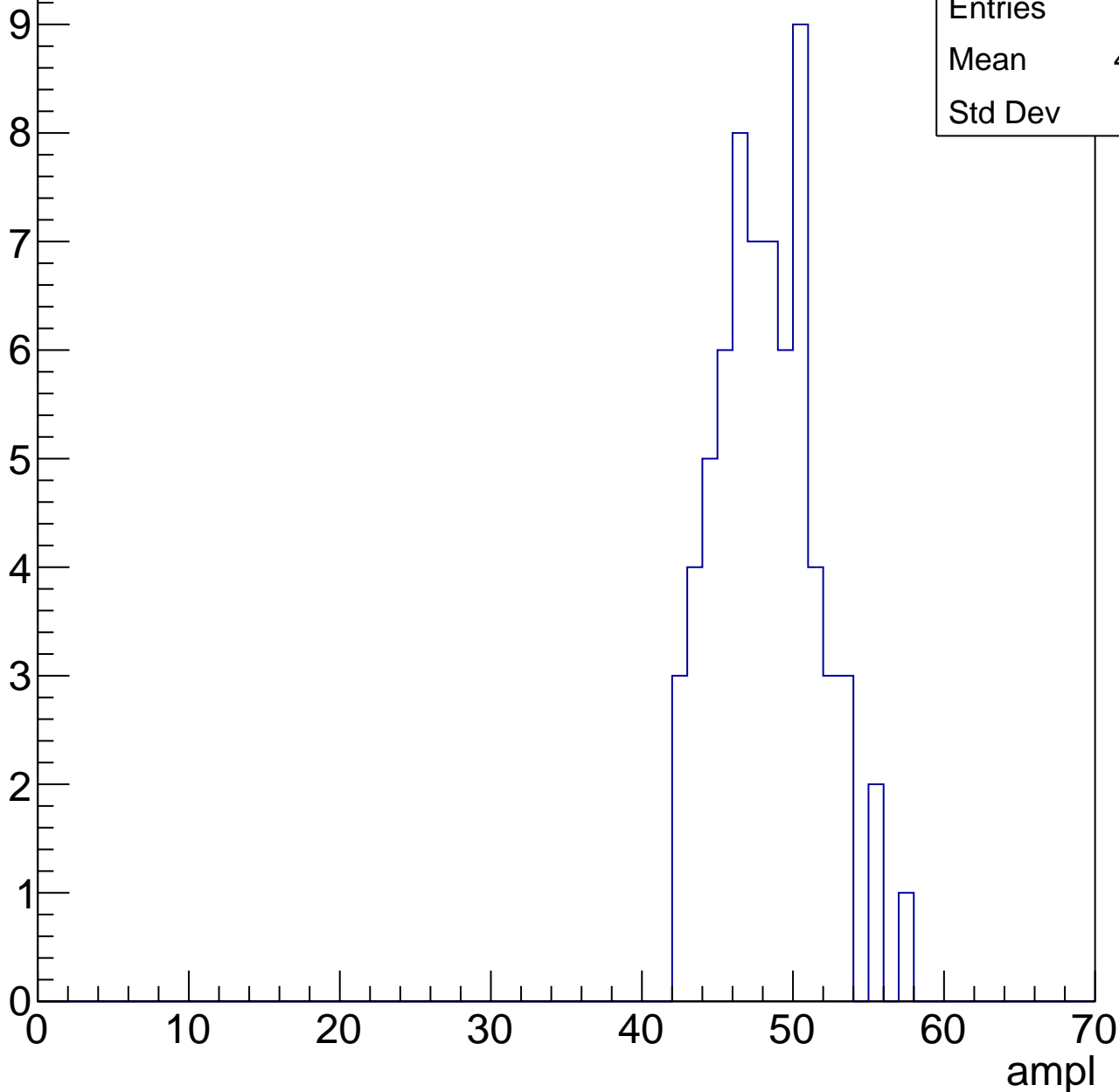


B1L003S, U16-ch86, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	47.81
Std Dev	3.34

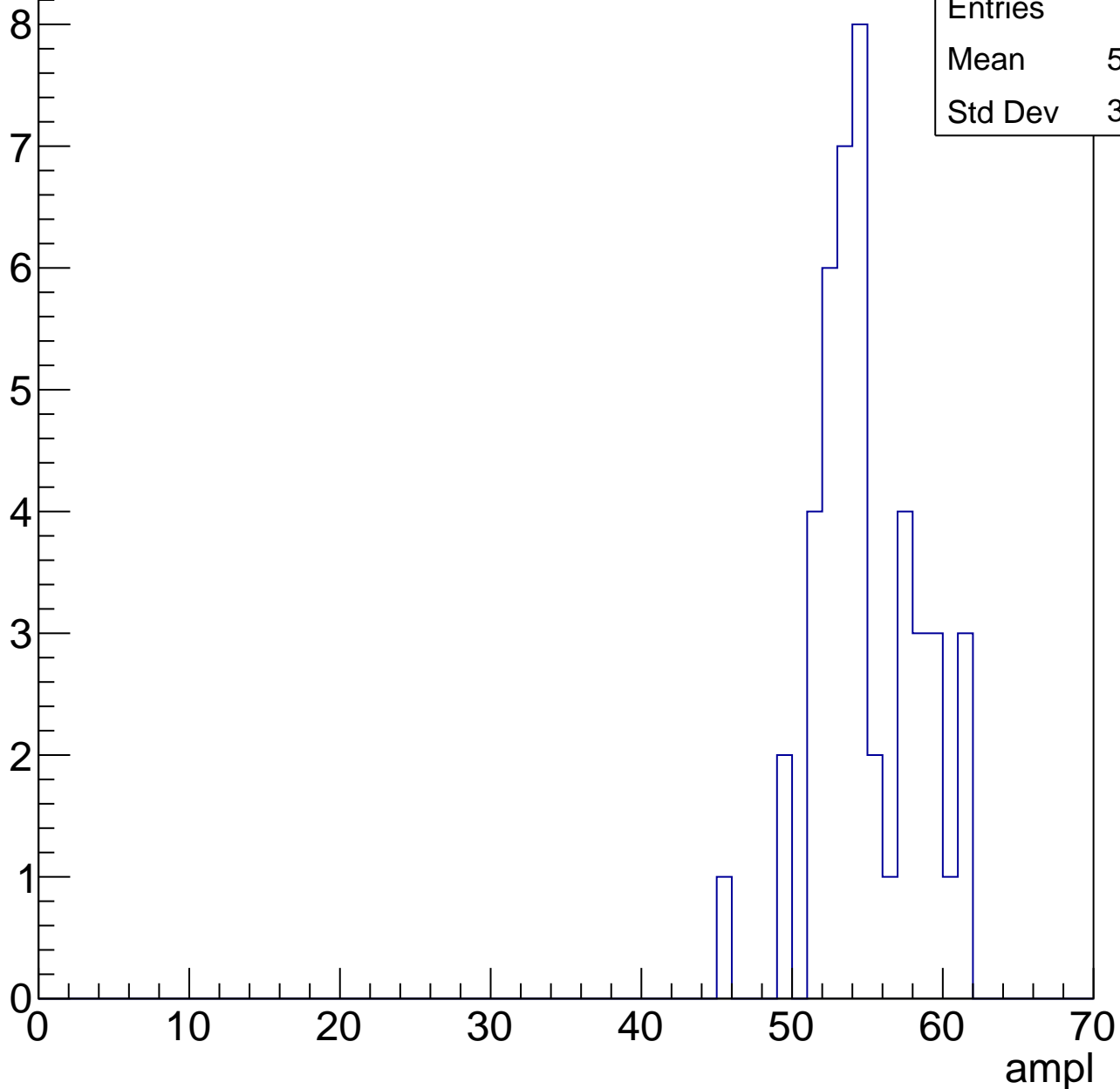


B1L003S, U16-ch86, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

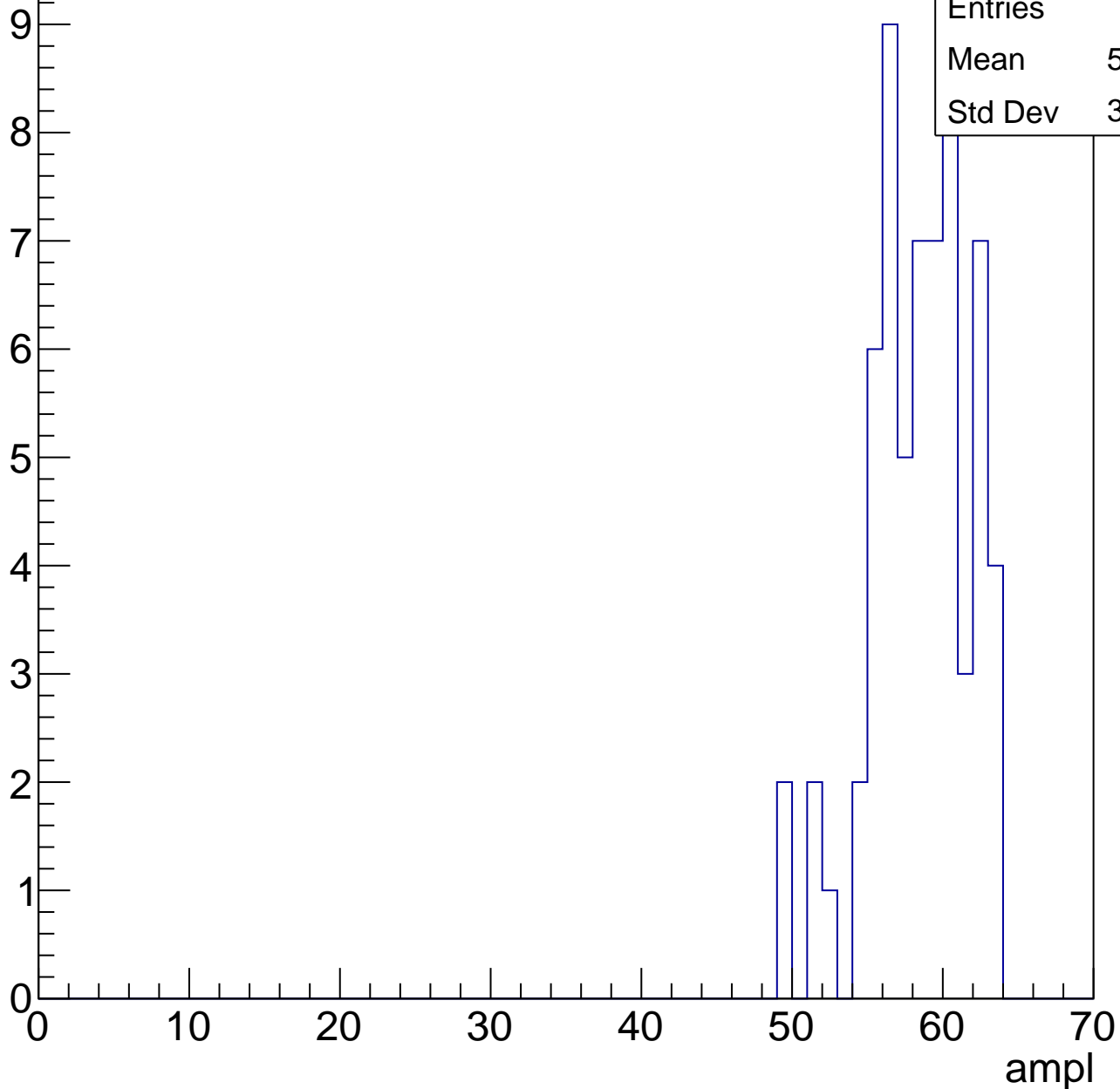
Entries	45
Mean	54.44
Std Dev	3.455



B1L003S, U16-ch86, adc5

calib_packv5_042523_0143.root, FC#13, port D2

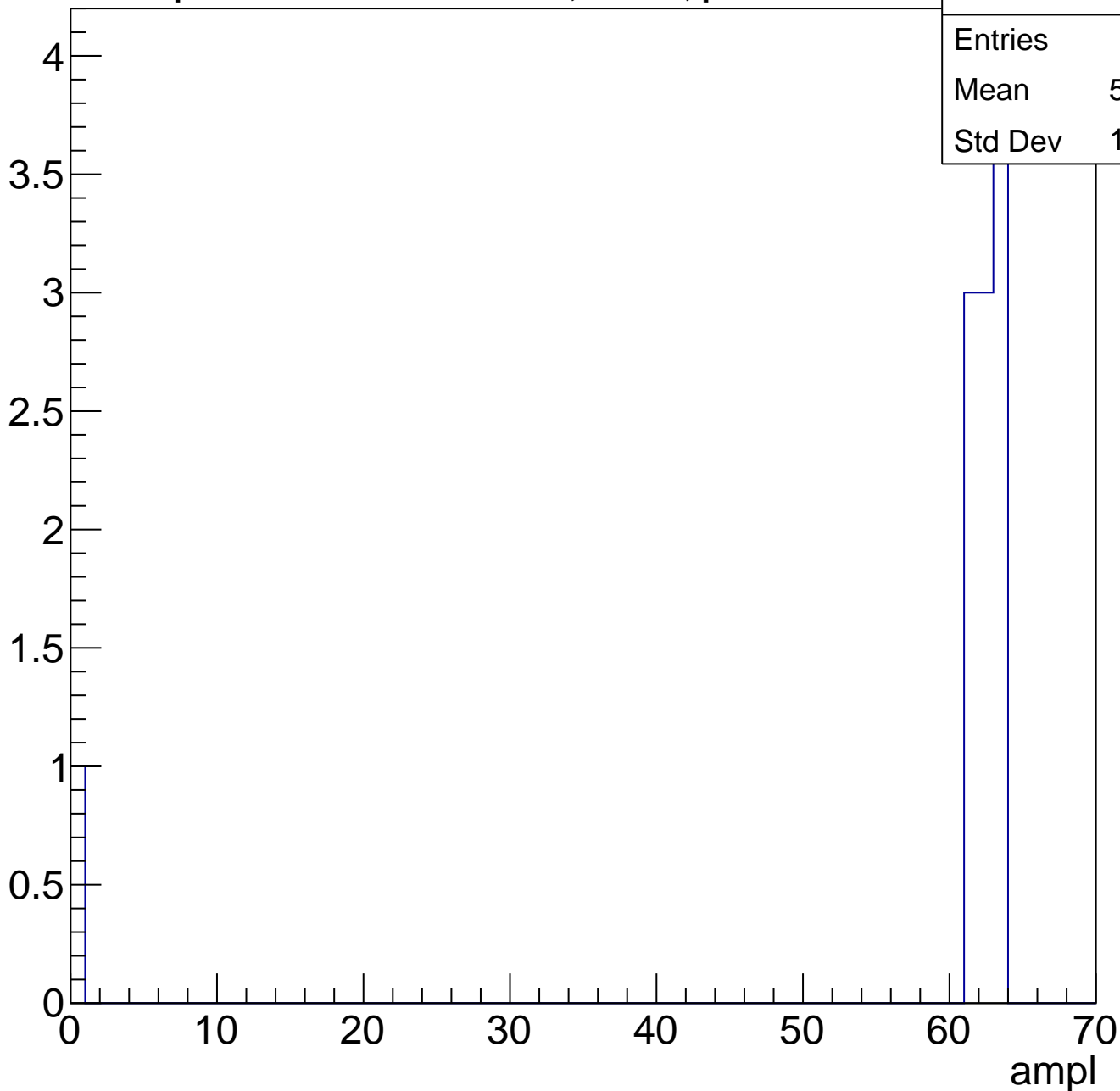
Entry



B1L003S, U16-ch86, adc6

calib_packv5_042523_0143.root, FC#13, port D2

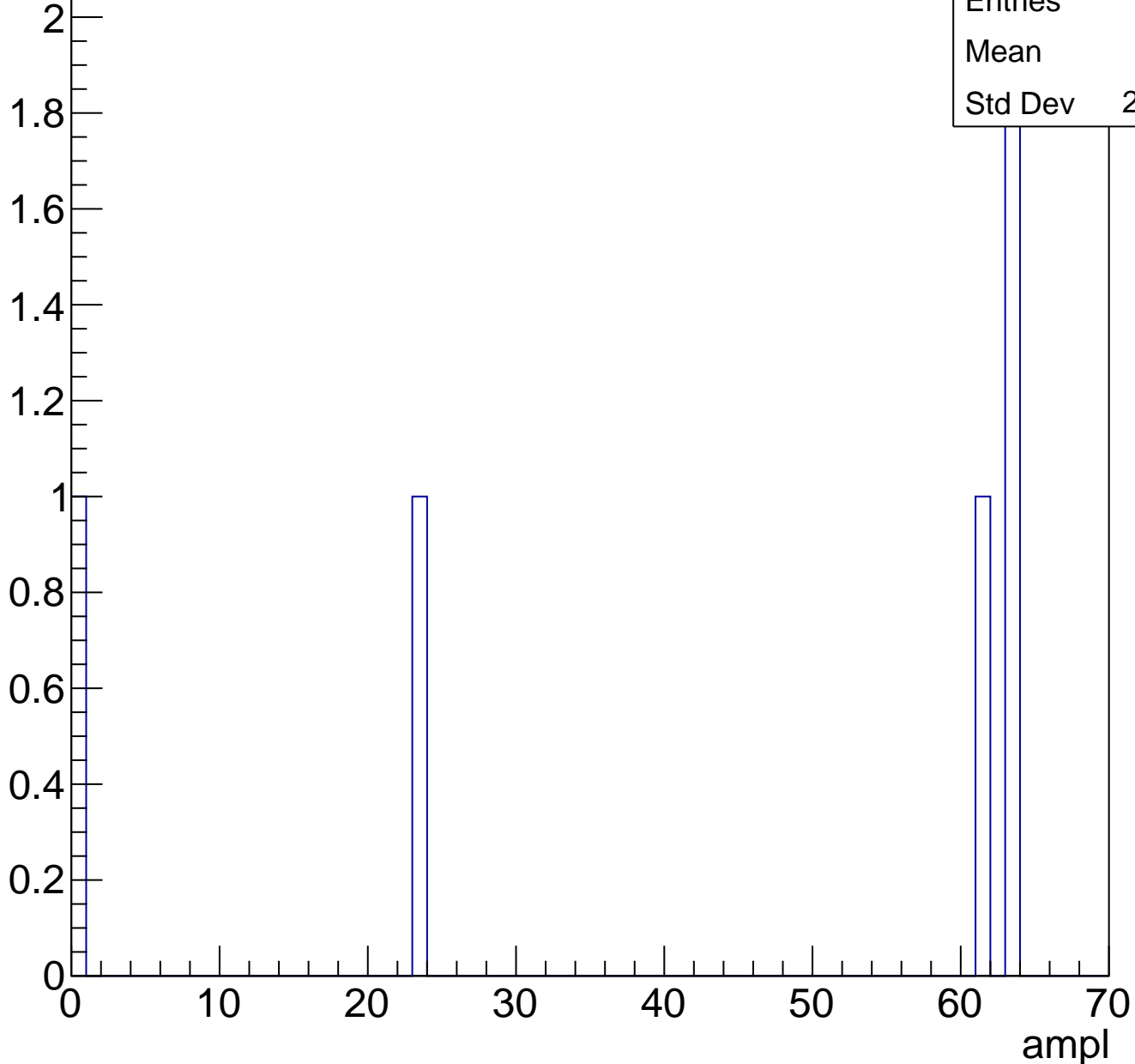
Entry



B1L003S, U16-ch86, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	42
Std Dev	25.95

B1L003S, U16-ch87, adc0

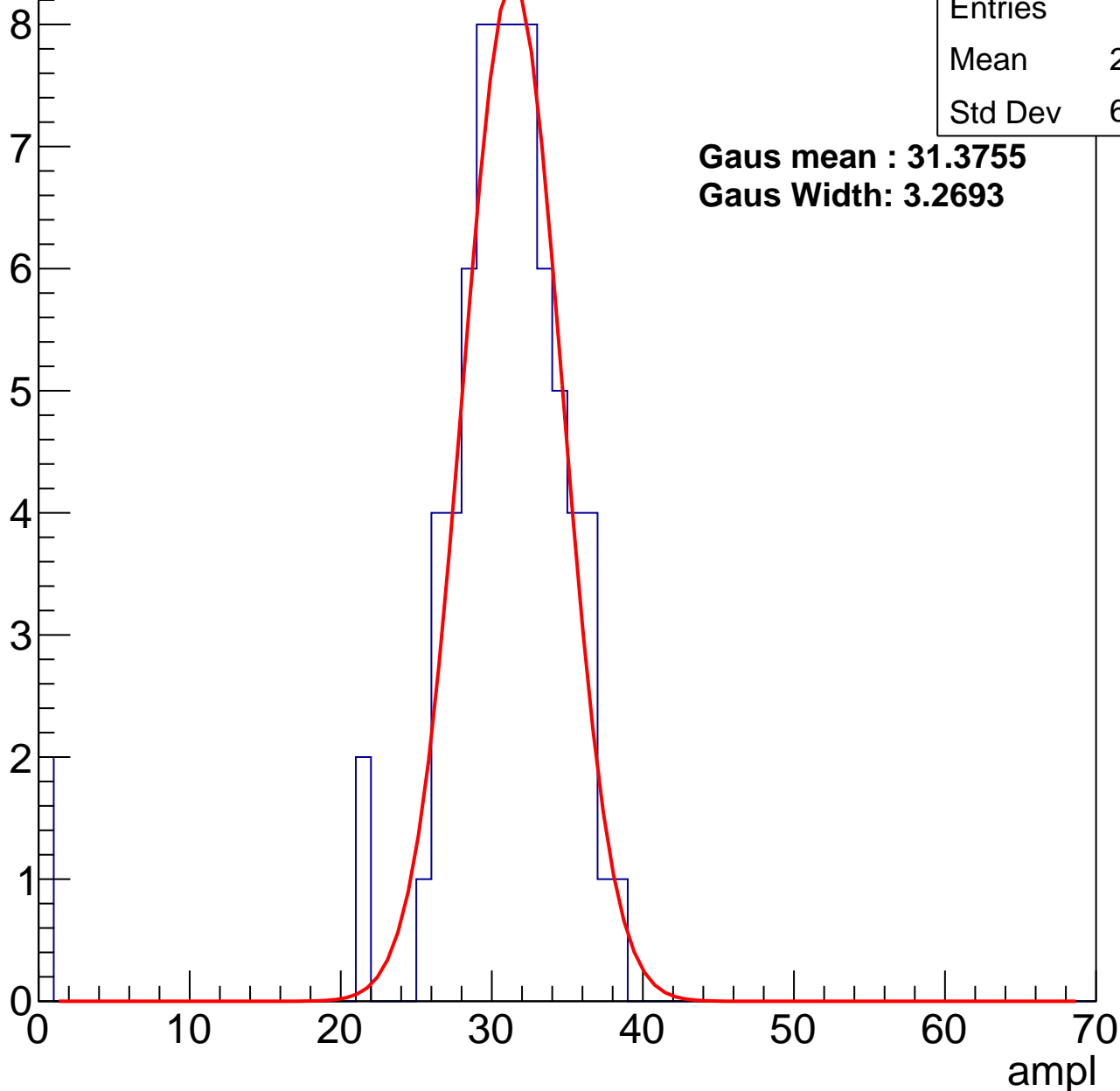
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.86
Std Dev	6.065

Gaus mean : 31.3755

Gaus Width: 3.2693



B1L003S, U16-ch87, adc1

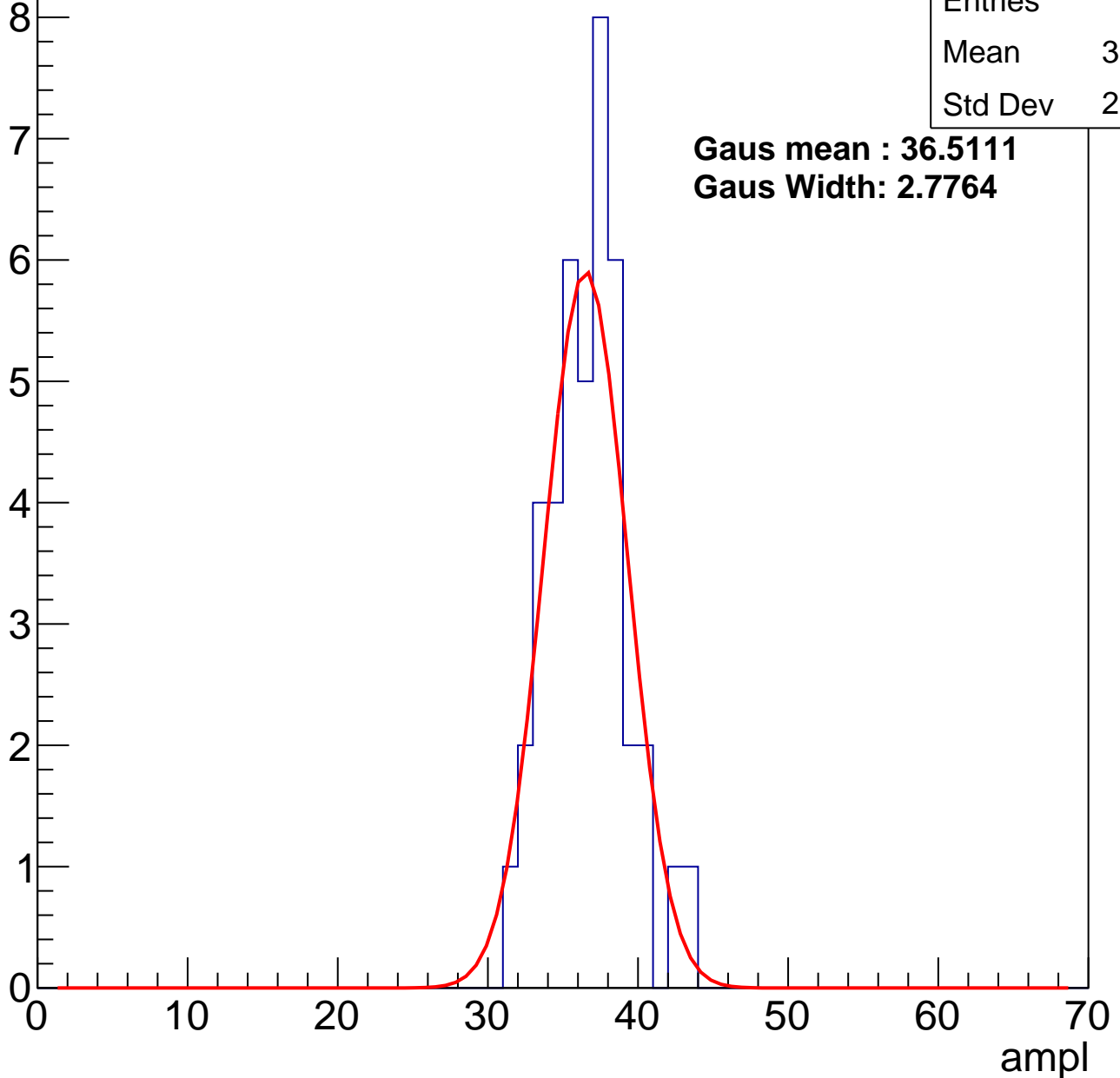
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	36.19
Std Dev	2.584

Gaus mean : 36.5111

Gaus Width: 2.7764



B1L003S, U16-ch87, adc2

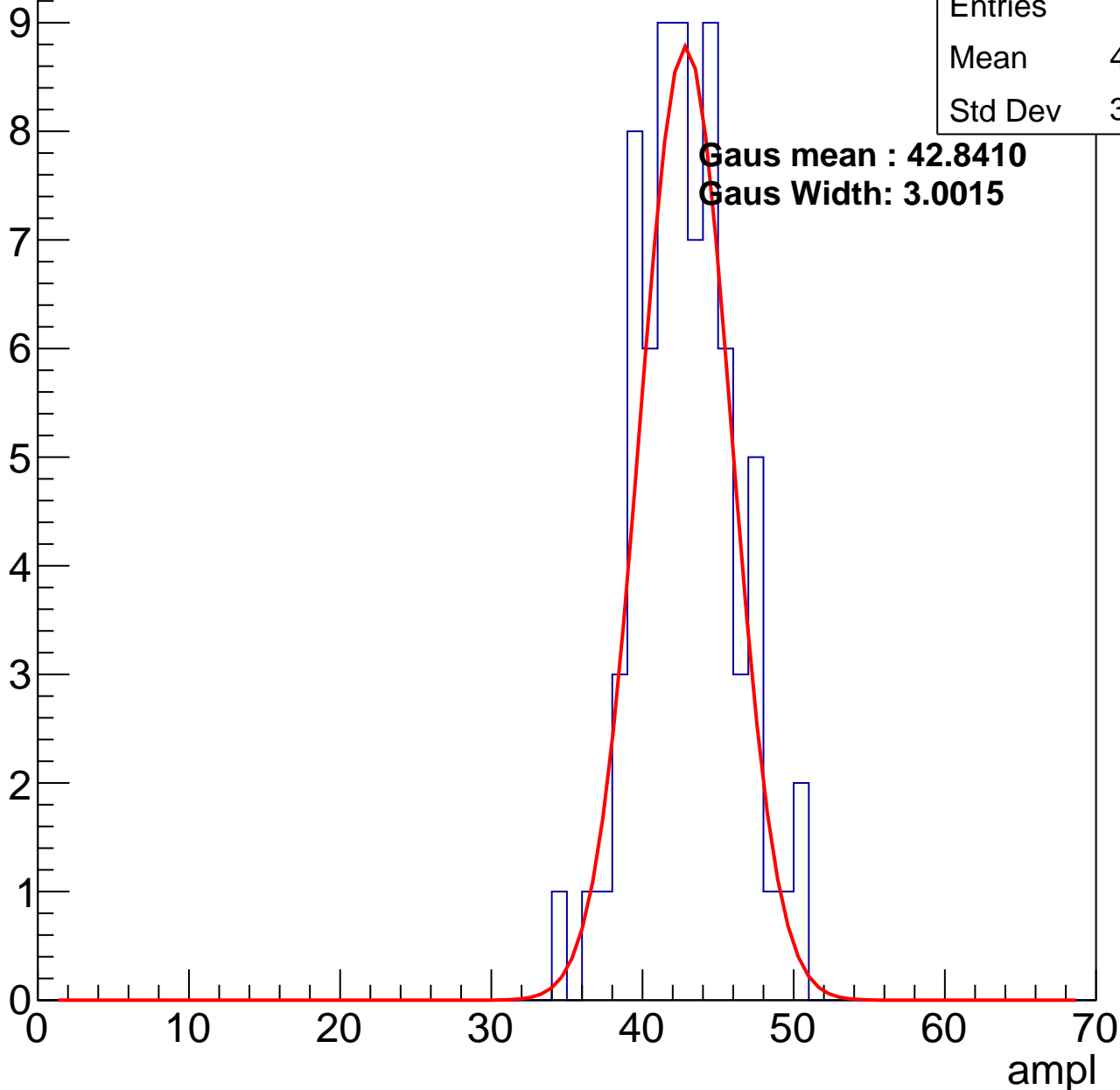
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.46
Std Dev	3.219

Gaus mean : 42.8410

Gaus Width: 3.0015

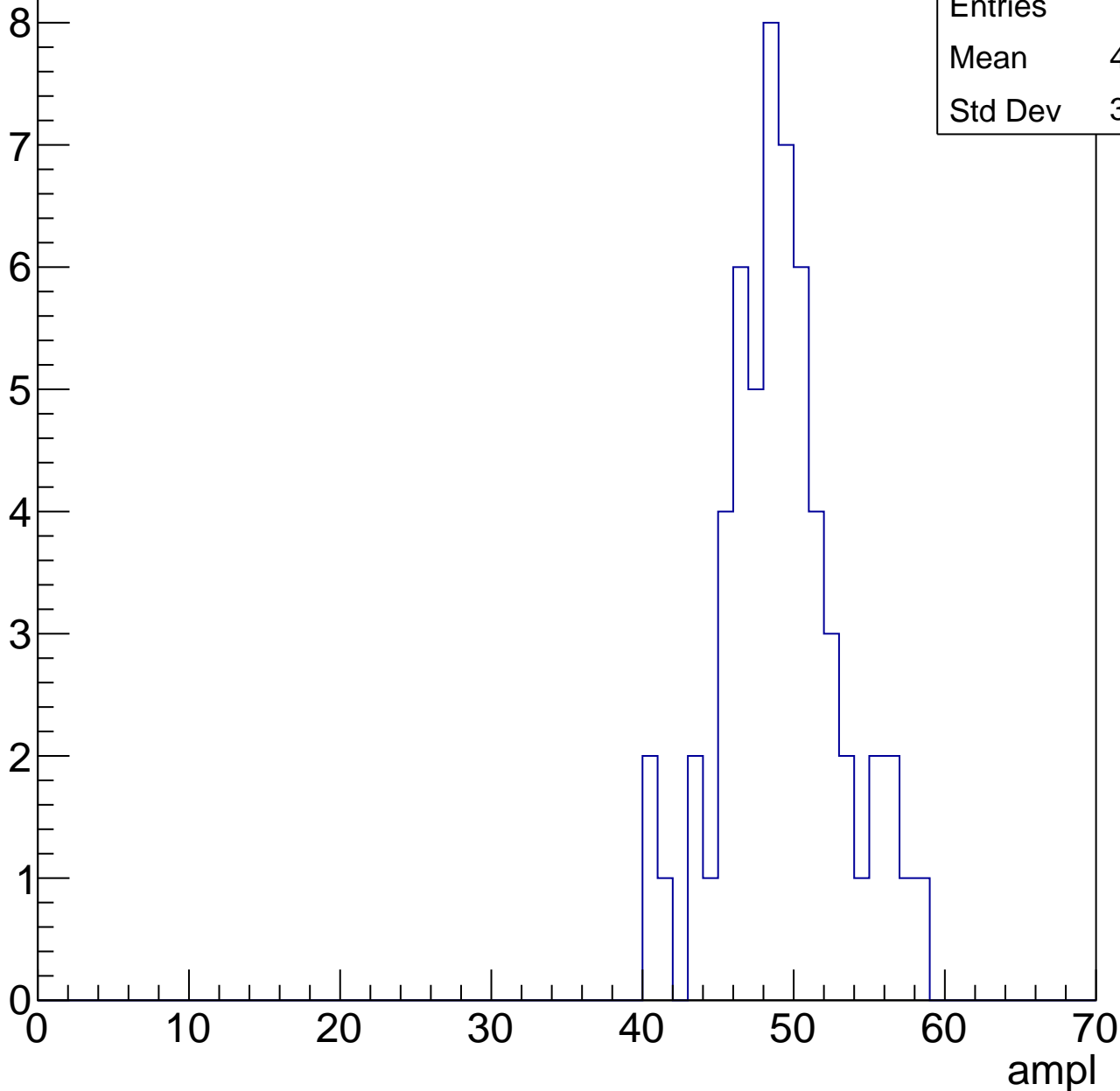


B1L003S, U16-ch87, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

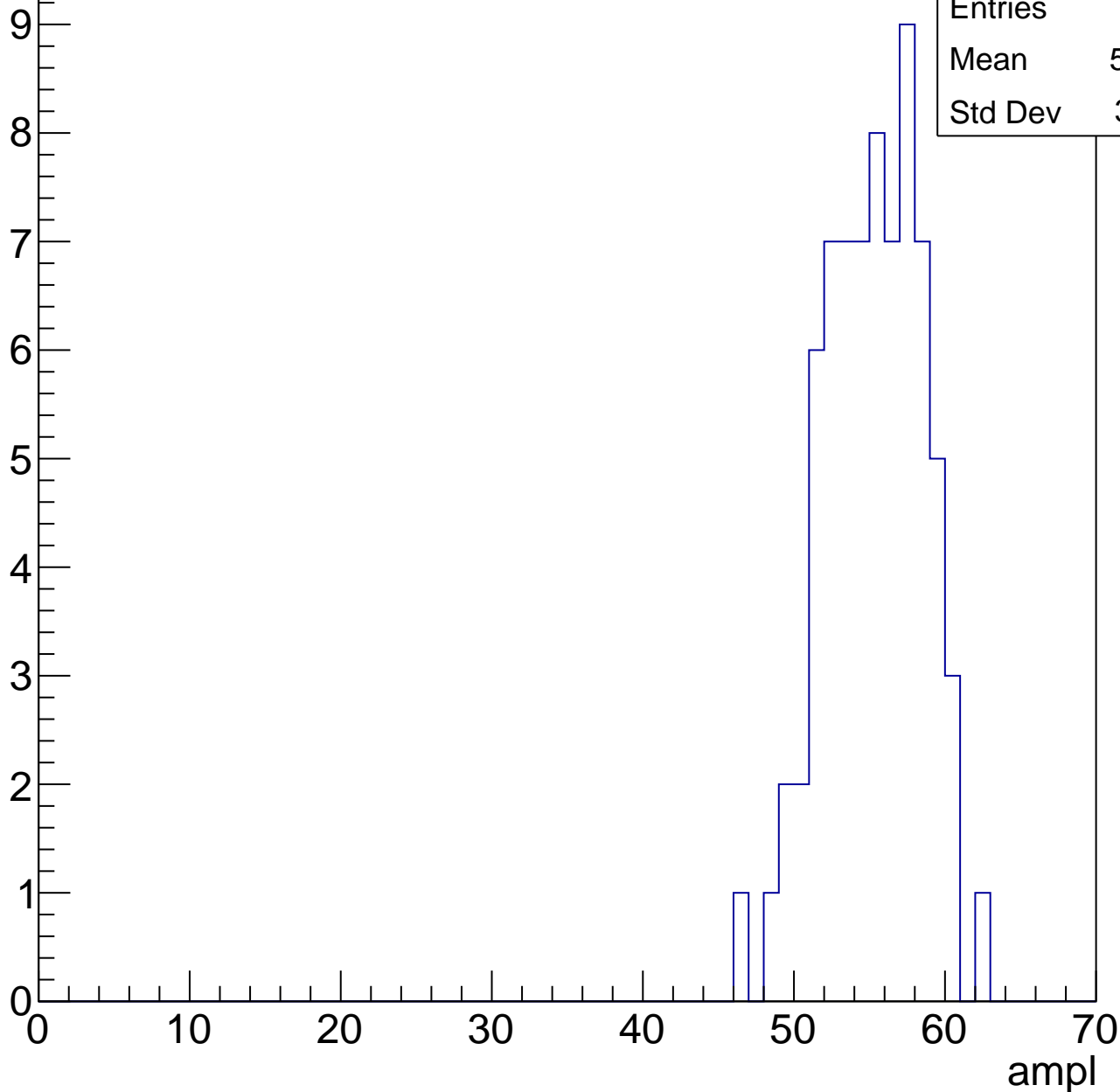
Entries	58
Mean	48.72
Std Dev	3.908



B1L003S, U16-ch87, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

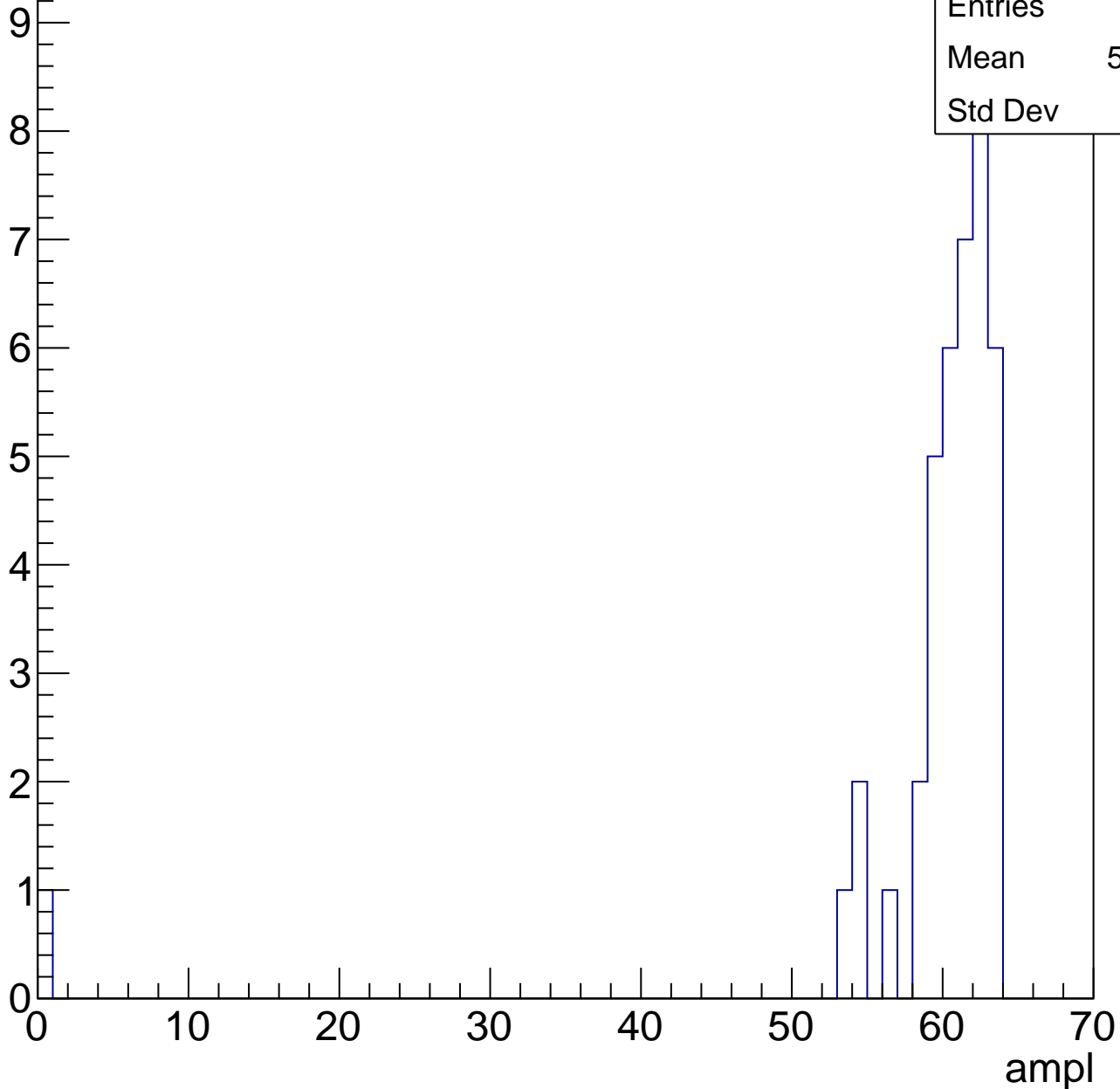


B1L003S, U16-ch87, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

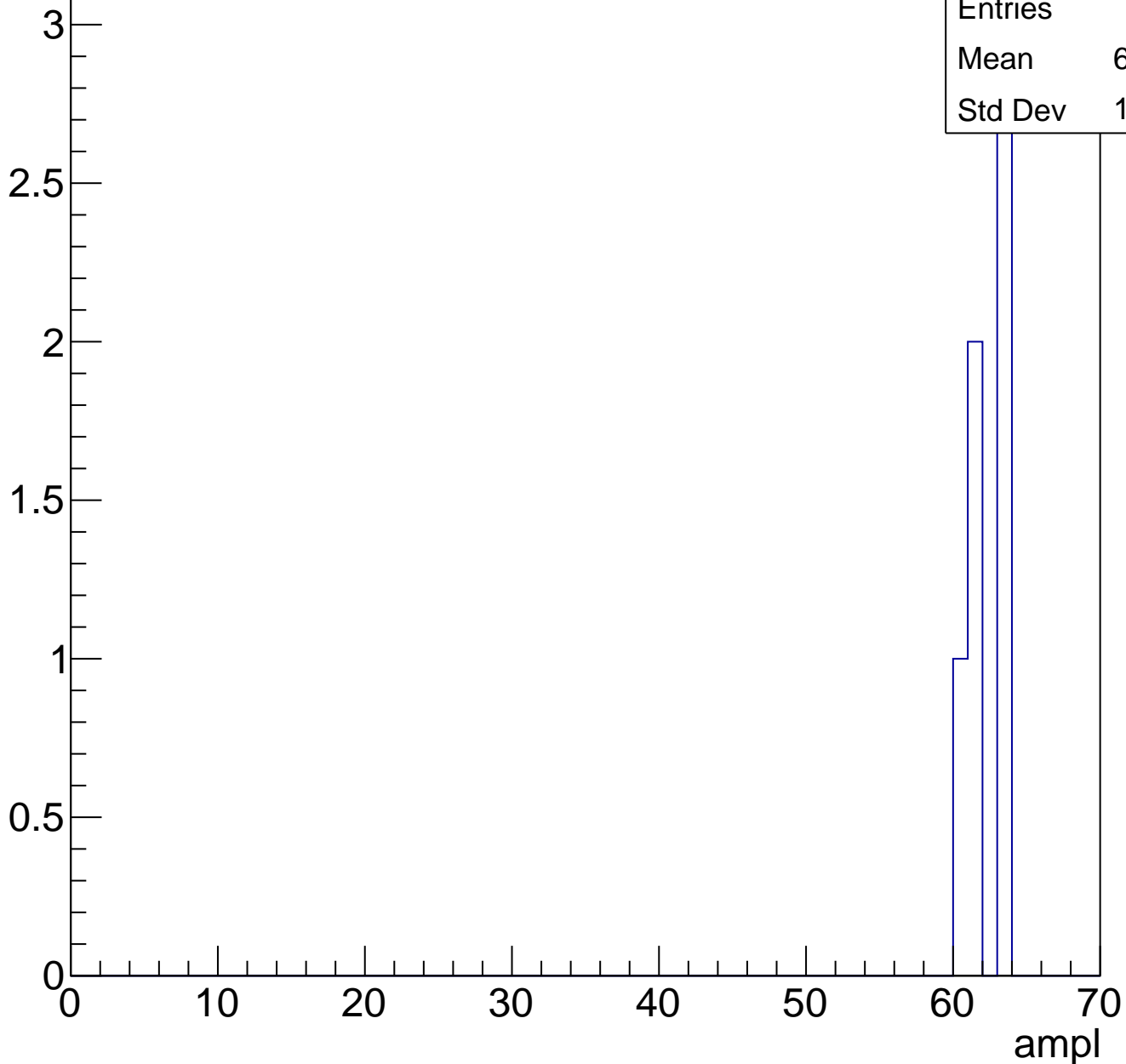
Entries	40
Mean	58.77
Std Dev	9.73



B1L003S, U16-ch87, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch87, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch88, adc0

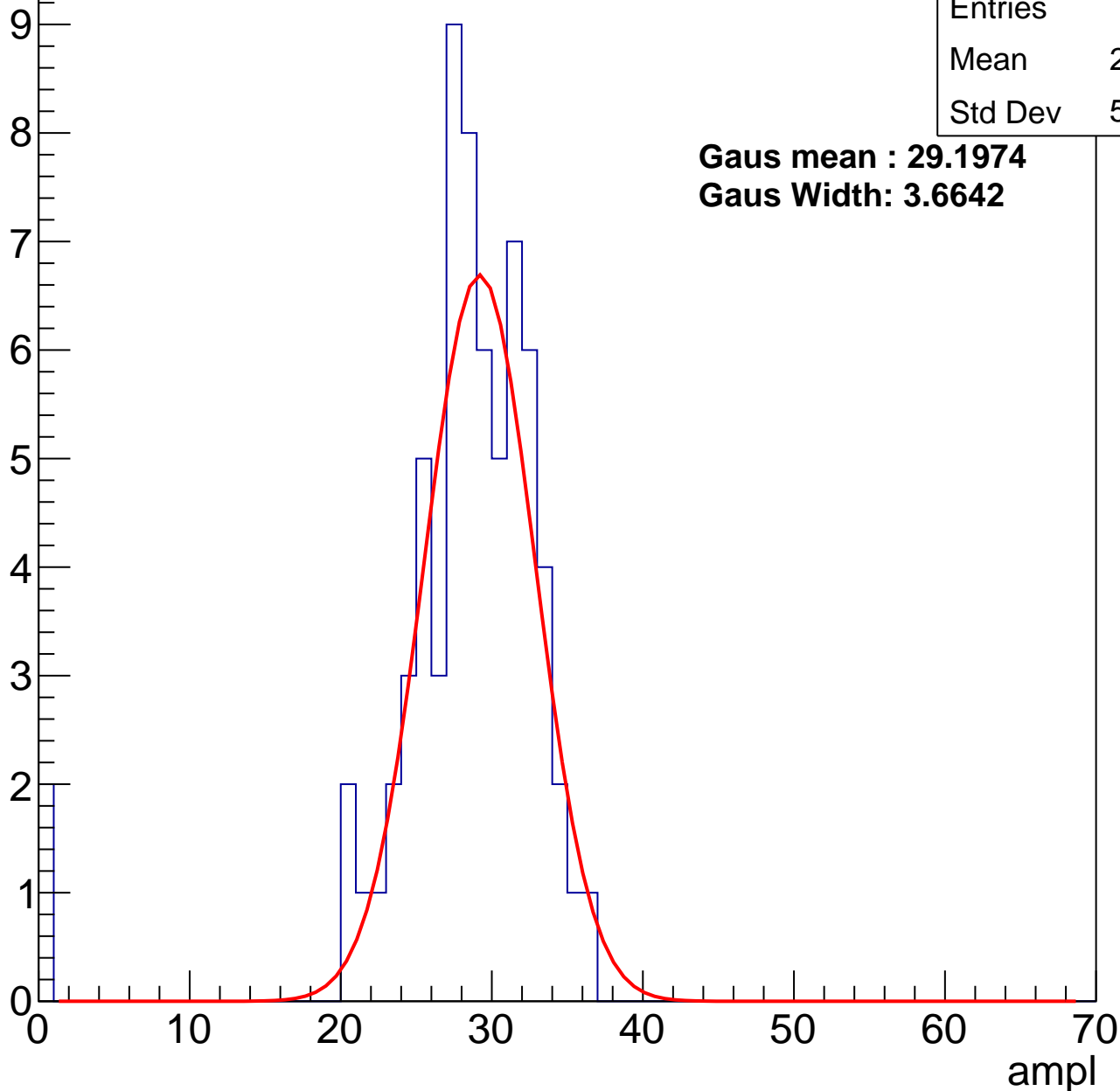
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	27.57
Std Dev	5.944

Gaus mean : 29.174

Gaus Width: 3.6642



B1L003S, U16-ch88, adc1

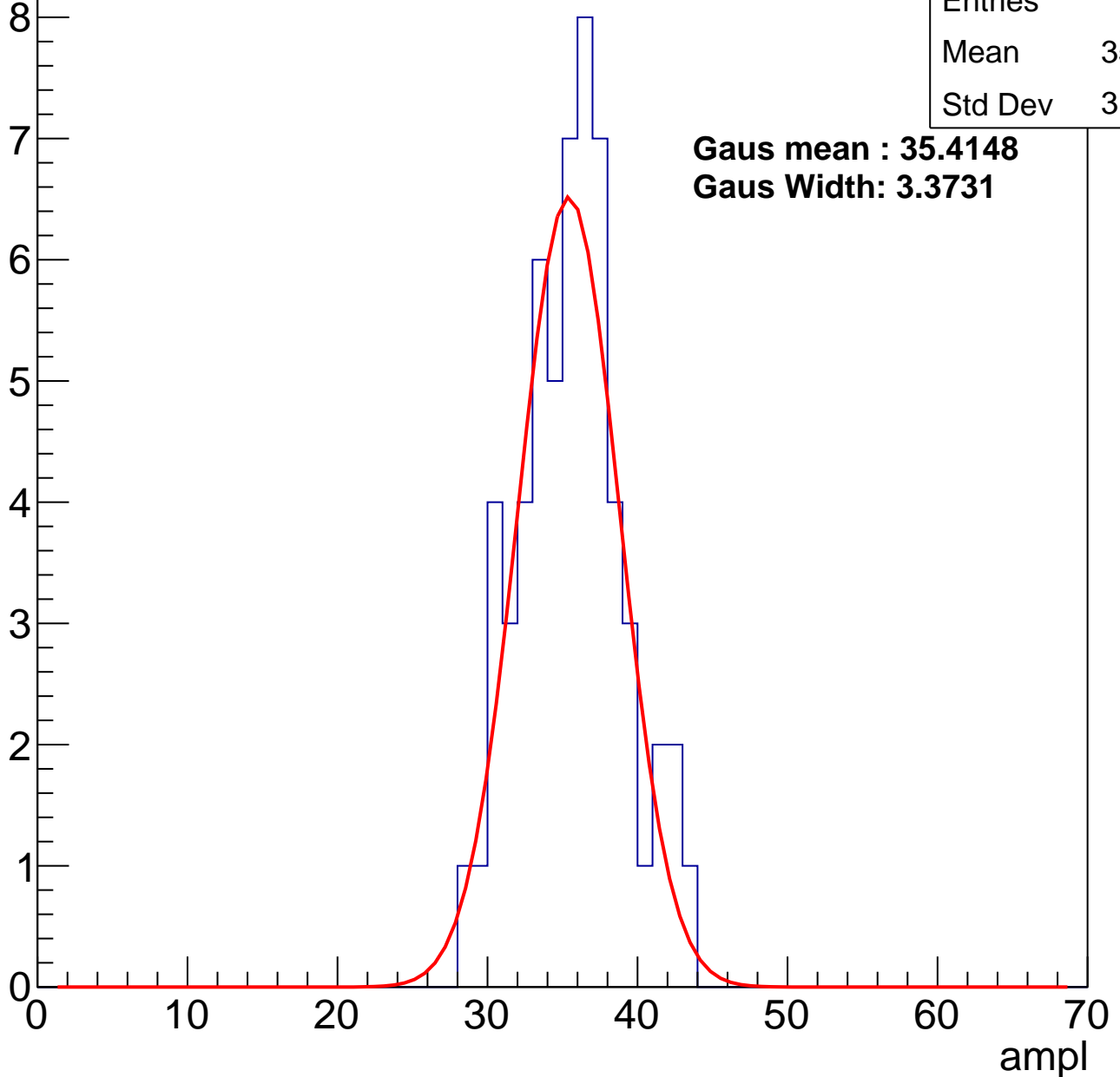
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.19
Std Dev	3.382

Gaus mean : 35.4148

Gaus Width: 3.3731



B1L003S, U16-ch88, adc2

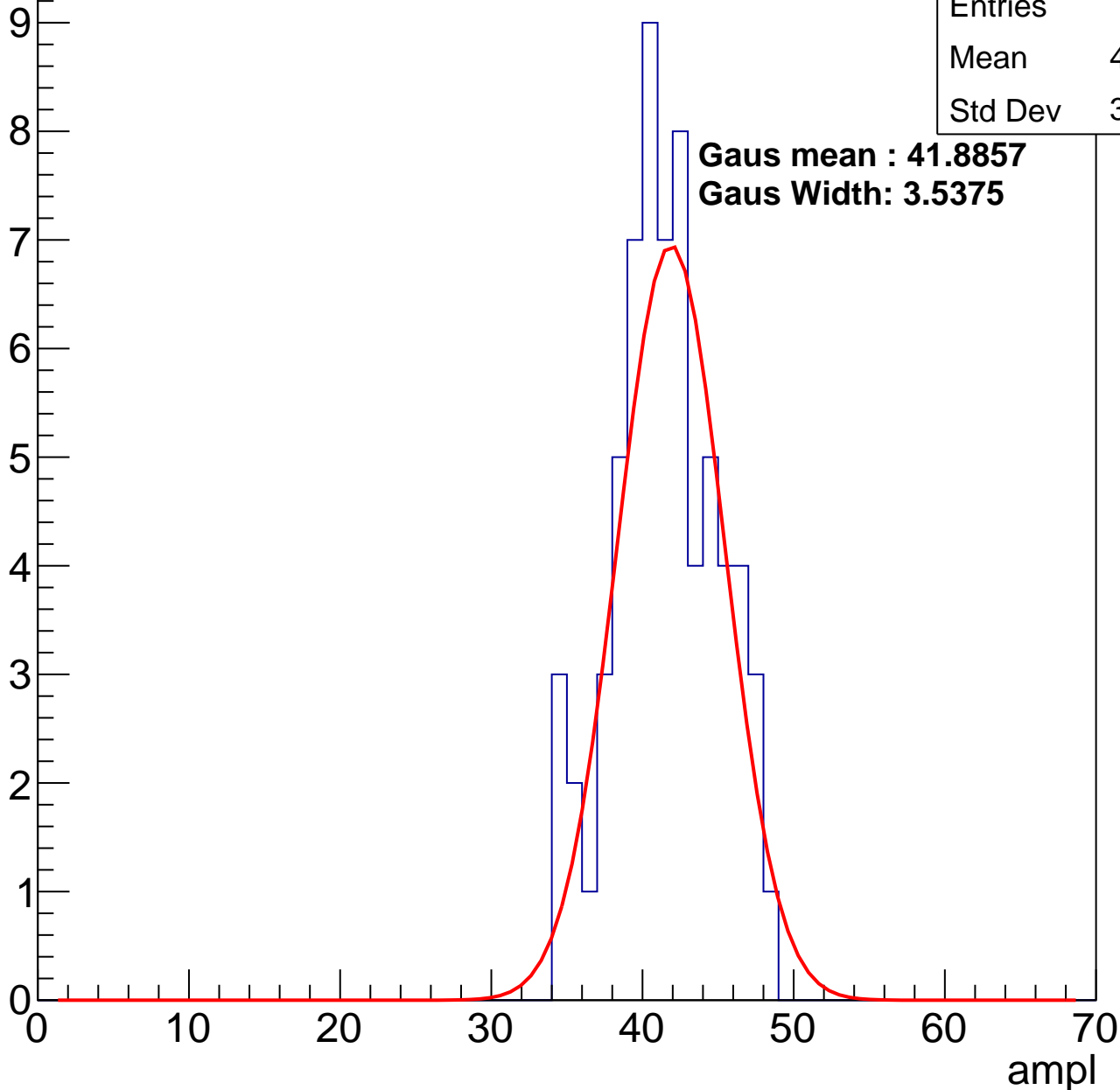
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.06
Std Dev	3.402

Gaus mean : 41.8857

Gaus Width: 3.5375

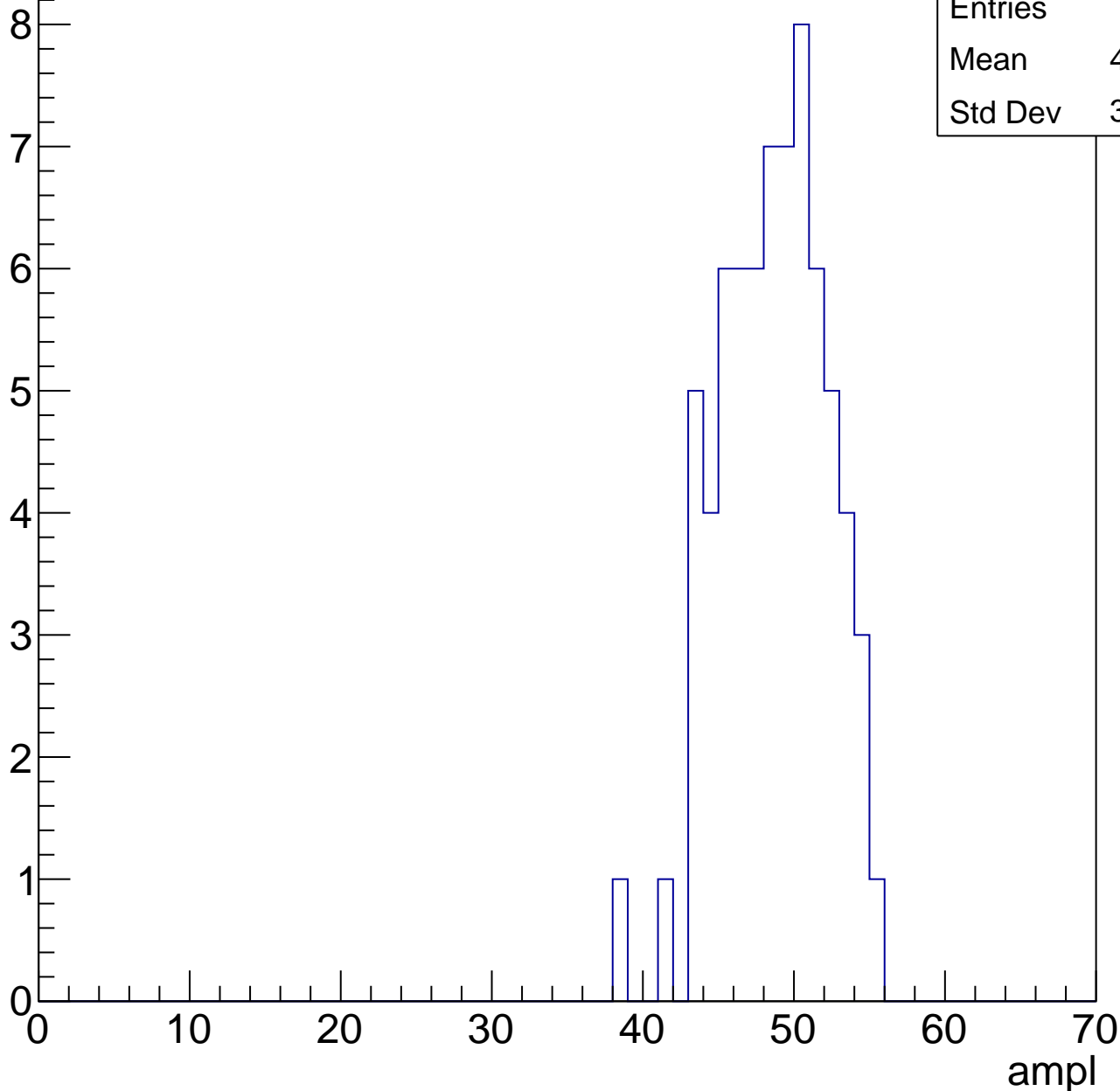


B1L003S, U16-ch88, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	48.17
Std Dev	3.485

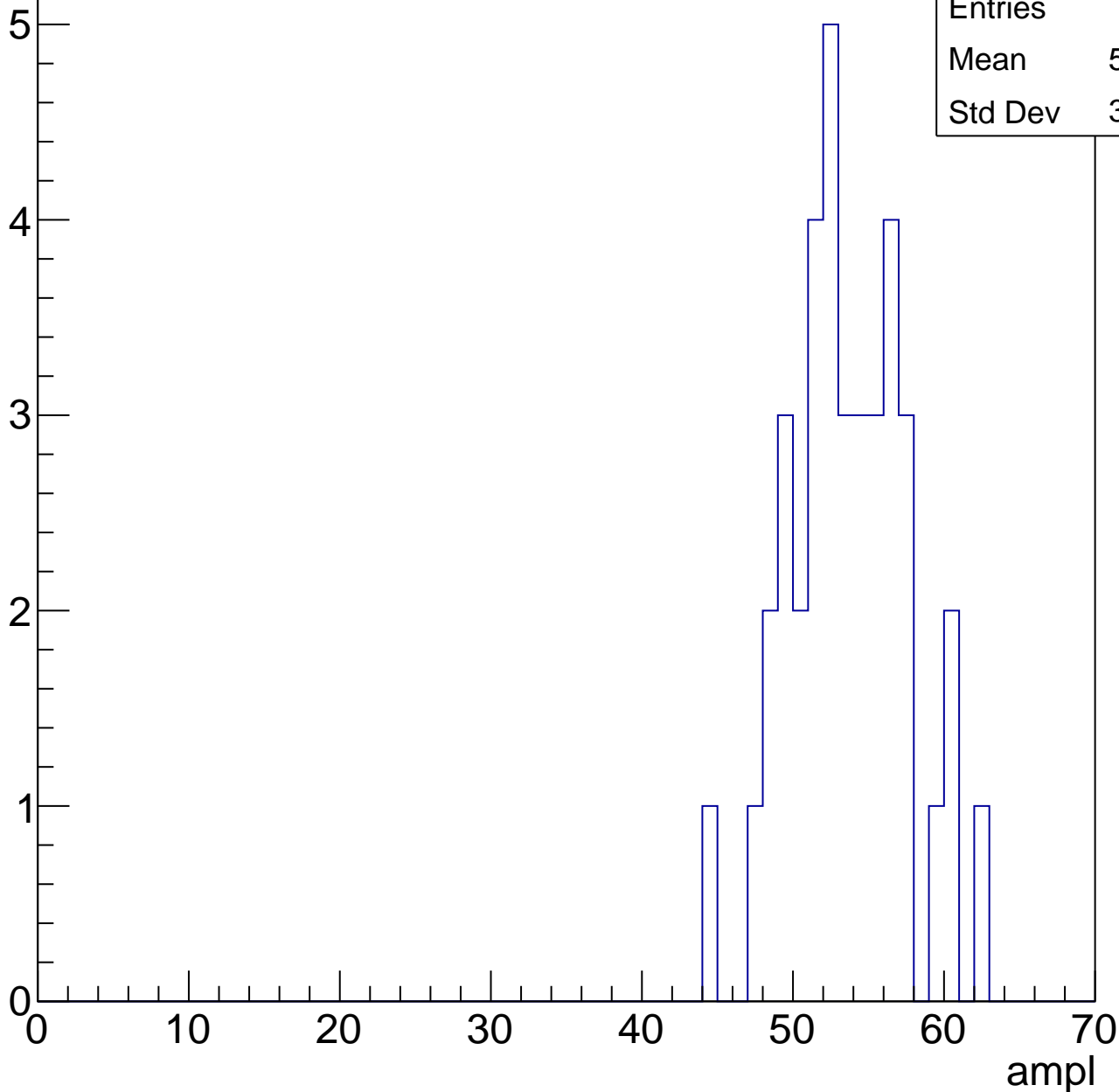


B1L003S, U16-ch88, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	53.16
Std Dev	3.863



B1L003S, U16-ch88, adc5

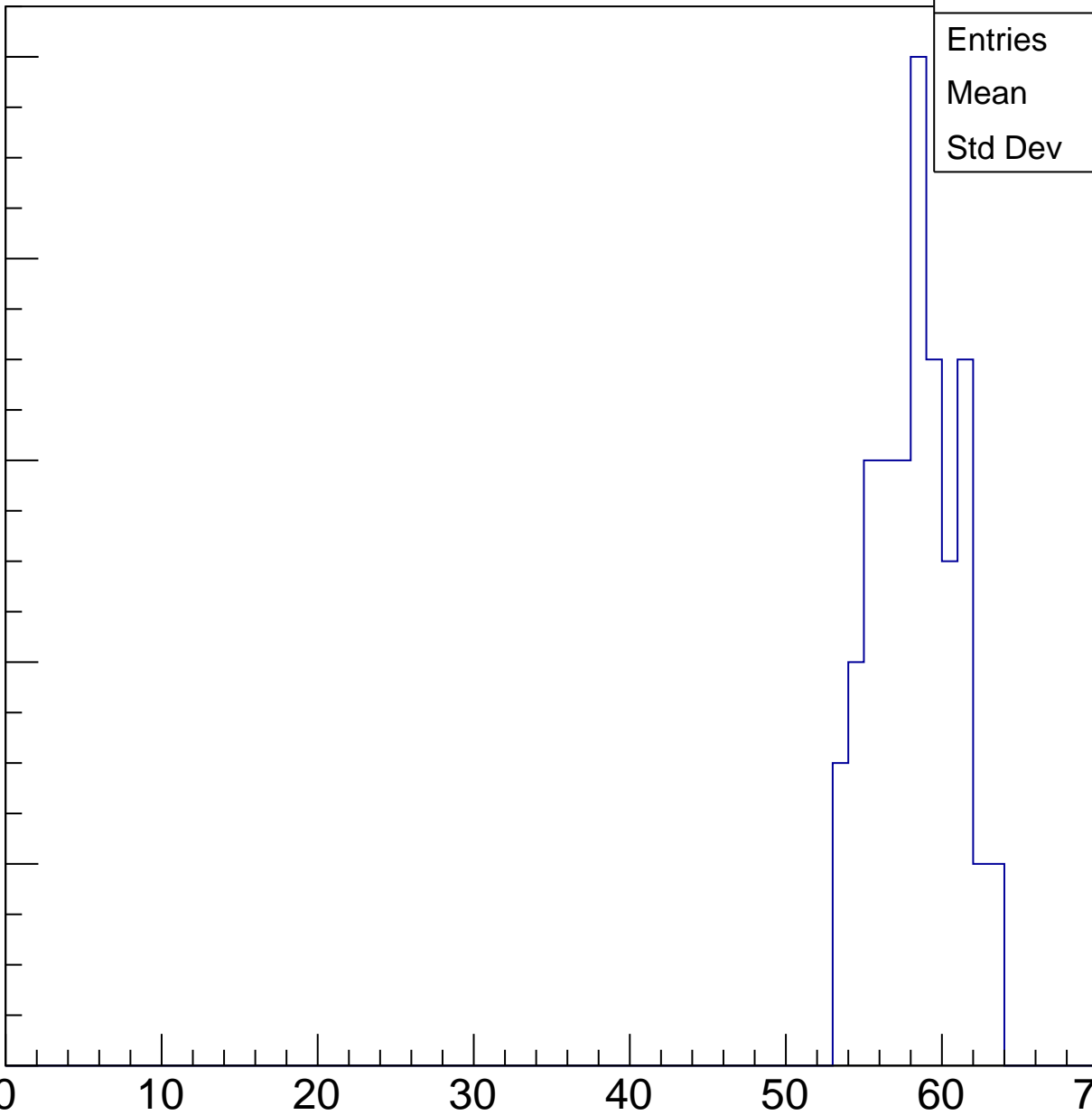
calib_packv5_042523_0143.root, FC#13, port D2

Entry

10
8
6
4
2
0

Entries	58
Mean	57.81
Std Dev	2.603

ampl

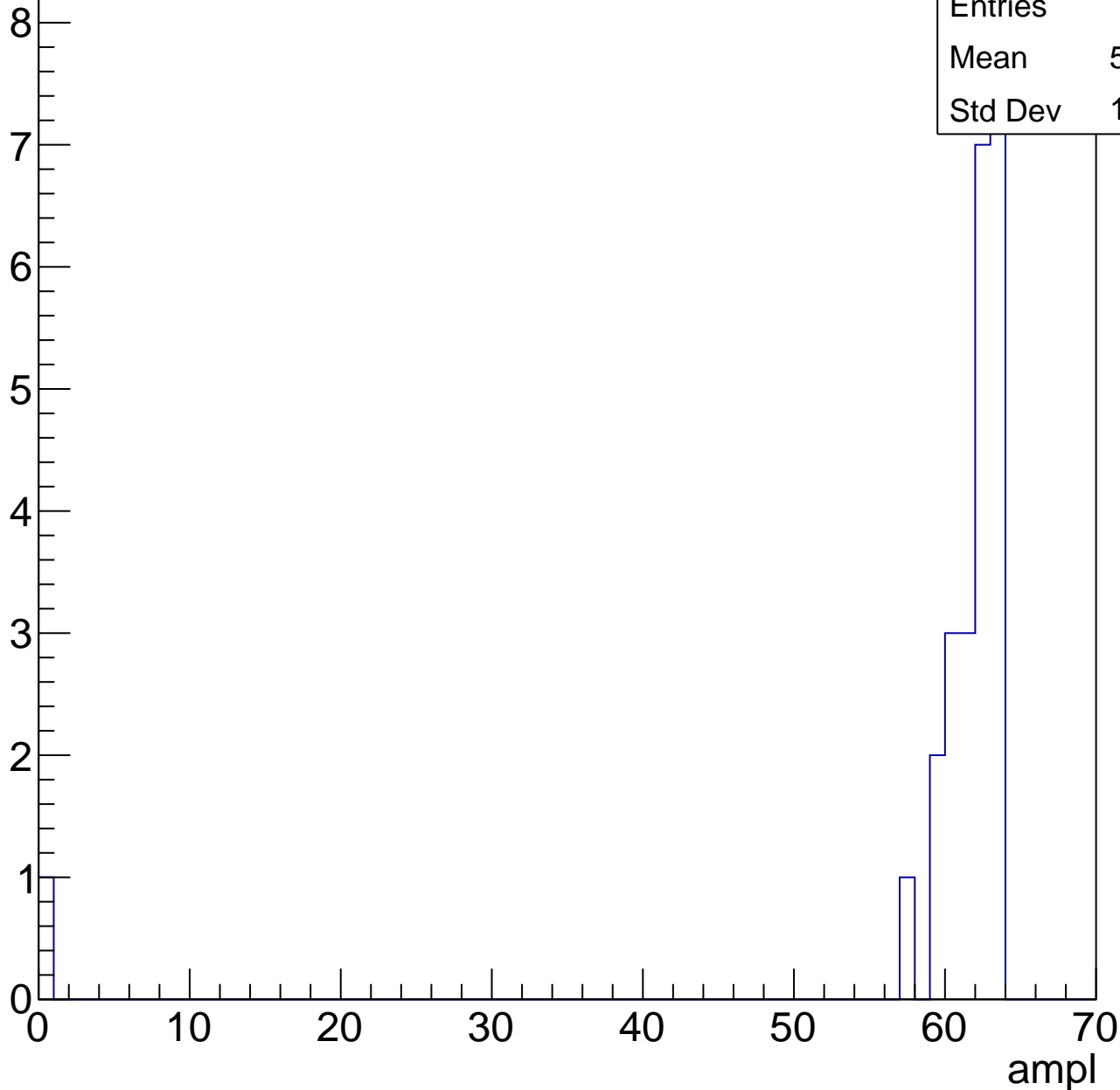


B1L003S, U16-ch88, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	25
Mean	59.04
Std Dev	12.15



B1L003S, U16-ch88, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U16-ch89, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	28.64
Std Dev	5.247

Gaus mean : 29.7815

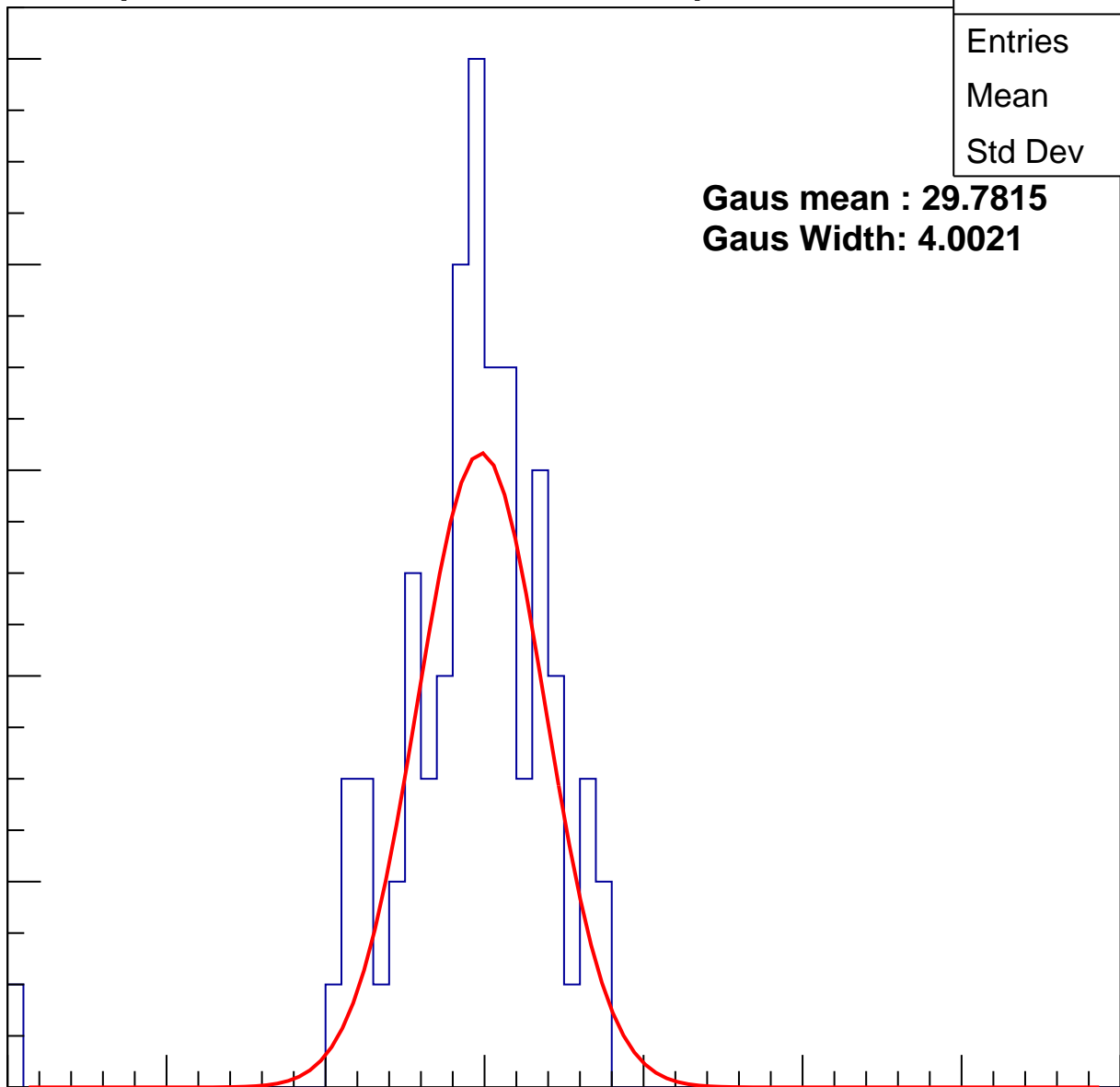
Gaus Width: 4.0021

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch89, adc1

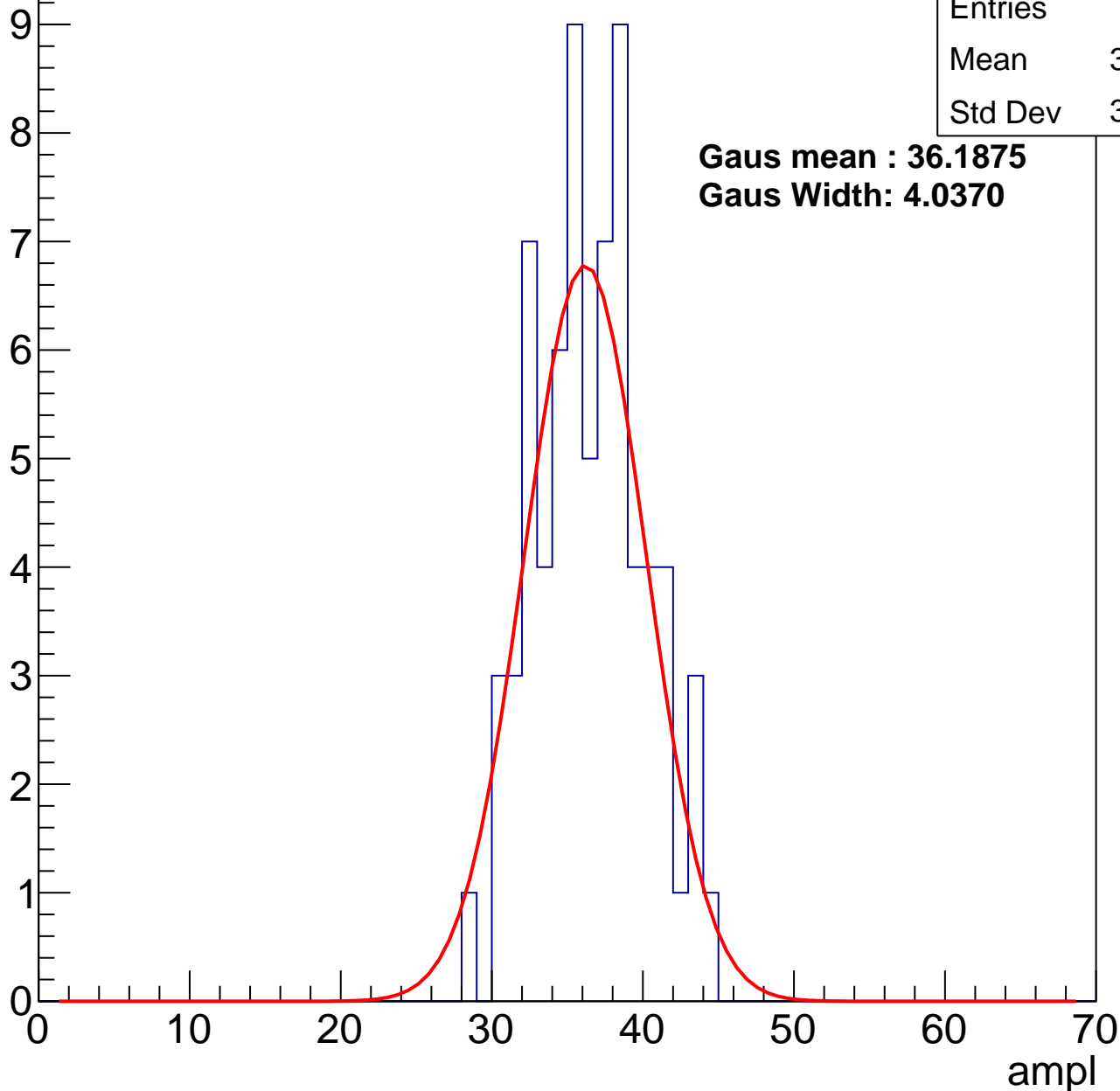
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.08
Std Dev	3.595

Gaus mean : 36.1875

Gaus Width: 4.0370



B1L003S, U16-ch89, adc2

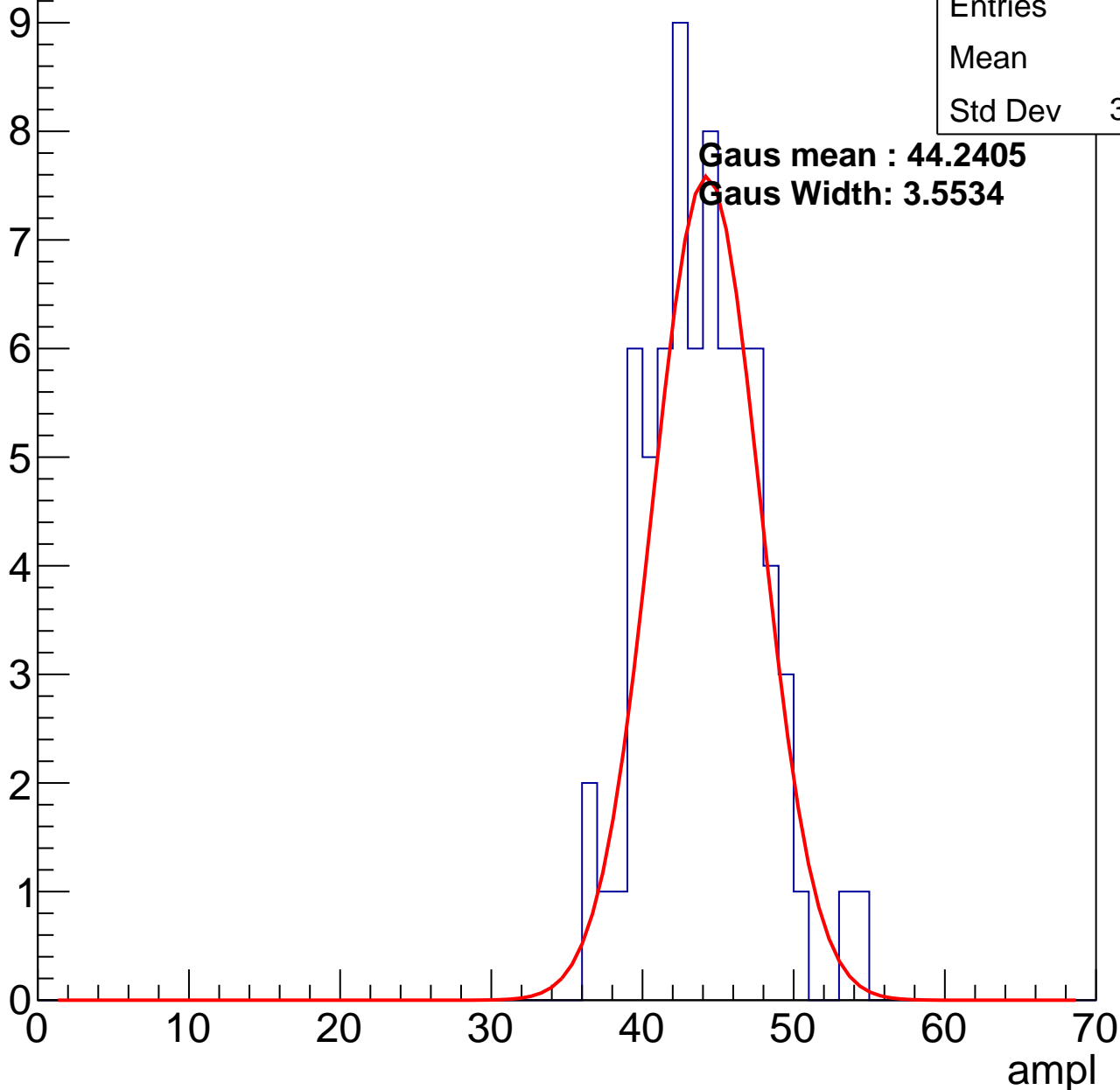
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.6
Std Dev	3.665

Gaus mean : 44.2405

Gaus Width: 3.5534

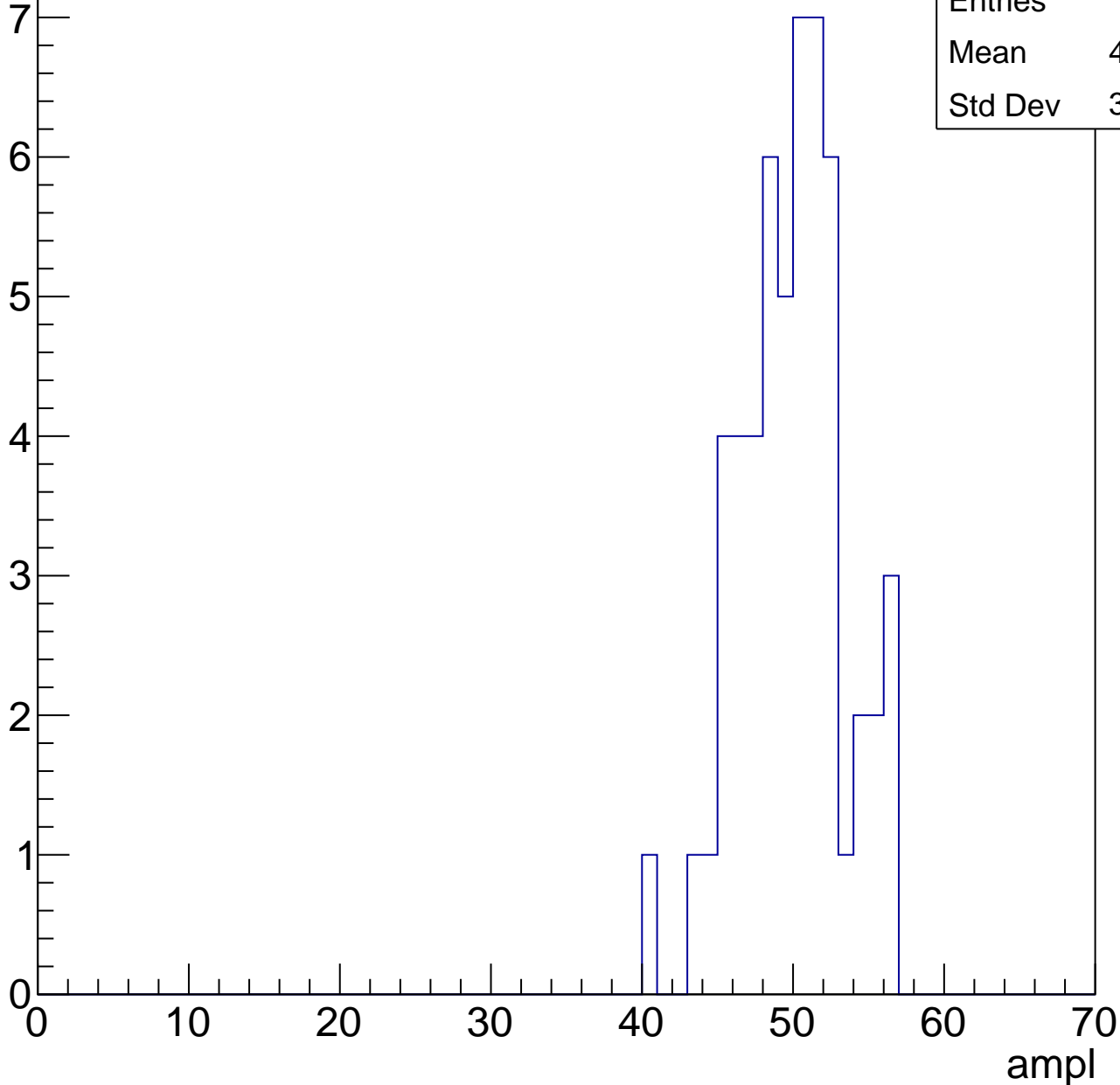


B1L003S, U16-ch89, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	49.44
Std Dev	3.408

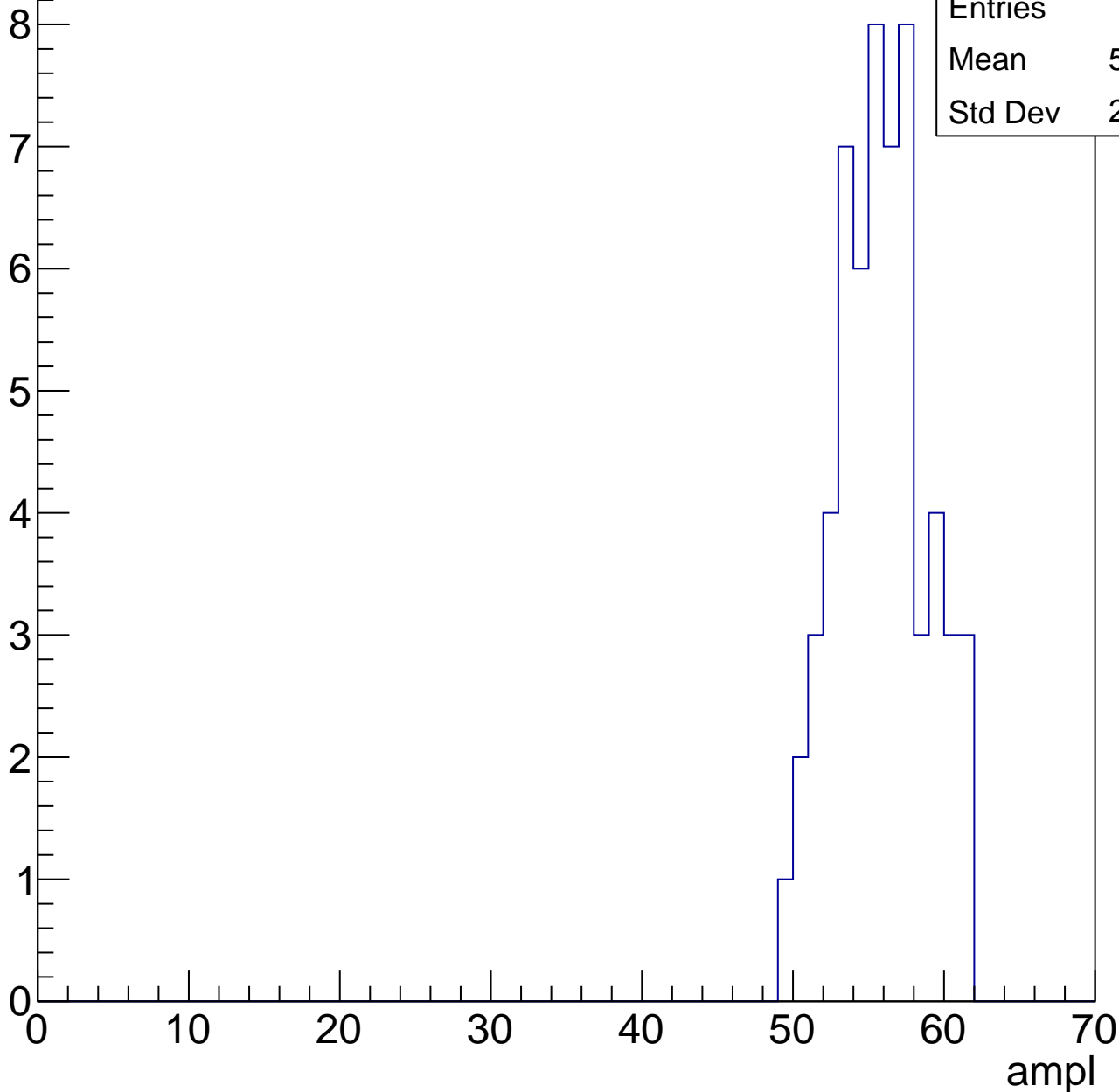


B1L003S, U16-ch89, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.36
Std Dev	2.939

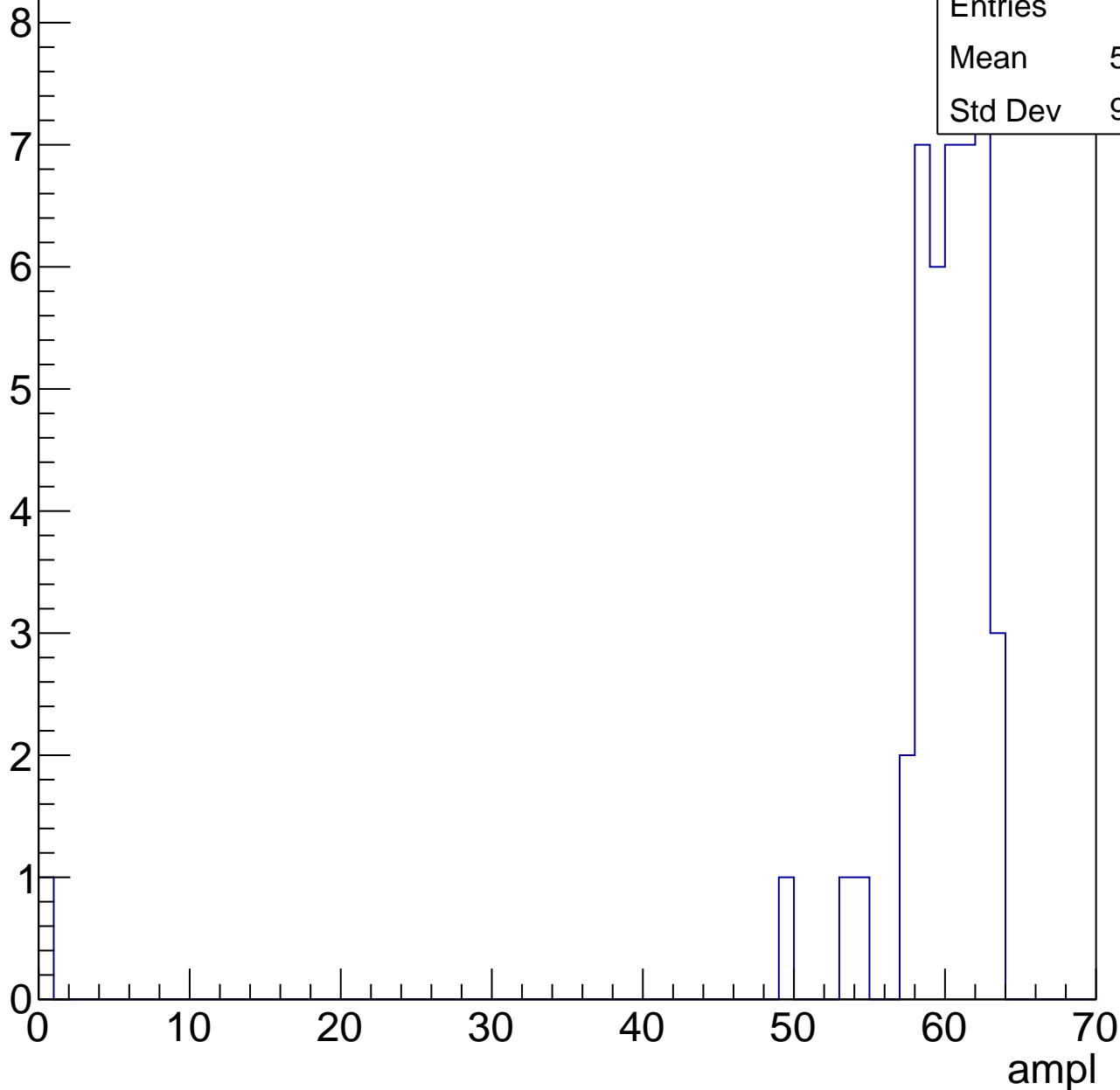


B1L003S, U16-ch89, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	58.23
Std Dev	9.276

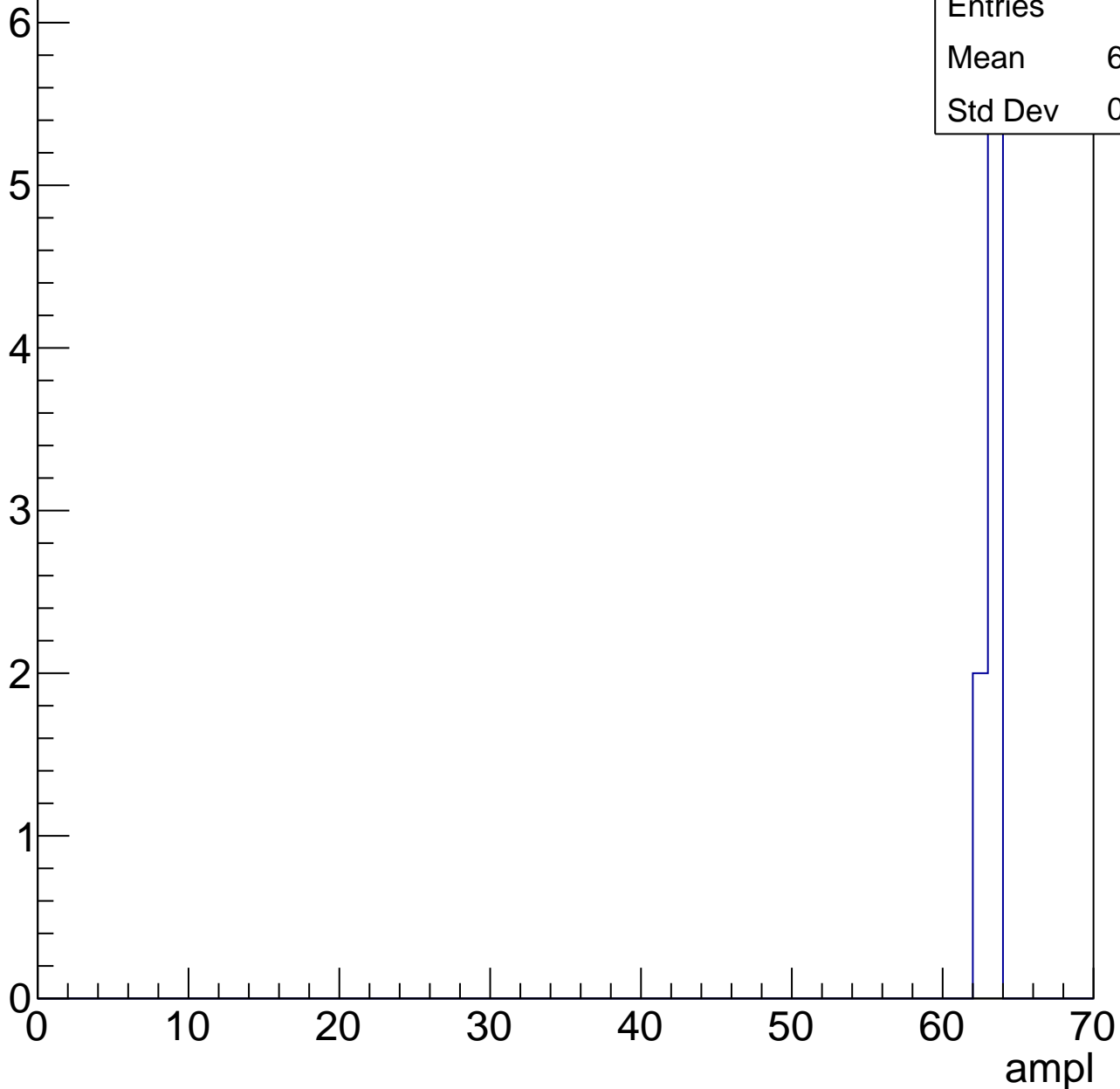


B1L003S, U16-ch89, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B1L003S, U16-ch89, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch90, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	28.08
Std Dev	3.775

Gaus mean : 28.4582

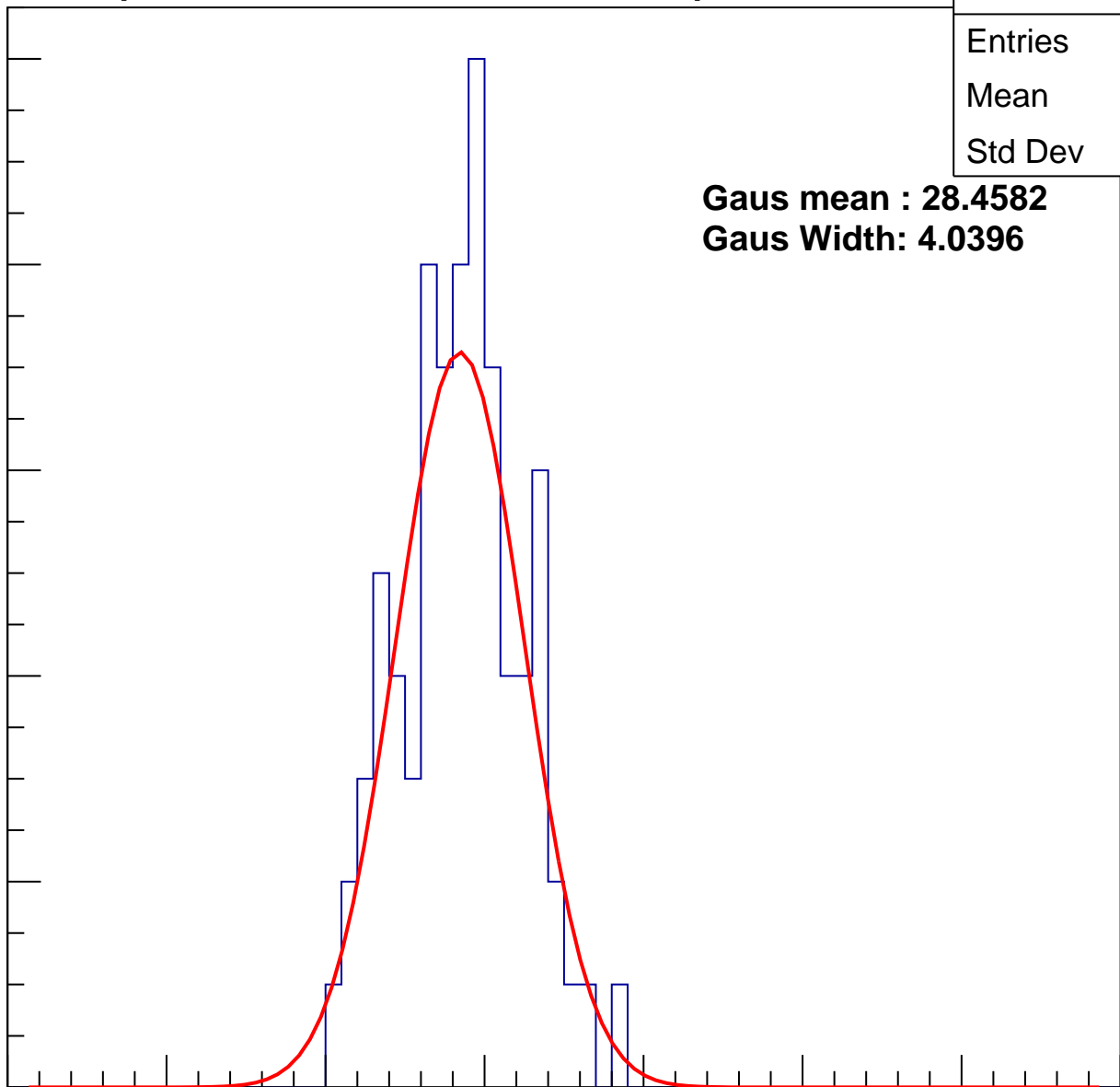
Gaus Width: 4.0396

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch90, adc1

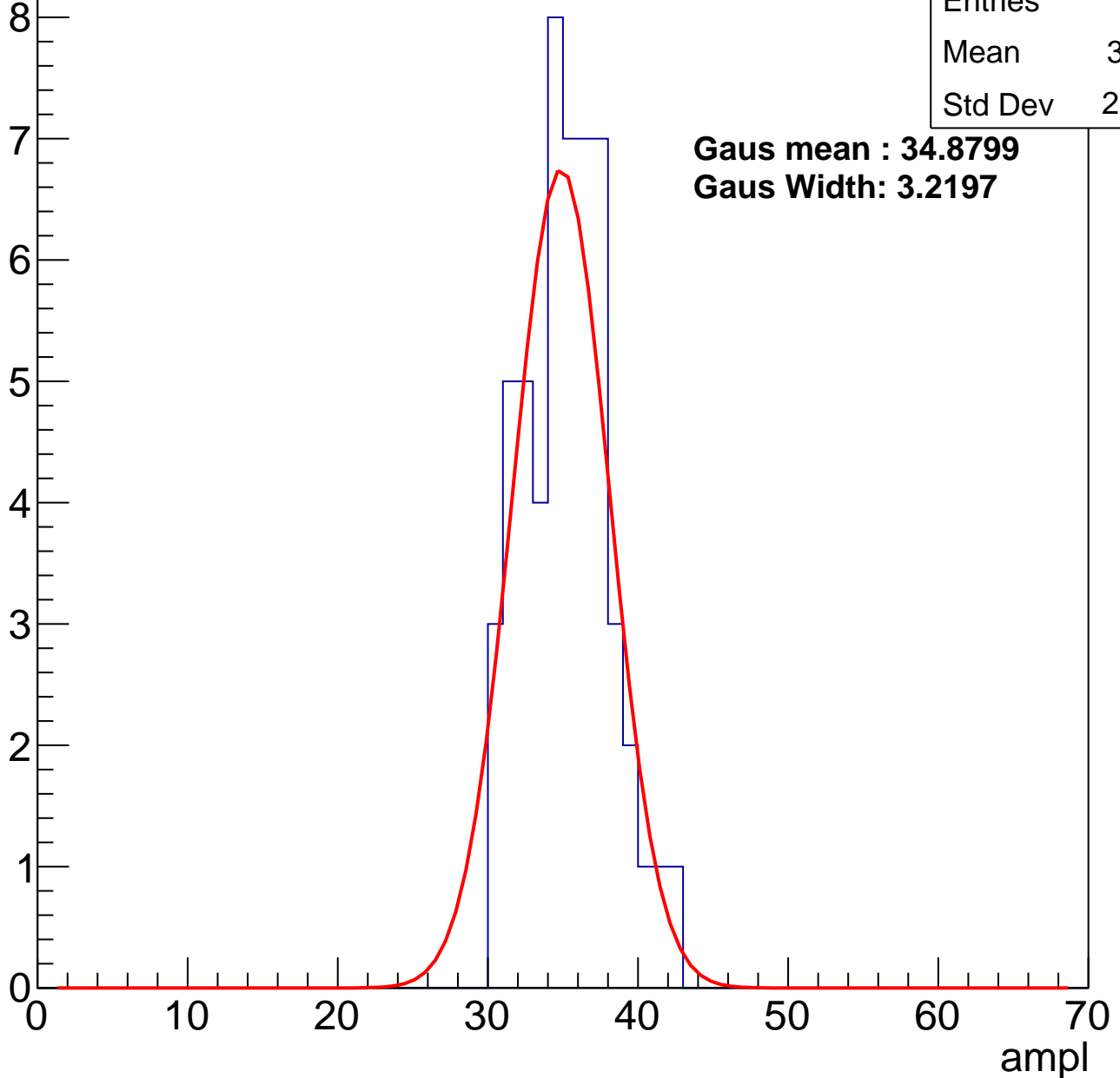
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	34.81
Std Dev	2.809

Gaus mean : 34.8799

Gaus Width: 3.2197



B1L003S, U16-ch90, adc2

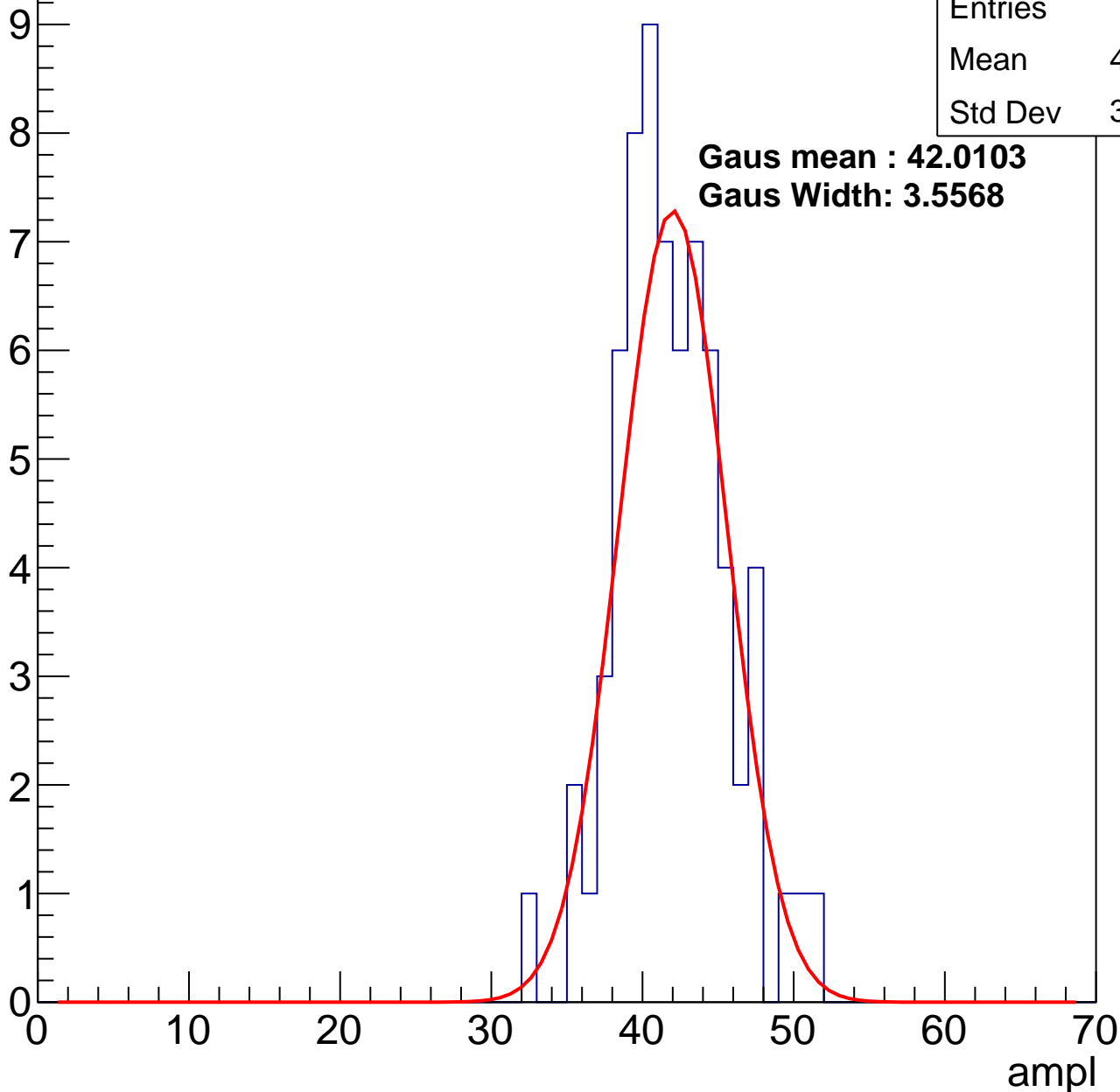
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.49
Std Dev	3.602

Gaus mean : 42.0103

Gaus Width: 3.5568

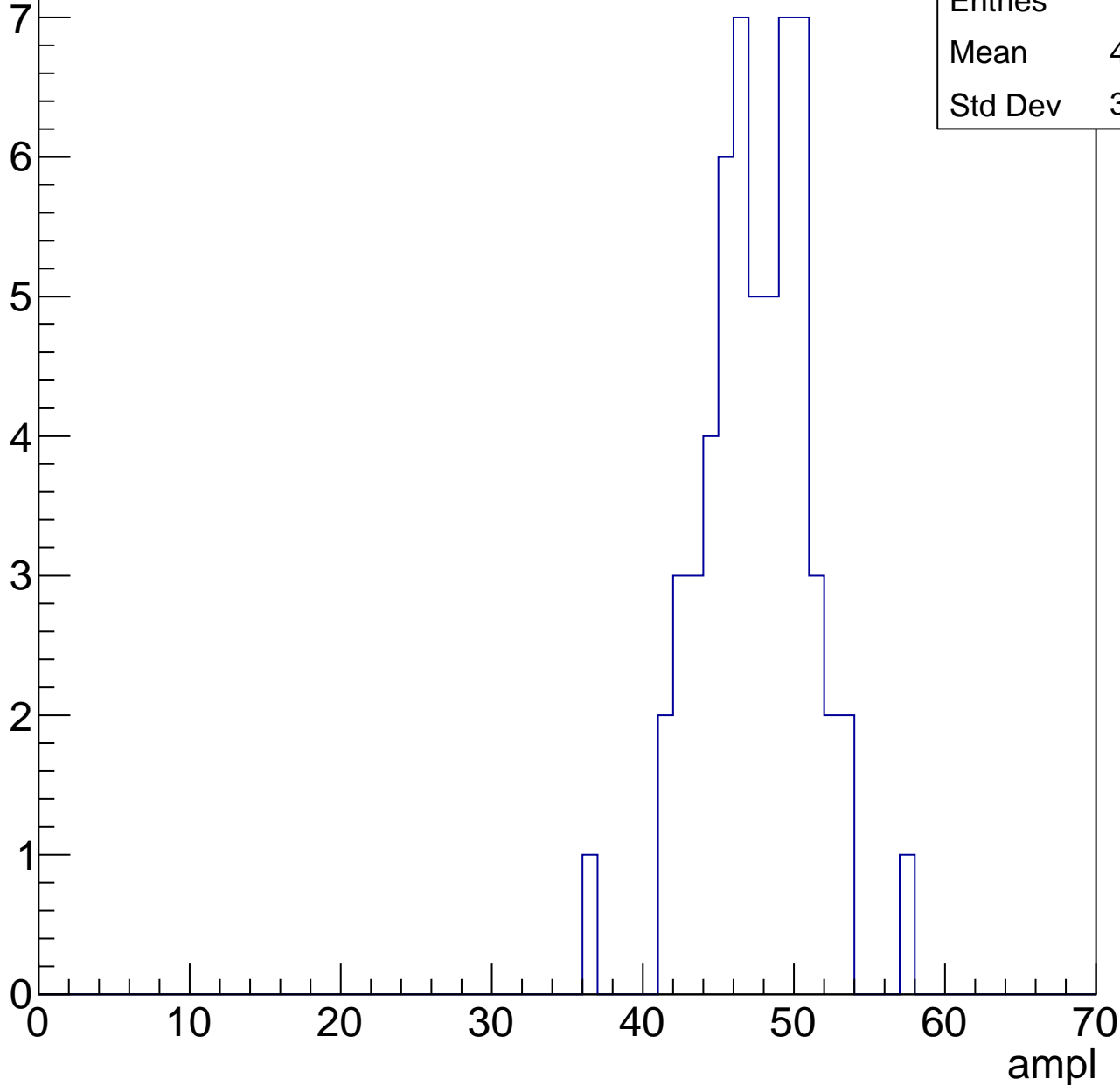


B1L003S, U16-ch90, adc3

calib_packv5_042523_0143.root, FC#13, port D2

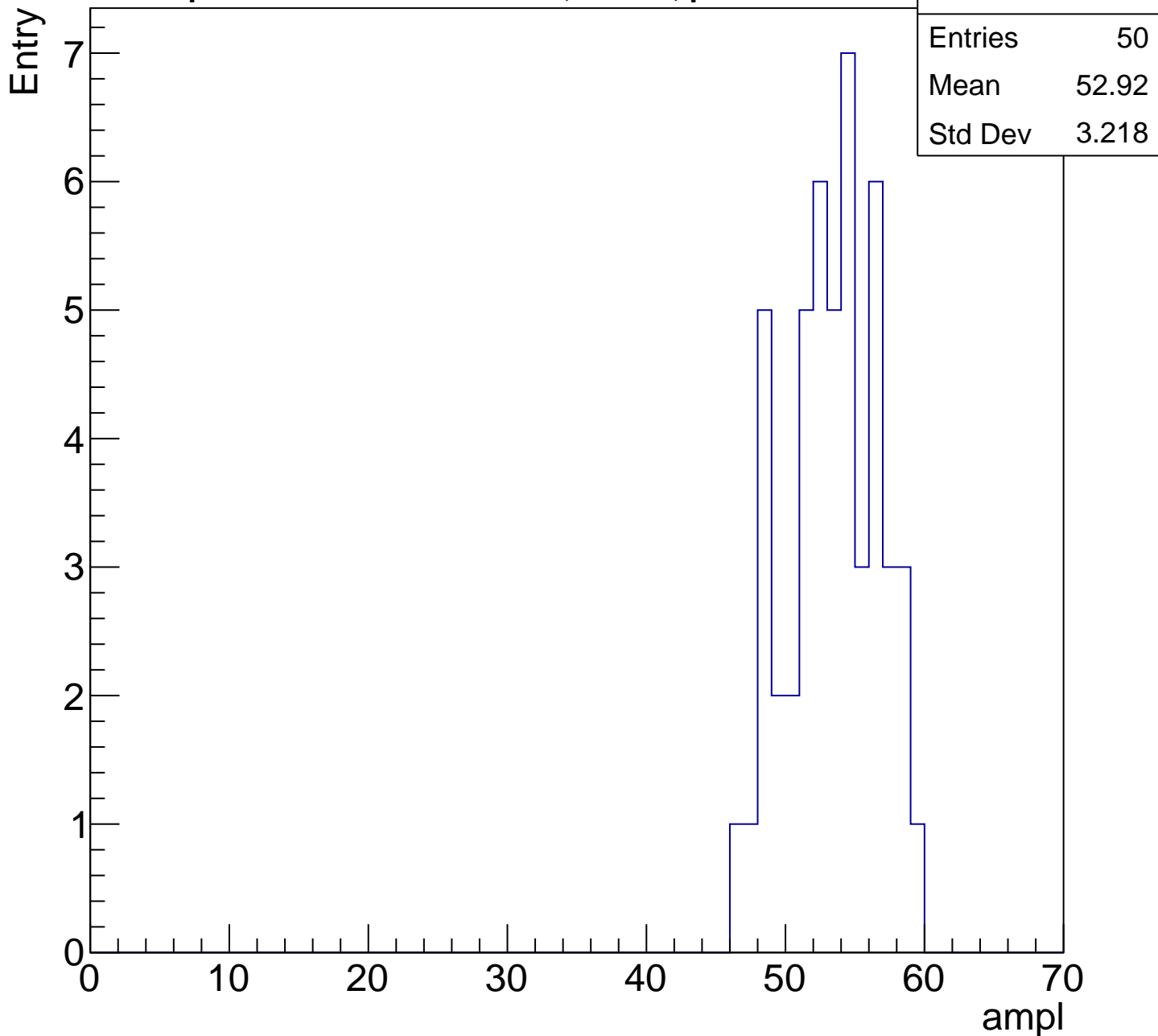
Entry

Entries	58
Mean	47.05
Std Dev	3.593



B1L003S, U16-ch90, adc4

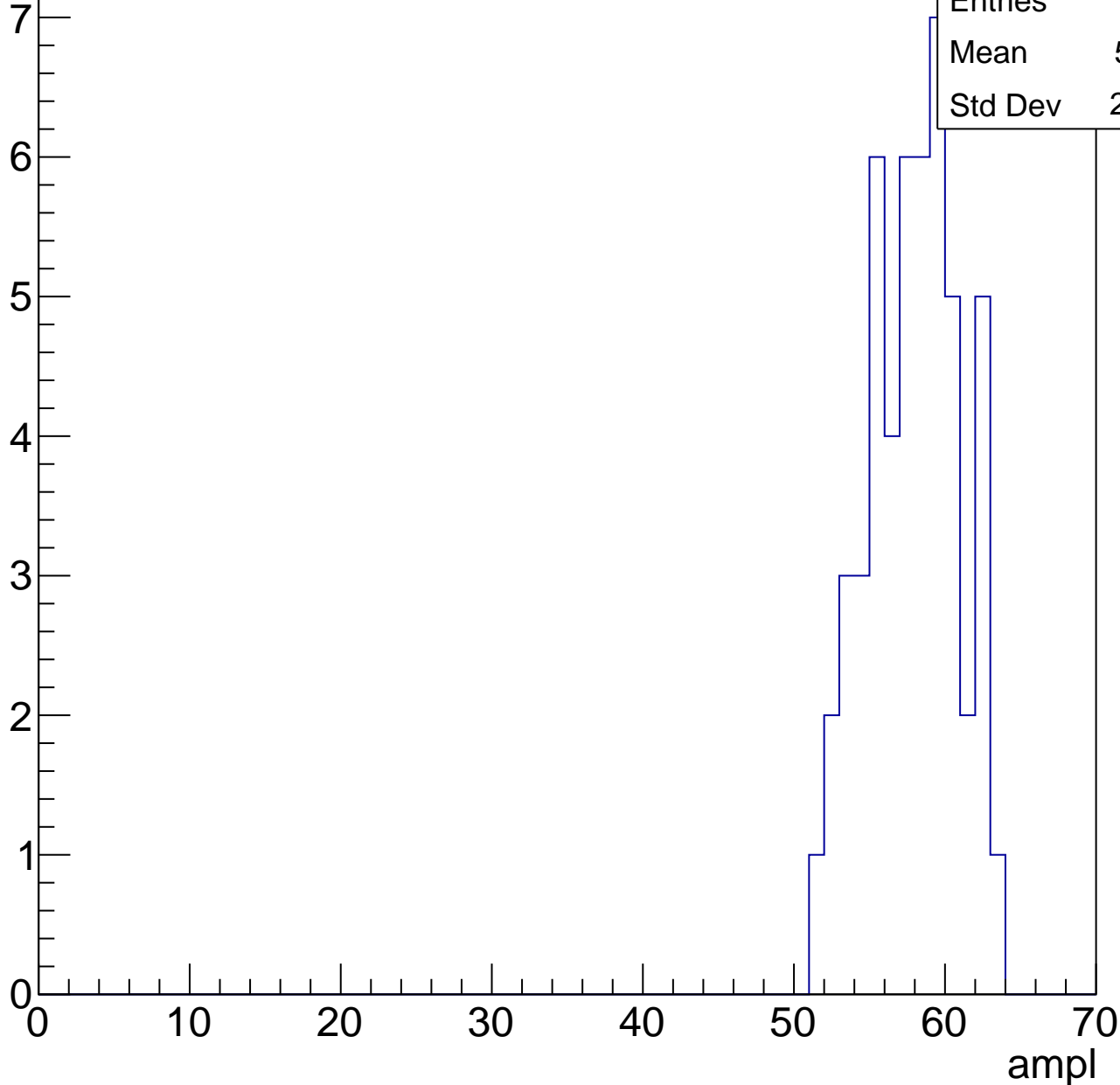
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch90, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



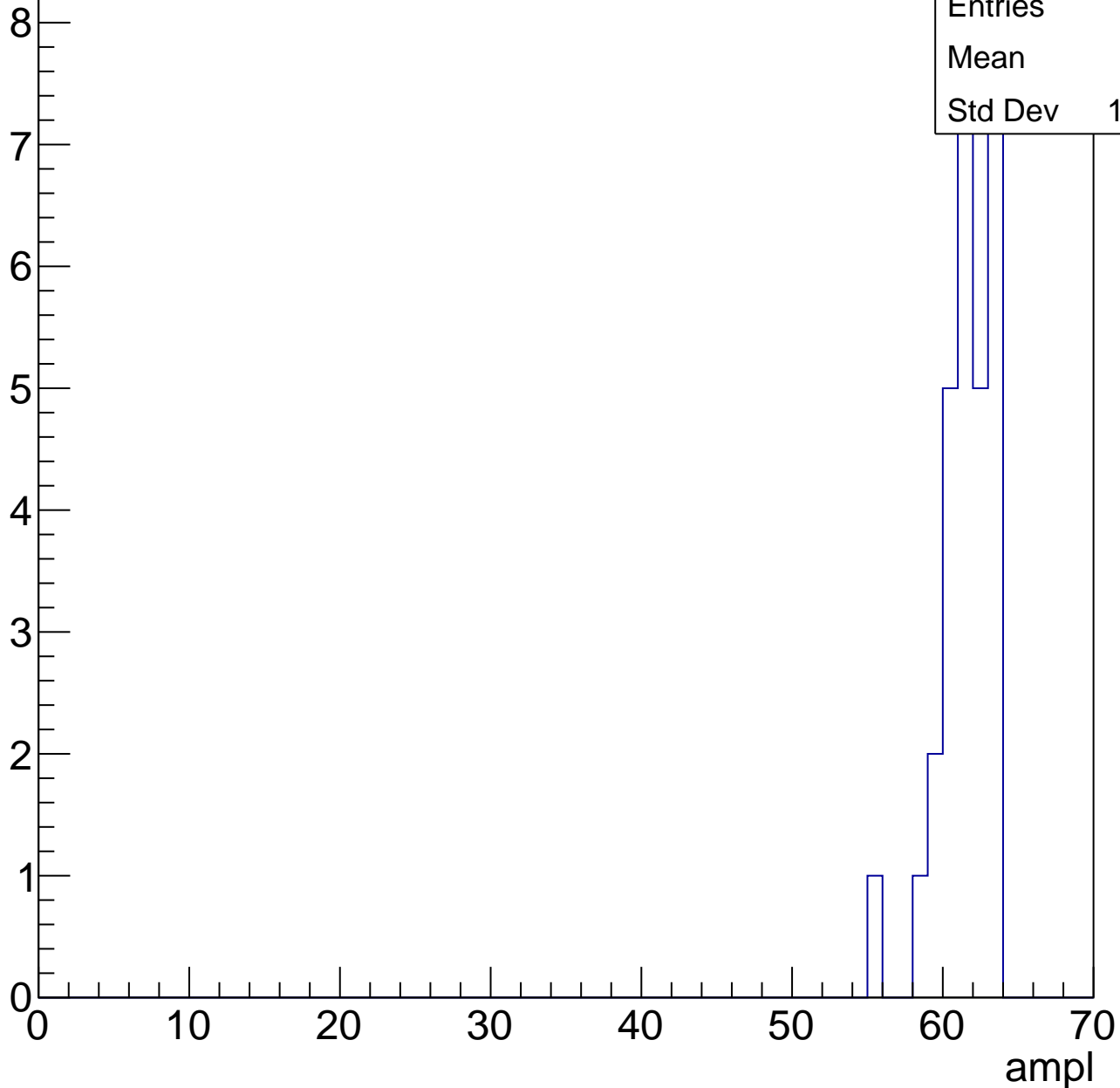
Entries	51
Mean	57.41
Std Dev	2.978

B1L003S, U16-ch90, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

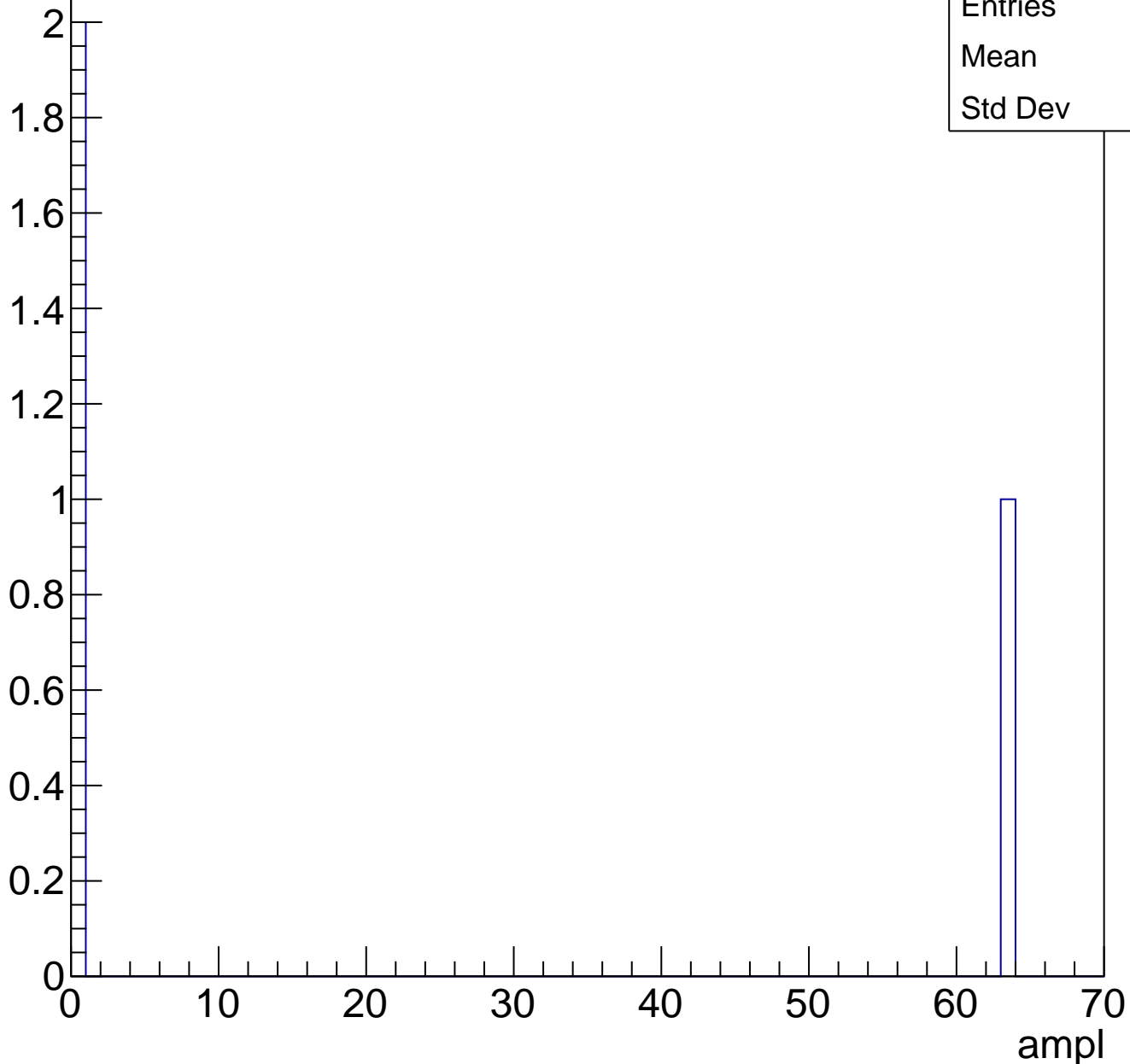
Entries	30
Mean	61.1
Std Dev	1.777



B1L003S, U16-ch90, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U16-ch91, adc0

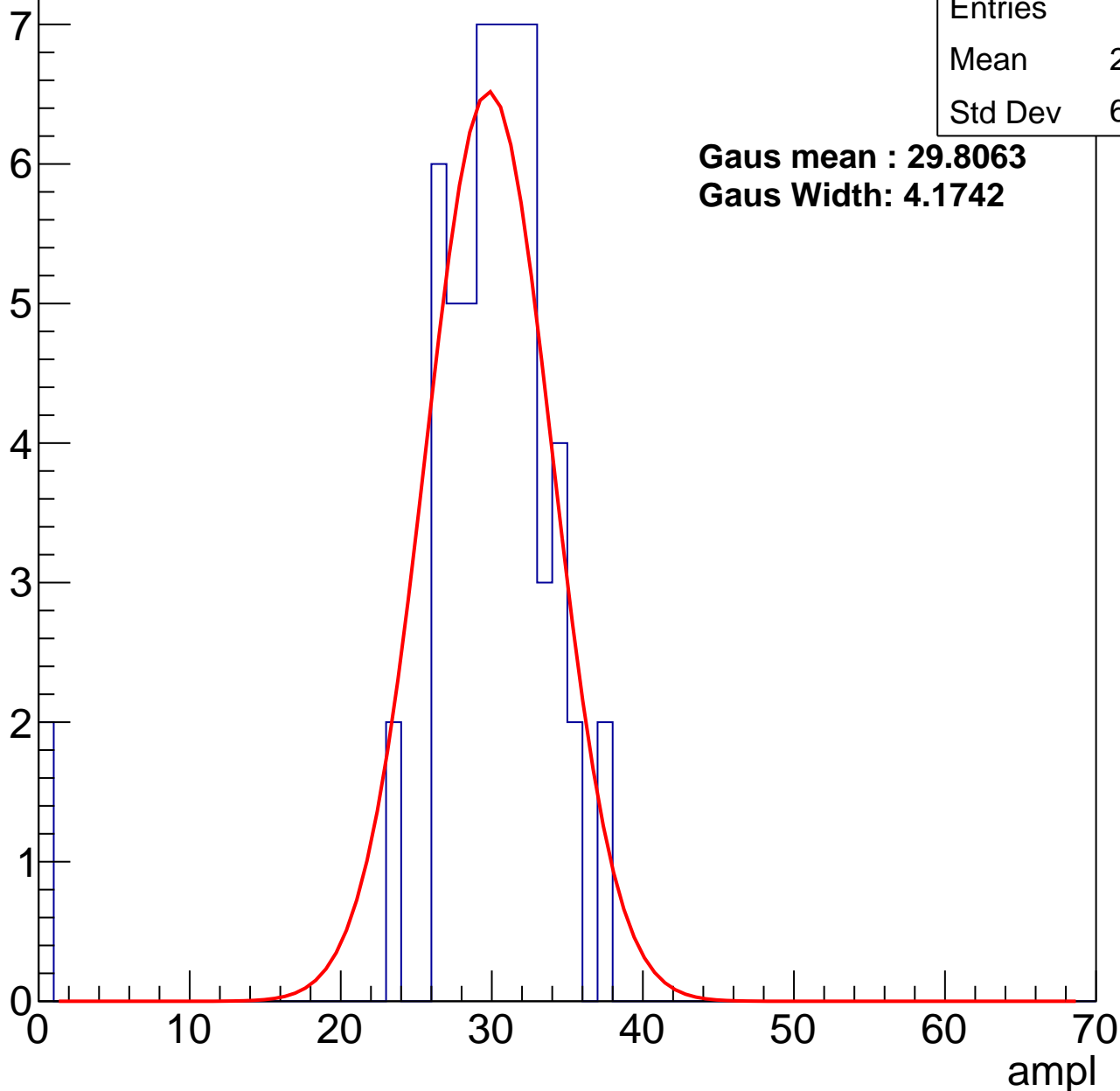
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28.98
Std Dev	6.215

Gaus mean : 29.8063

Gaus Width: 4.1742



B1L003S, U16-ch91, adc1

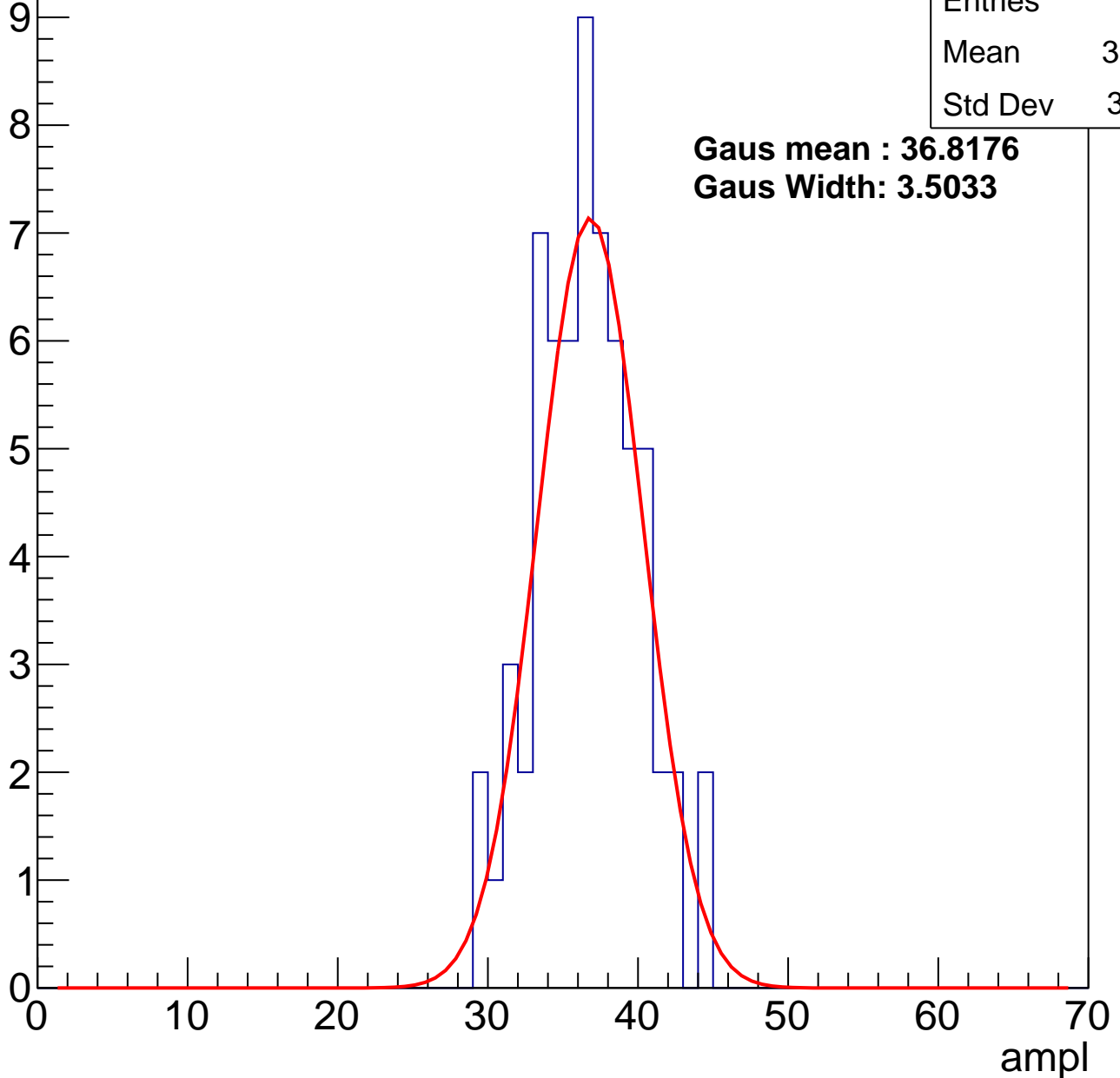
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.15
Std Dev	3.371

Gaus mean : 36.8176

Gaus Width: 3.5033



B1L003S, U16-ch91, adc2

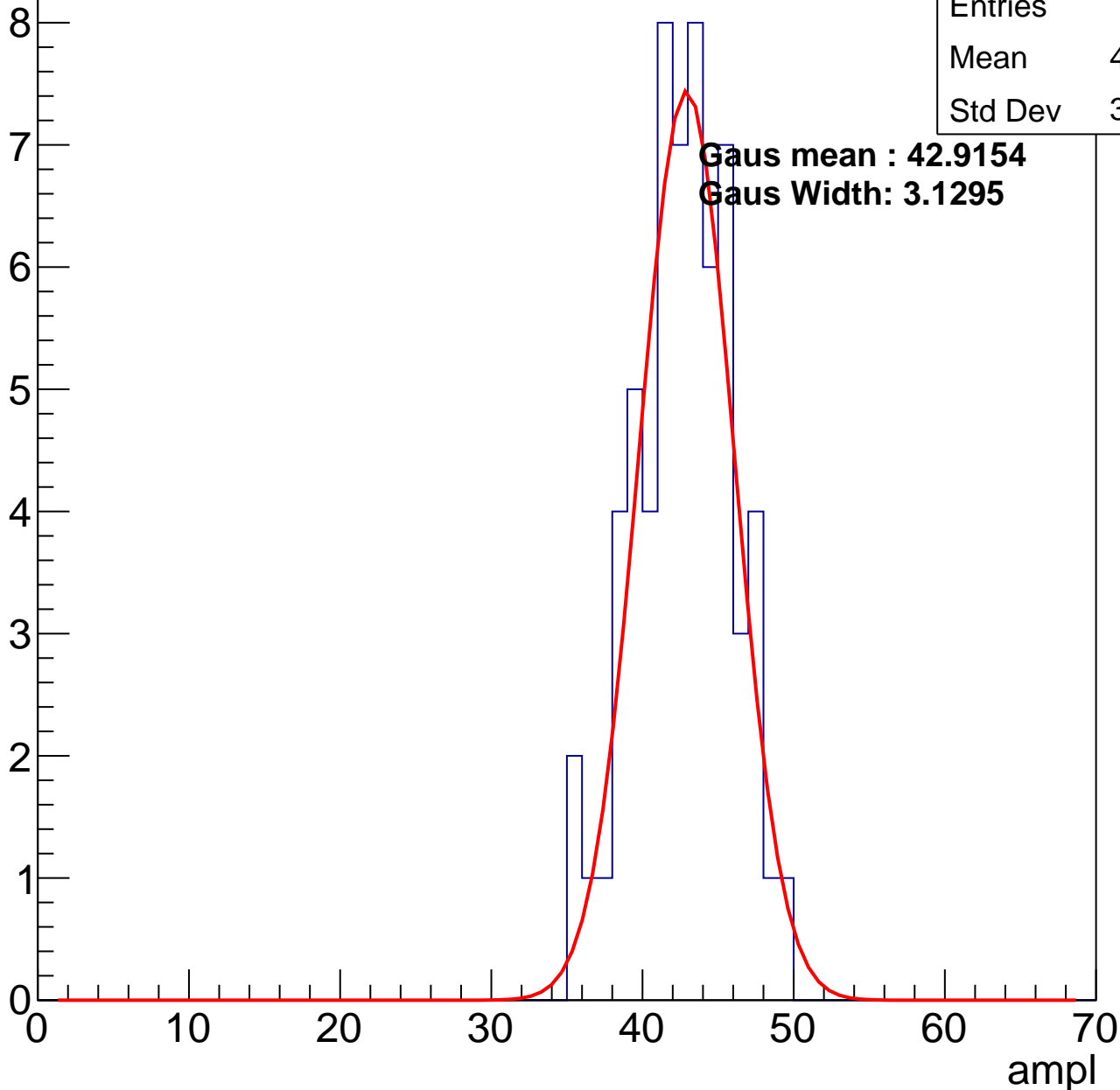
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.23
Std Dev	3.154

Gaus mean : 42.9154

Gaus Width: 3.1295



B1L003S, U16-ch91, adc3

calib_packv5_042523_0143.root, FC#13, port D2

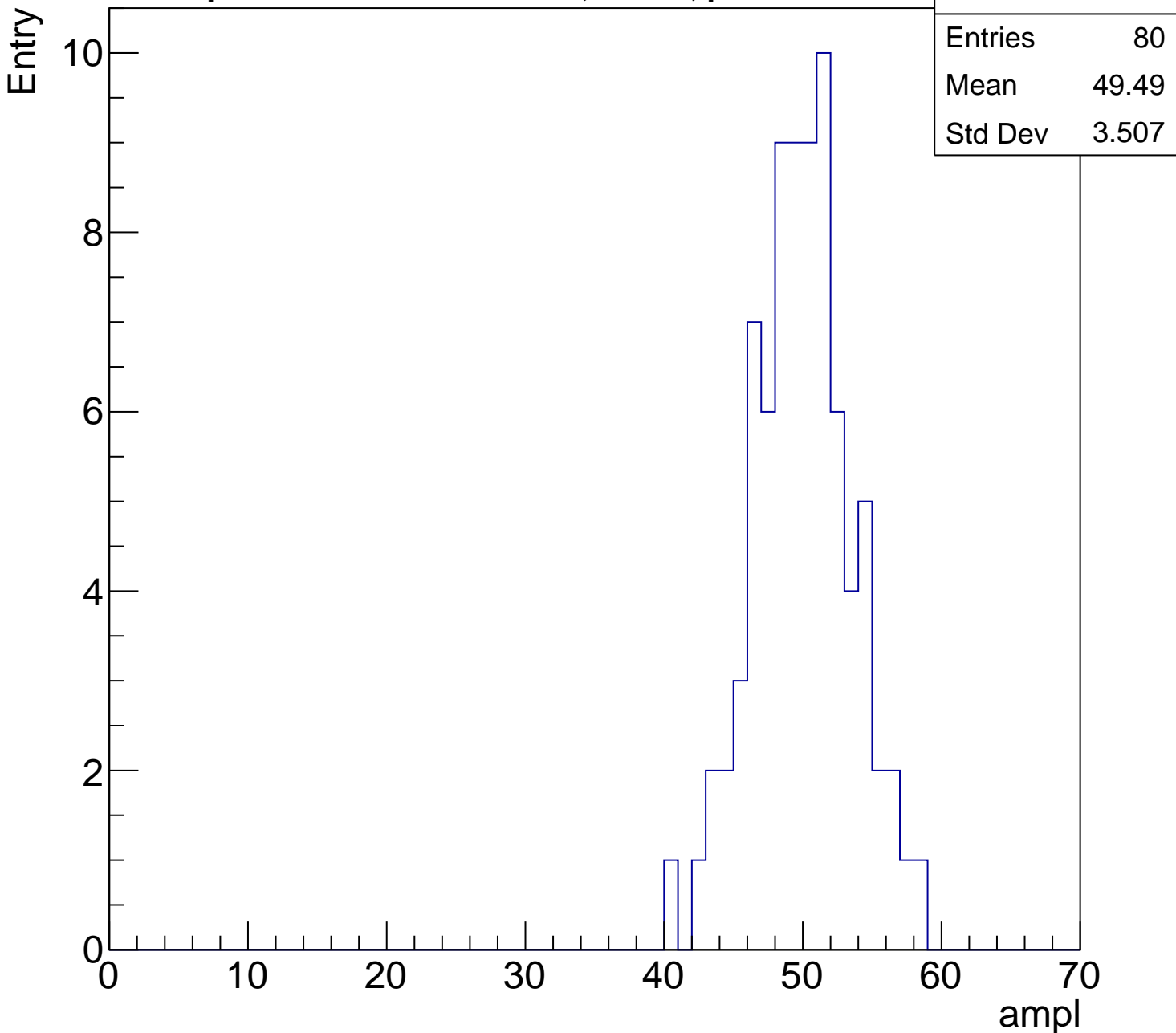
Entries	80
Mean	49.49
Std Dev	3.507

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

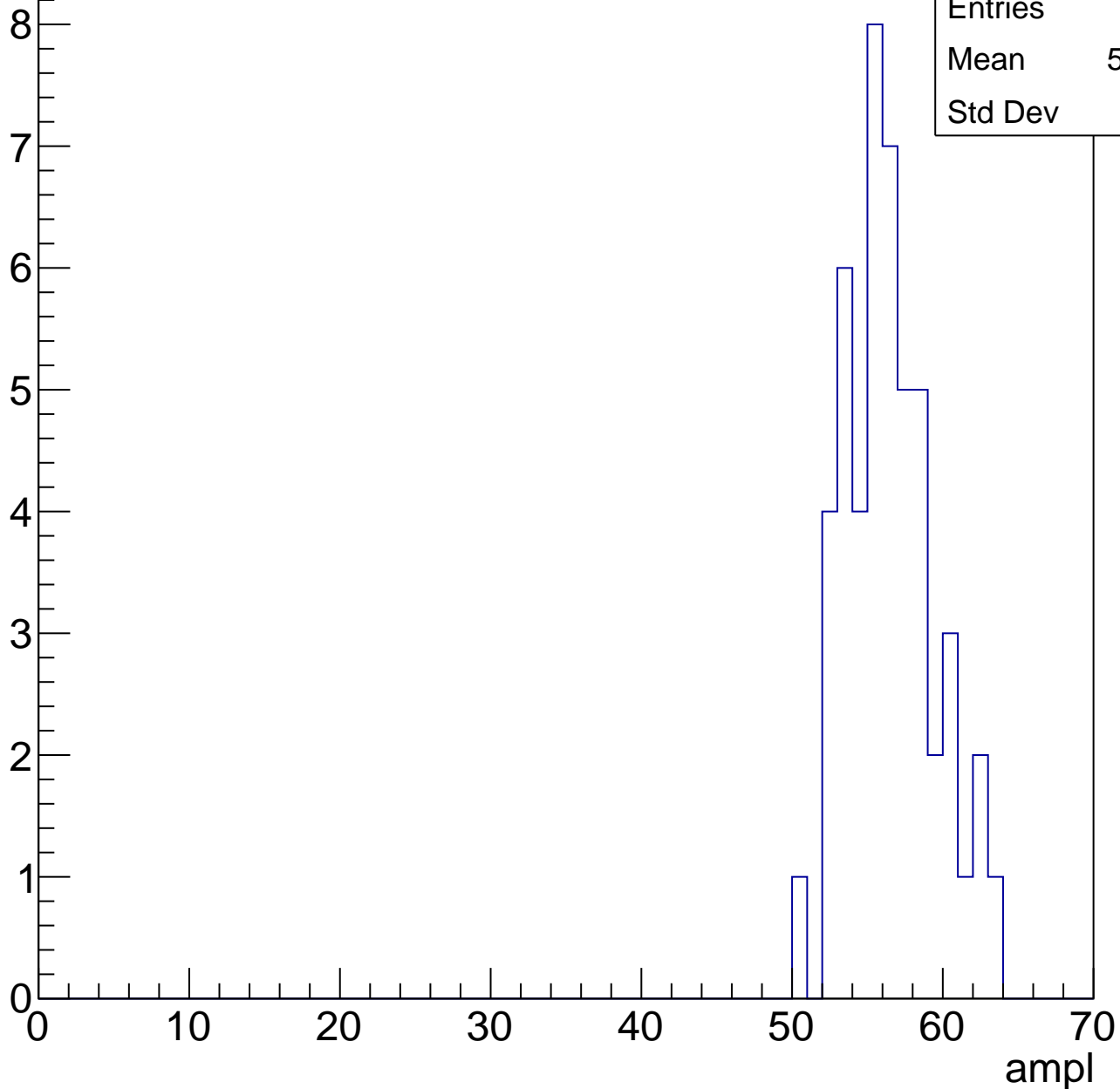


B1L003S, U16-ch91, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

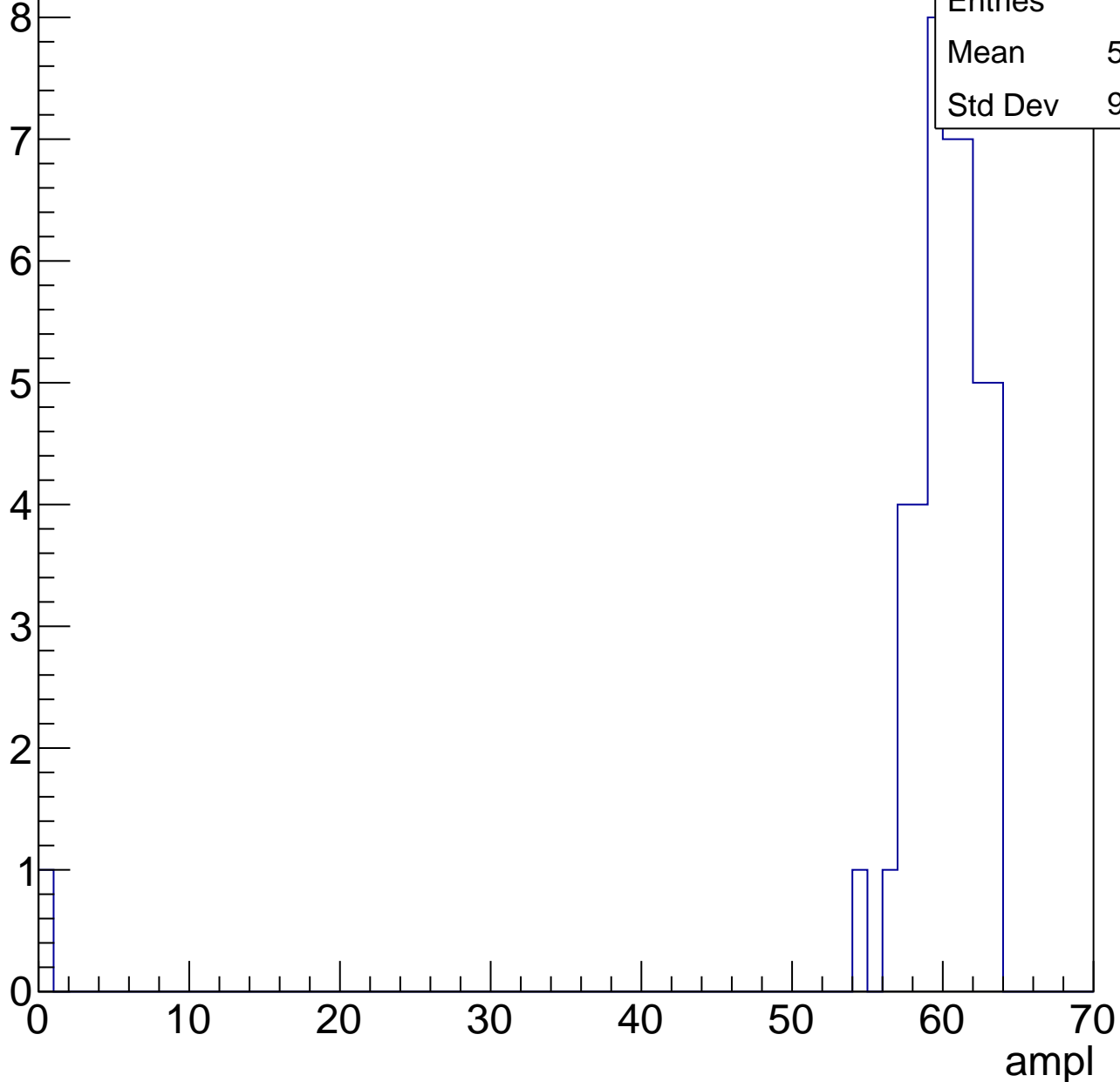
Entries	49
Mean	56.02
Std Dev	2.91



B1L003S, U16-ch91, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch91, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch91, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch92, adc0

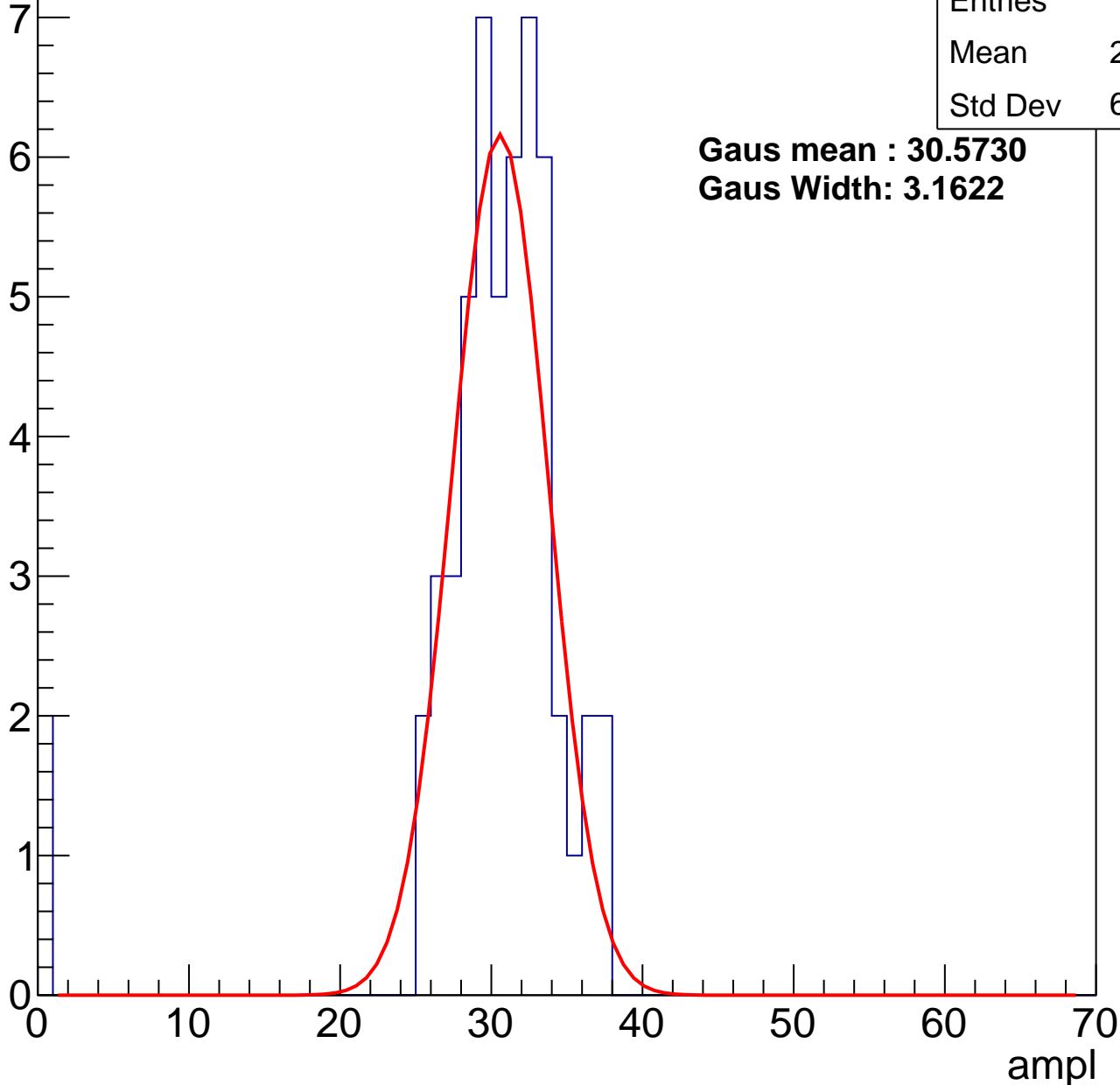
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	29.42
Std Dev	6.514

Gaus mean : 30.5730

Gaus Width: 3.1622



B1L003S, U16-ch92, adc1

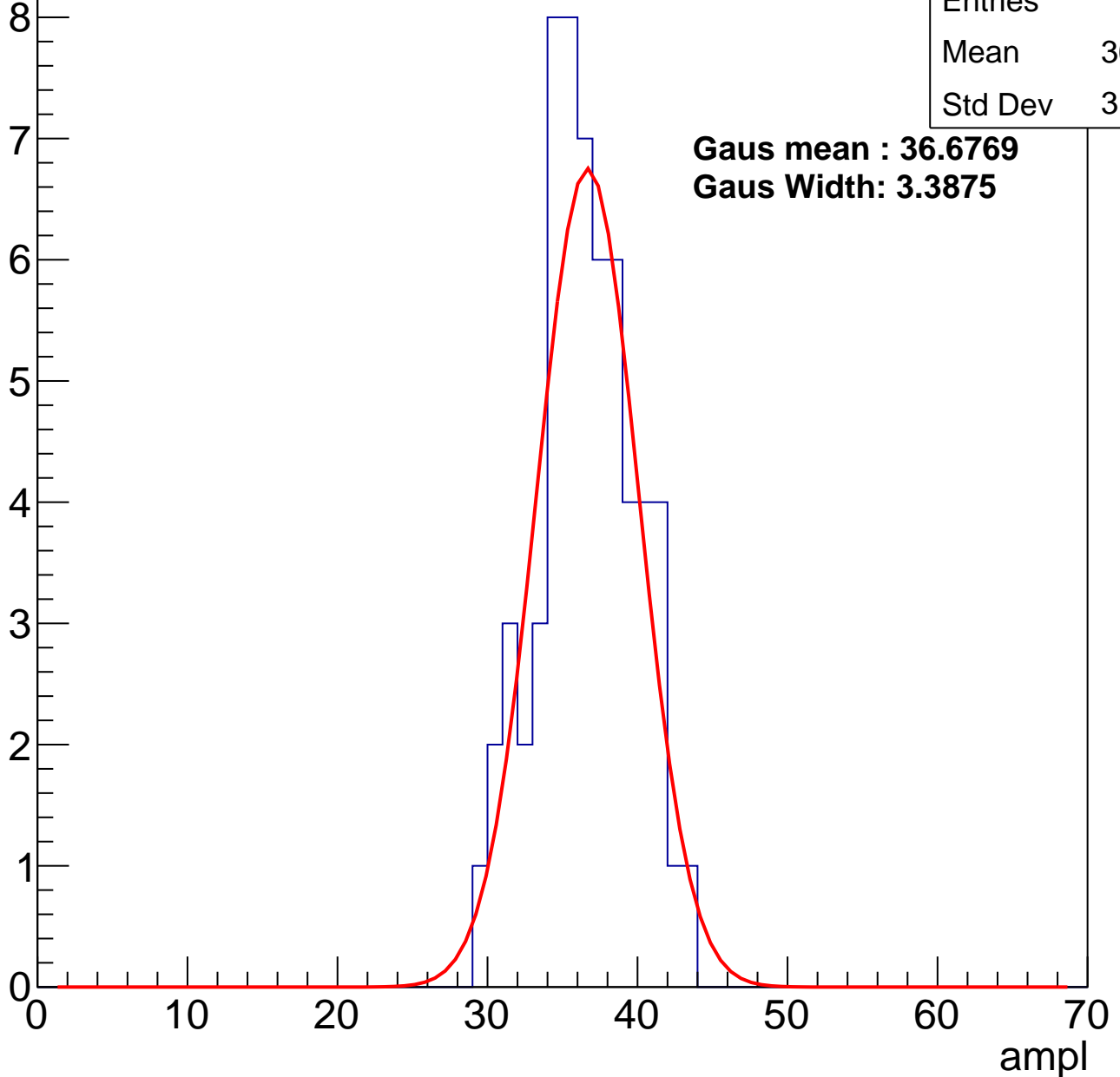
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.07
Std Dev	3.188

Gaus mean : 36.6769

Gaus Width: 3.3875



B1L003S, U16-ch92, adc2

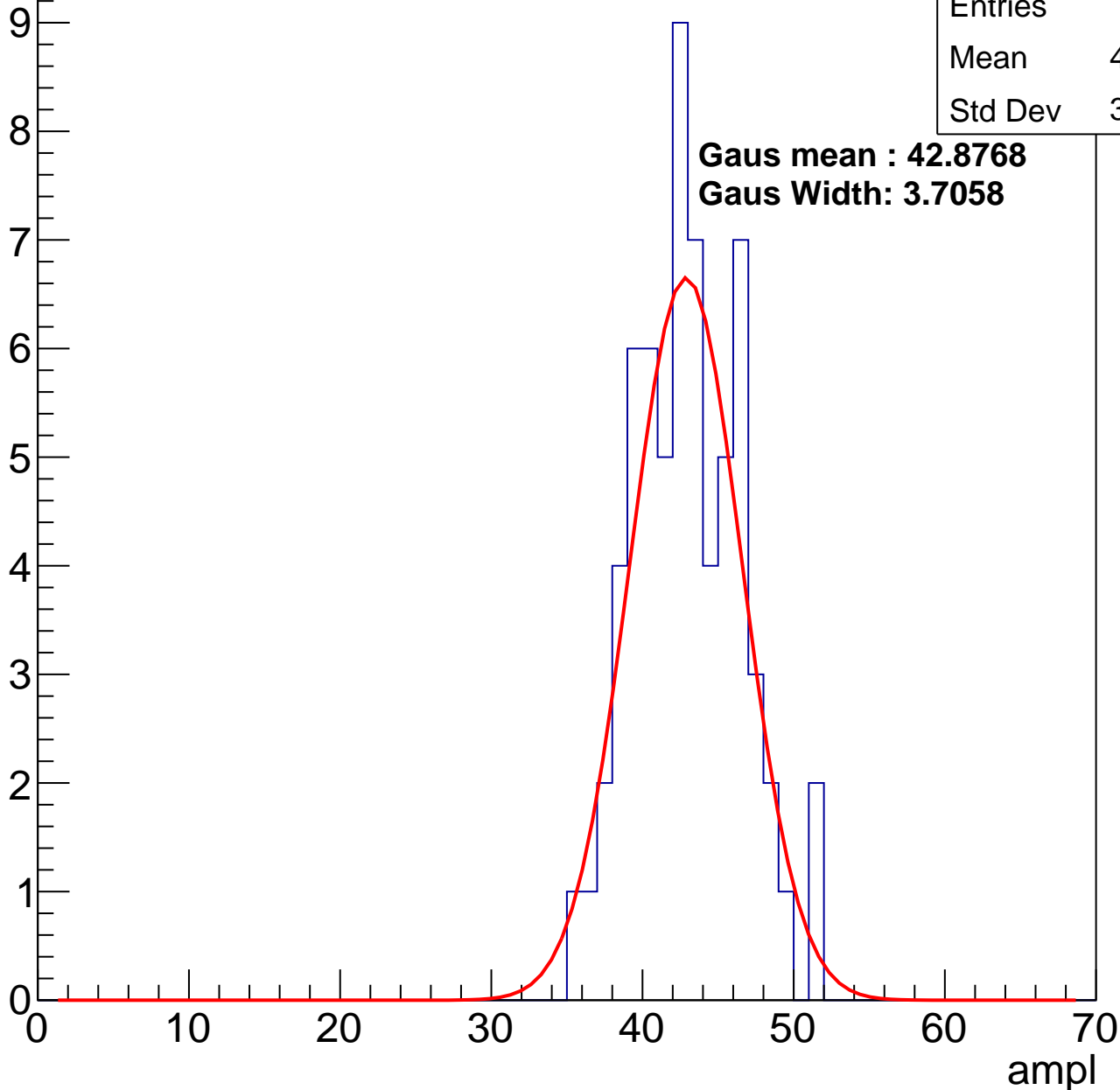
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.55
Std Dev	3.508

Gaus mean : 42.8768

Gaus Width: 3.7058

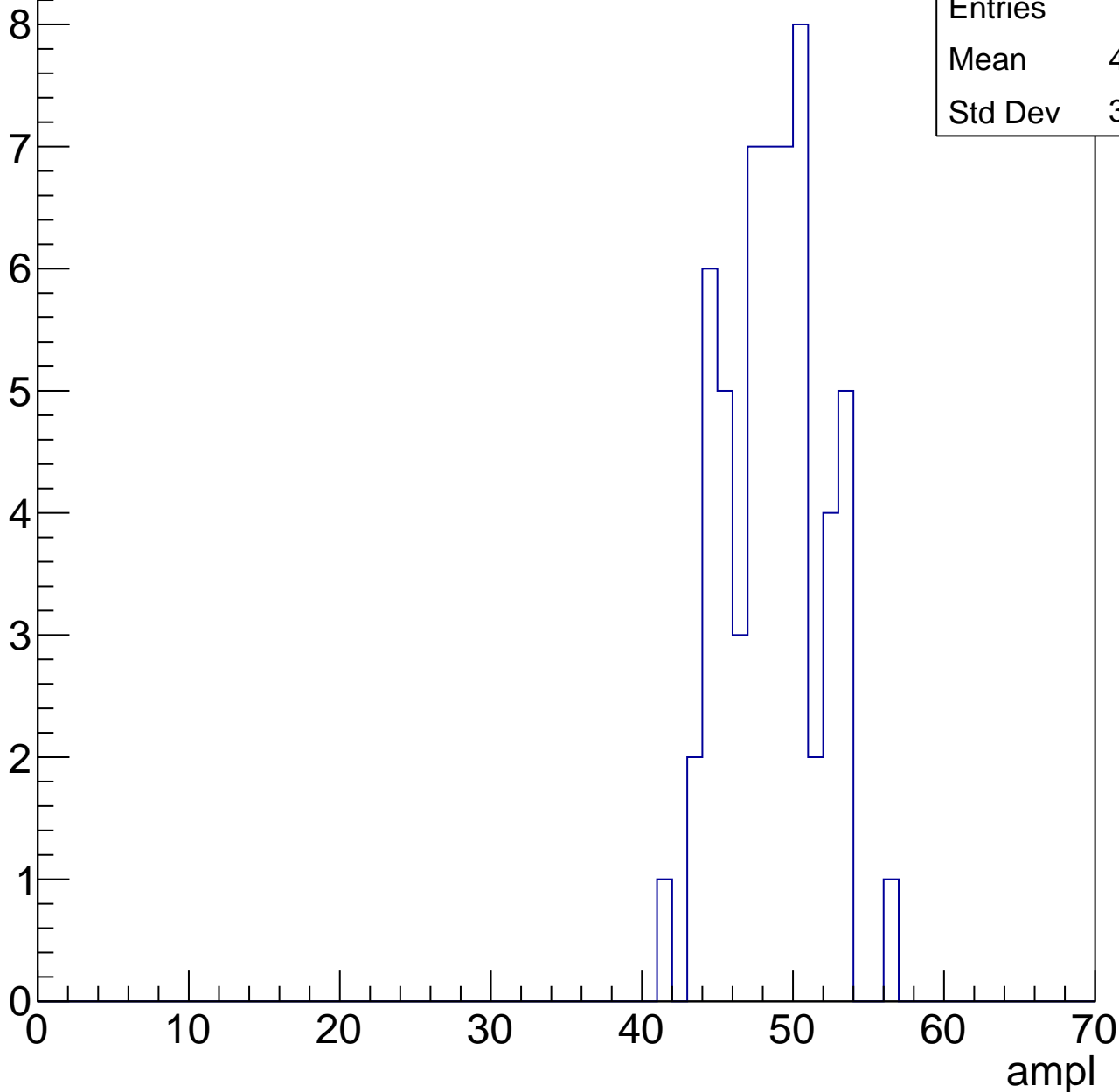


B1L003S, U16-ch92, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	48.16
Std Dev	3.128

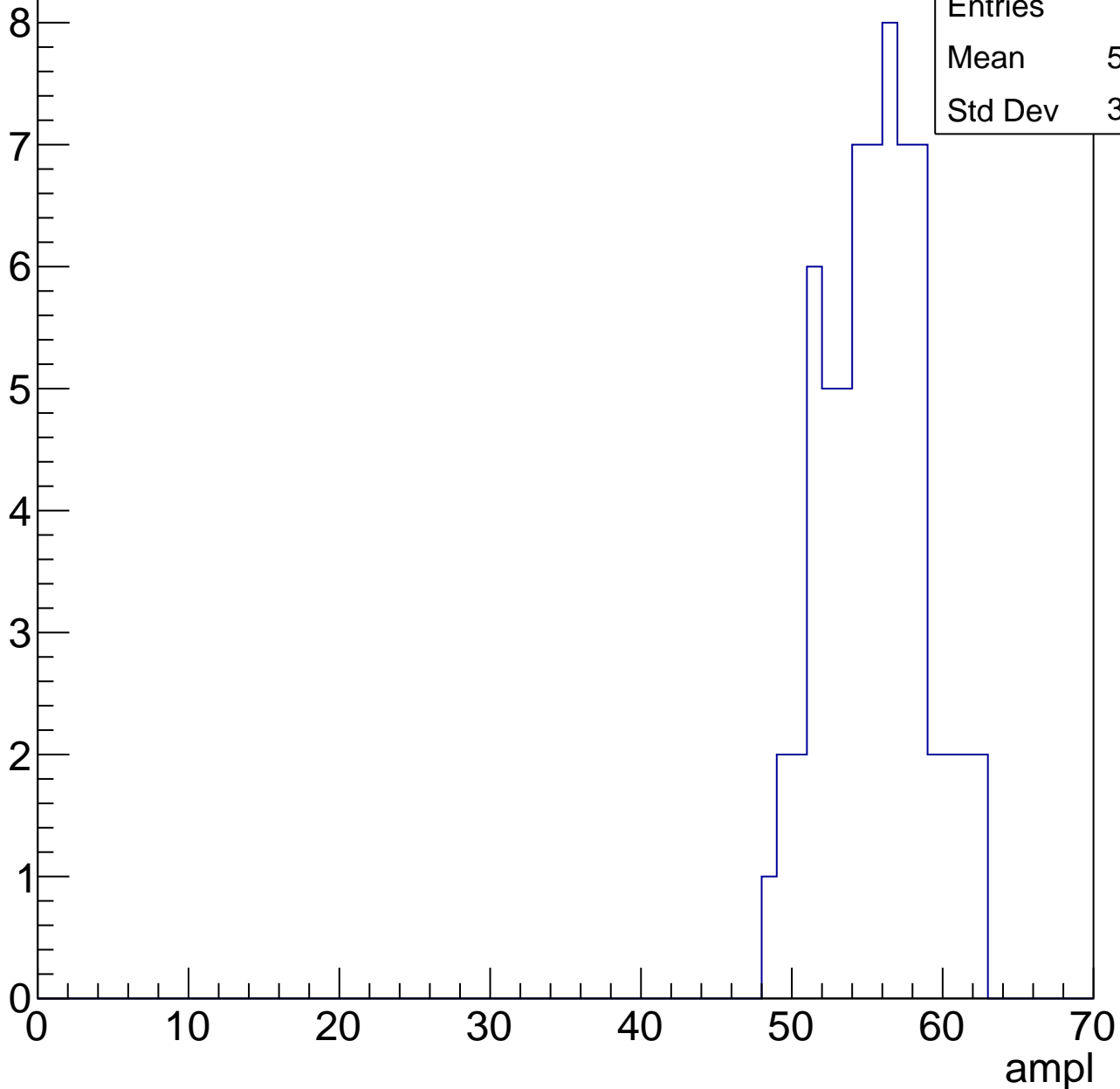


B1L003S, U16-ch92, adc4

calib_packv5_042523_0143.root, FC#13, port D2

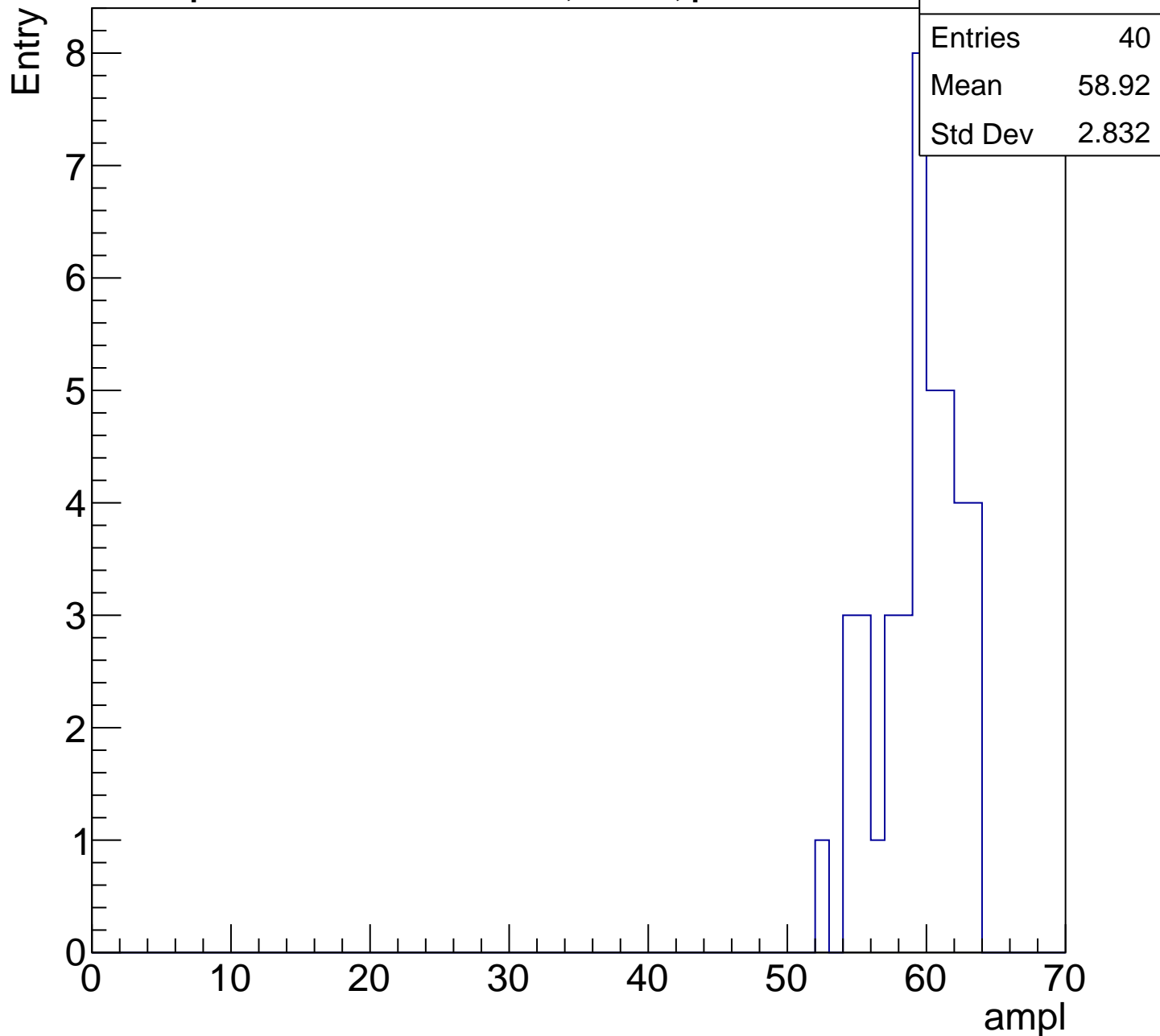
Entry

Entries	65
Mean	55.03
Std Dev	3.258



B1L003S, U16-ch92, adc5

calib_packv5_042523_0143.root, FC#13, port D2

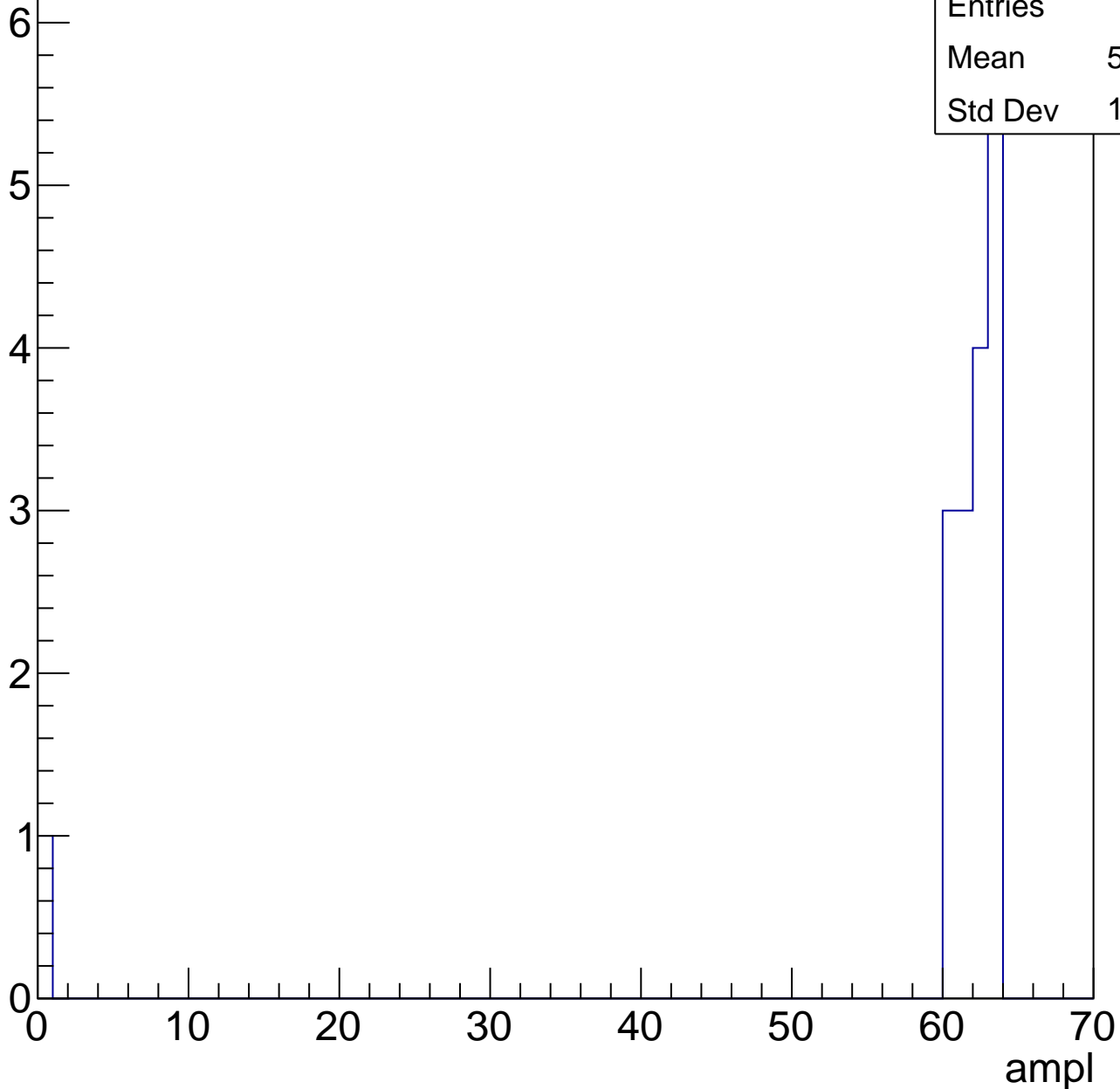


B1L003S, U16-ch92, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.18
Std Dev	14.59



B1L003S, U16-ch92, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch93, adc0

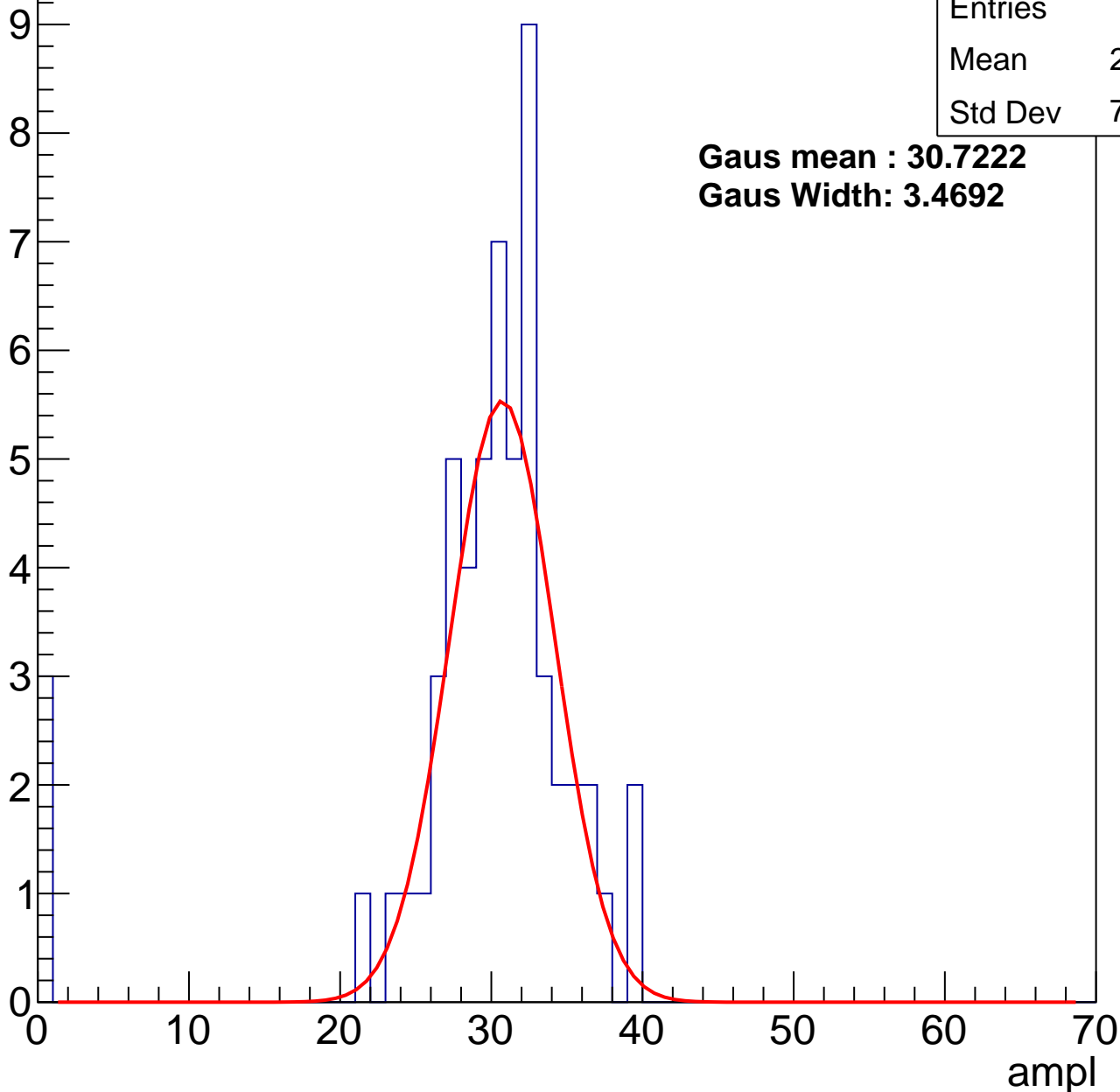
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	28.77
Std Dev	7.669

Gaus mean : 30.7222

Gaus Width: 3.4692



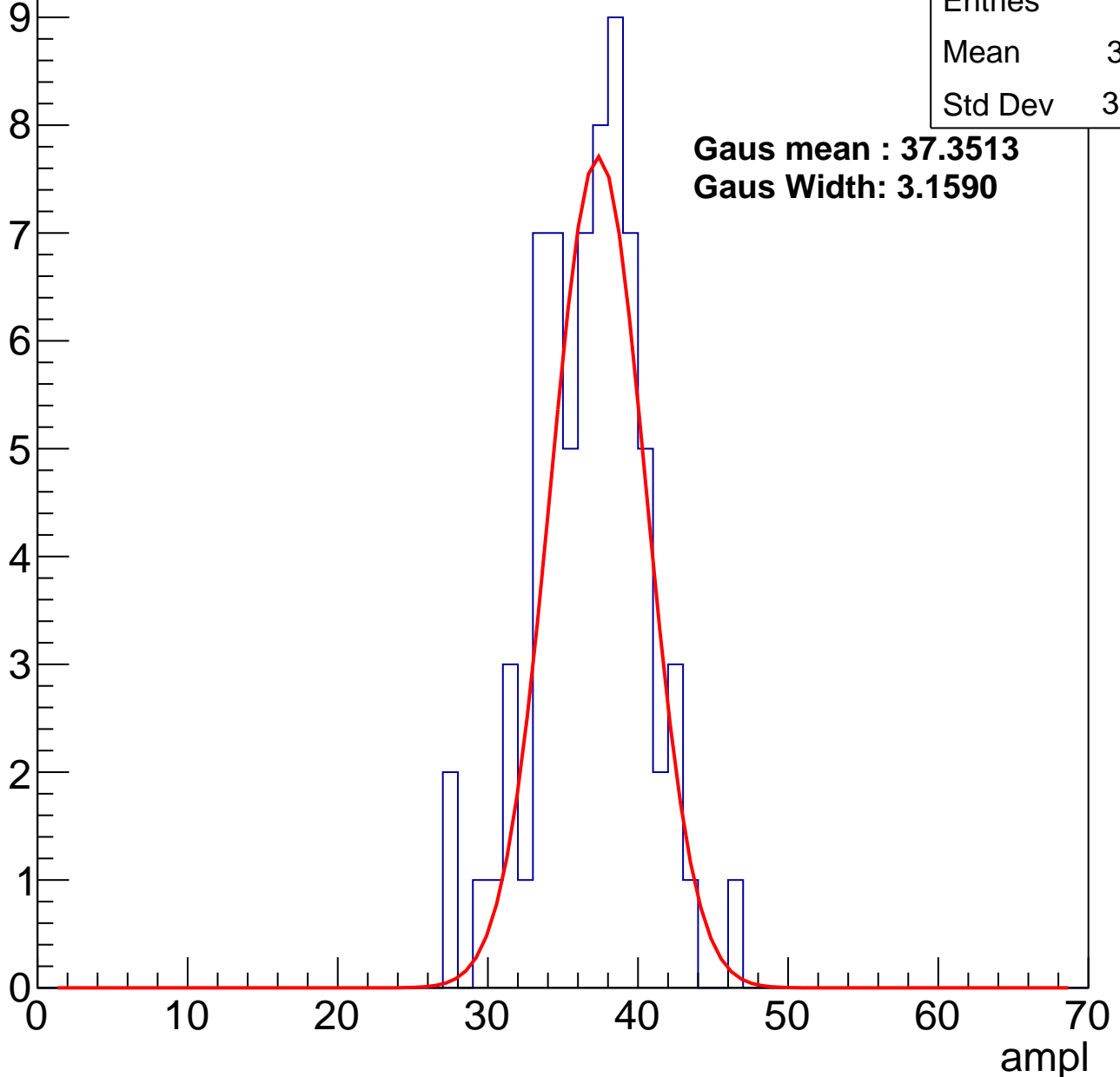
B1L003S, U16-ch93, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.31
Std Dev	3.627

Gaus mean : 37.3513
Gaus Width: 3.1590



B1L003S, U16-ch93, adc2

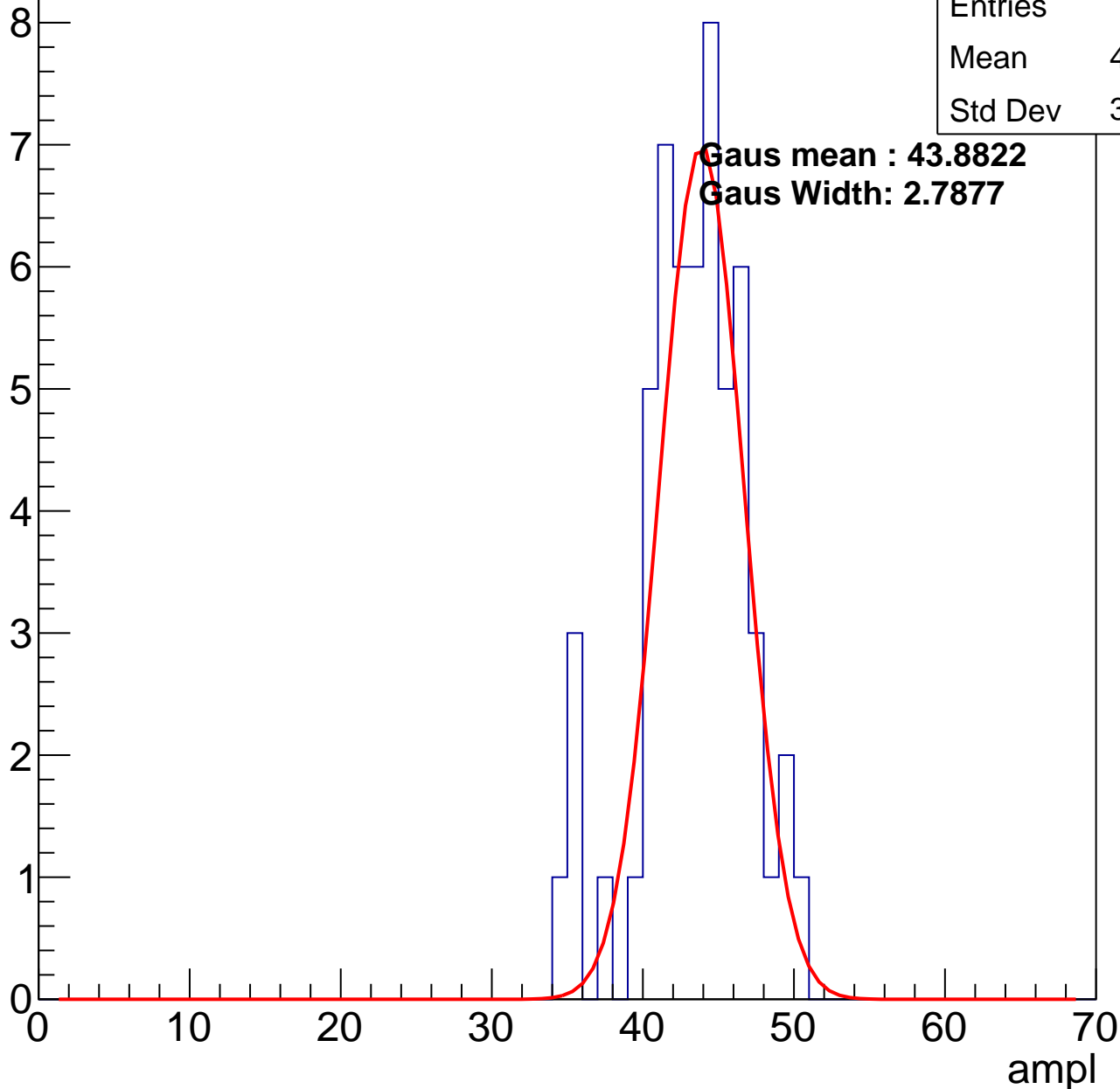
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.89
Std Dev	3.483

Gaus mean : 43.8822

Gaus Width: 2.7877

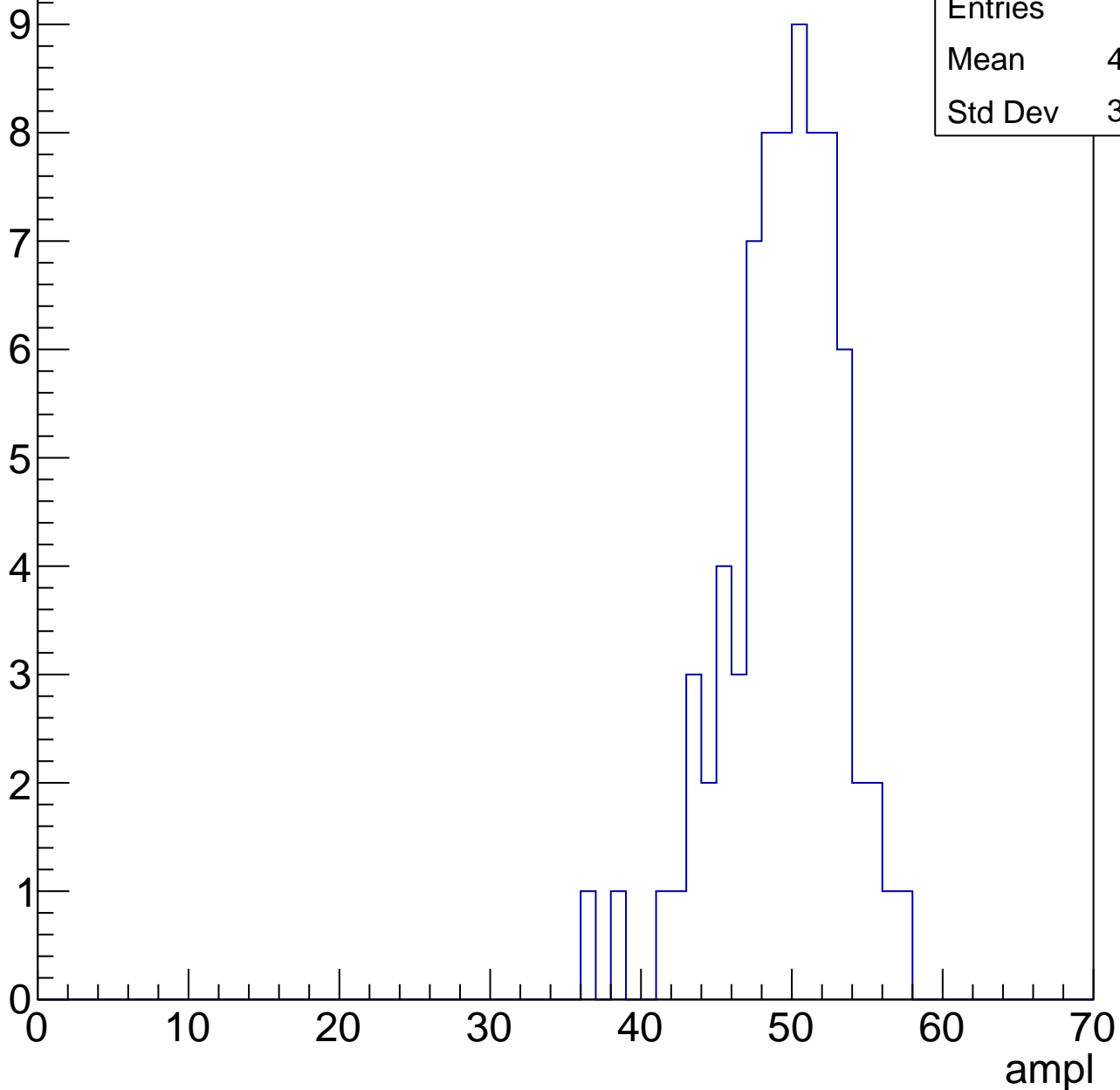


B1L003S, U16-ch93, adc3

calib_packv5_042523_0143.root, FC#13, port D2

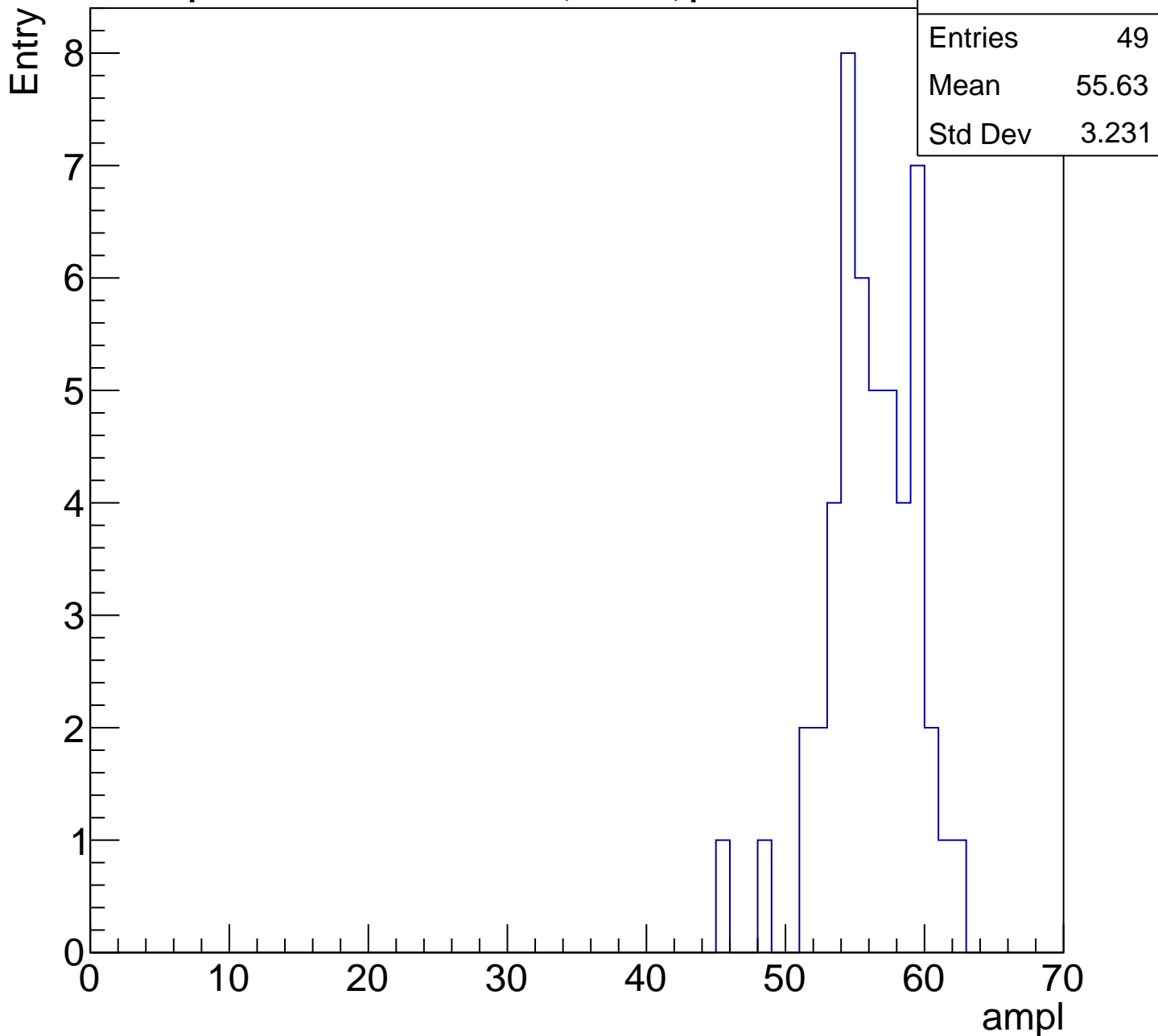
Entry

Entries	76
Mean	48.95
Std Dev	3.859



B1L003S, U16-ch93, adc4

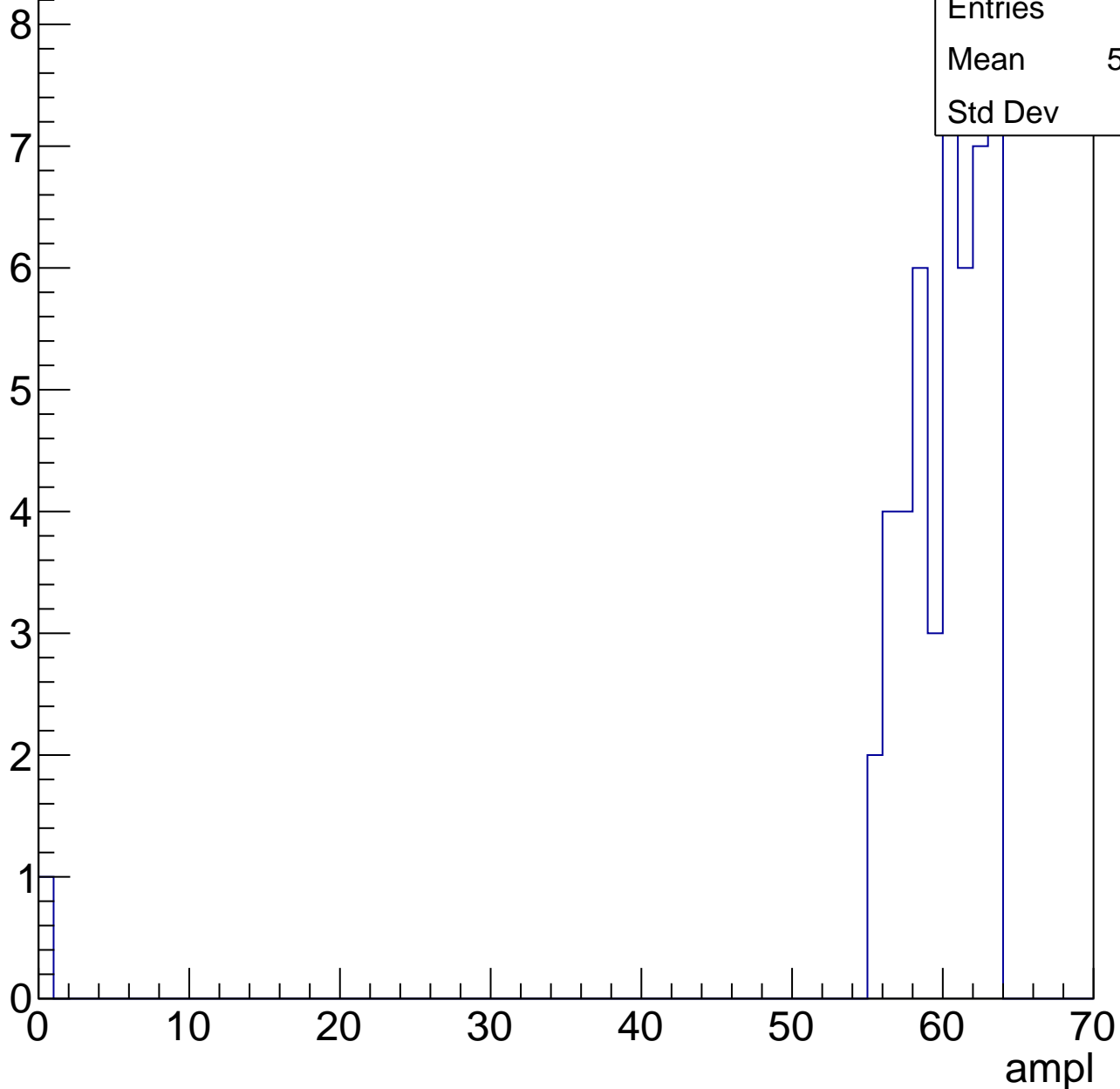
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch93, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch93, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch93, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch94, adc0

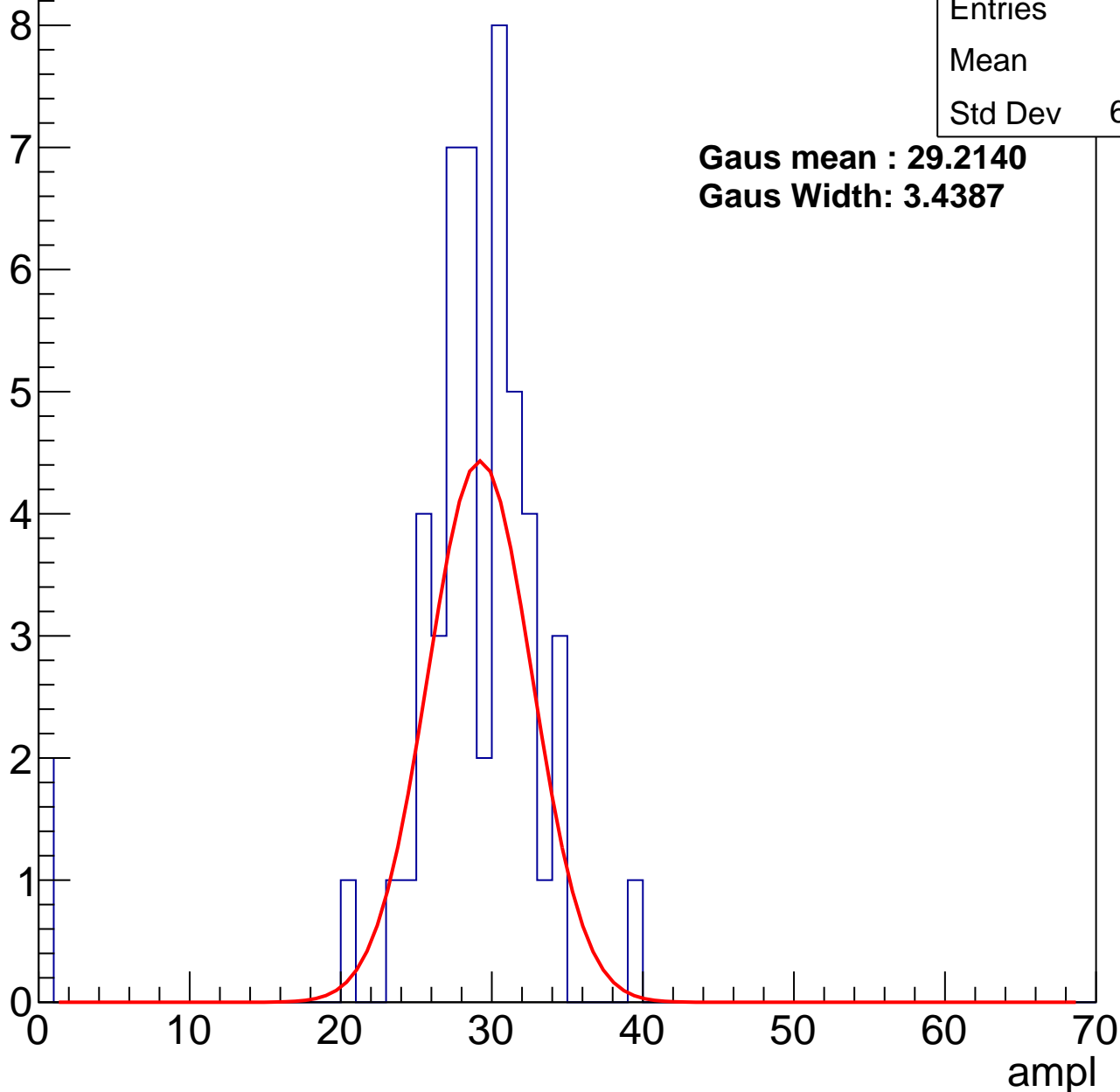
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	27.7
Std Dev	6.512

Gaus mean : 29.2140

Gaus Width: 3.4387



B1L003S, U16-ch94, adc1

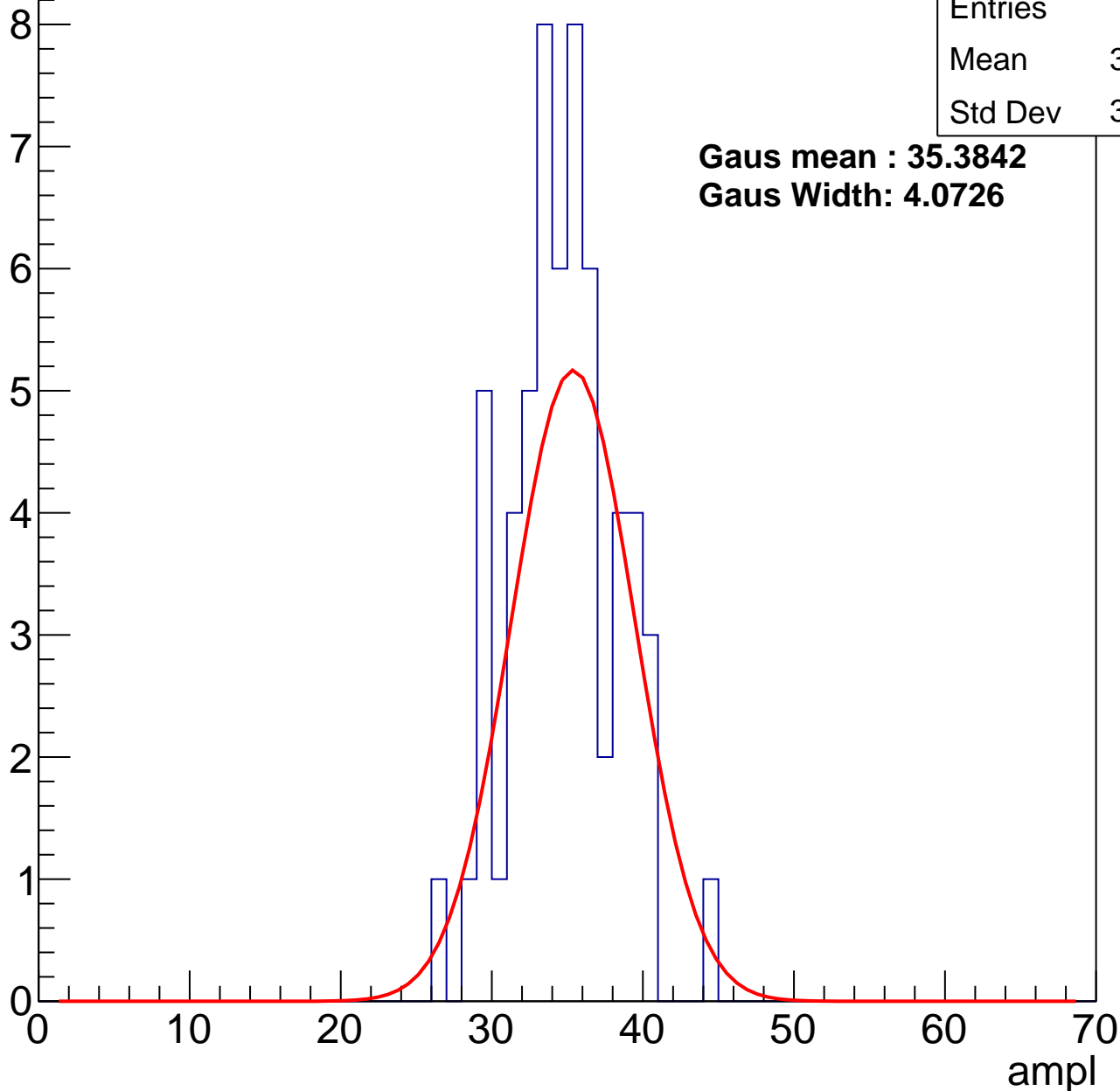
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	34.29
Std Dev	3.498

Gaus mean : 35.3842

Gaus Width: 4.0726



B1L003S, U16-ch94, adc2

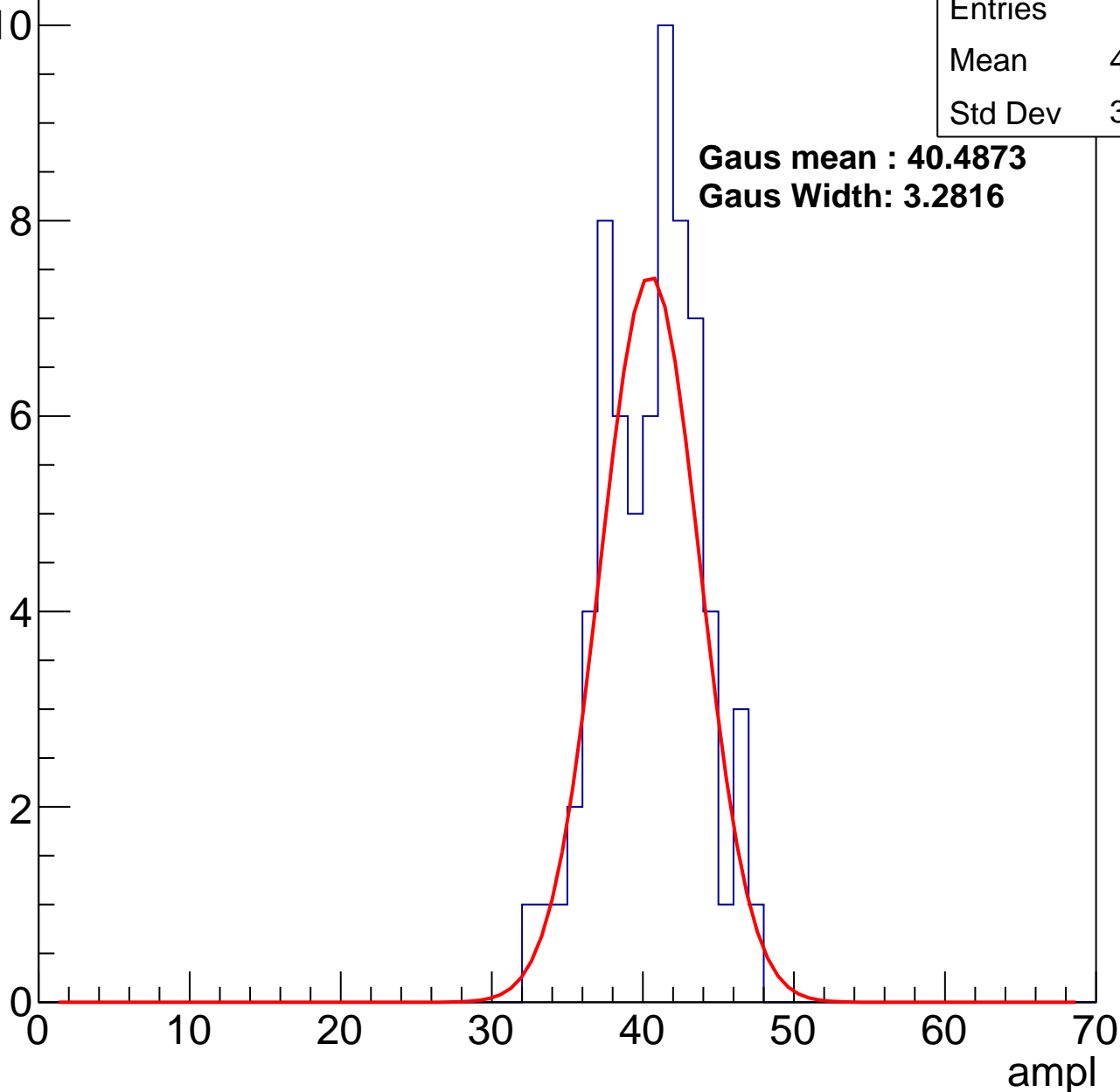
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	40.07
Std Dev	3.242

Gaus mean : 40.4873

Gaus Width: 3.2816

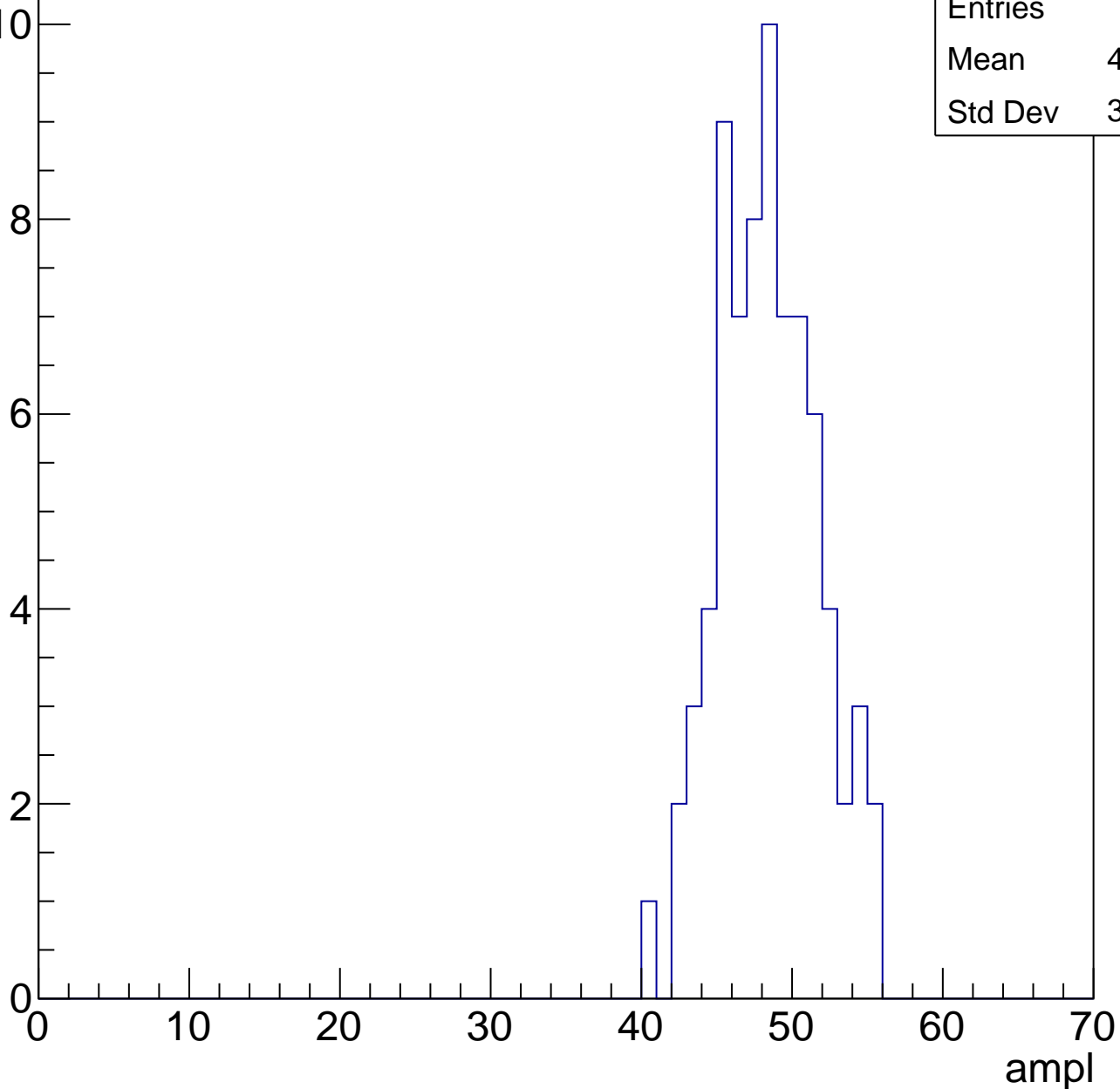


B1L003S, U16-ch94, adc3

calib_packv5_042523_0143.root, FC#13, port D2

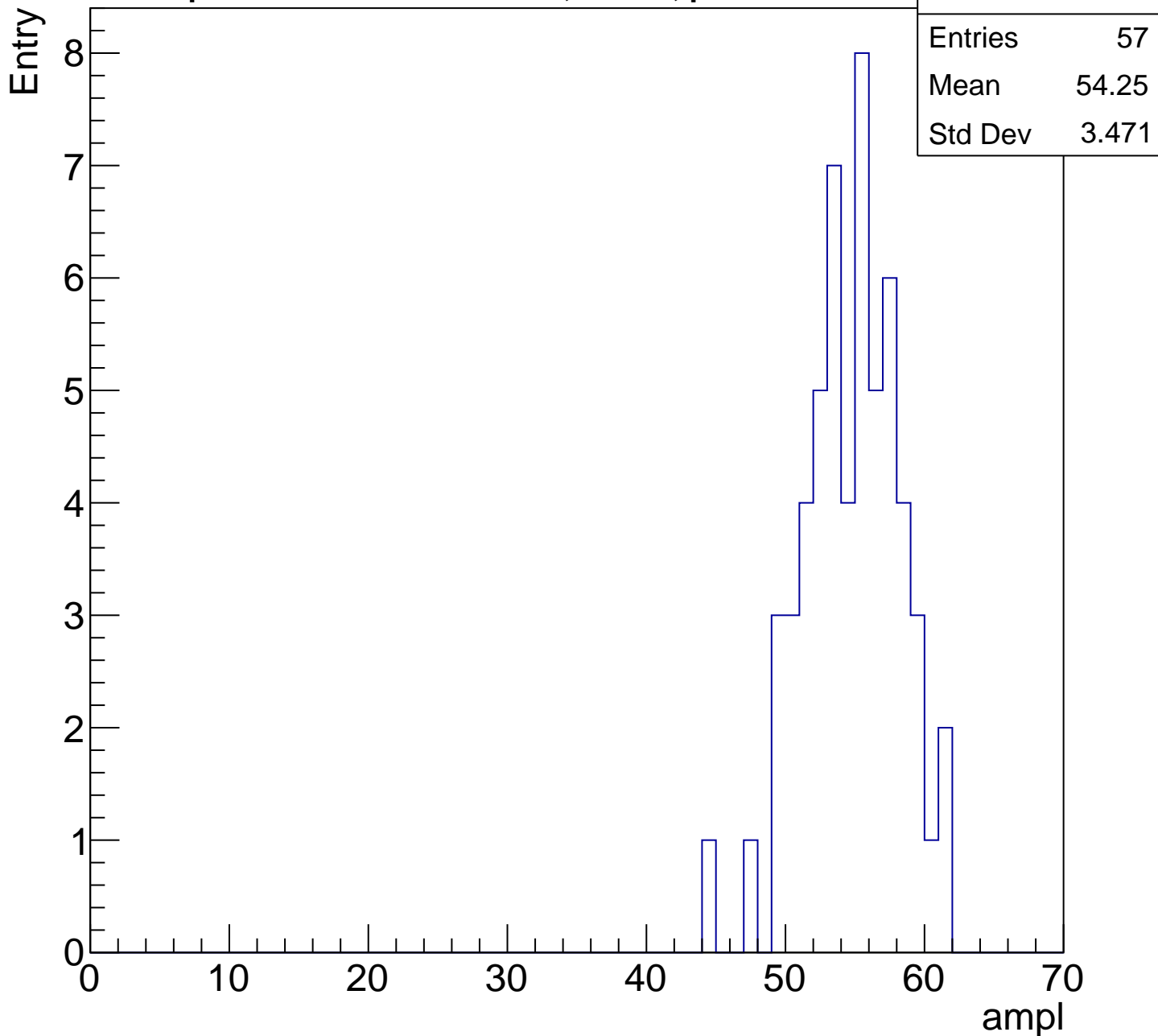
Entry

Entries	75
Mean	47.96
Std Dev	3.268



B1L003S, U16-ch94, adc4

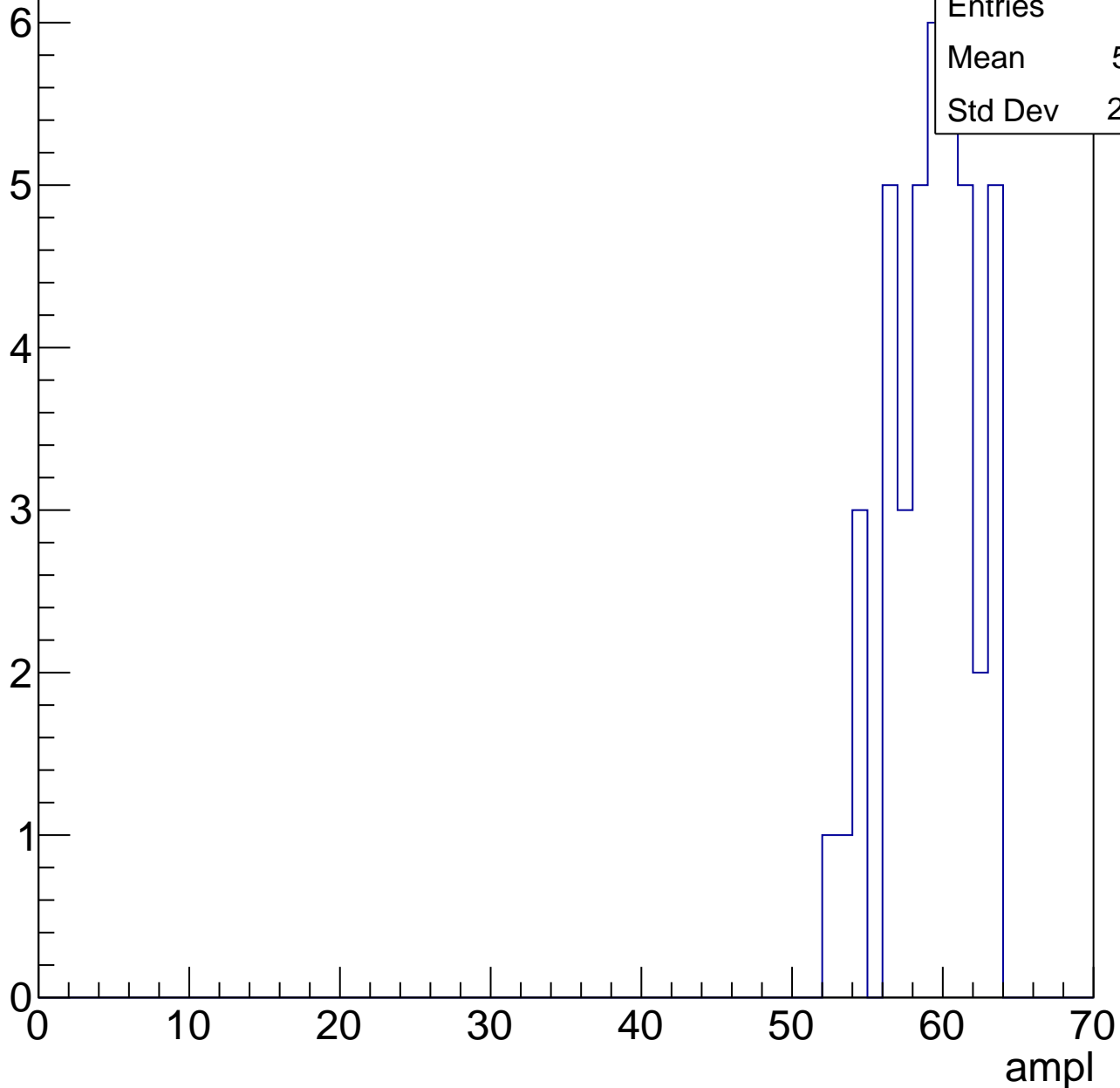
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch94, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

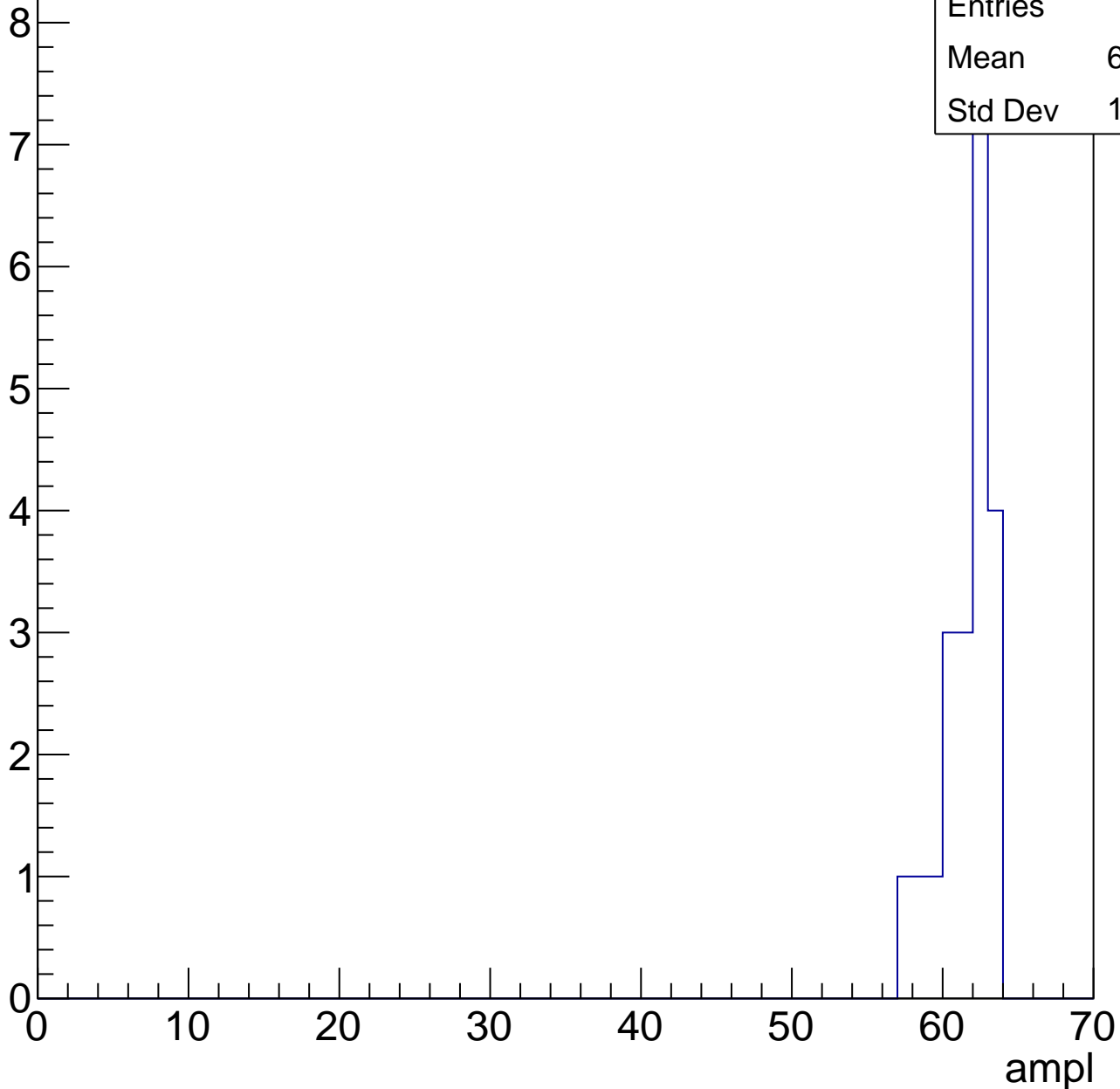


B1L003S, U16-ch94, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	21
Mean	61.19
Std Dev	1.622



B1L003S, U16-ch94, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U16-ch95, adc0

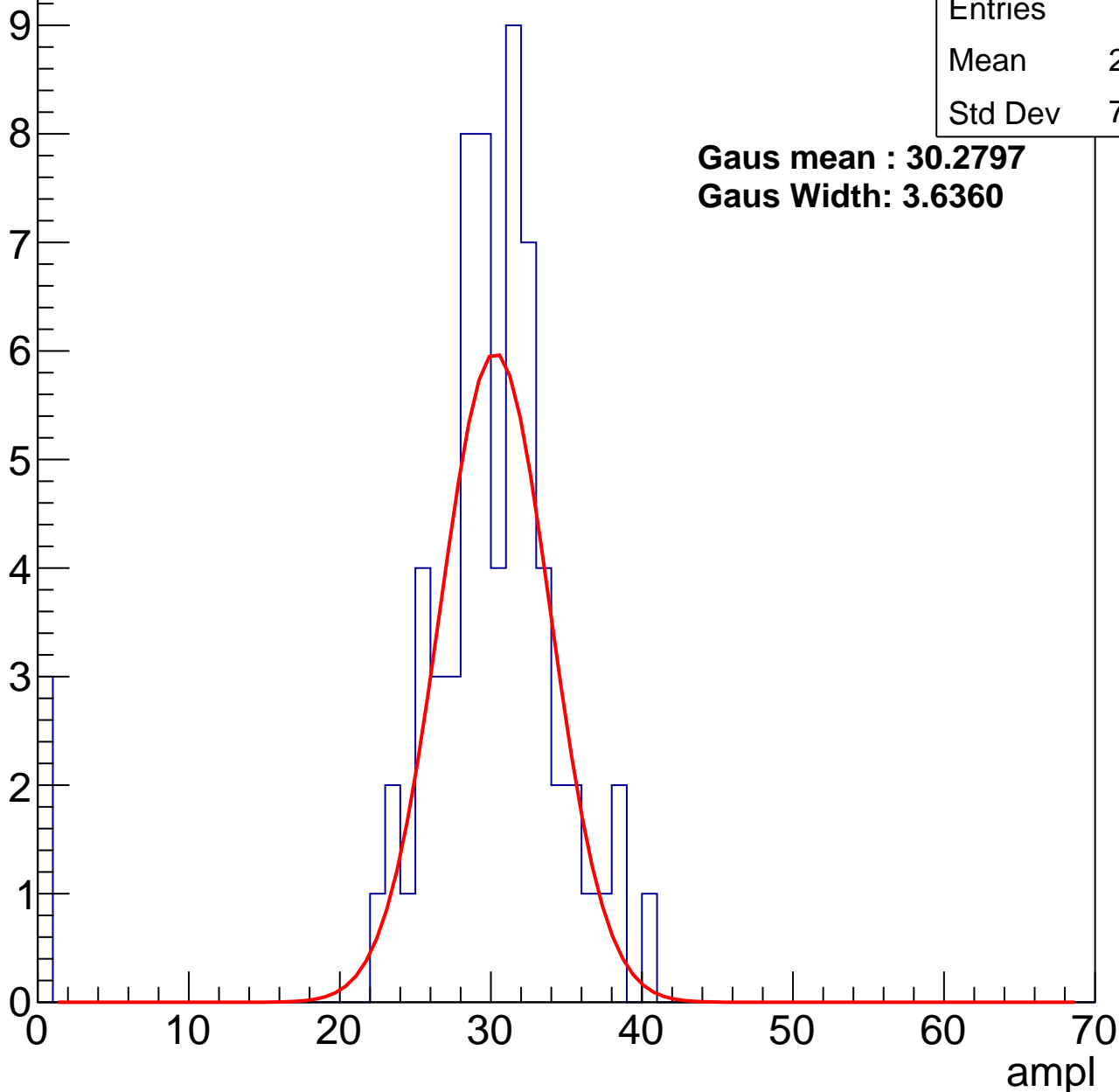
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.62
Std Dev	7.236

Gaus mean : 30.2797

Gaus Width: 3.6360



B1L003S, U16-ch95, adc1

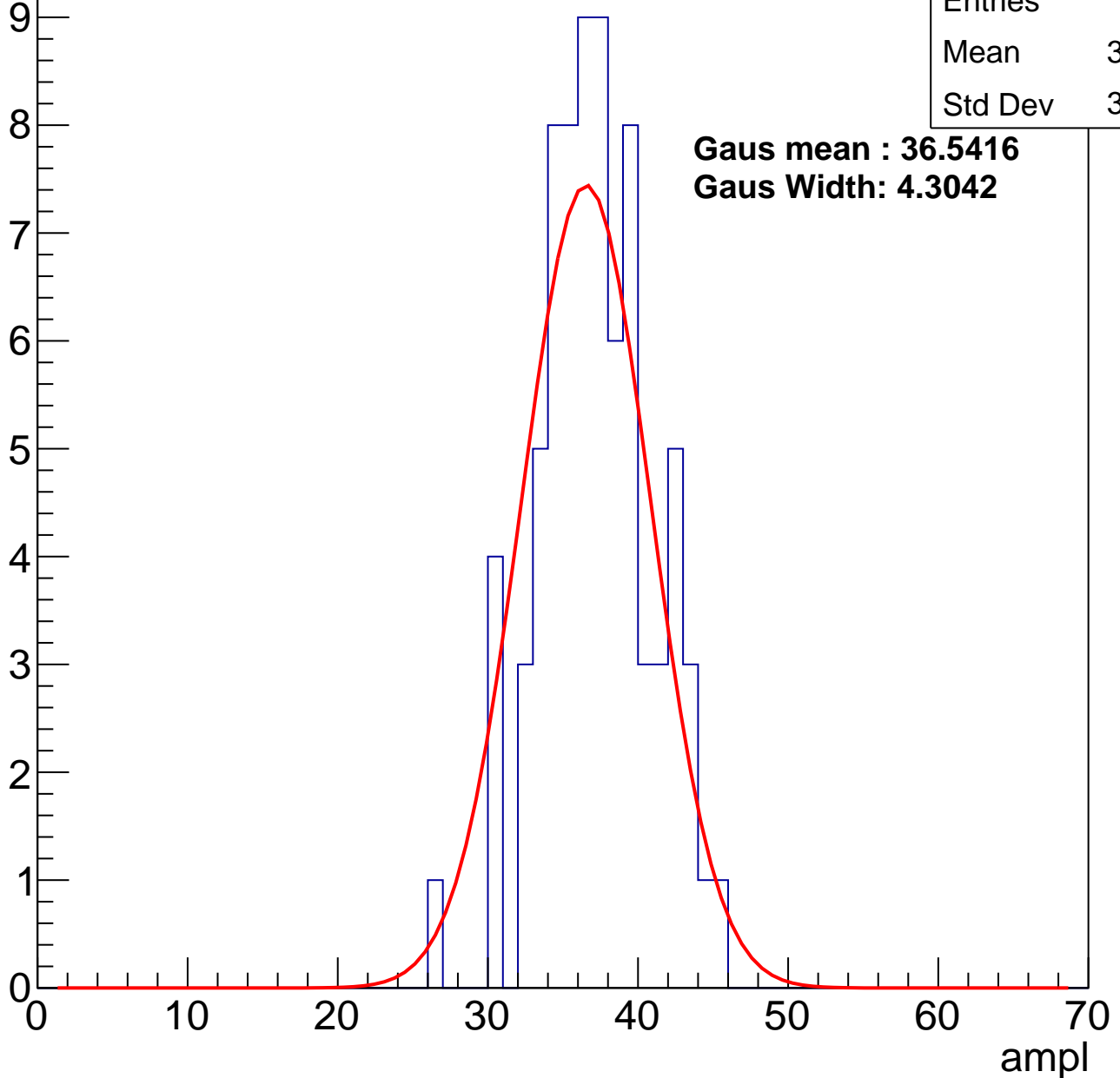
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	36.71
Std Dev	3.671

Gaus mean : 36.5416

Gaus Width: 4.3042



B1L003S, U16-ch95, adc2

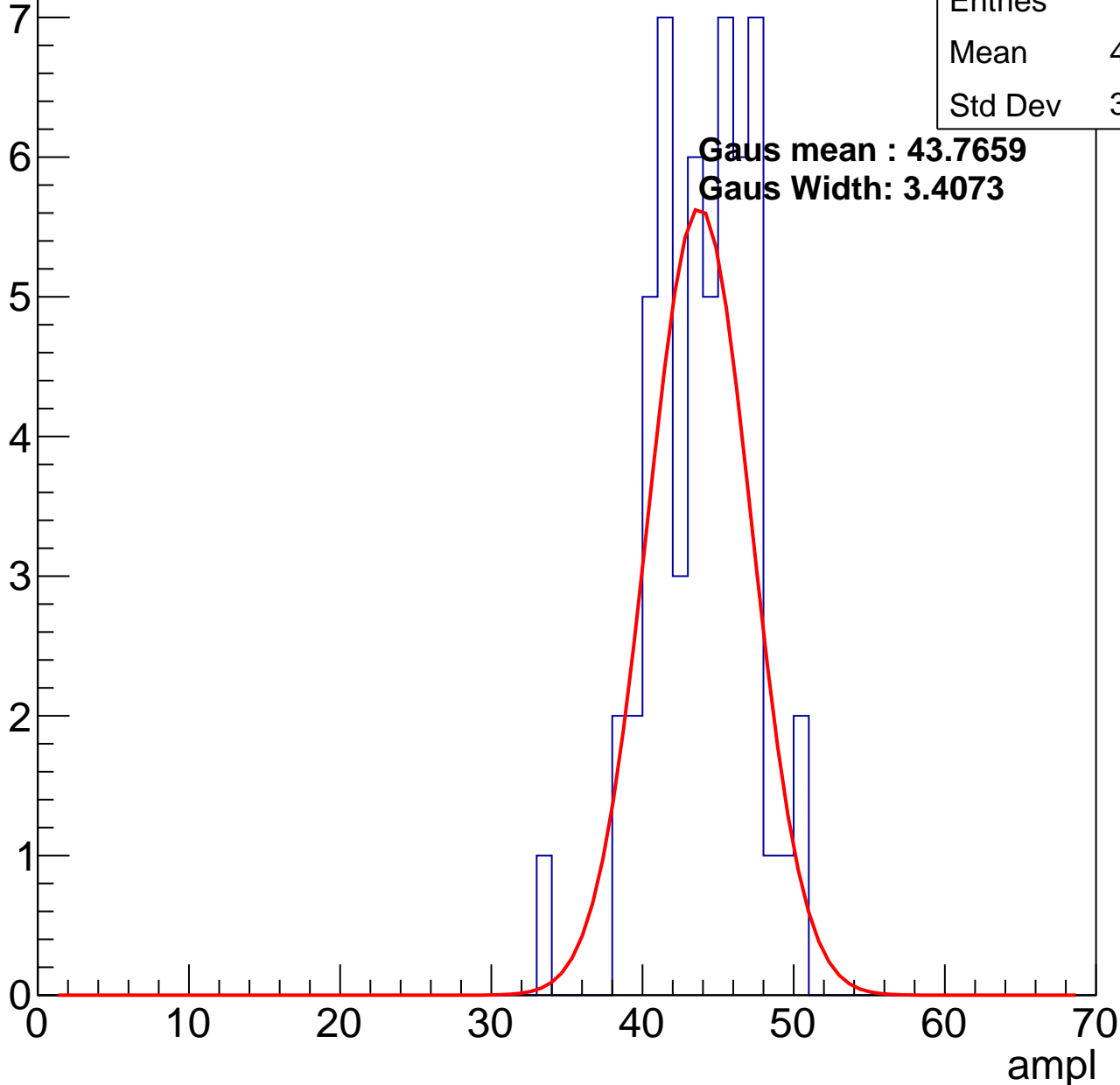
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.55
Std Dev	3.302

Gaus mean : 43.7659

Gaus Width: 3.4073

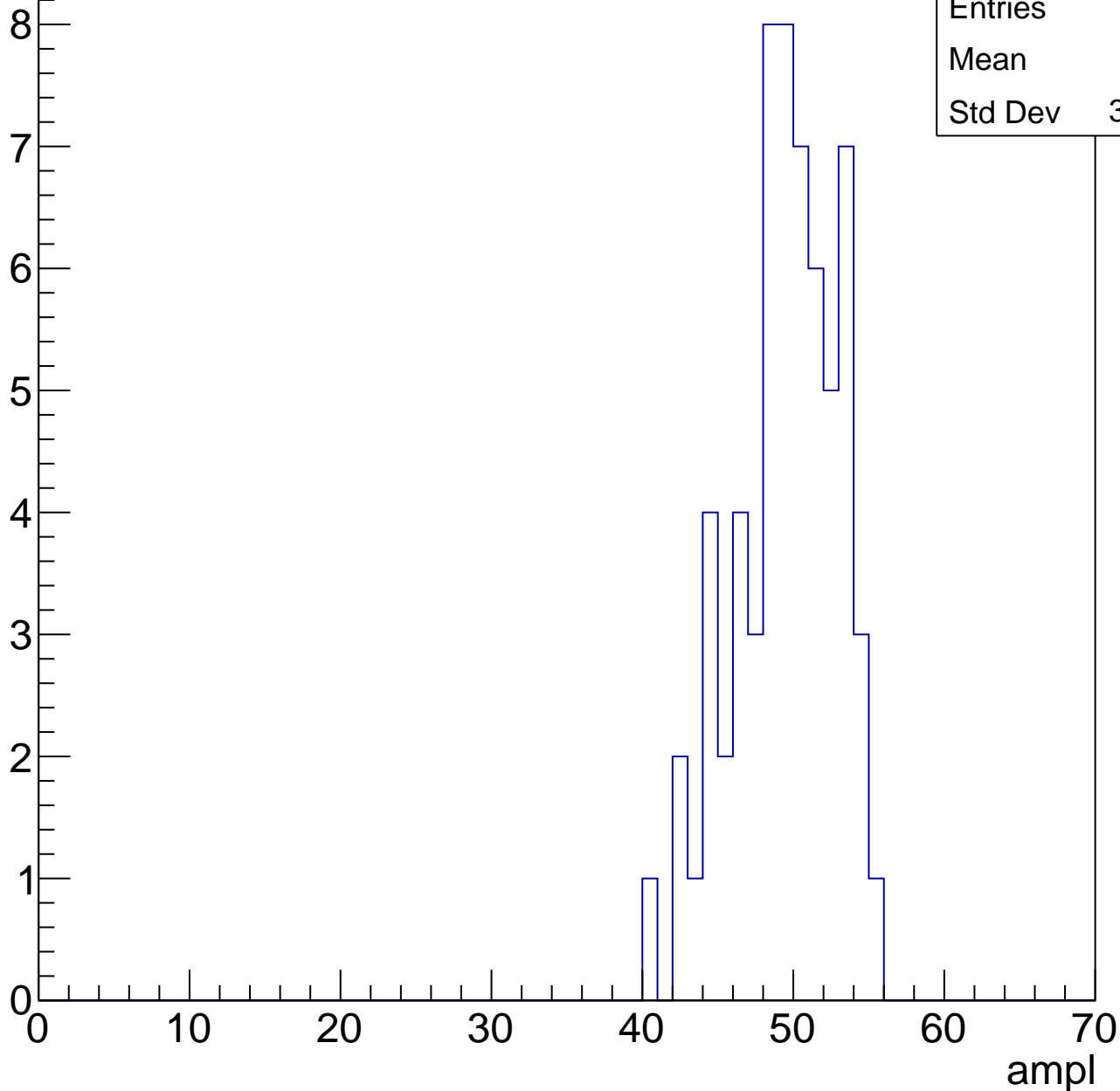


B1L003S, U16-ch95, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49
Std Dev	3.365

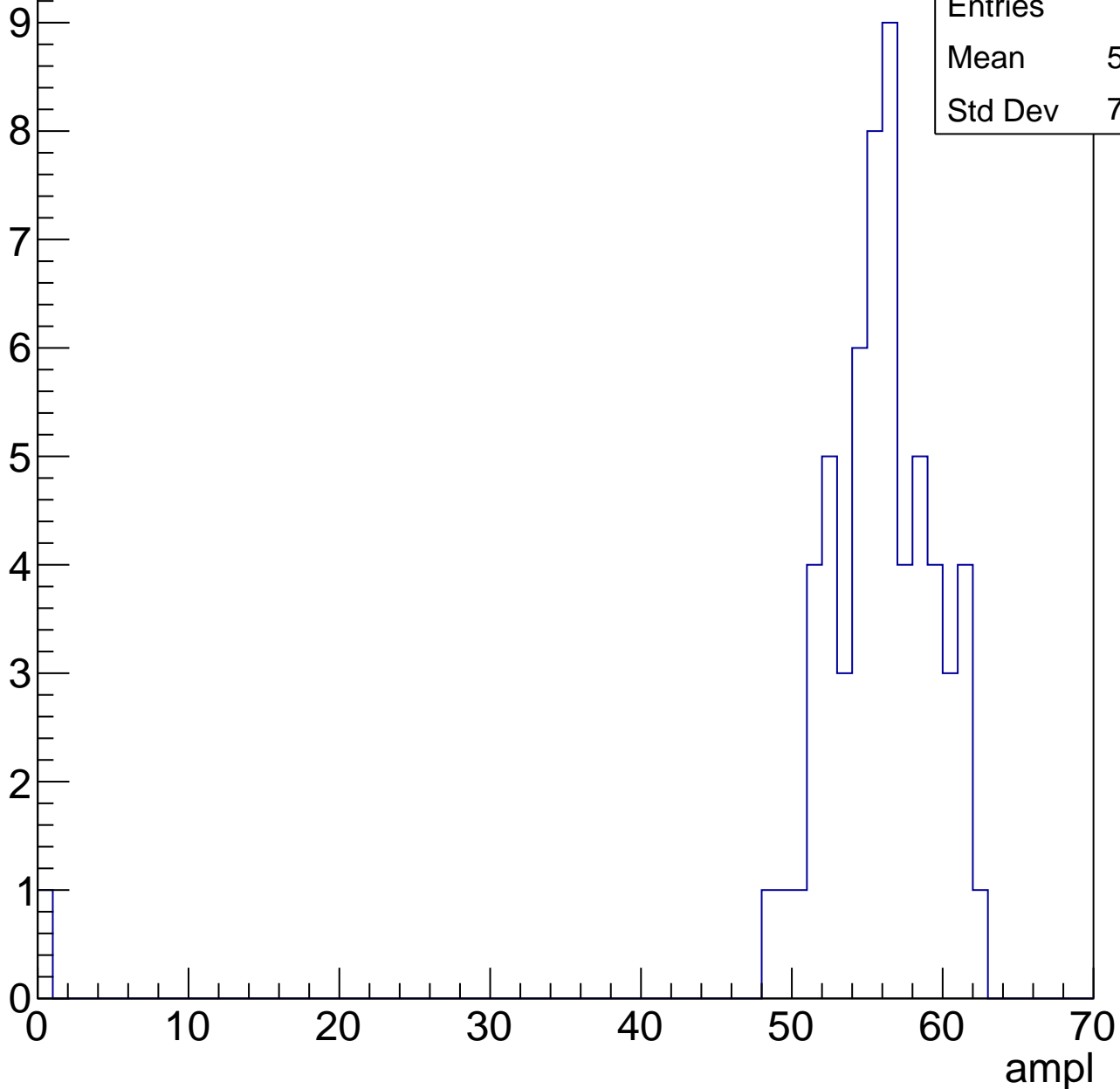


B1L003S, U16-ch95, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.63
Std Dev	7.806



B1L003S, U16-ch95, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries

42

Mean

59.62

Std Dev

2.267

ampl

0

10

20

30

40

50

60

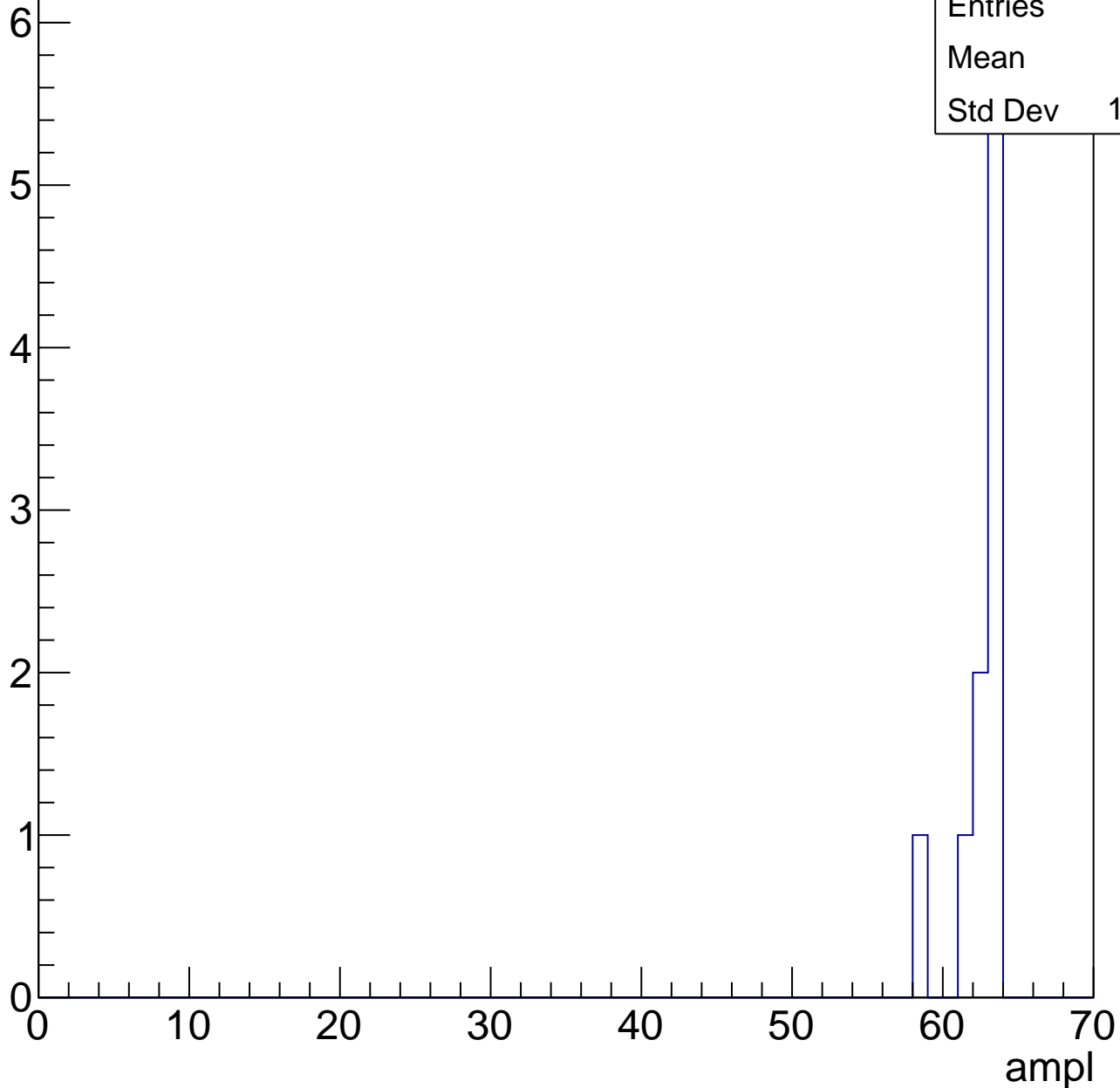
70

B1L003S, U16-ch95, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	62.1
Std Dev	1.513



B1L003S, U16-ch95, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch96, adc0

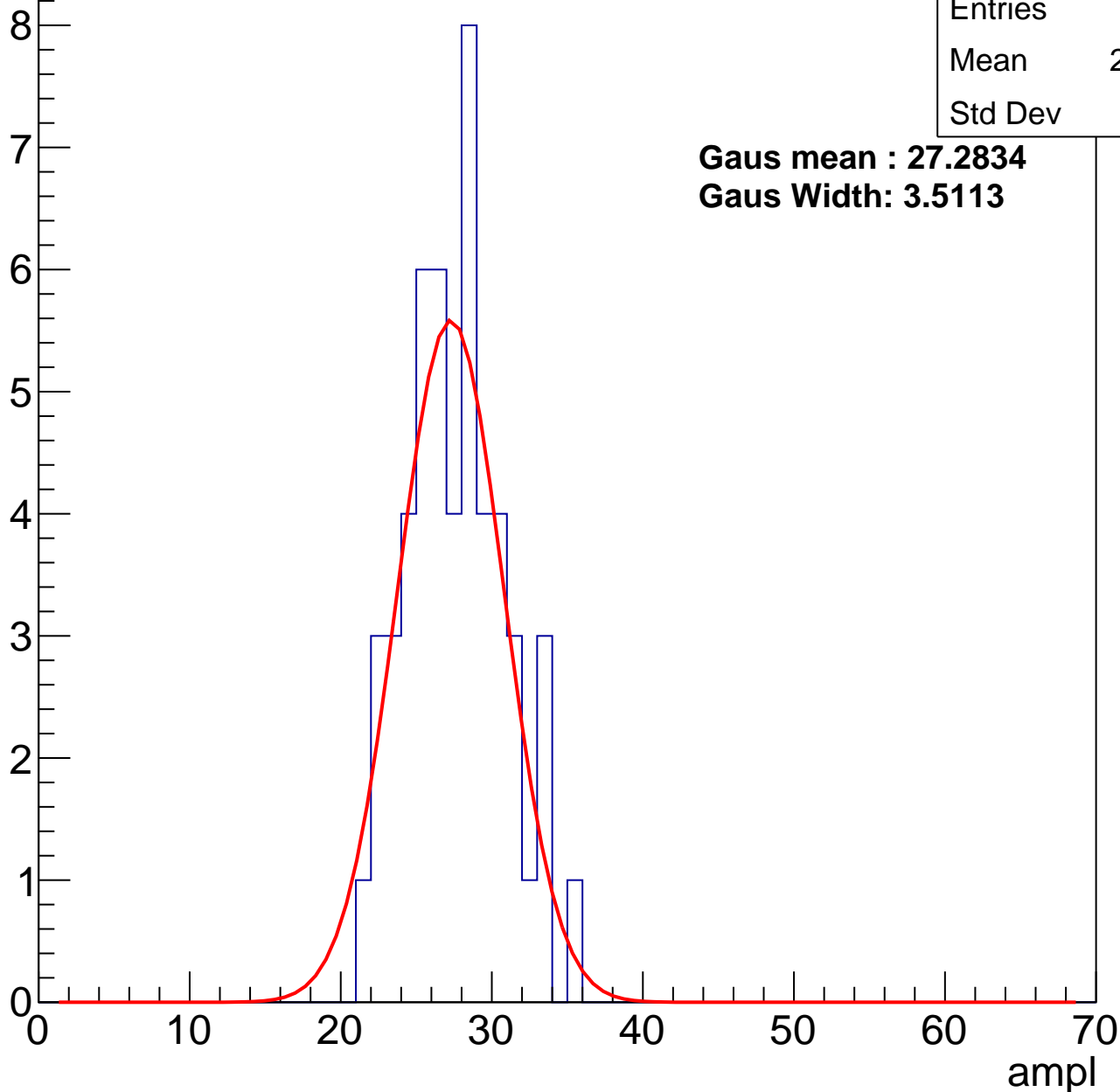
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	27.16
Std Dev	3.22

Gaus mean : 27.2834

Gaus Width: 3.5113



B1L003S, U16-ch96, adc1

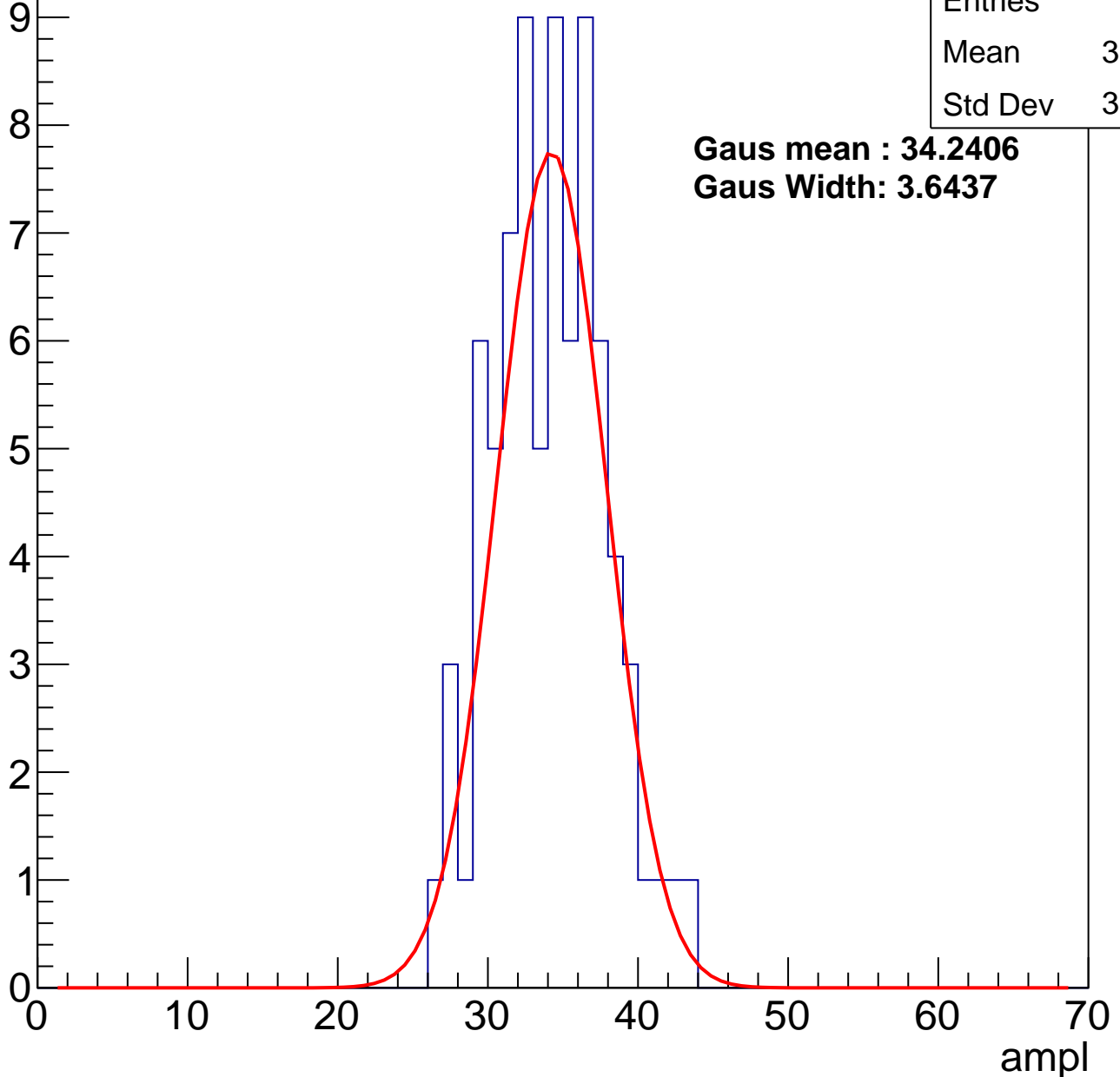
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	33.67
Std Dev	3.647

Gaus mean : 34.2406

Gaus Width: 3.6437



B1L003S, U16-ch96, adc2

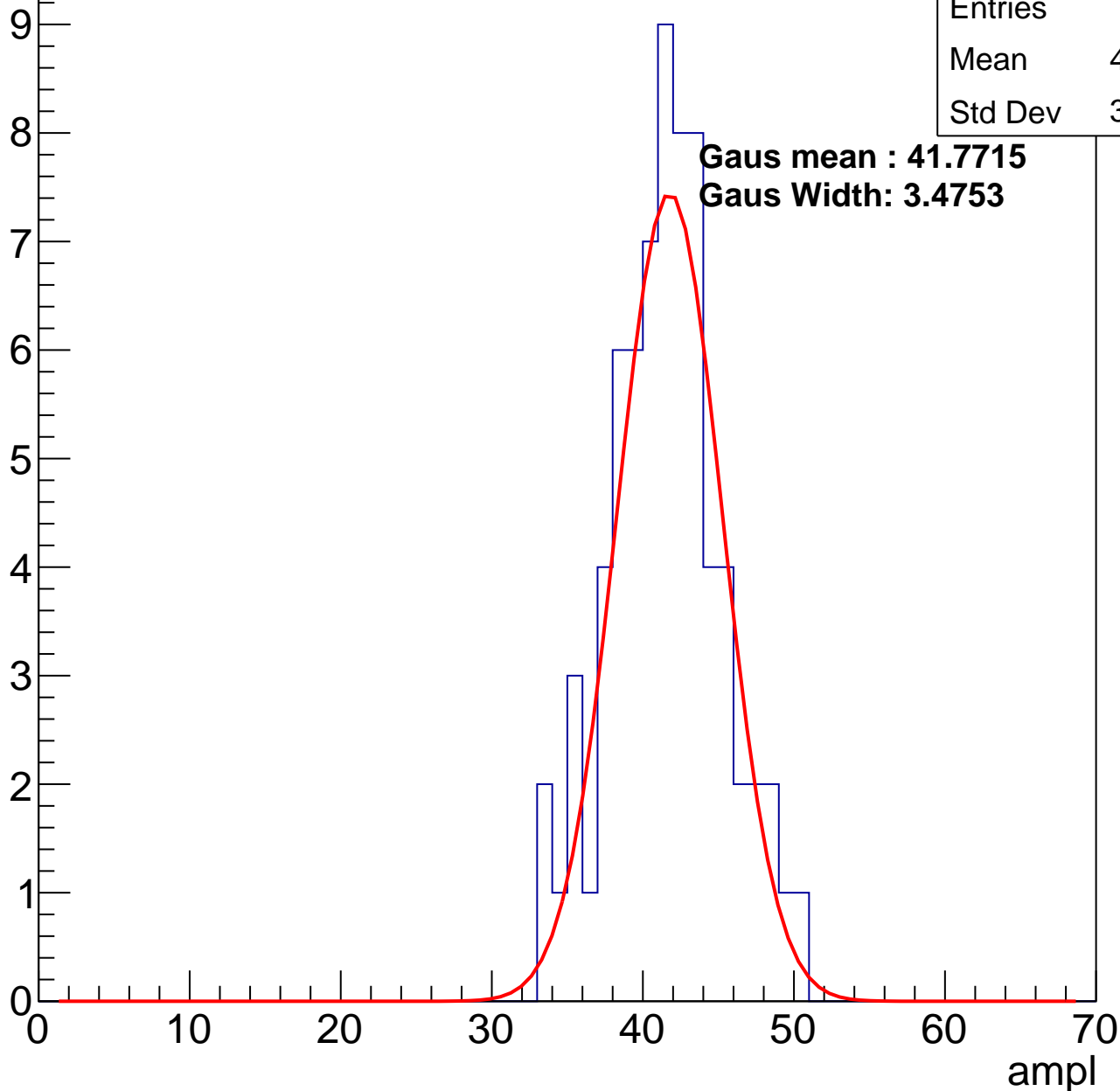
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.08
Std Dev	3.684

Gaus mean : 41.7715

Gaus Width: 3.4753

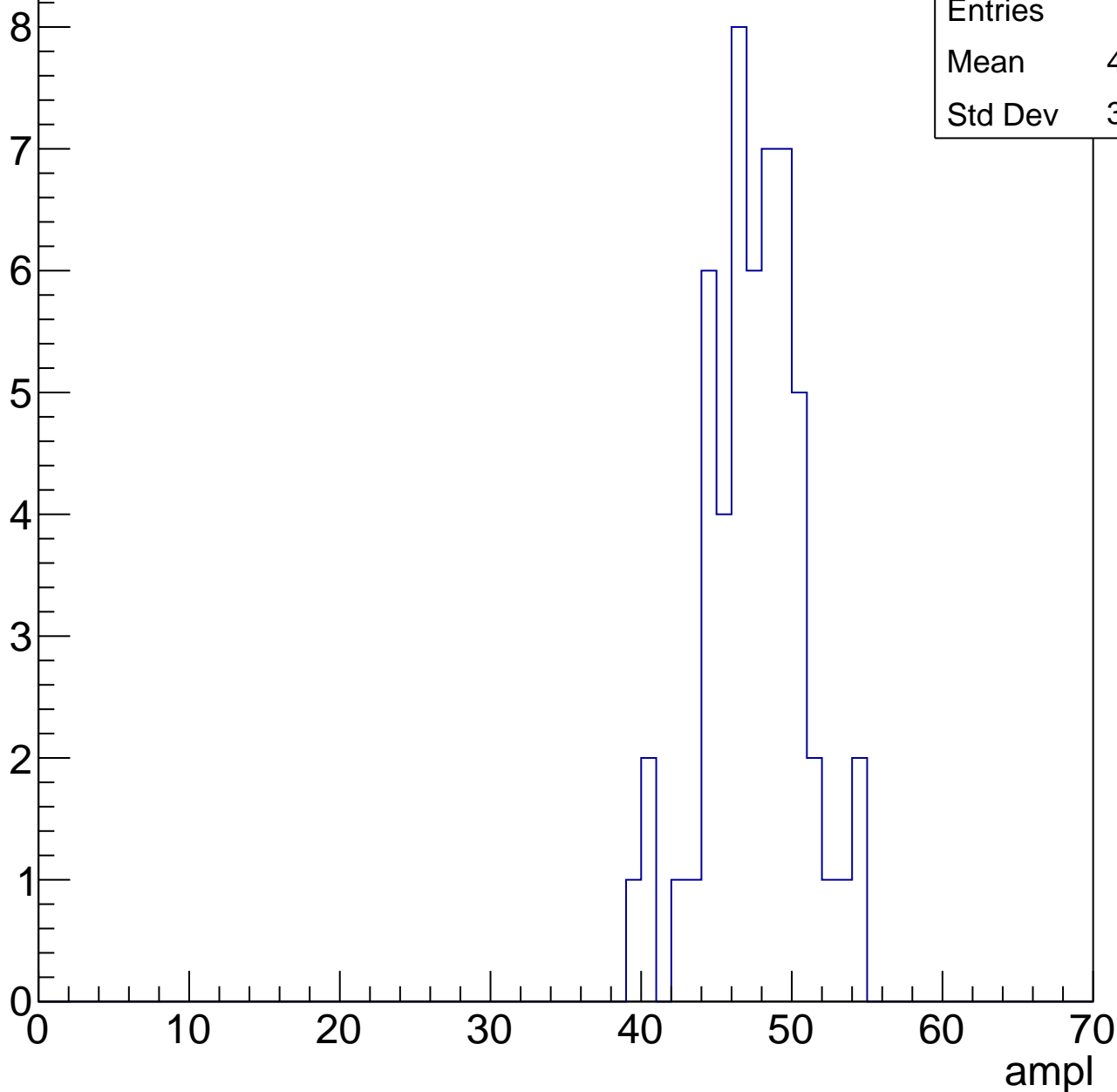


B1L003S, U16-ch96, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	47.07
Std Dev	3.196

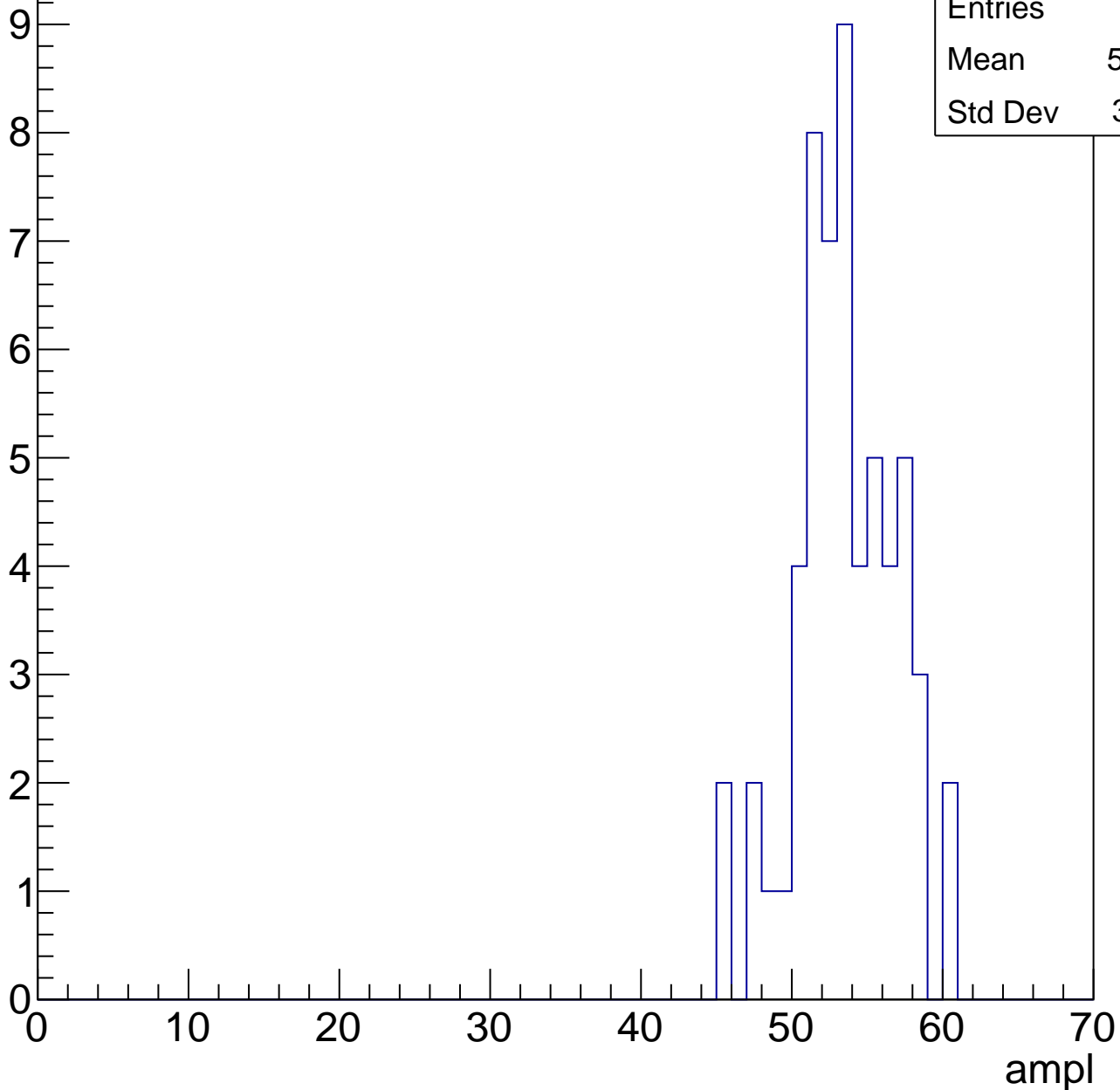


B1L003S, U16-ch96, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.05
Std Dev	3.321

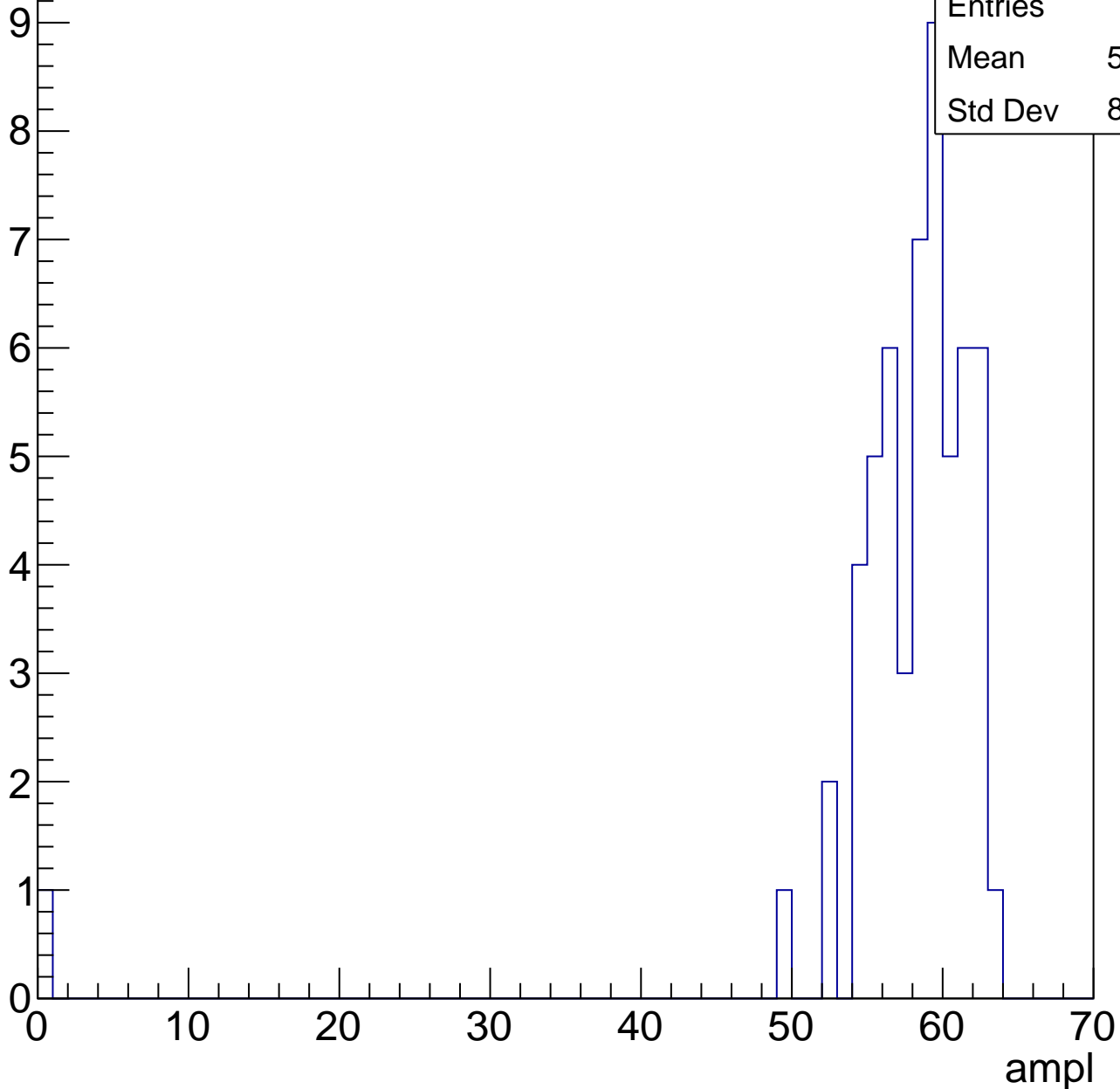


B1L003S, U16-ch96, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	56.95
Std Dev	8.232

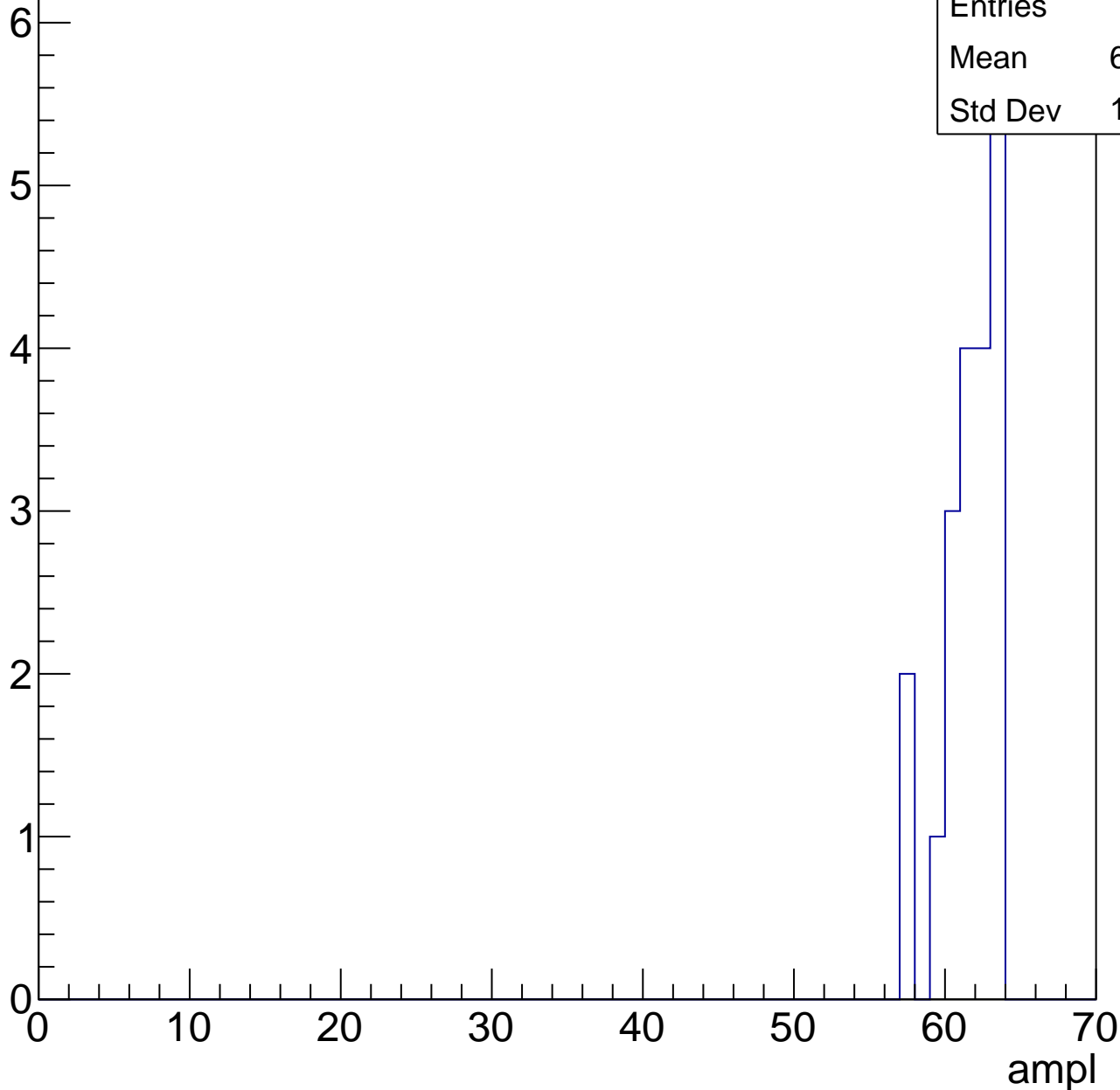


B1L003S, U16-ch96, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

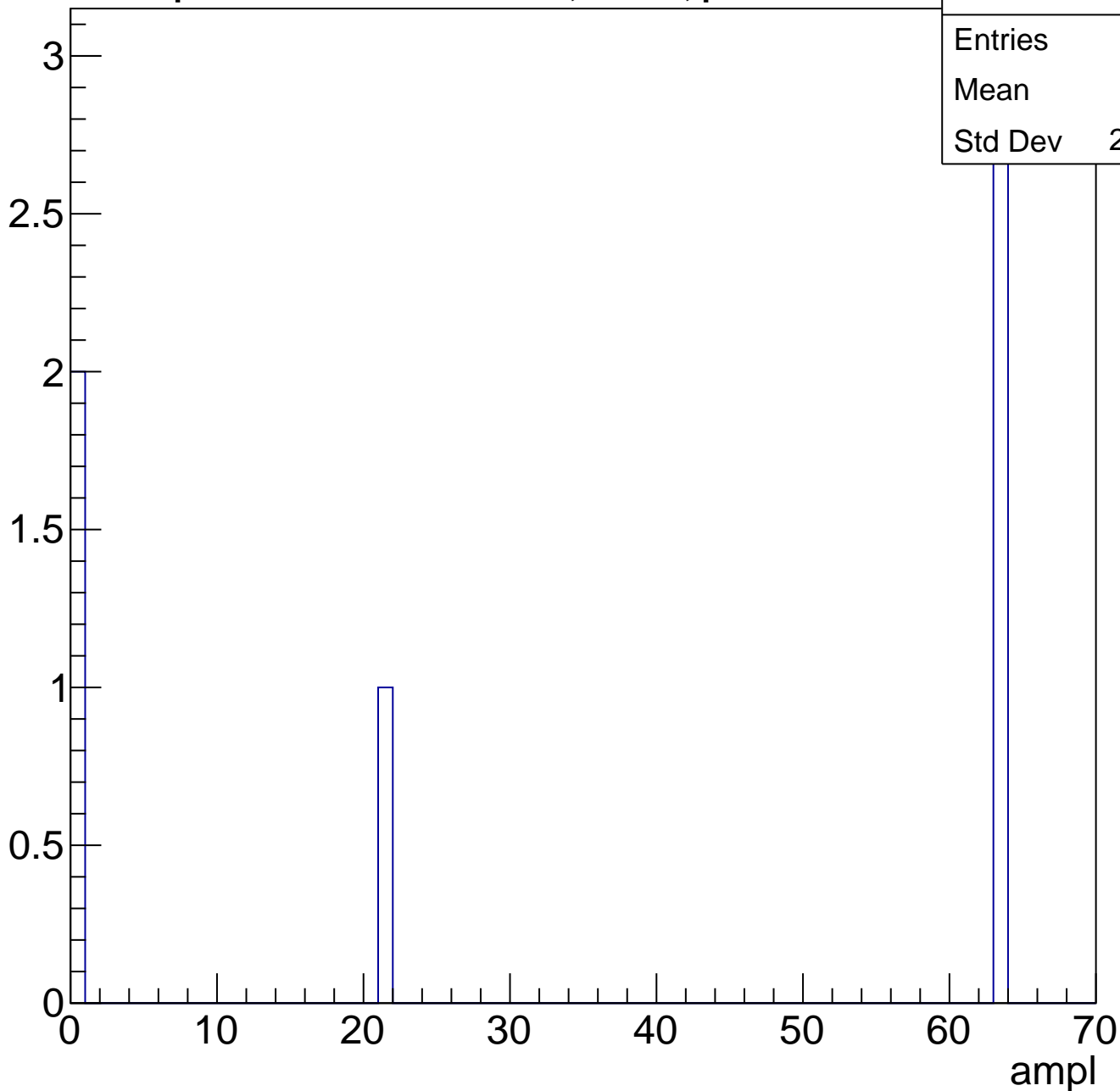
Entries	20
Mean	61.15
Std Dev	1.824



B1L003S, U16-ch96, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch97, adc0

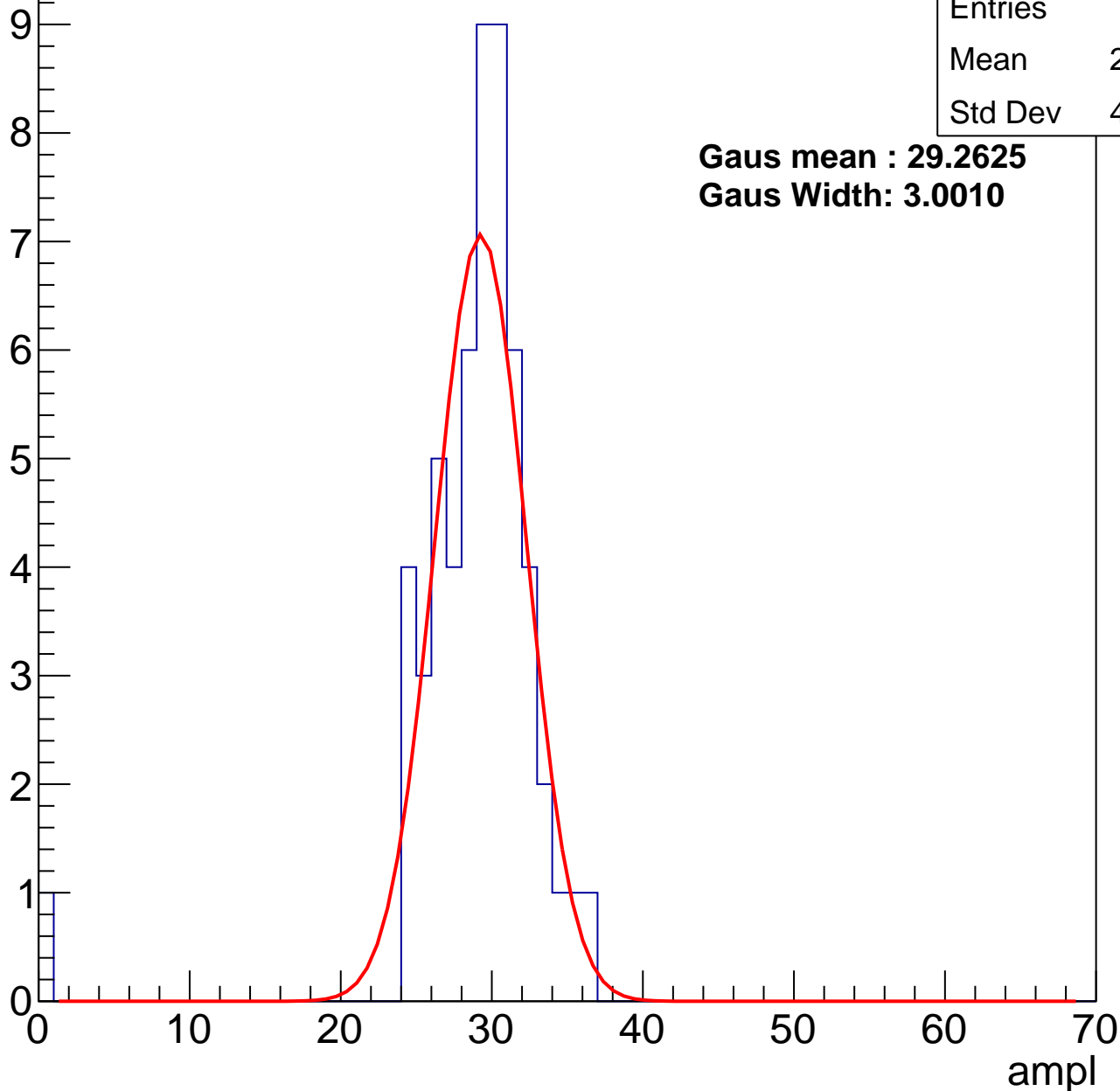
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	28.45
Std Dev	4.724

Gaus mean : 29.2625

Gaus Width: 3.0010

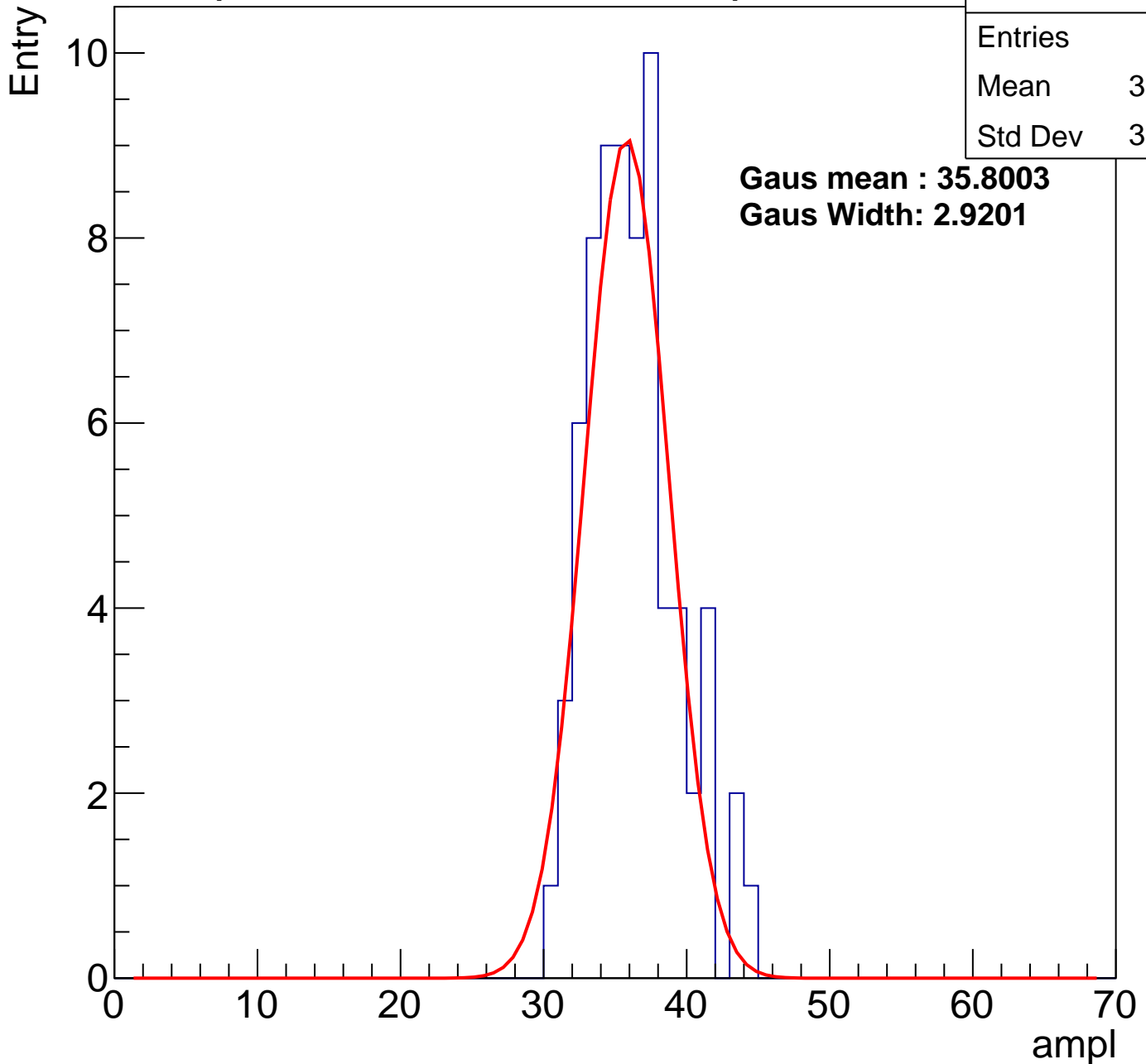


B1L003S, U16-ch97, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	35.77
Std Dev	3.087

Gaus mean : 35.8003
Gaus Width: 2.9201



B1L003S, U16-ch97, adc2

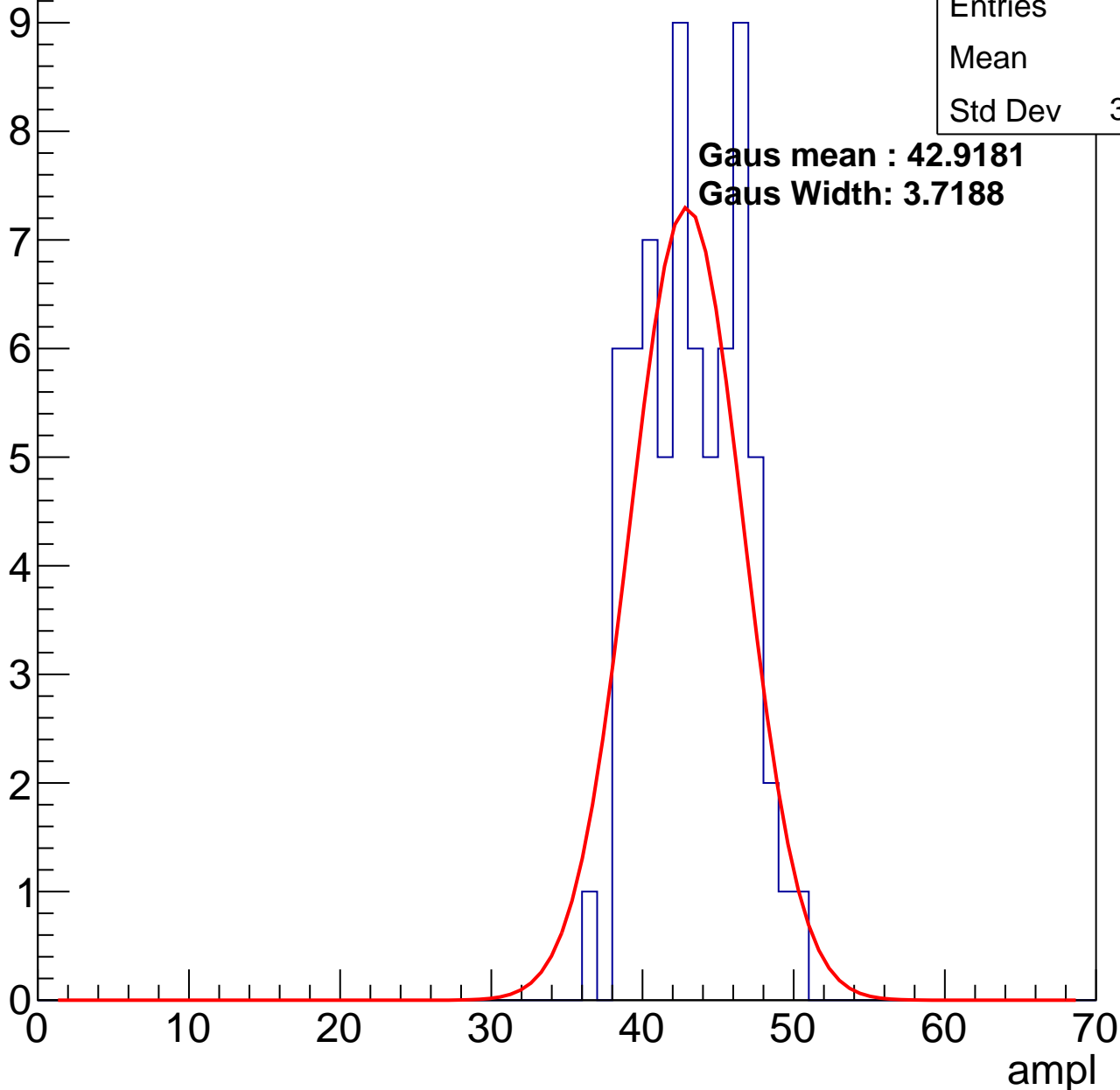
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.8
Std Dev	3.206

Gaus mean : 42.9181

Gaus Width: 3.7188

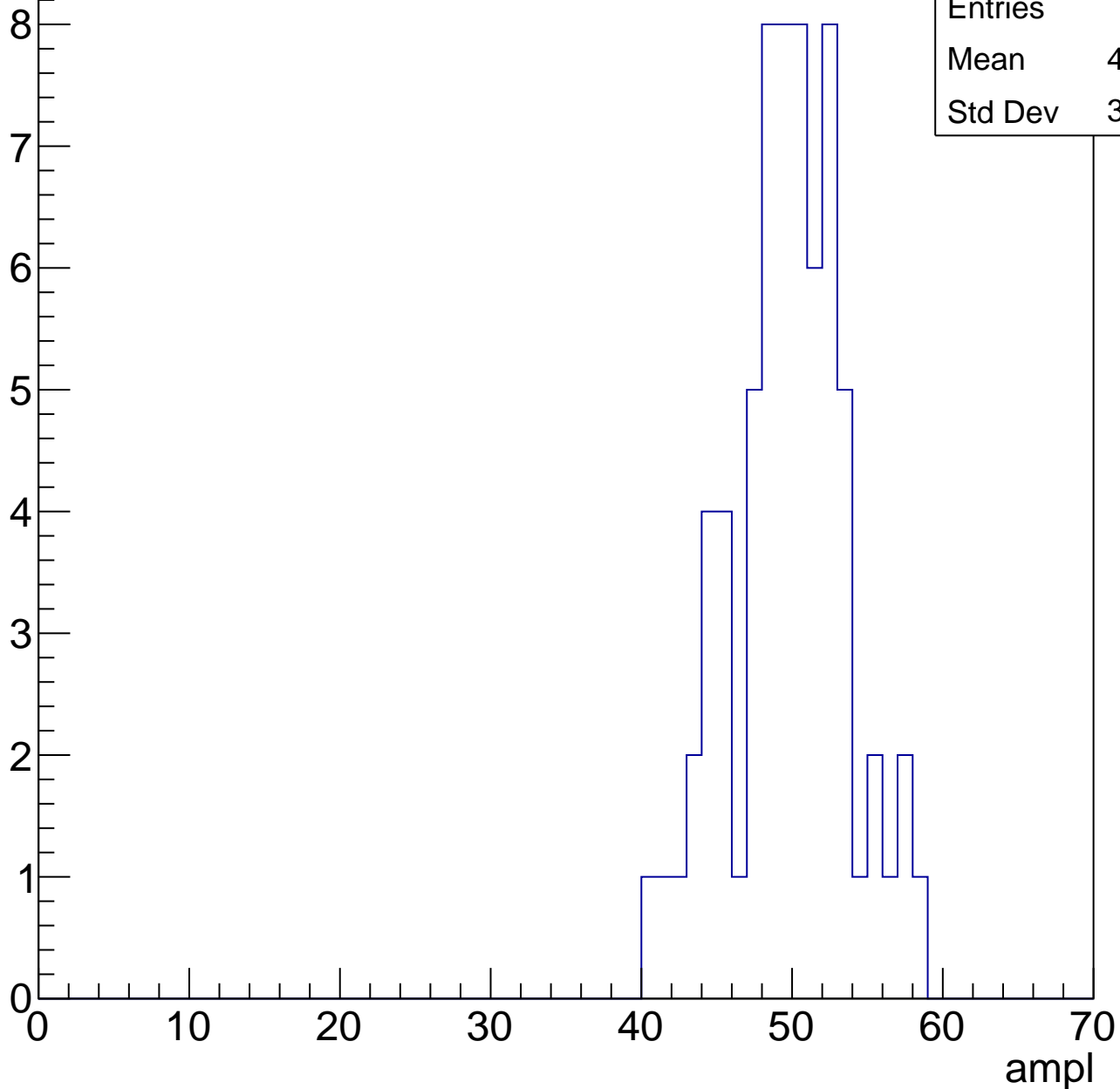


B1L003S, U16-ch97, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

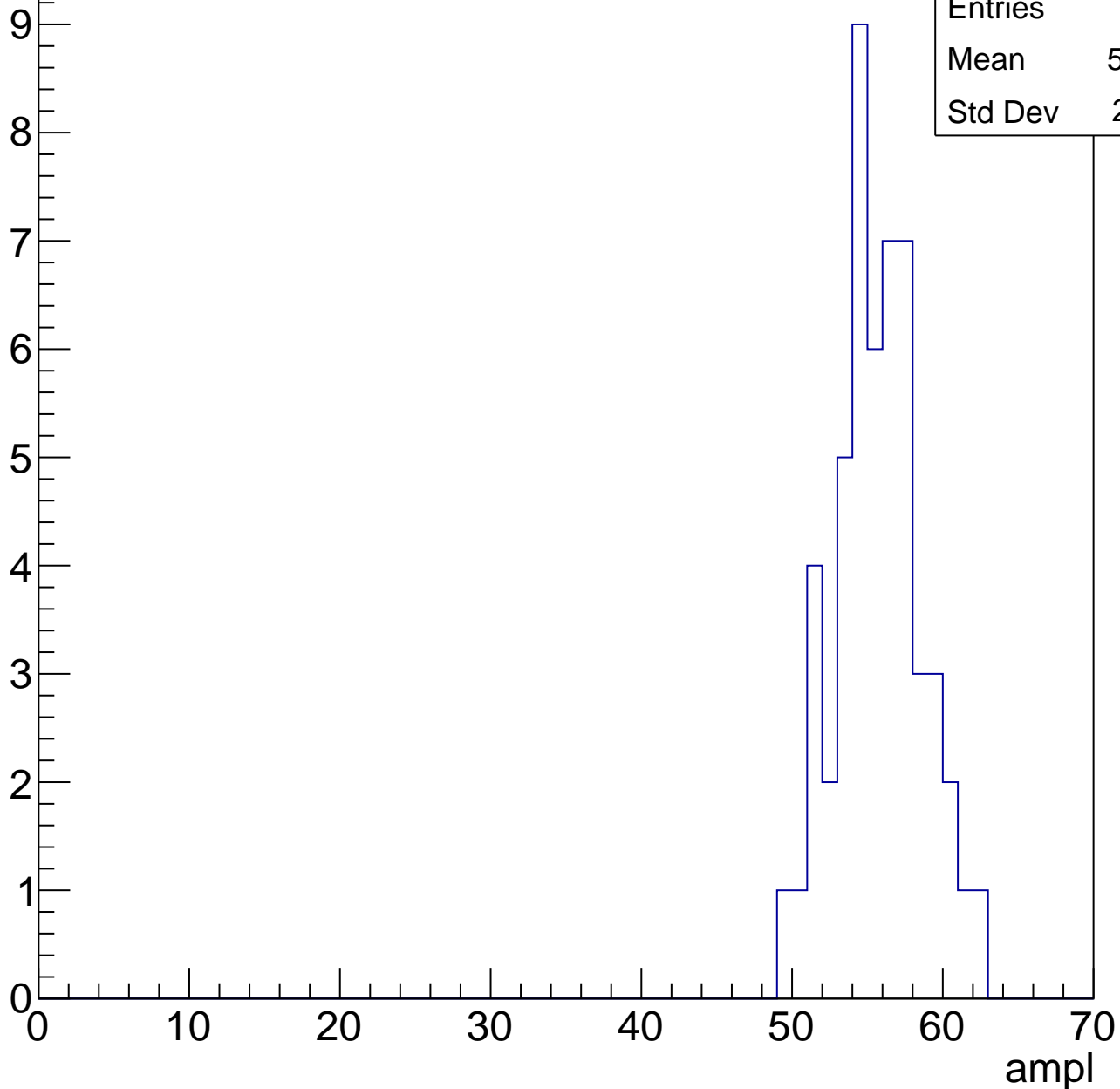
Entries	69
Mean	49.29
Std Dev	3.807



B1L003S, U16-ch97, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



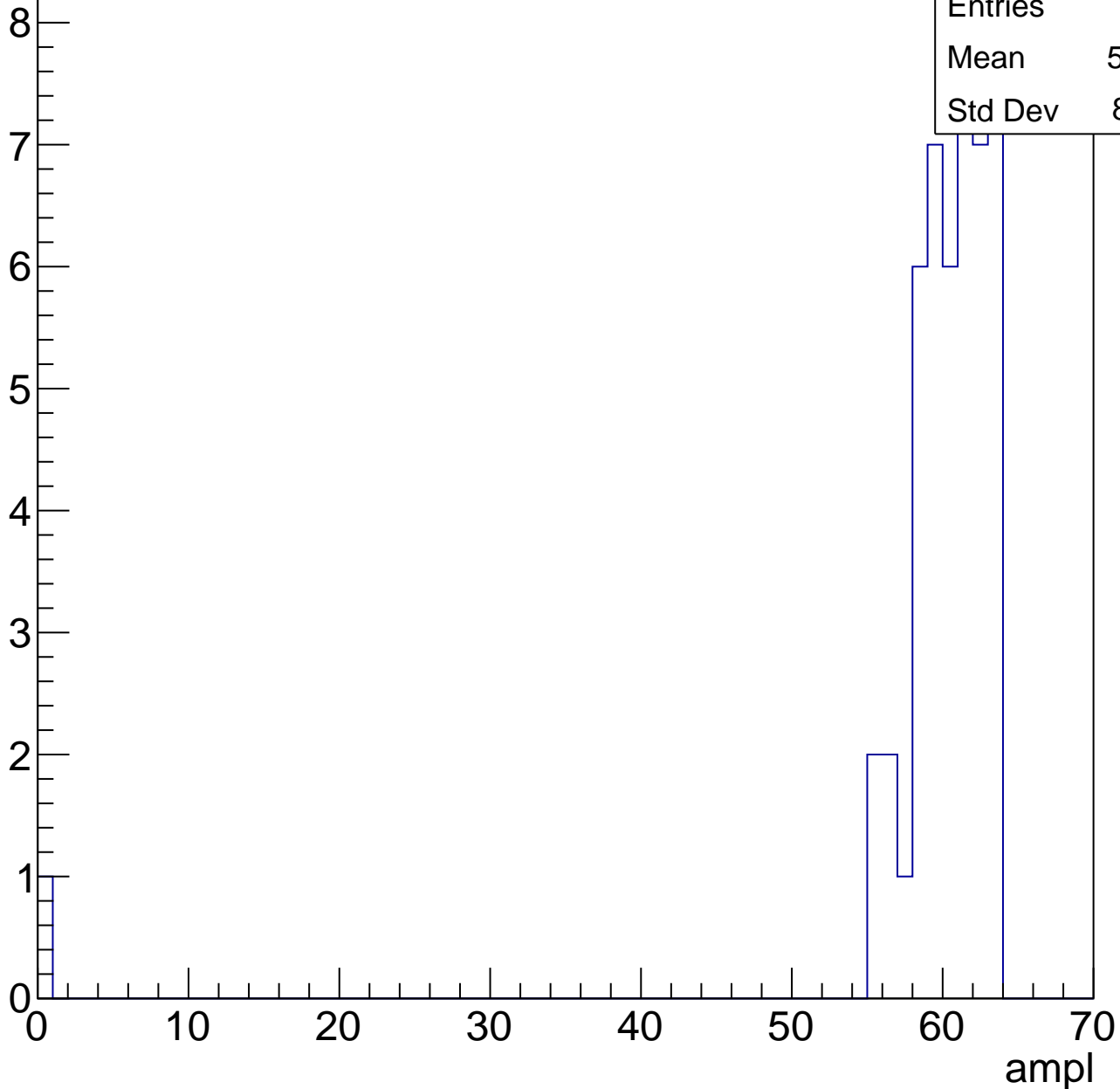
Entries	52
Mean	55.25
Std Dev	2.821

B1L003S, U16-ch97, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

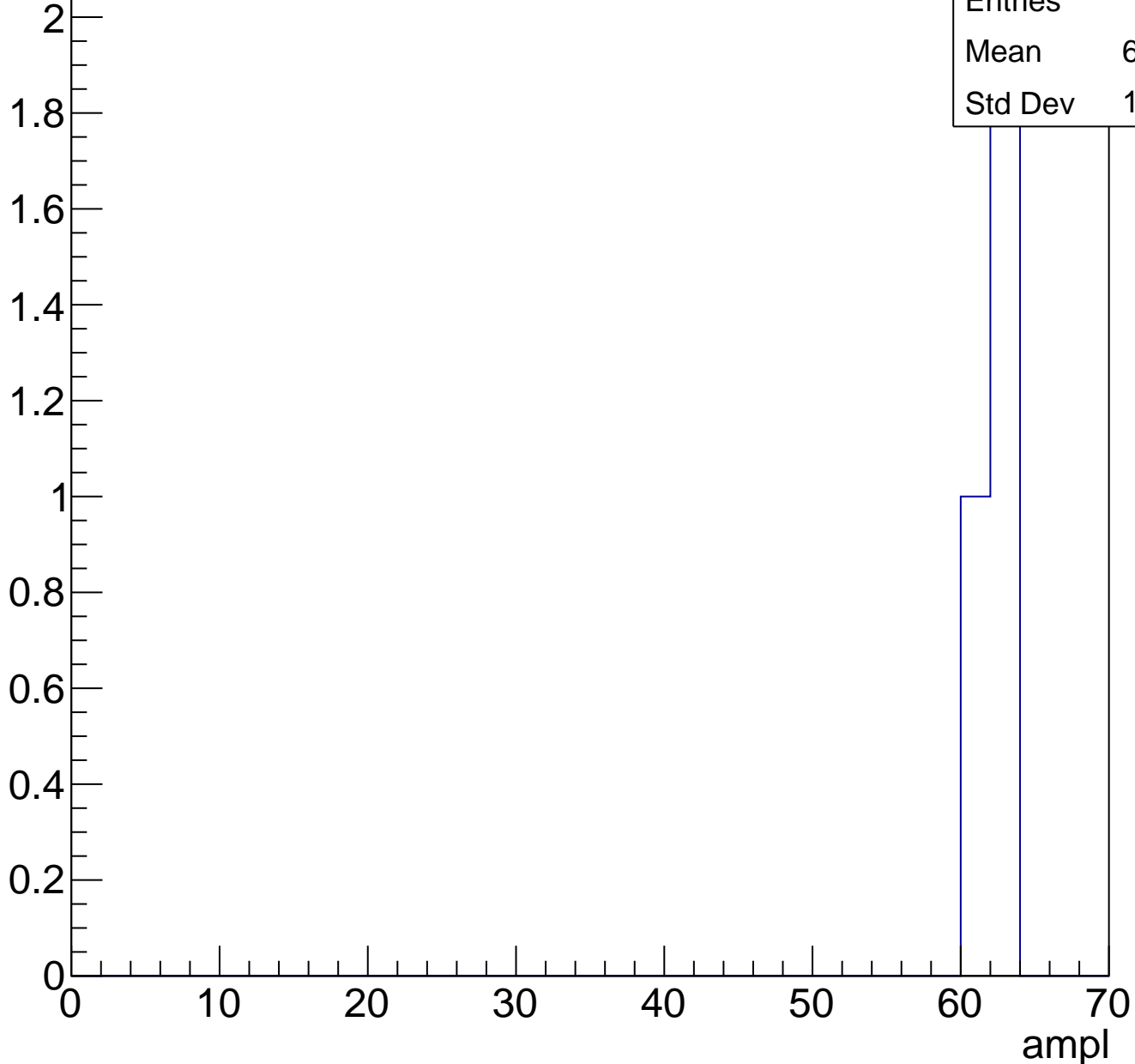
Entries	48
Mean	58.88
Std Dev	8.861



B1L003S, U16-ch97, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch97, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	60
Std Dev	0

B1L003S, U16-ch98, adc0

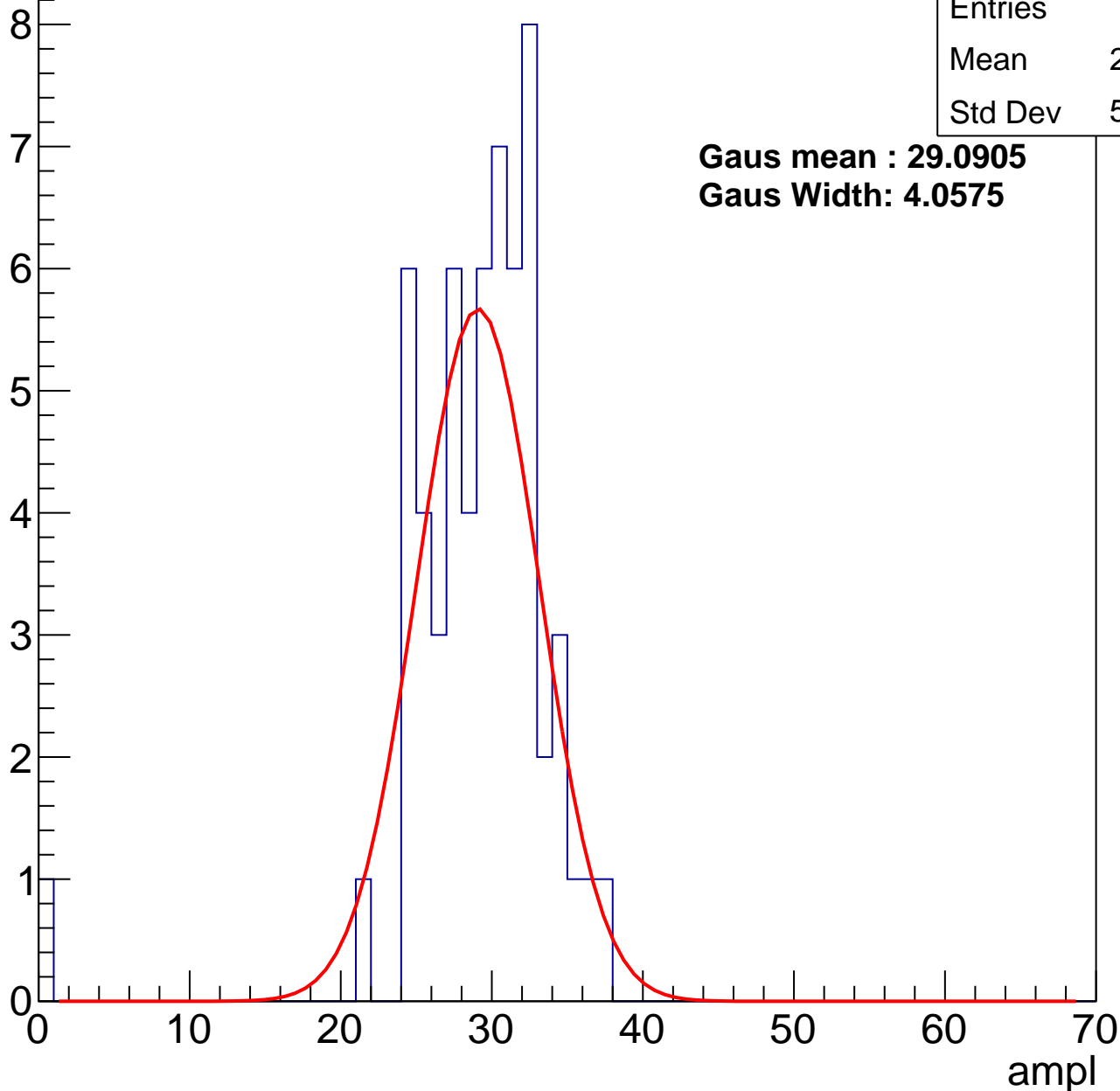
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.65
Std Dev	5.043

Gaus mean : 29.0905

Gaus Width: 4.0575



B1L003S, U16-ch98, adc1

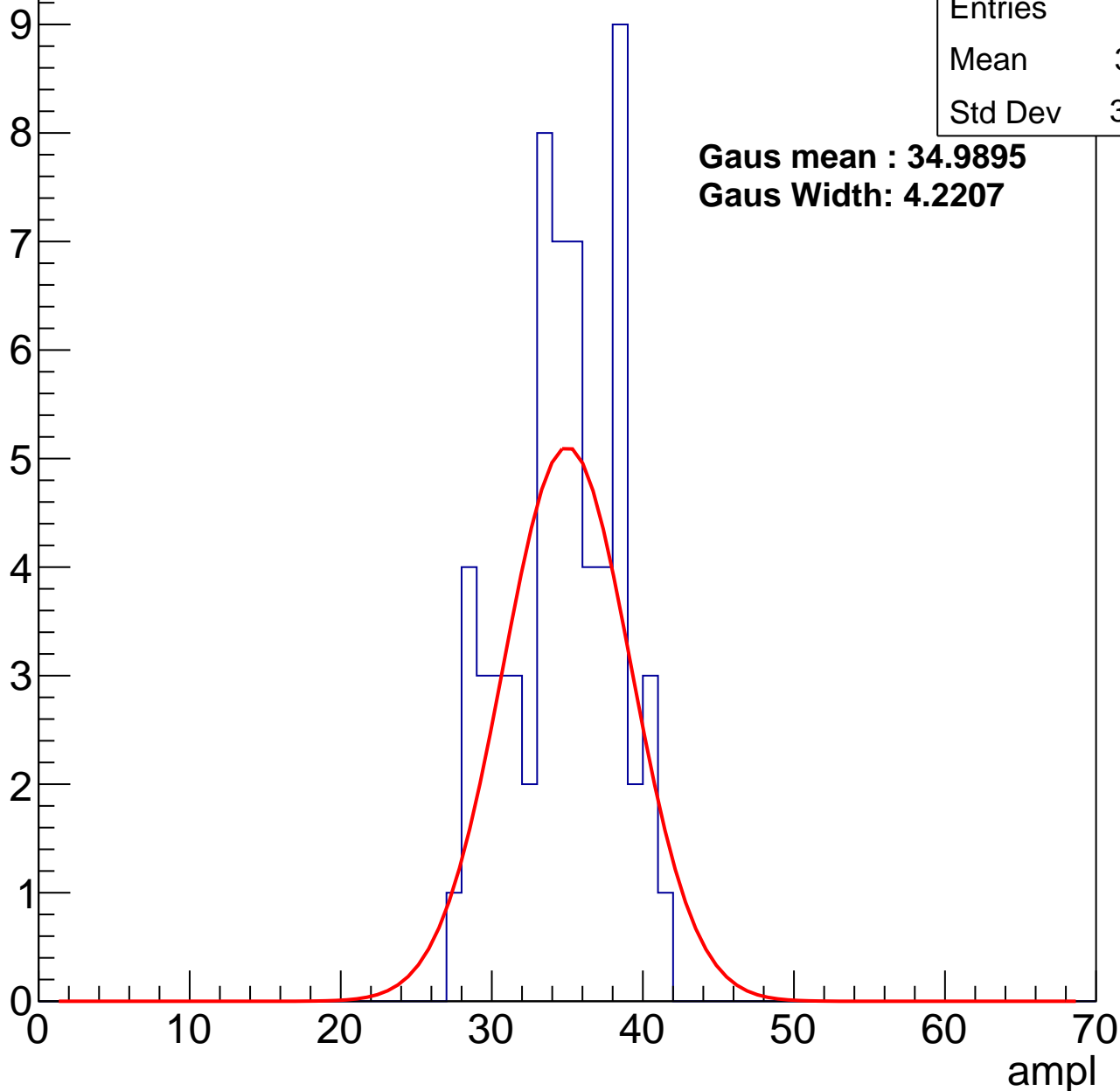
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	34.31
Std Dev	3.537

Gaus mean : 34.9895

Gaus Width: 4.2207



B1L003S, U16-ch98, adc2

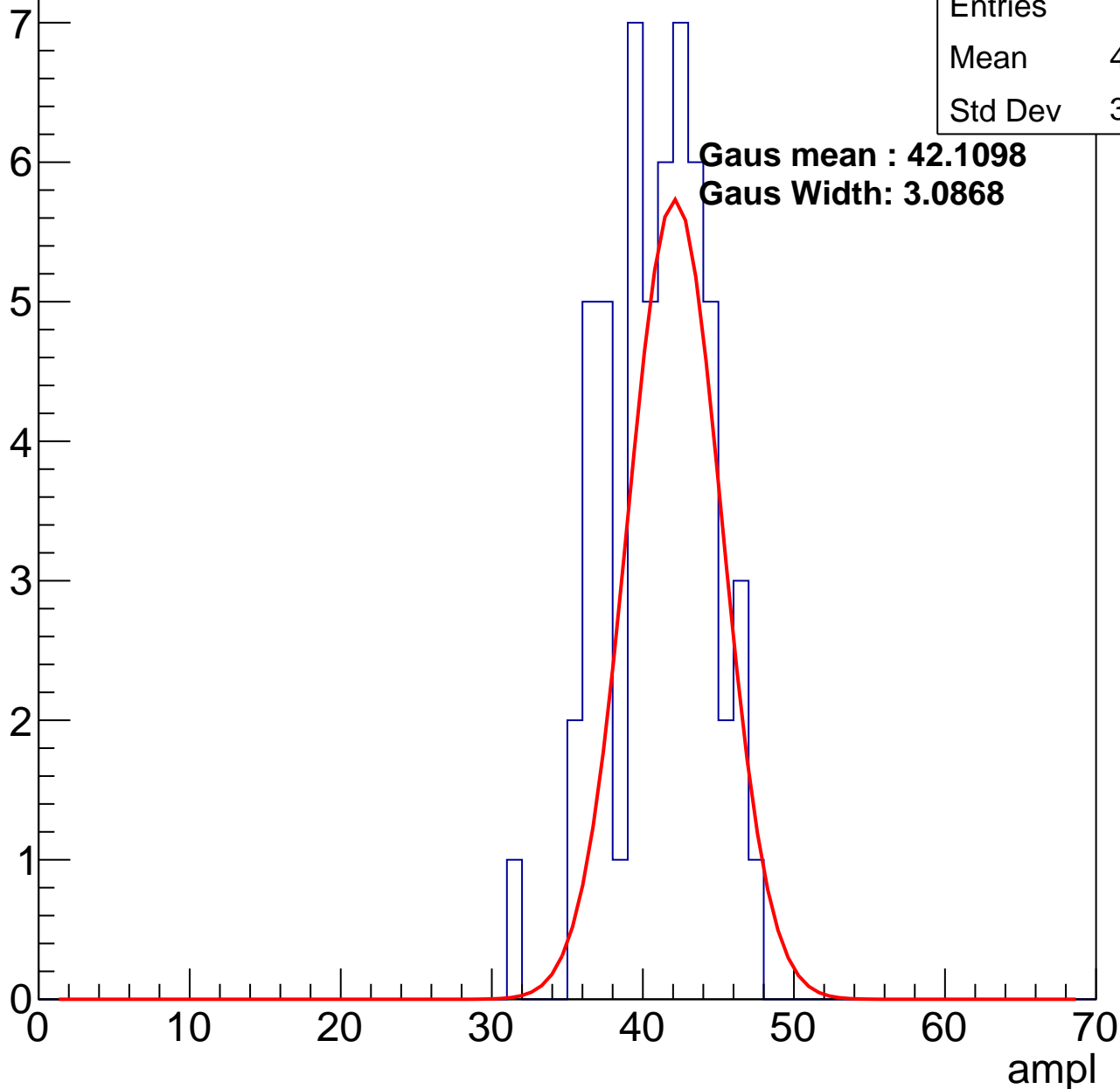
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	40.54
Std Dev	3.343

Gaus mean : 42.1098

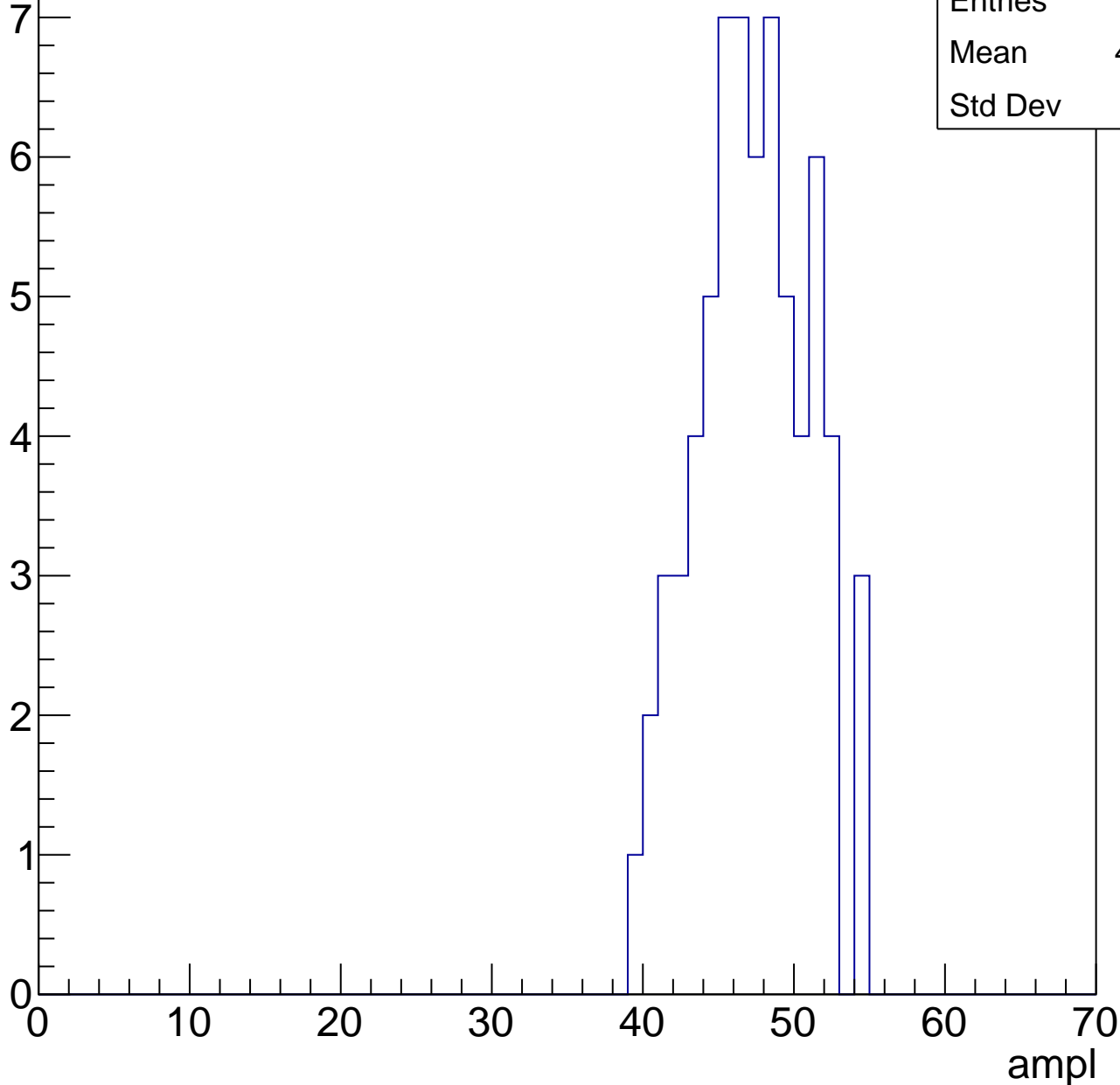
Gaus Width: 3.0868



B1L003S, U16-ch98, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



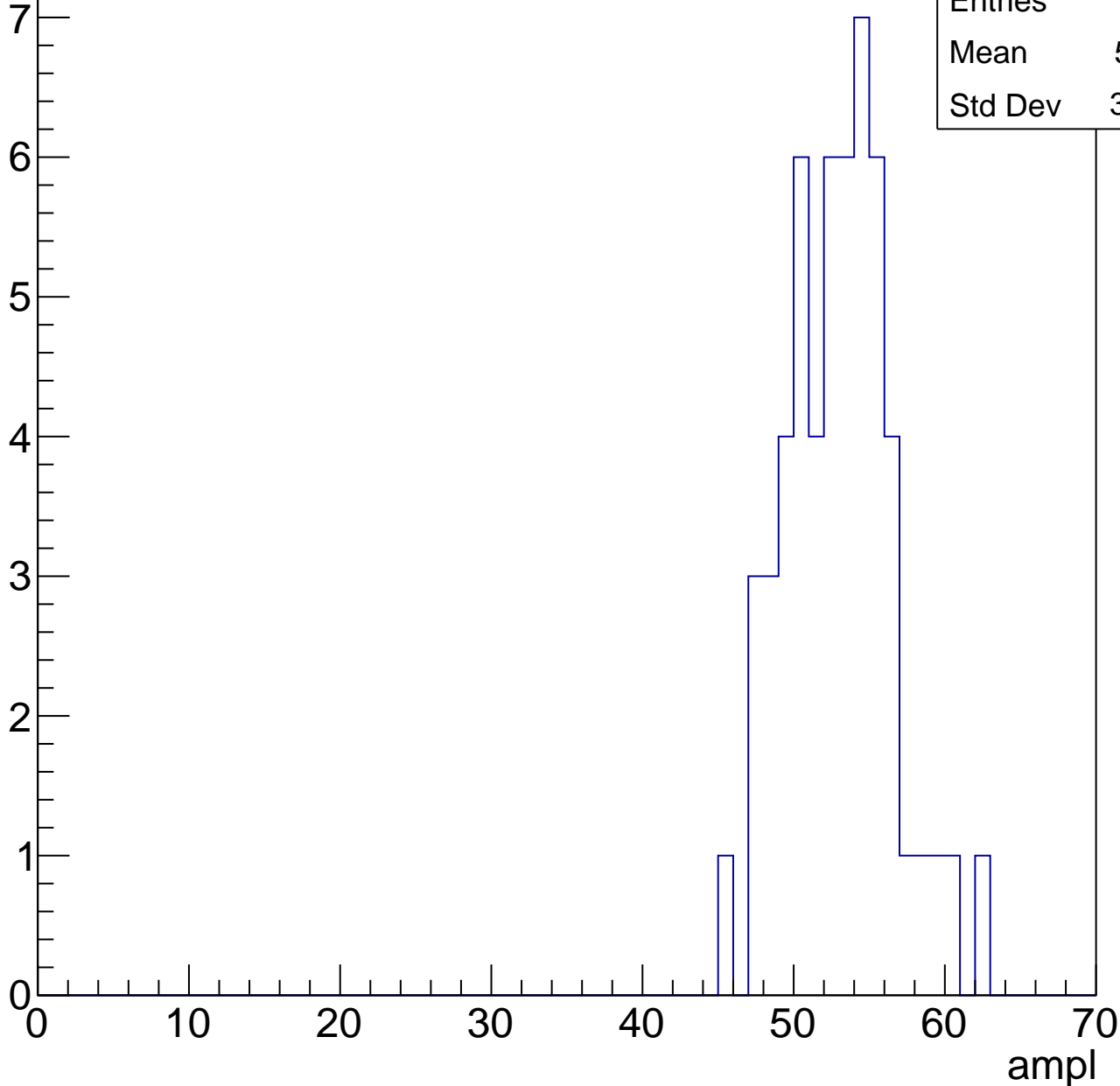
Entries	67
Mean	46.81
Std Dev	3.65

B1L003S, U16-ch98, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	52.51
Std Dev	3.432



B1L003S, U16-ch98, adc5

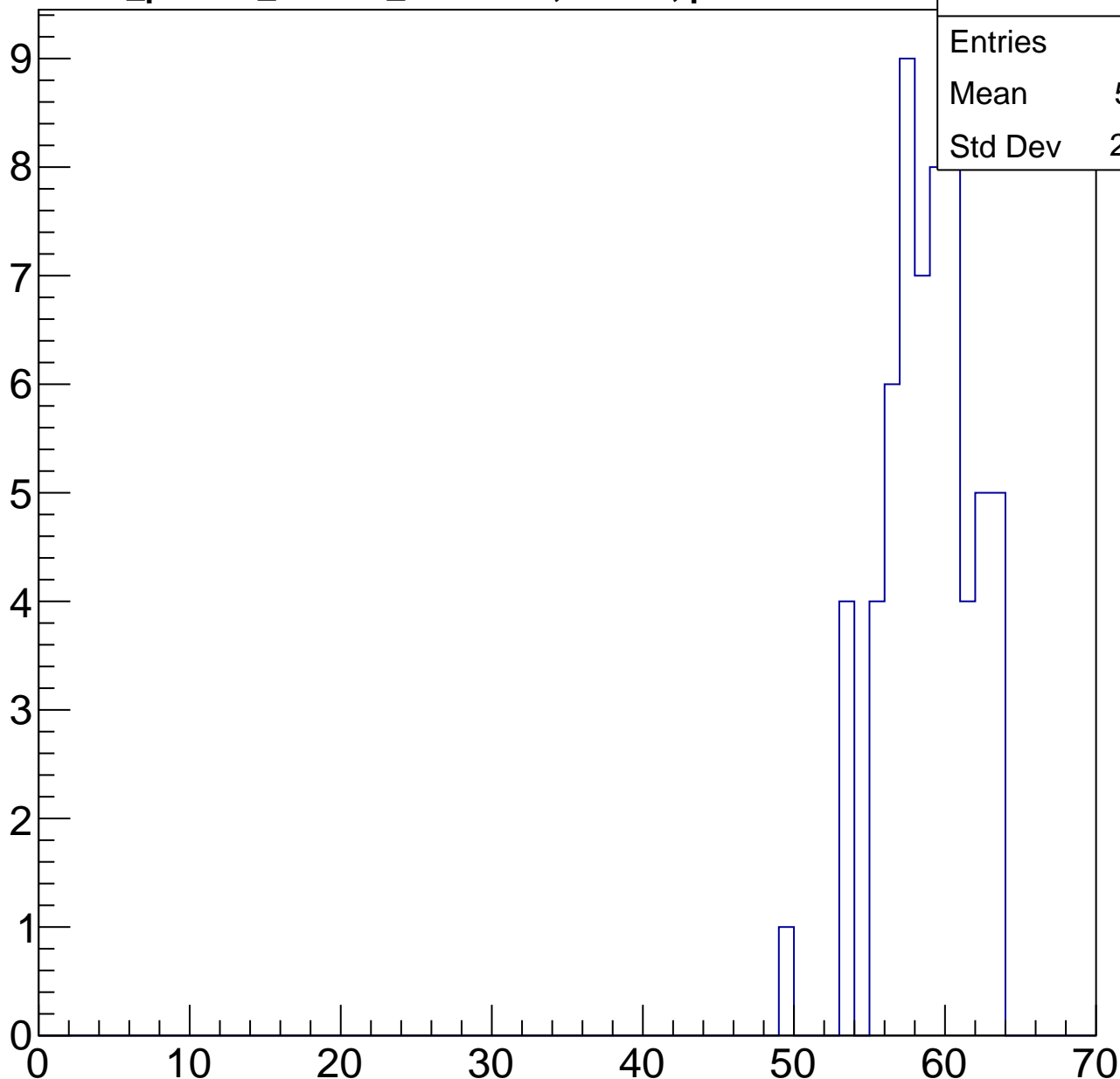
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	58.31
Std Dev	2.934

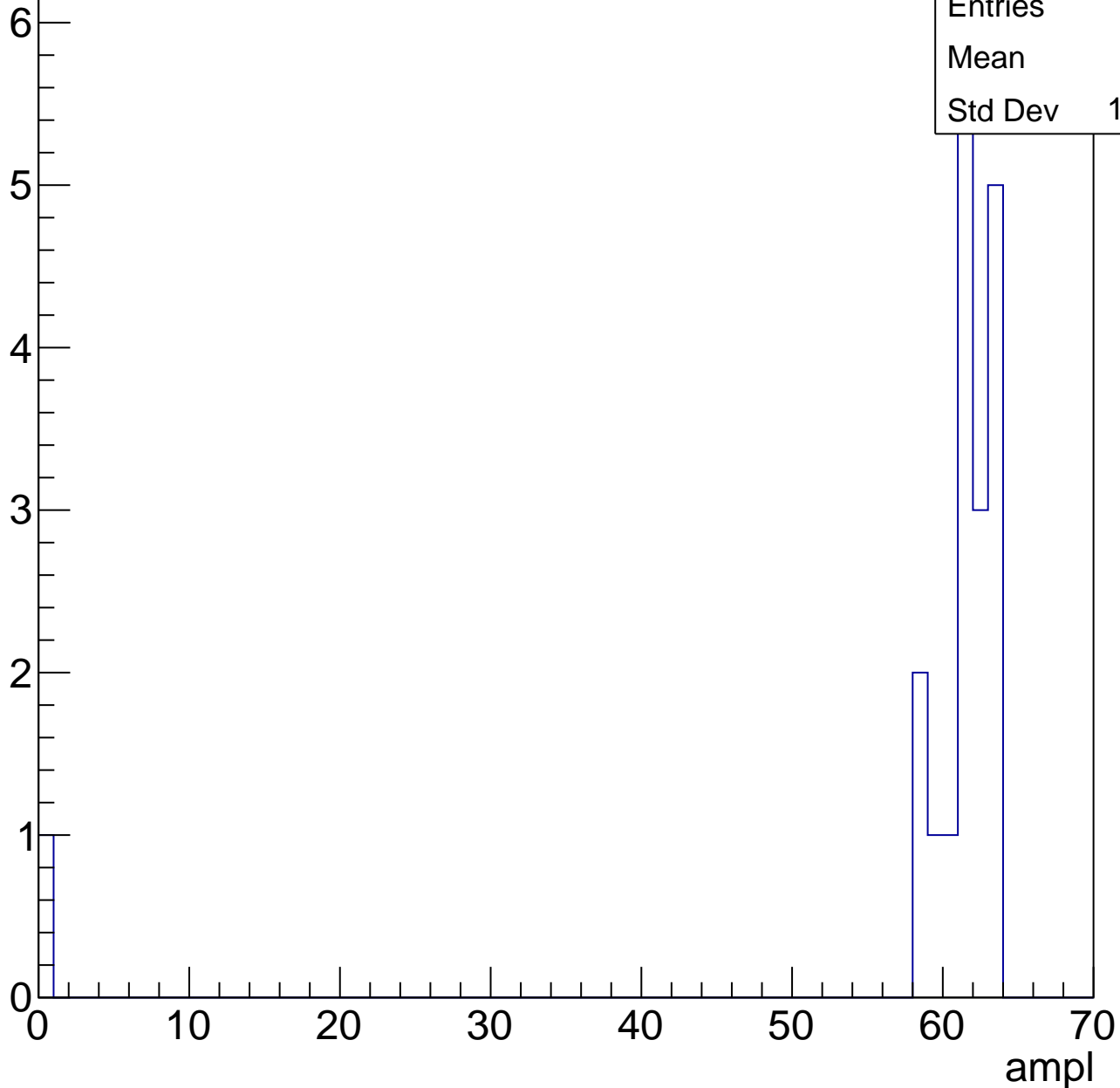
ampl



B1L003S, U16-ch98, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch98, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch99, adc0

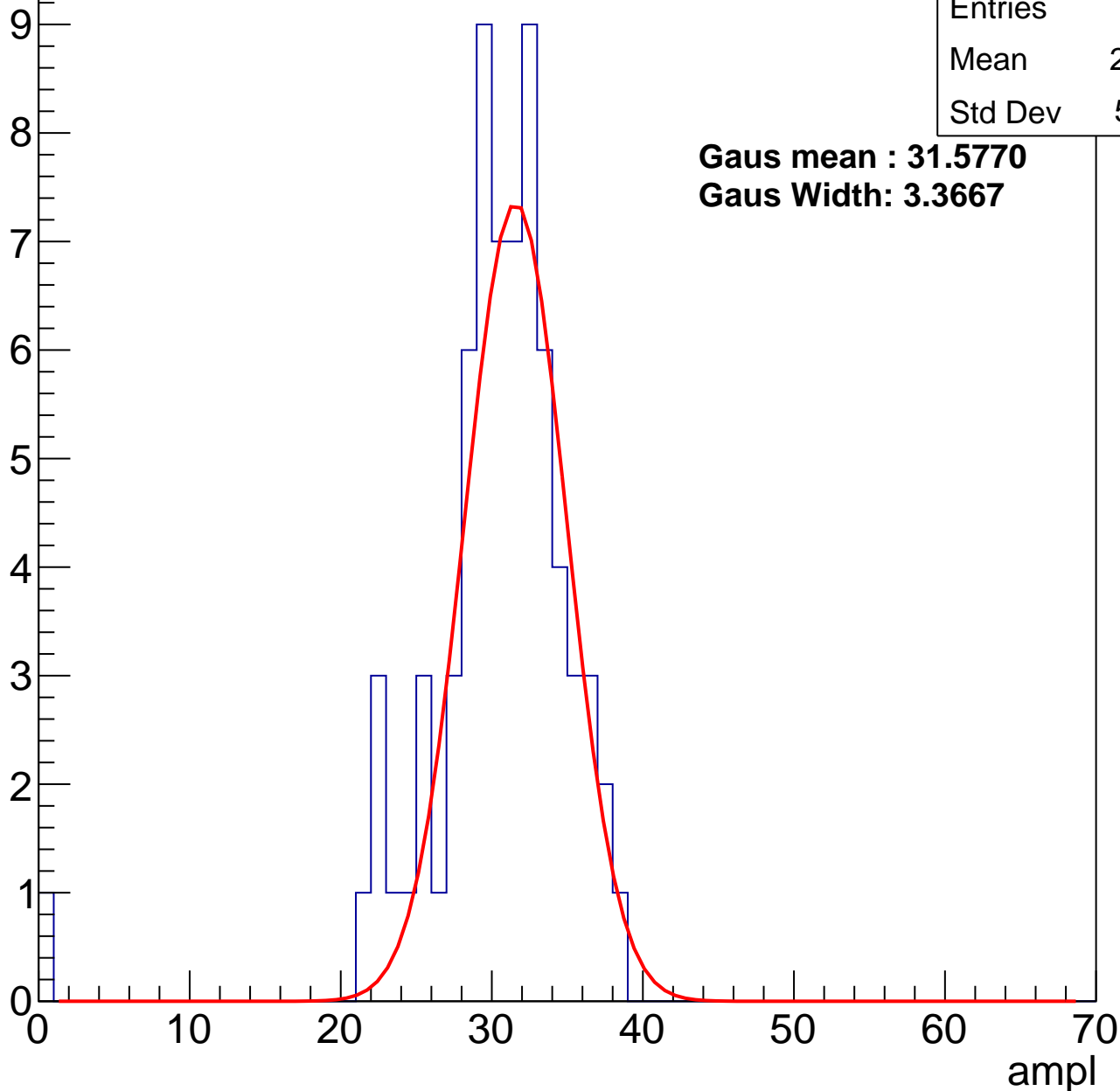
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29.85
Std Dev	5.191

Gaus mean : 31.5770

Gaus Width: 3.3667



B1L003S, U16-ch99, adc1

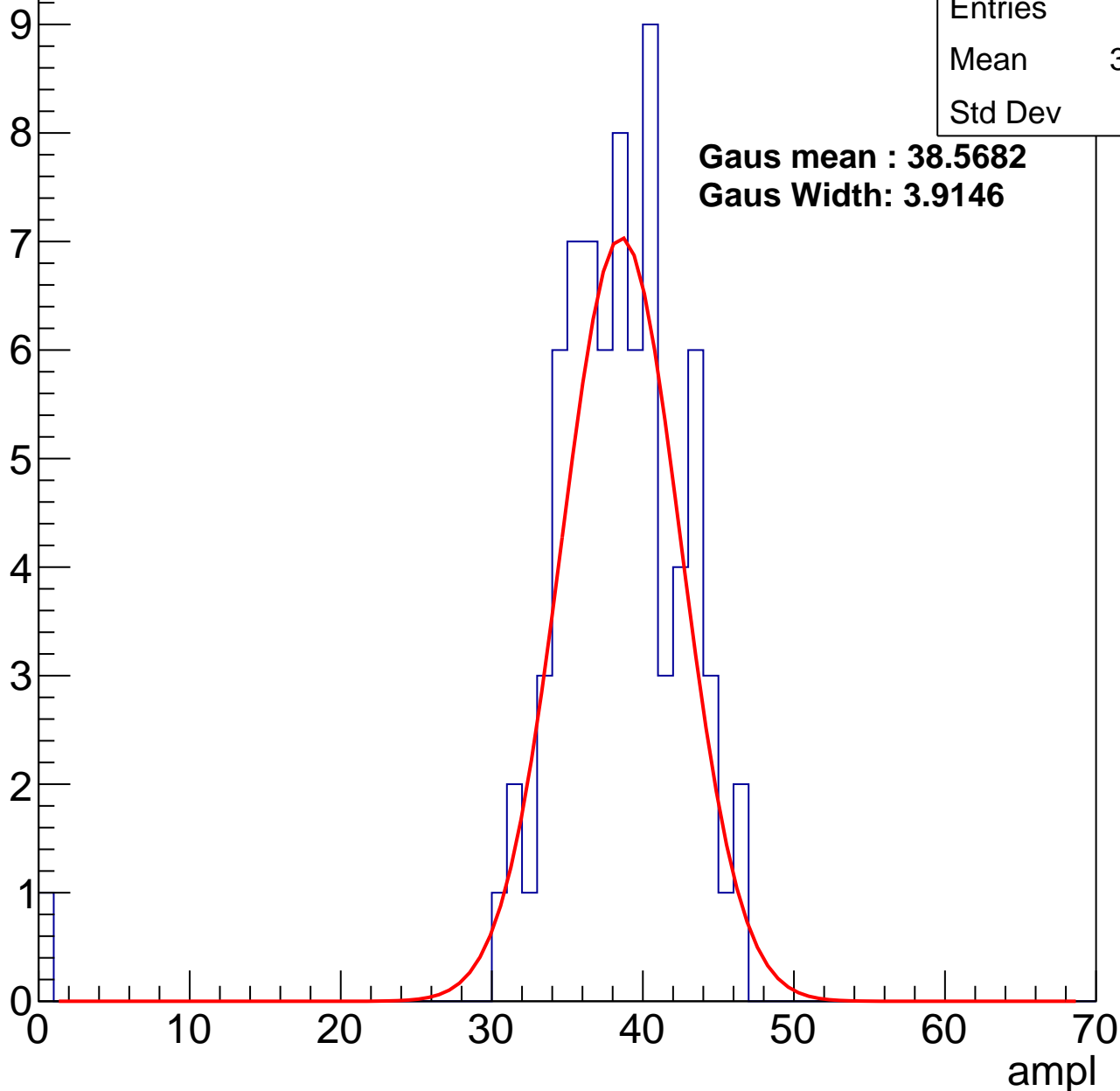
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	37.66
Std Dev	5.7

Gaus mean : 38.5682

Gaus Width: 3.9146



B1L003S, U16-ch99, adc2

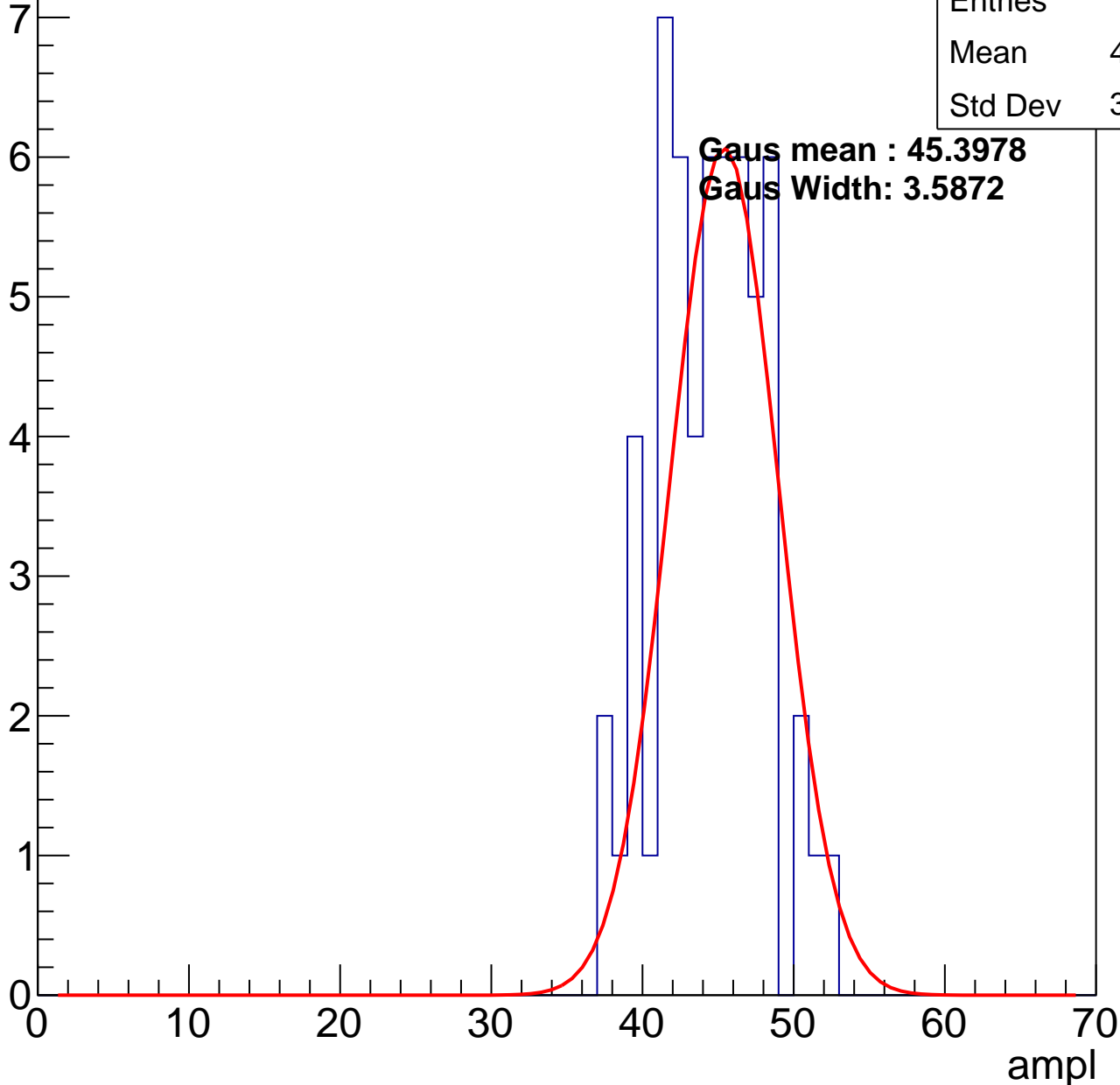
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.05
Std Dev	3.466

Gaus mean : 45.3978

Gaus Width: 3.5872



B1L003S, U16-ch99, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	50.69
Std Dev	3.074

Entry

10

8

6

4

2

0

0

10

20

30

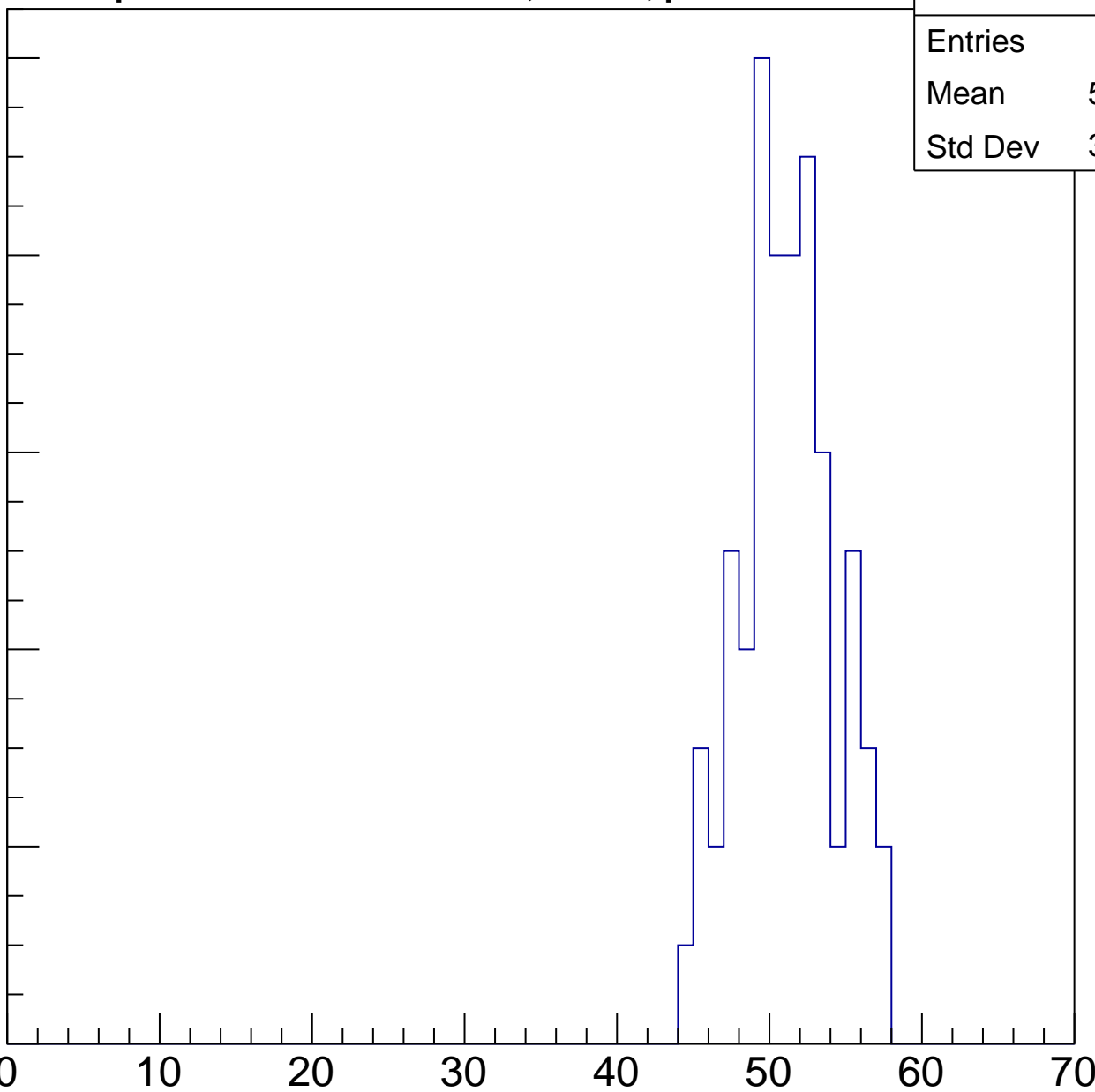
40

50

60

70

ampl

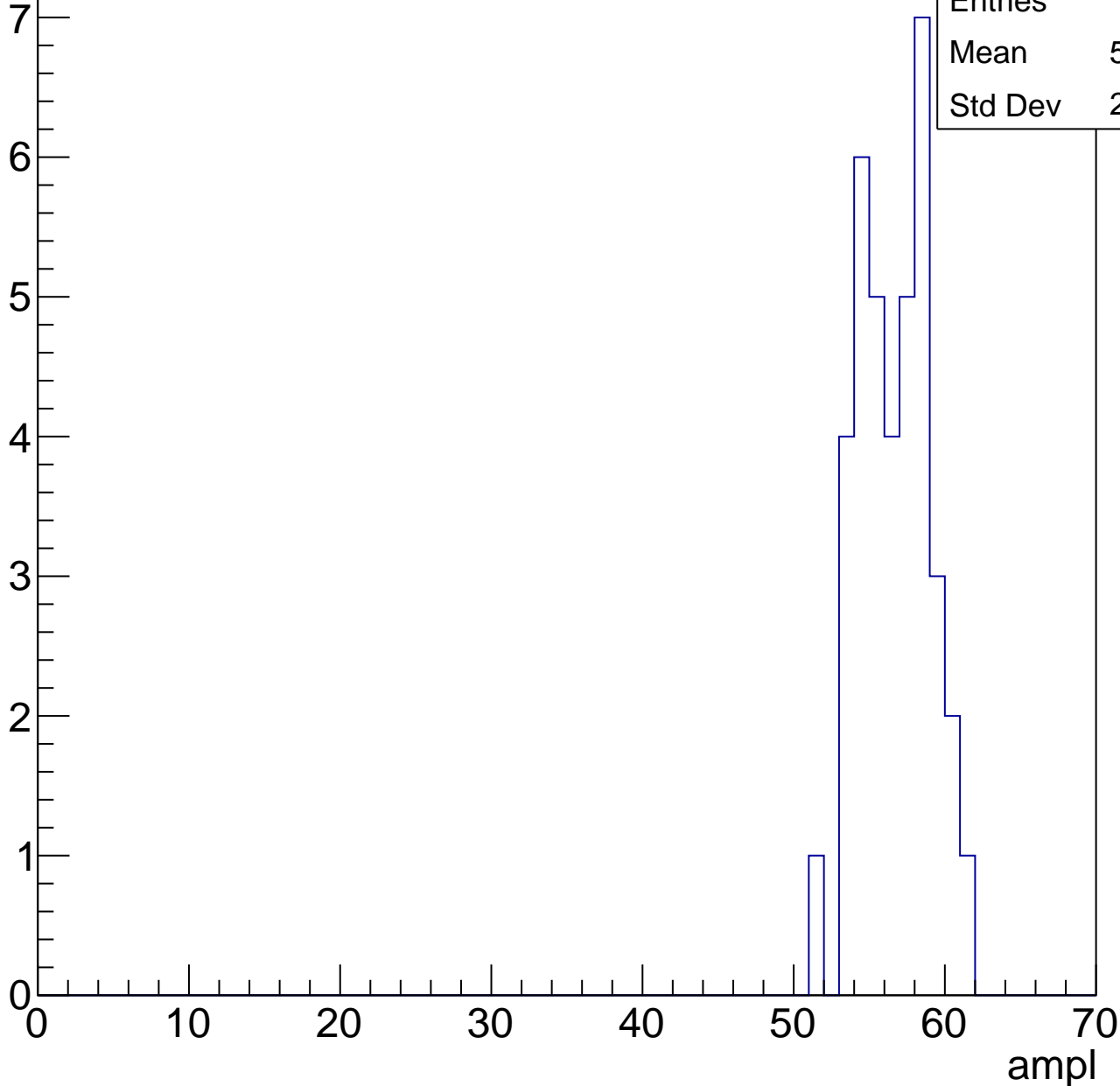


B1L003S, U16-ch99, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	56.18
Std Dev	2.327



B1L003S, U16-ch99, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	60.24
Std Dev	2.343

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L003S, U16-ch99, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch99, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch100, adc0

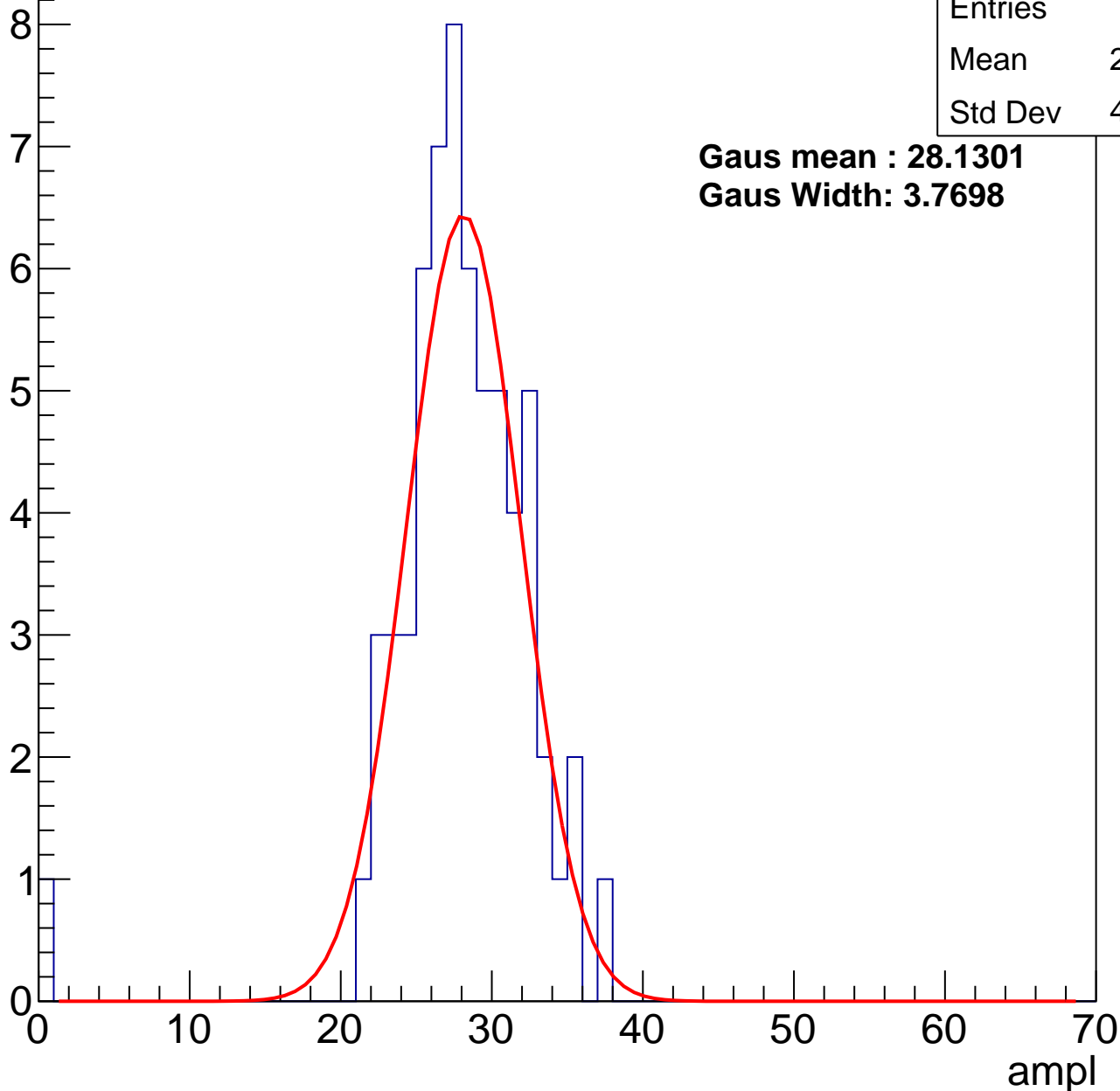
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	27.46
Std Dev	4.947

Gaus mean : 28.1301

Gaus Width: 3.7698



B1L003S, U16-ch100, adc1

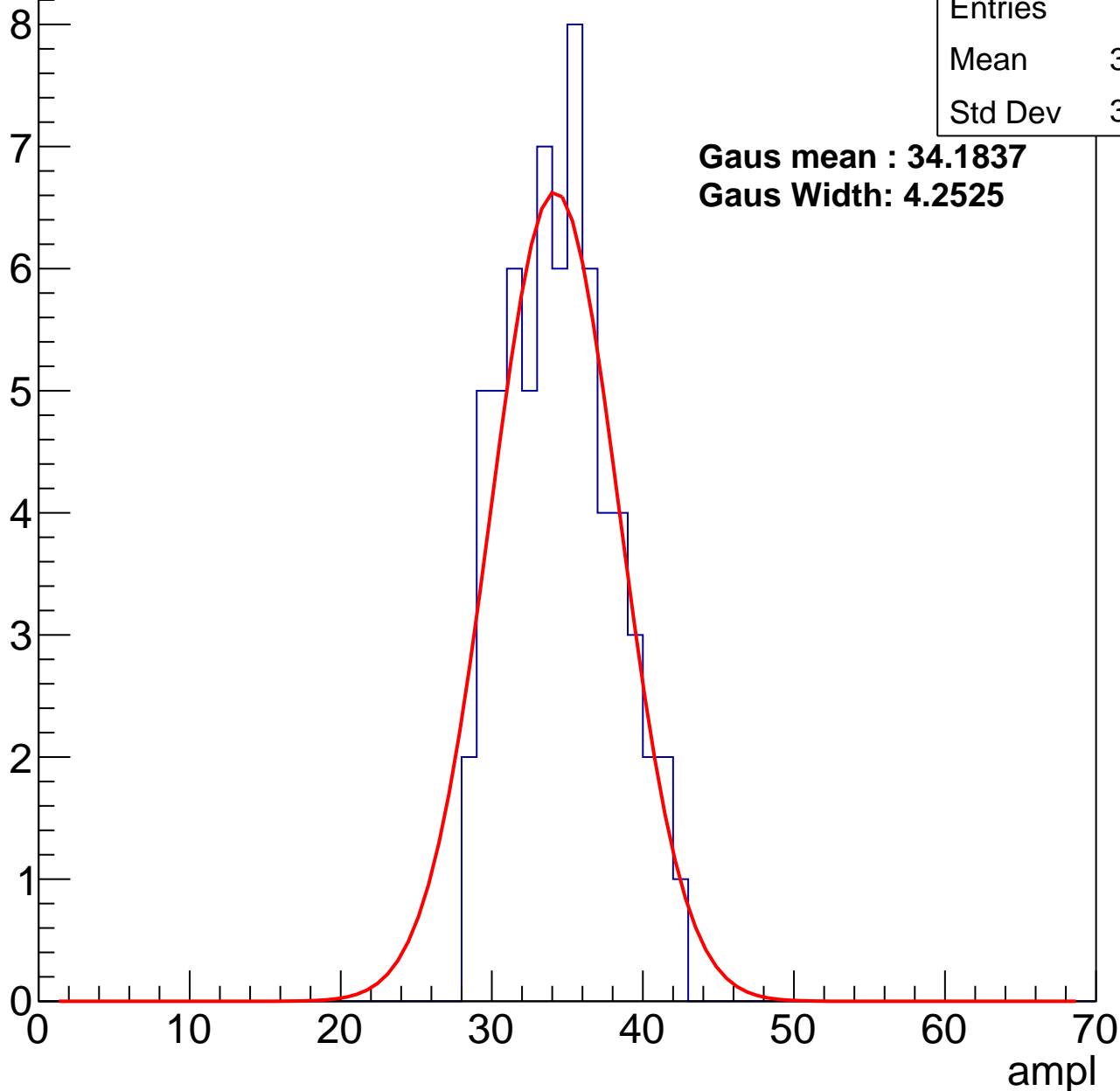
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.08
Std Dev	3.479

Gaus mean : 34.1837

Gaus Width: 4.2525



B1L003S, U16-ch100, adc2

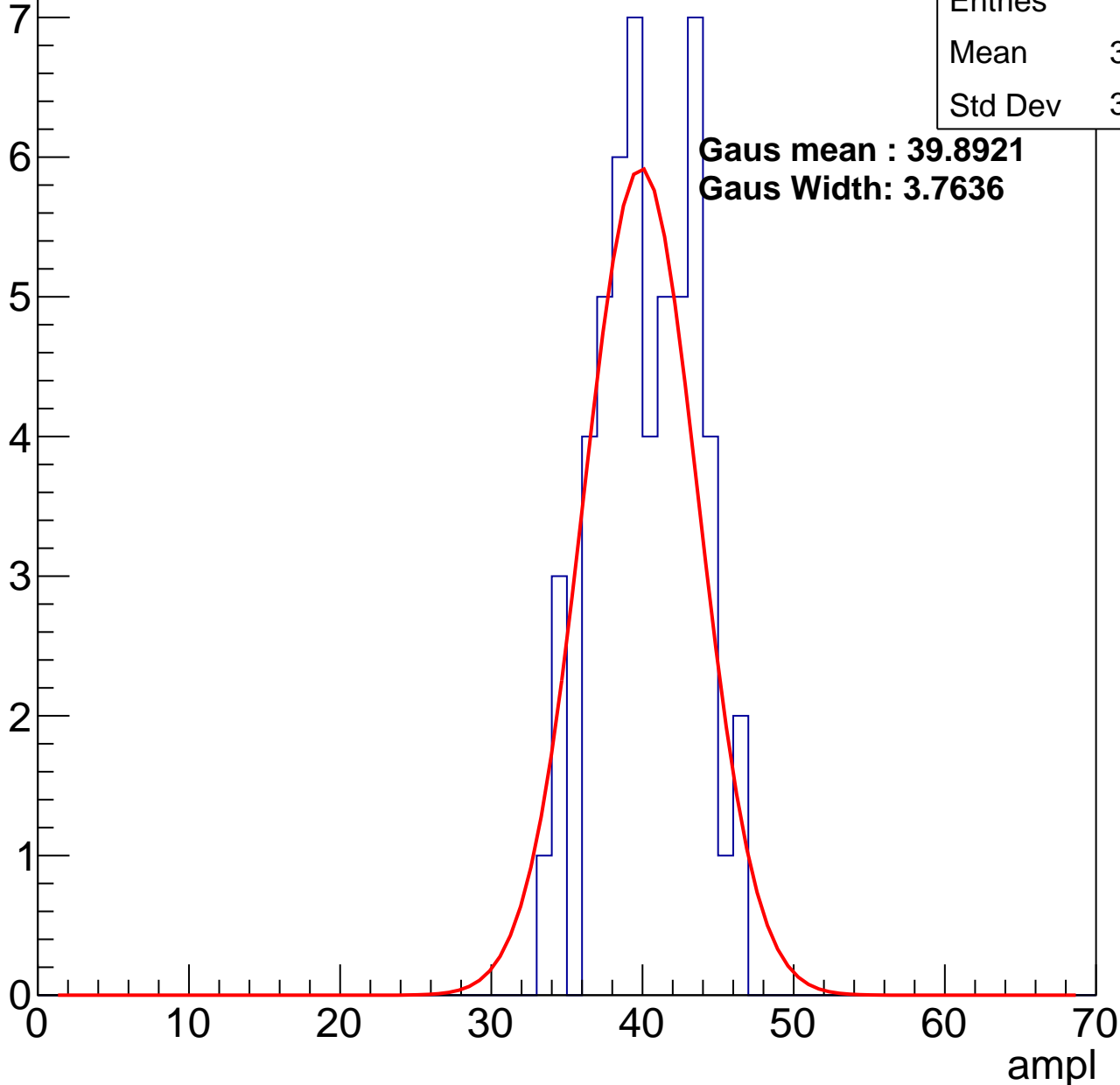
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	39.89
Std Dev	3.178

Gaus mean : 39.8921

Gaus Width: 3.7636

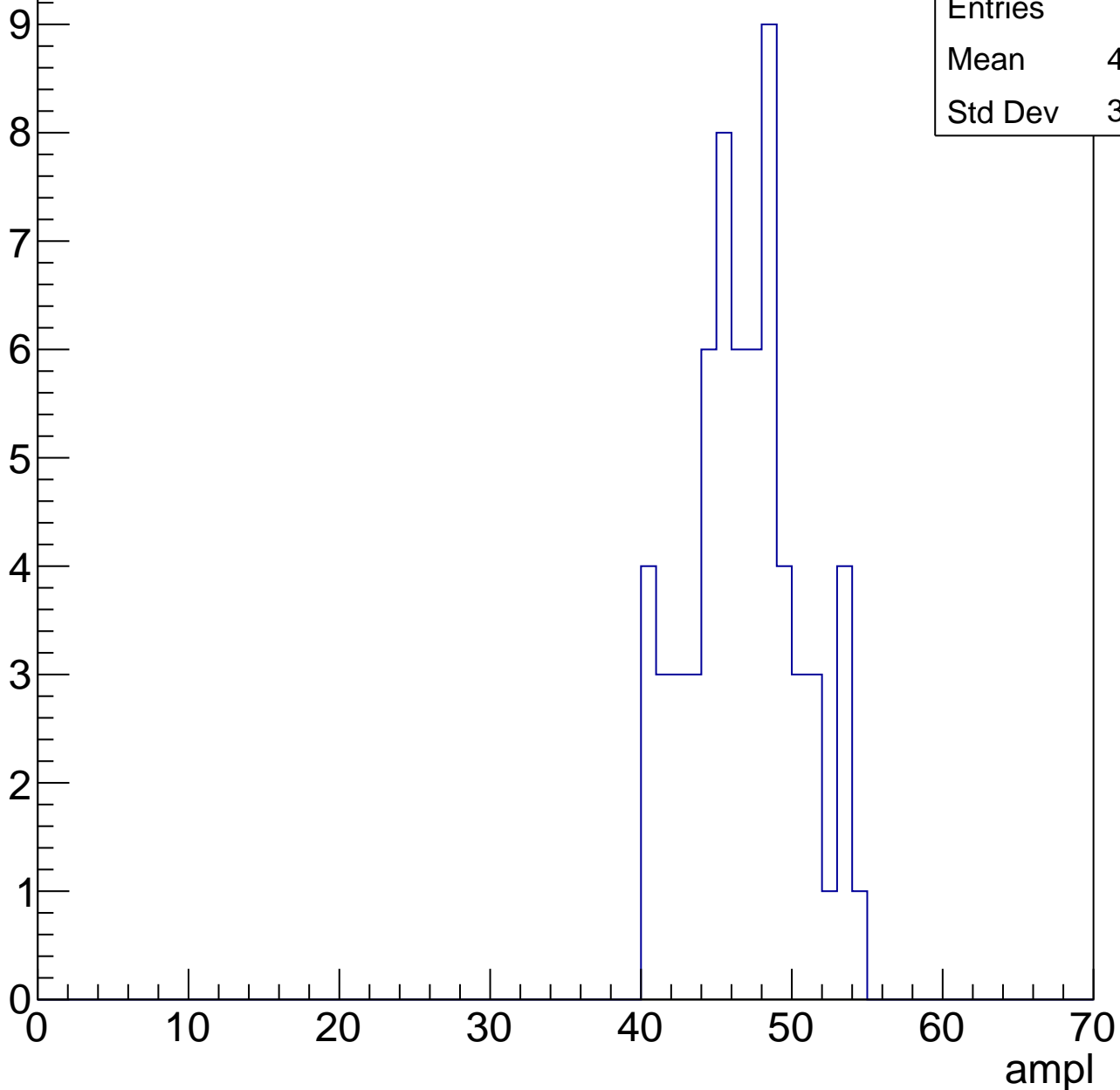


B1L003S, U16-ch100, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	46.39
Std Dev	3.565

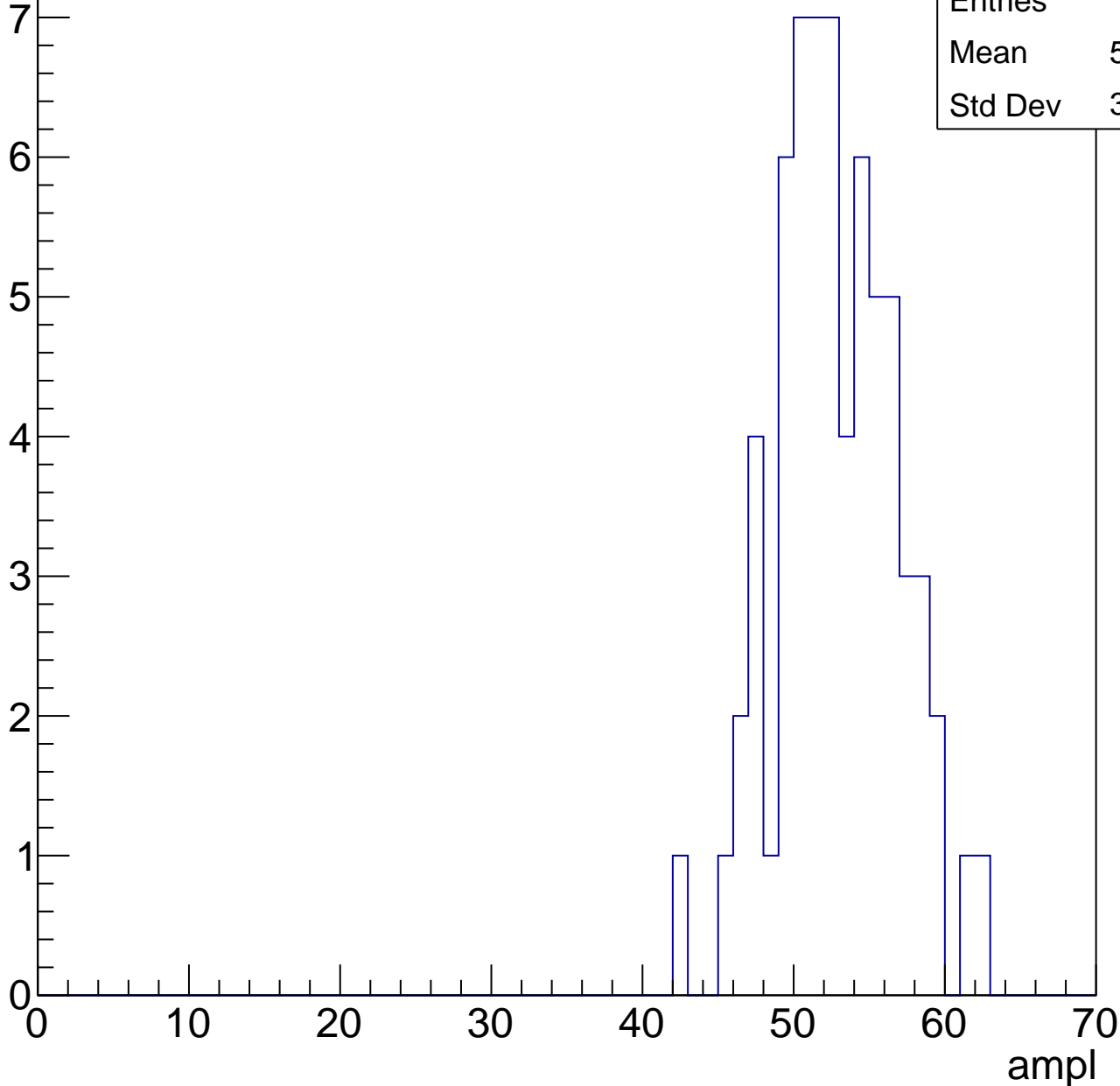


B1L003S, U16-ch100, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

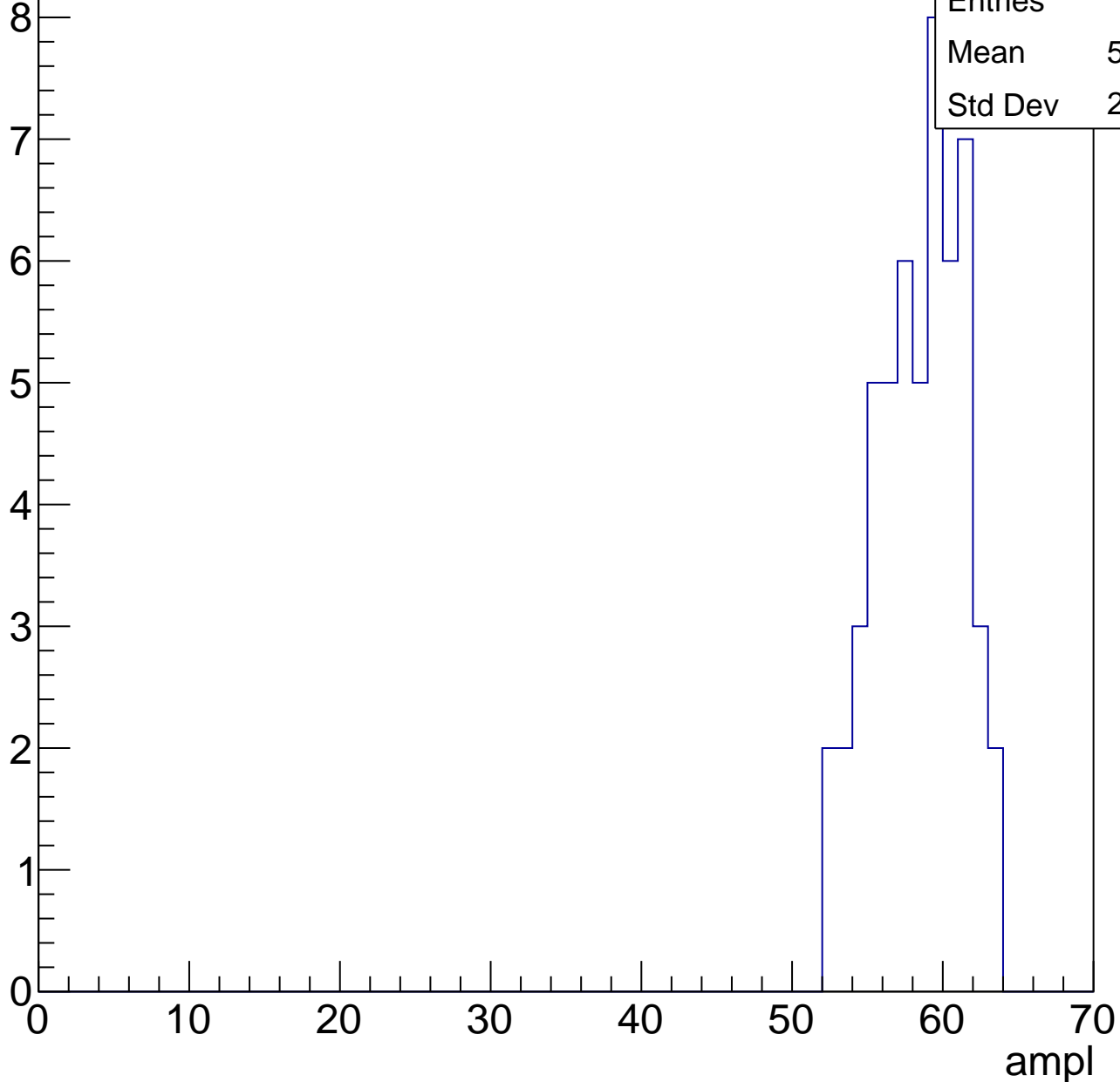
Entries	66
Mean	52.38
Std Dev	3.953



B1L003S, U16-ch100, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

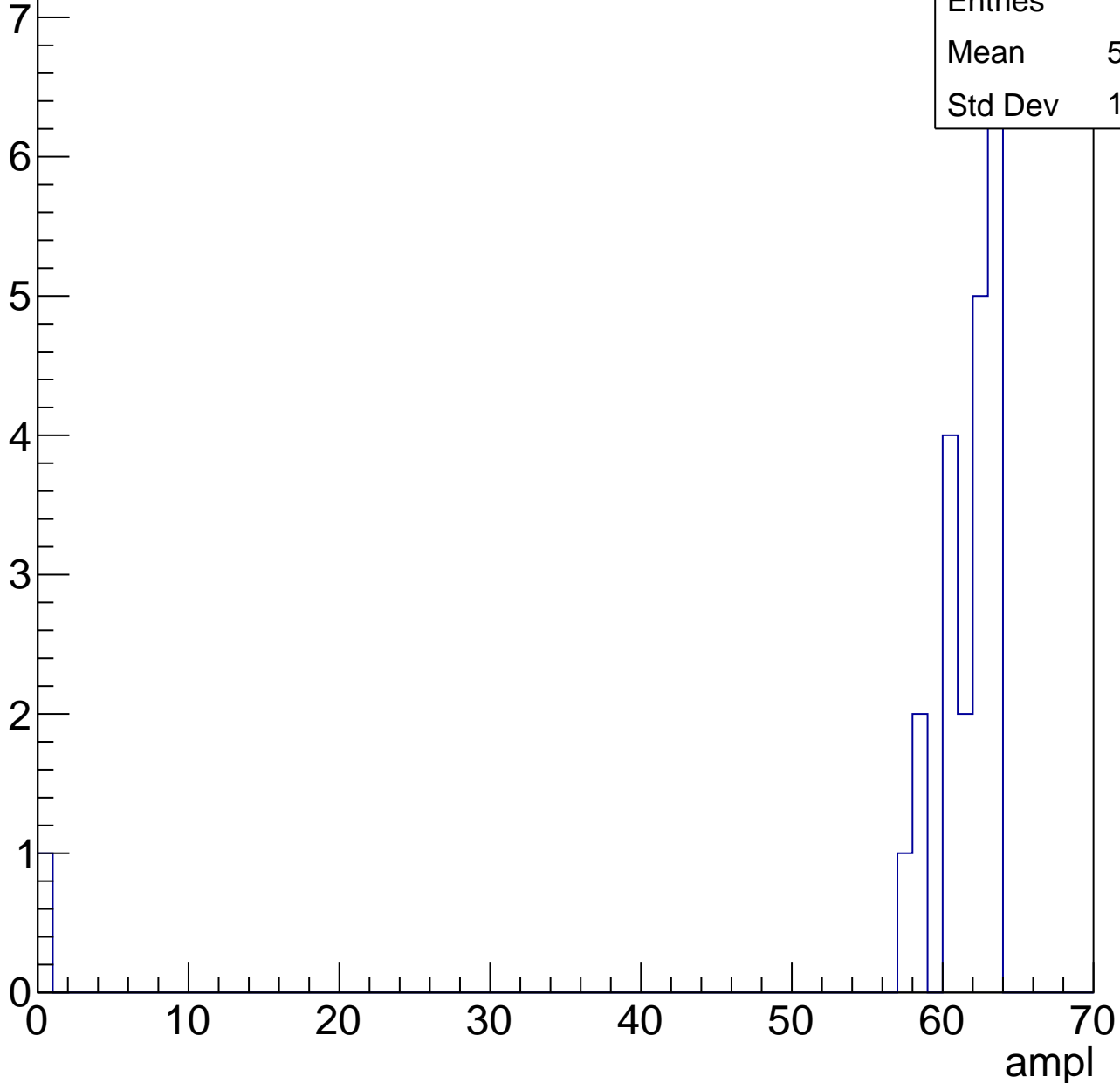


B1L003S, U16-ch100, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

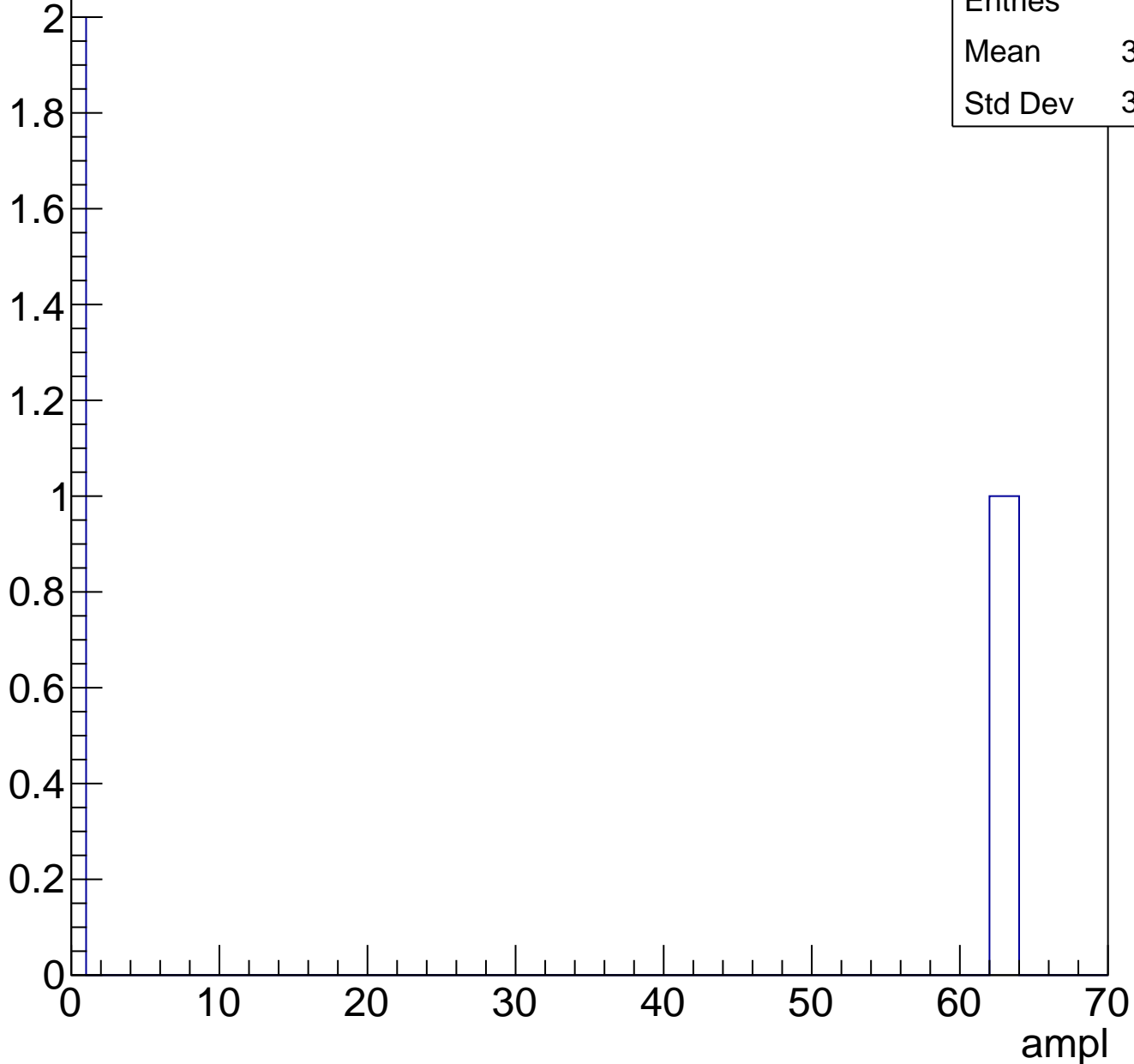
Entries	22
Mean	58.45
Std Dev	12.88



B1L003S, U16-ch100, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch101, adc0

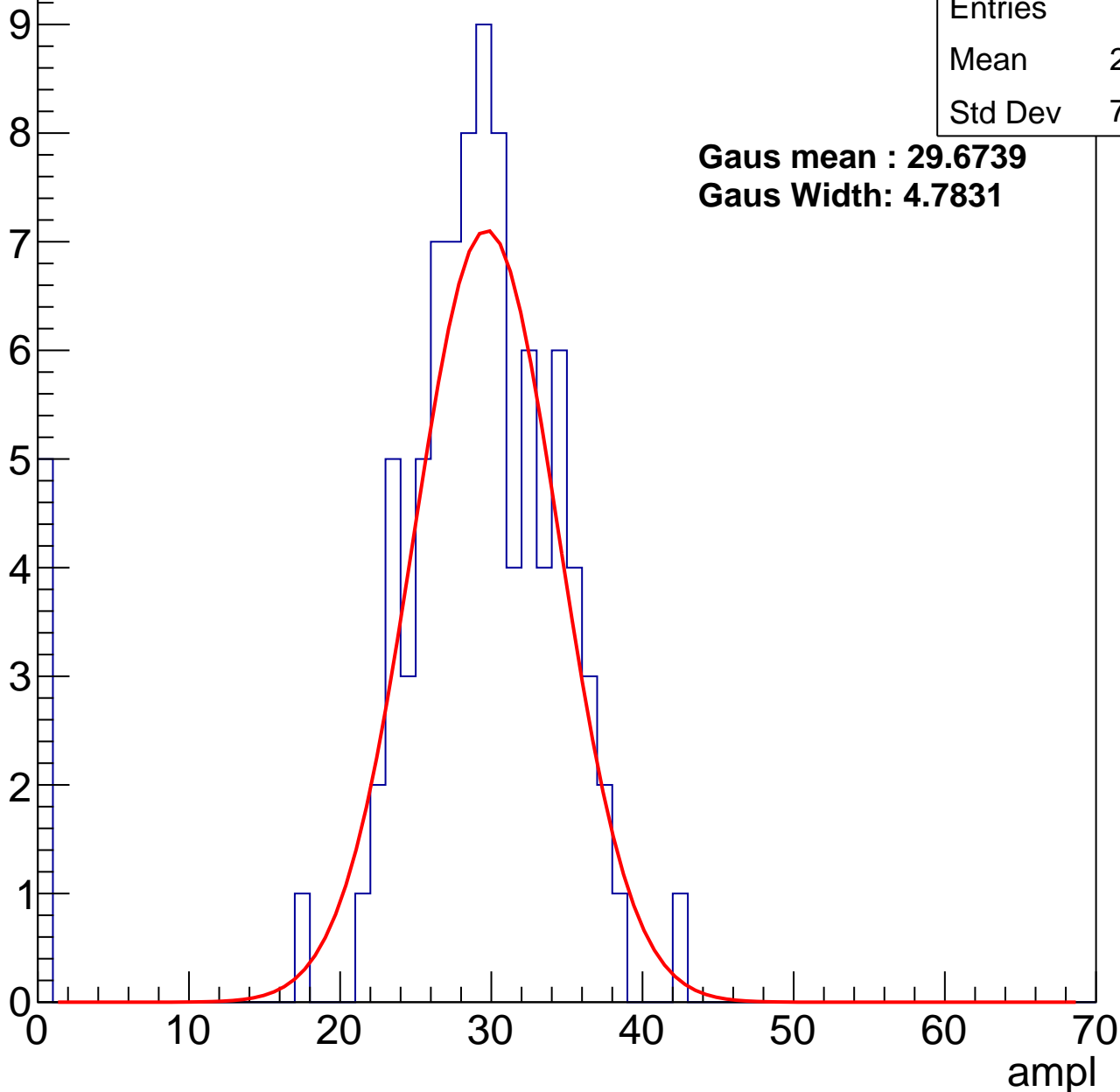
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	92
Mean	27.65
Std Dev	7.898

Gaus mean : 29.6739

Gaus Width: 4.7831



B1L003S, U16-ch101, adc1

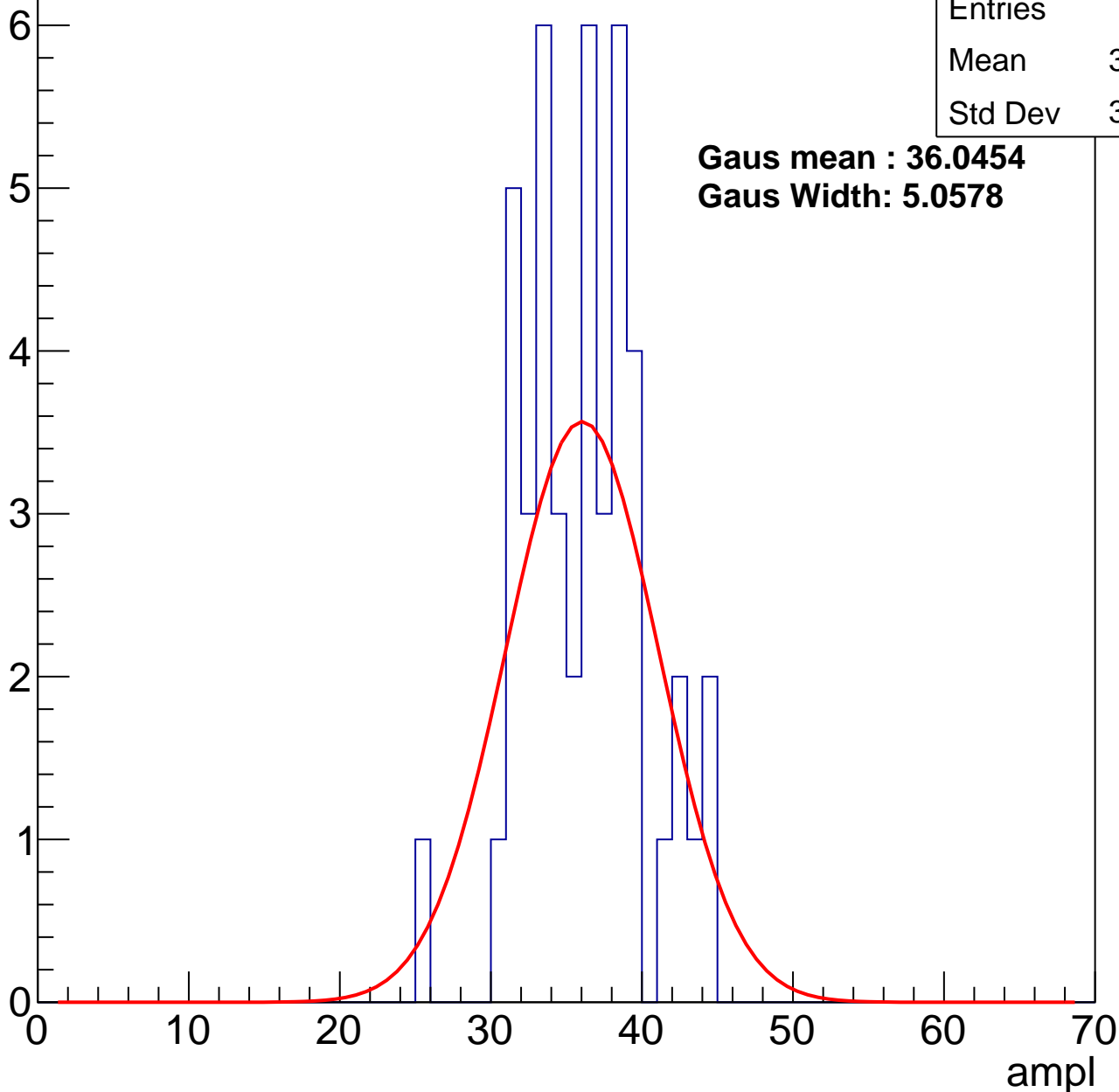
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	35.72
Std Dev	3.987

Gaus mean : 36.0454

Gaus Width: 5.0578



B1L003S, U16-ch101, adc2

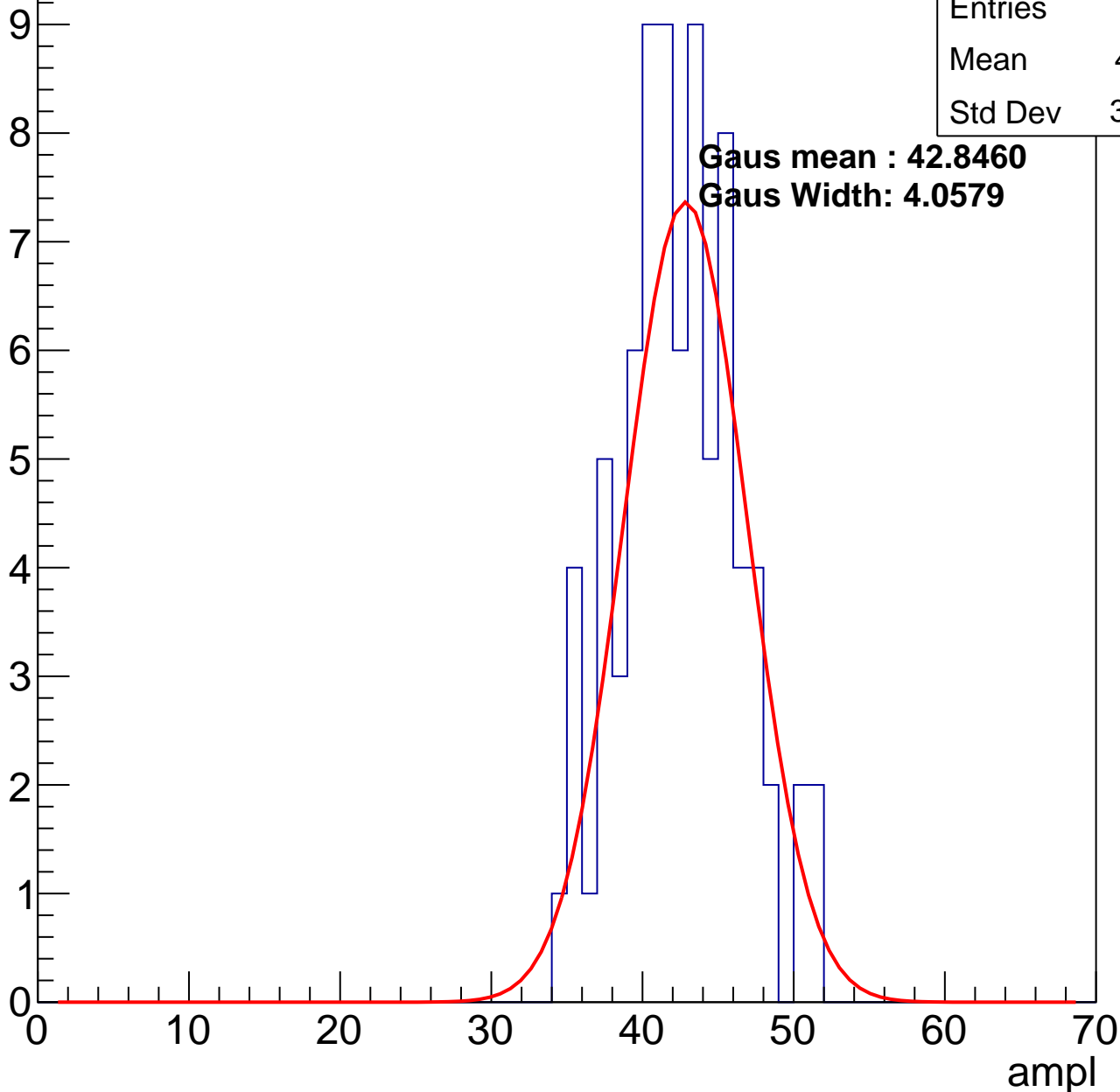
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	42.01
Std Dev	3.865

Gaus mean : 42.8460

Gaus Width: 4.0579

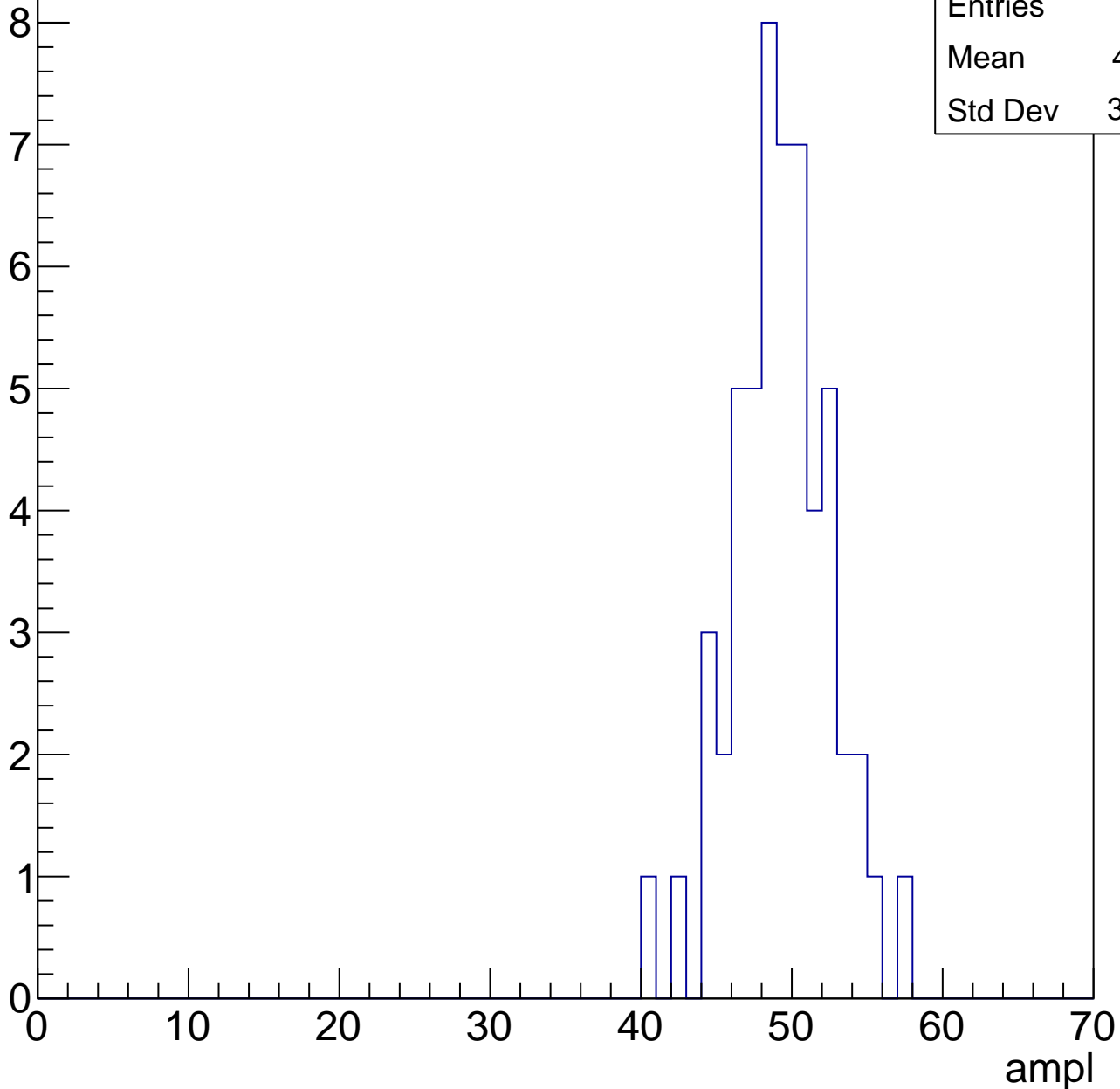


B1L003S, U16-ch101, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.81
Std Dev	3.215

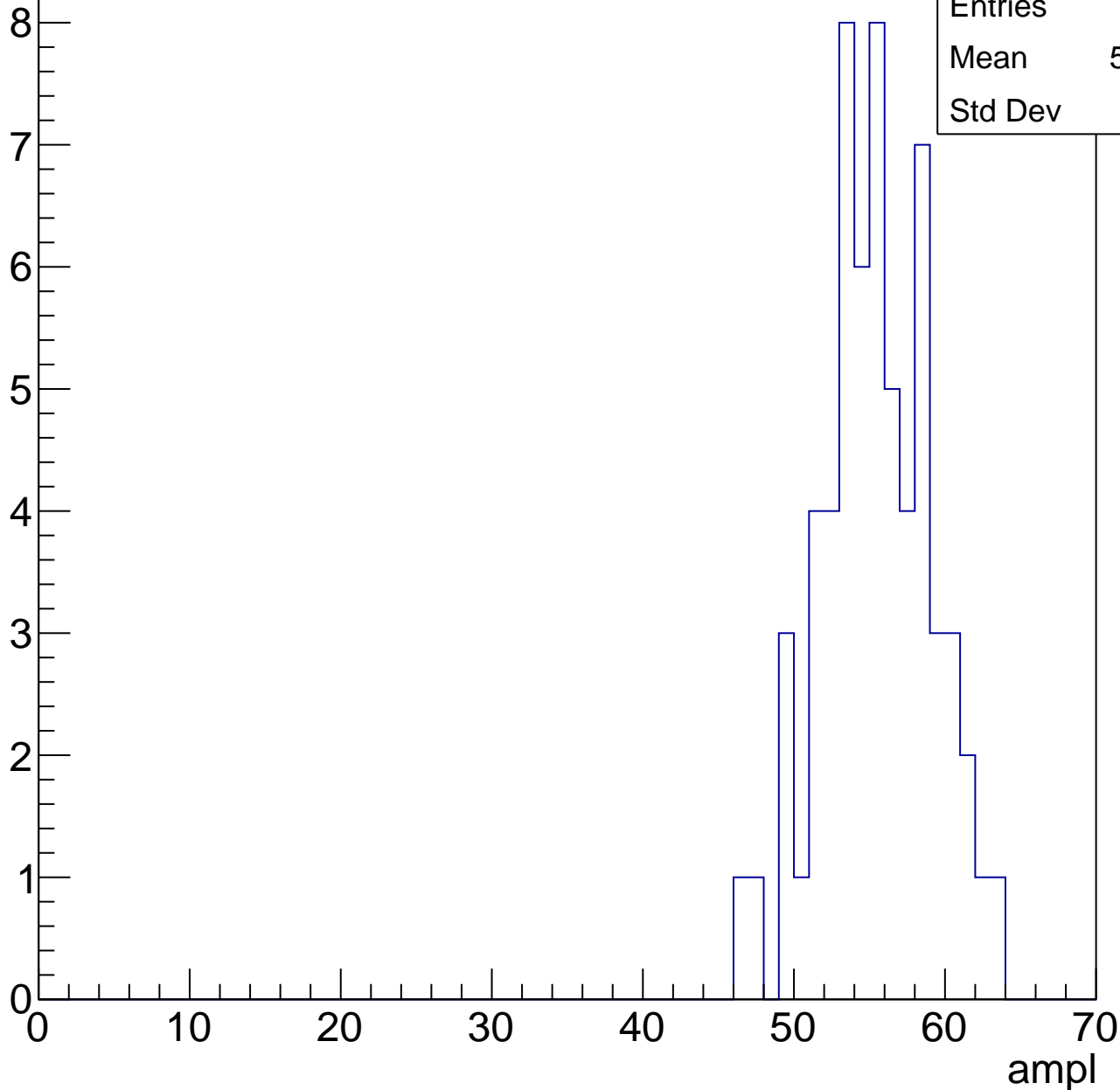


B1L003S, U16-ch101, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	54.97
Std Dev	3.61

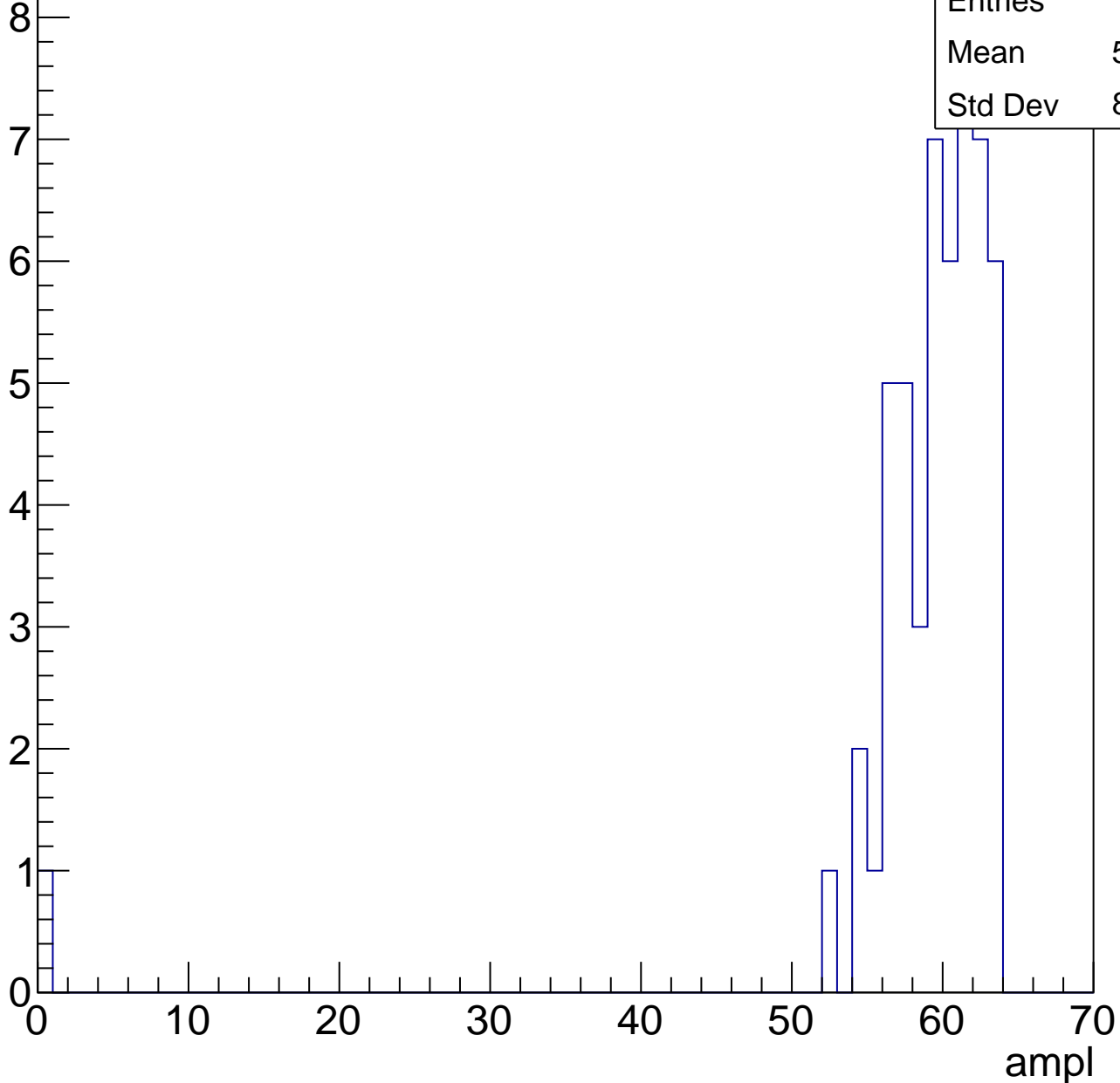


B1L003S, U16-ch101, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

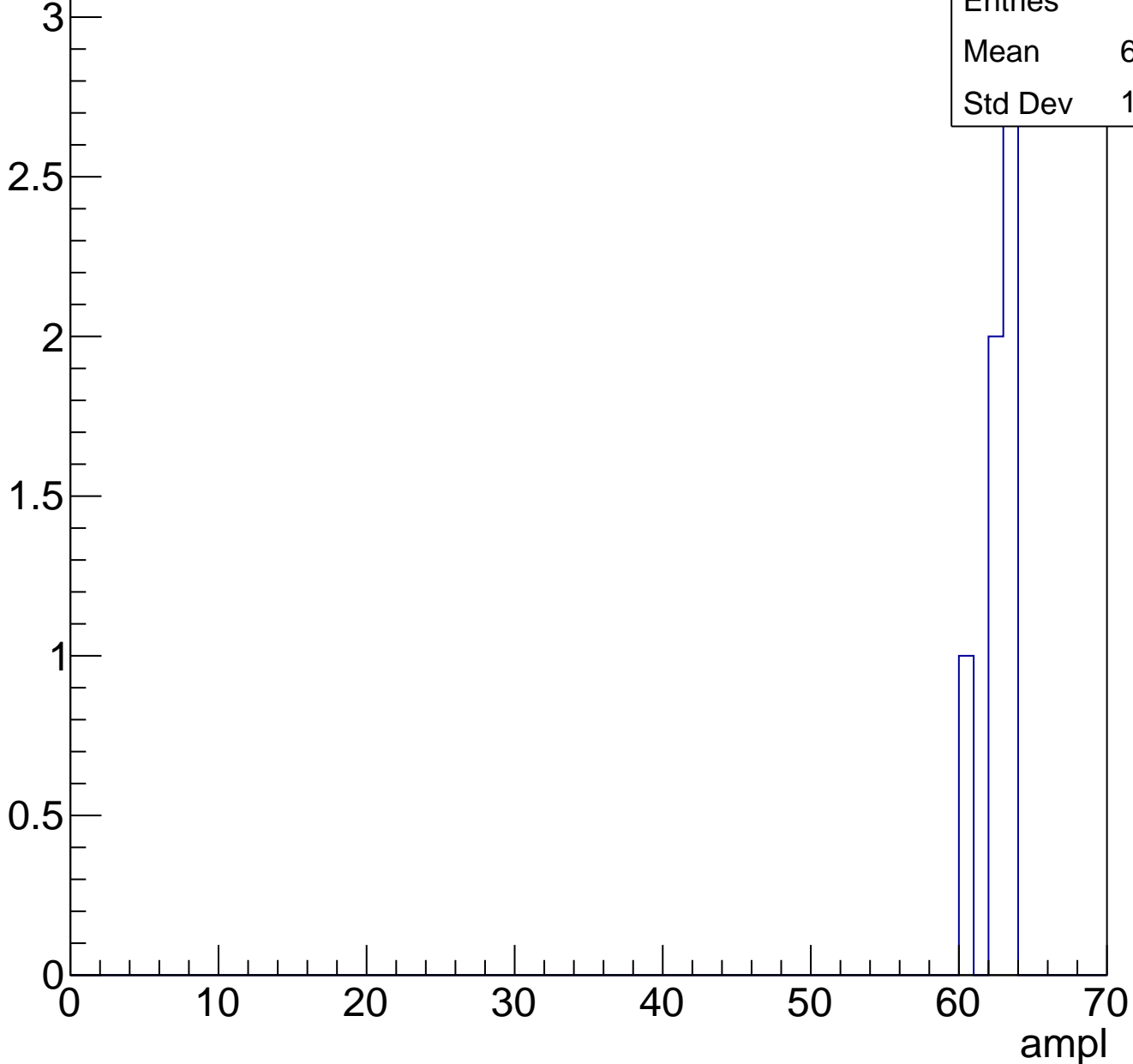
Entries	52
Mean	58.21
Std Dev	8.581



B1L003S, U16-ch101, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch101, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch102, adc0

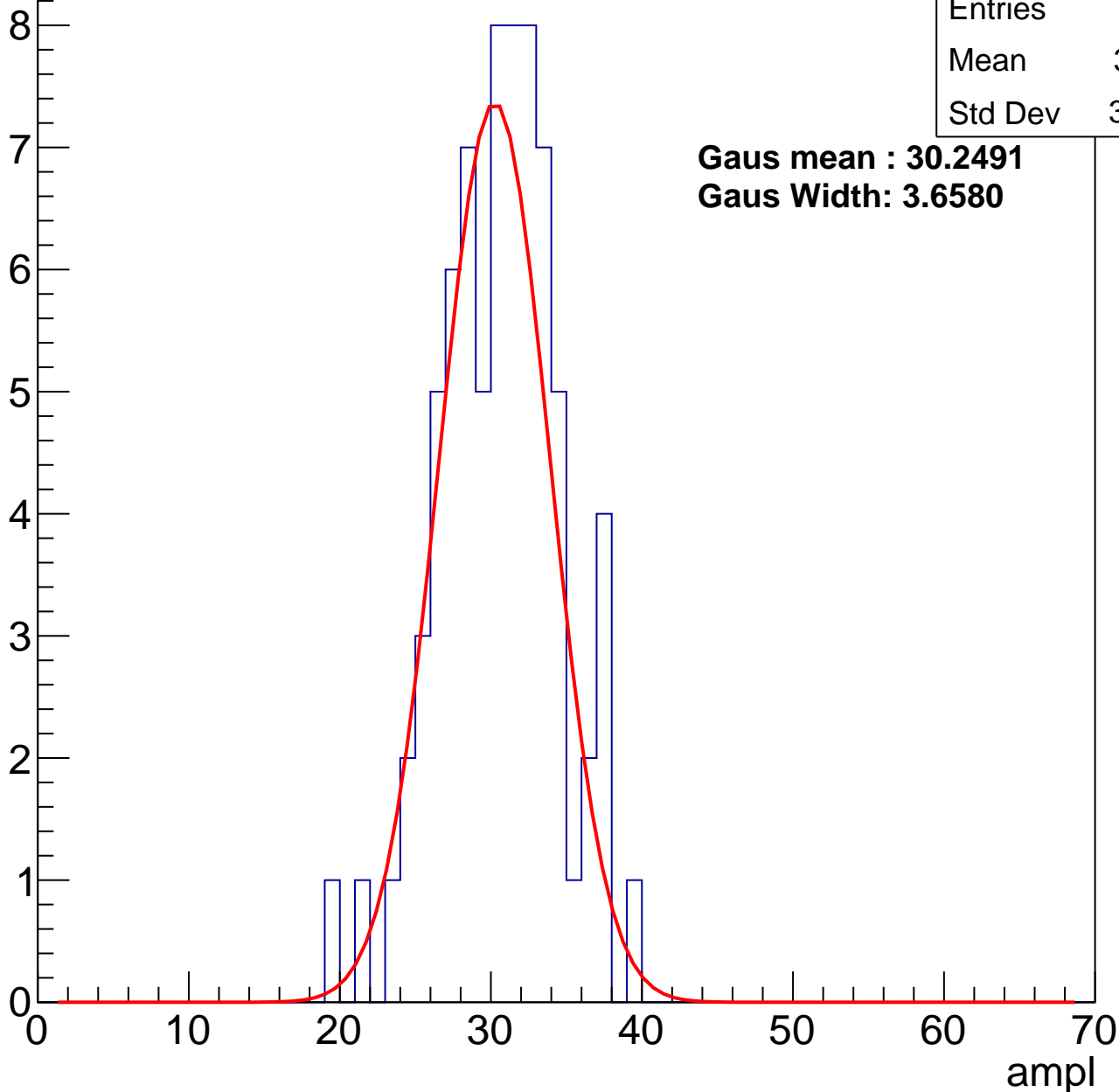
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30.11
Std Dev	3.866

Gaus mean : 30.2491

Gaus Width: 3.6580



B1L003S, U16-ch102, adc1

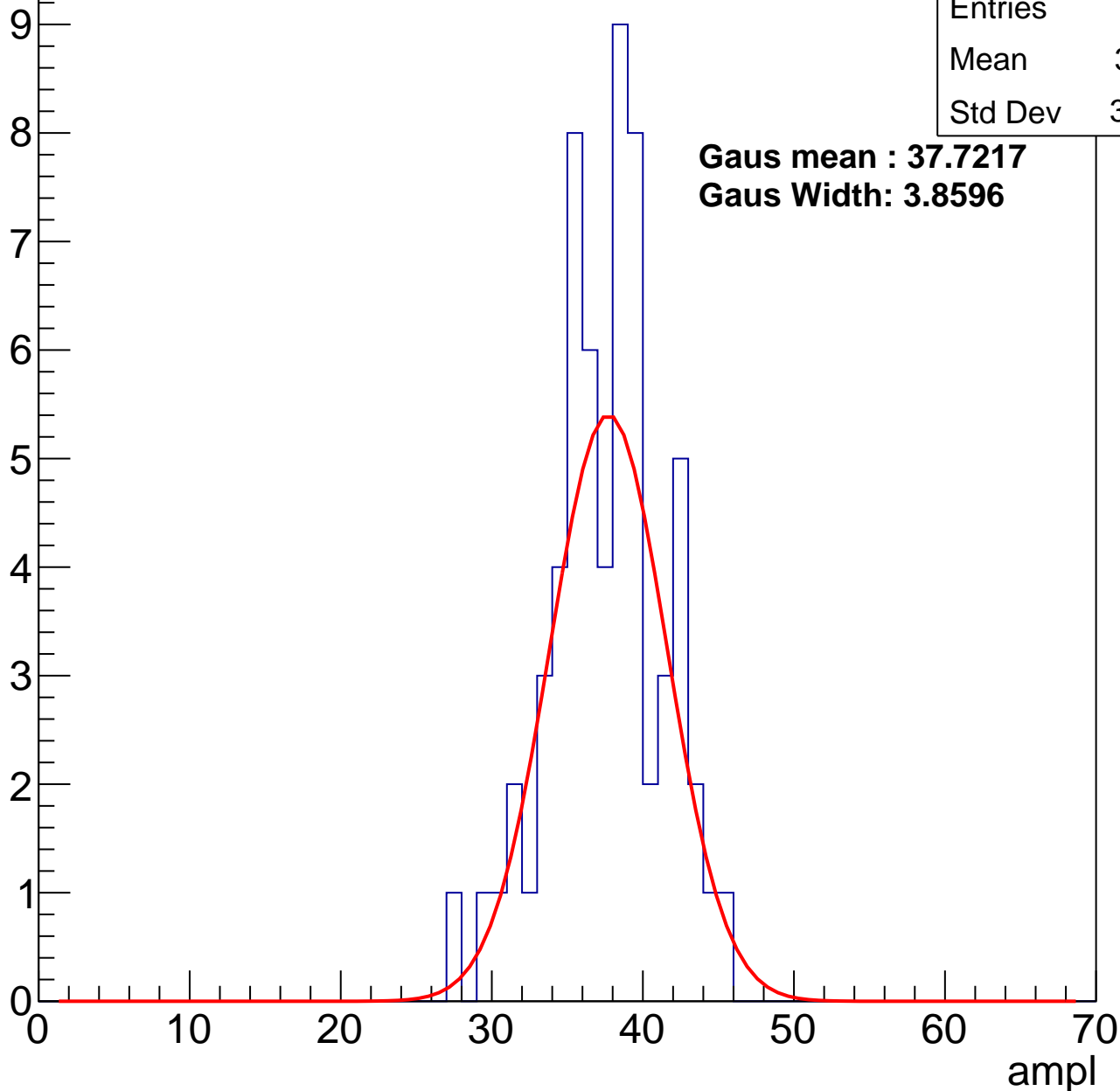
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.11
Std Dev	3.699

Gaus mean : 37.7217

Gaus Width: 3.8596



B1L003S, U16-ch102, adc2

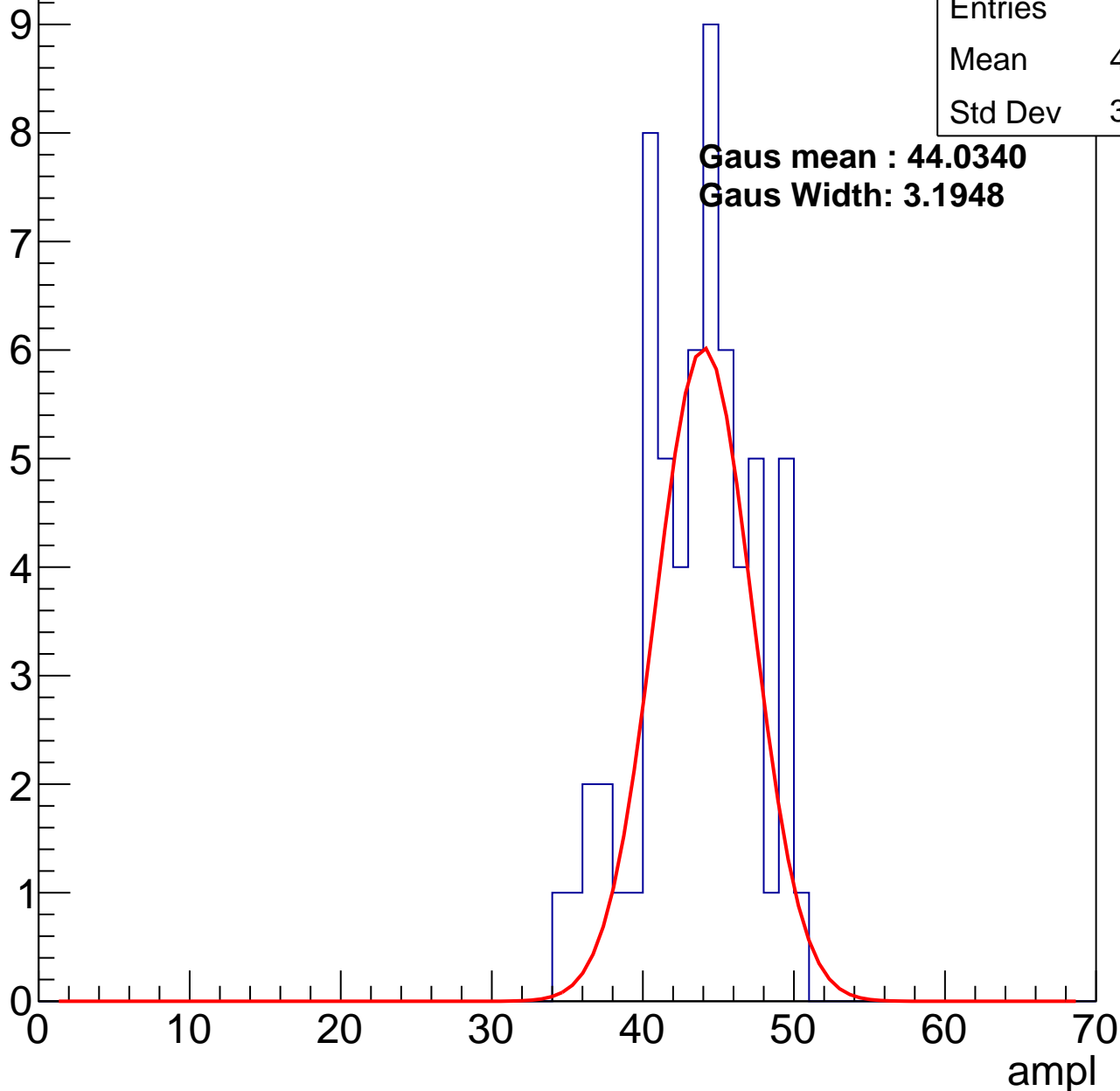
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.08
Std Dev	3.713

Gaus mean : 44.0340

Gaus Width: 3.1948

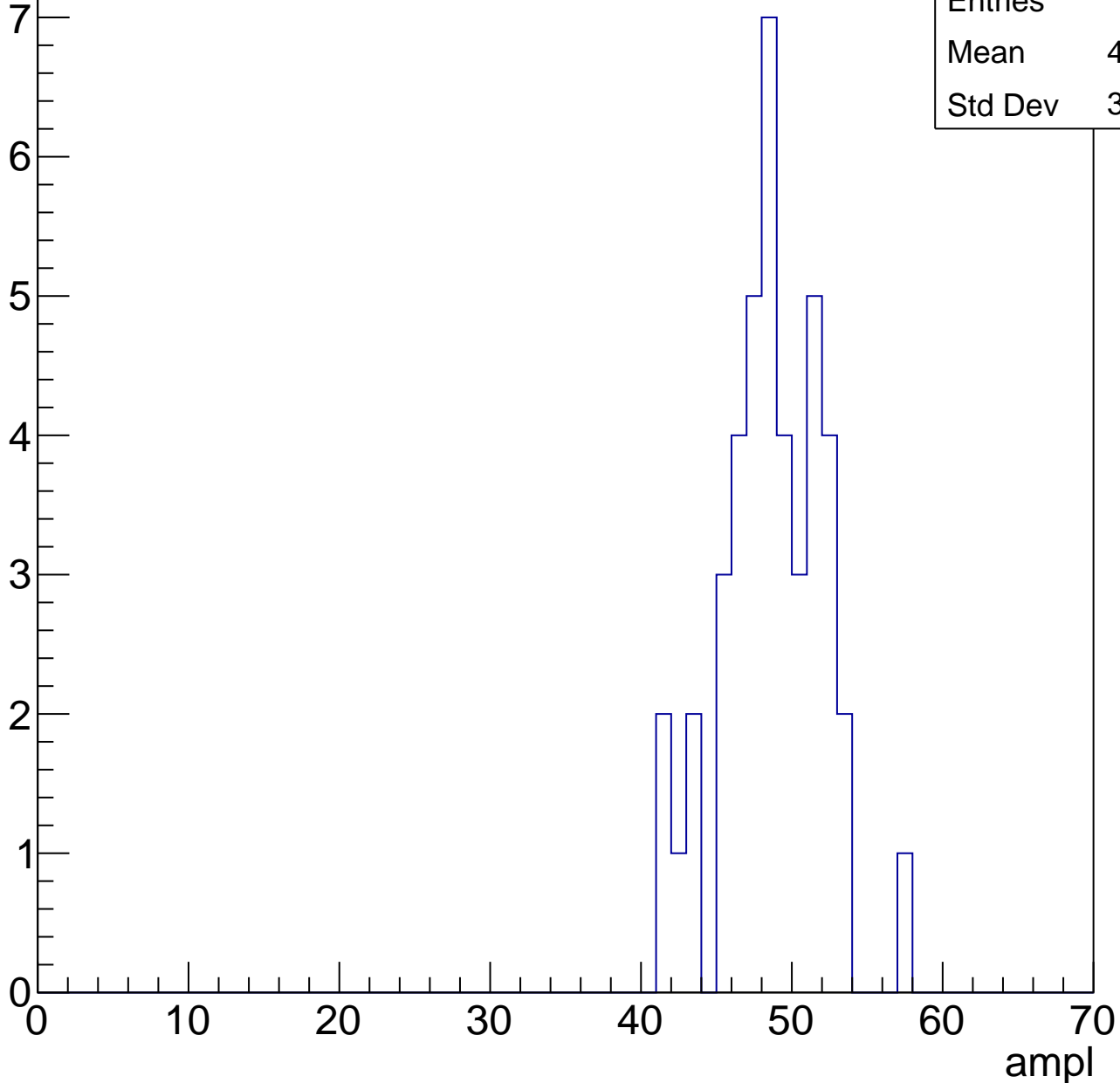


B1L003S, U16-ch102, adc3

calib_packv5_042523_0143.root, FC#13, port D2

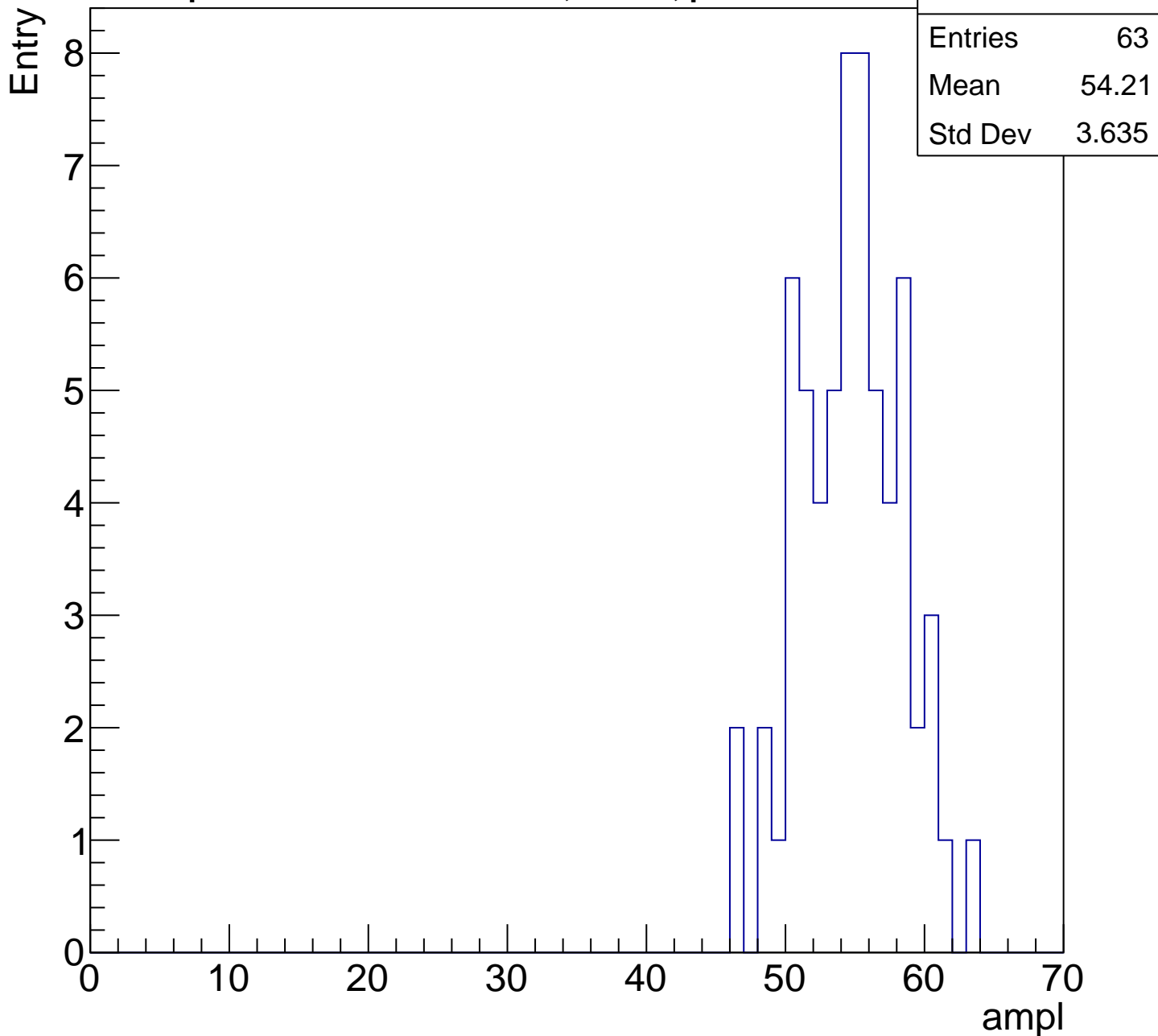
Entry

Entries	43
Mean	48.19
Std Dev	3.364



B1L003S, U16-ch102, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U16-ch102, adc5

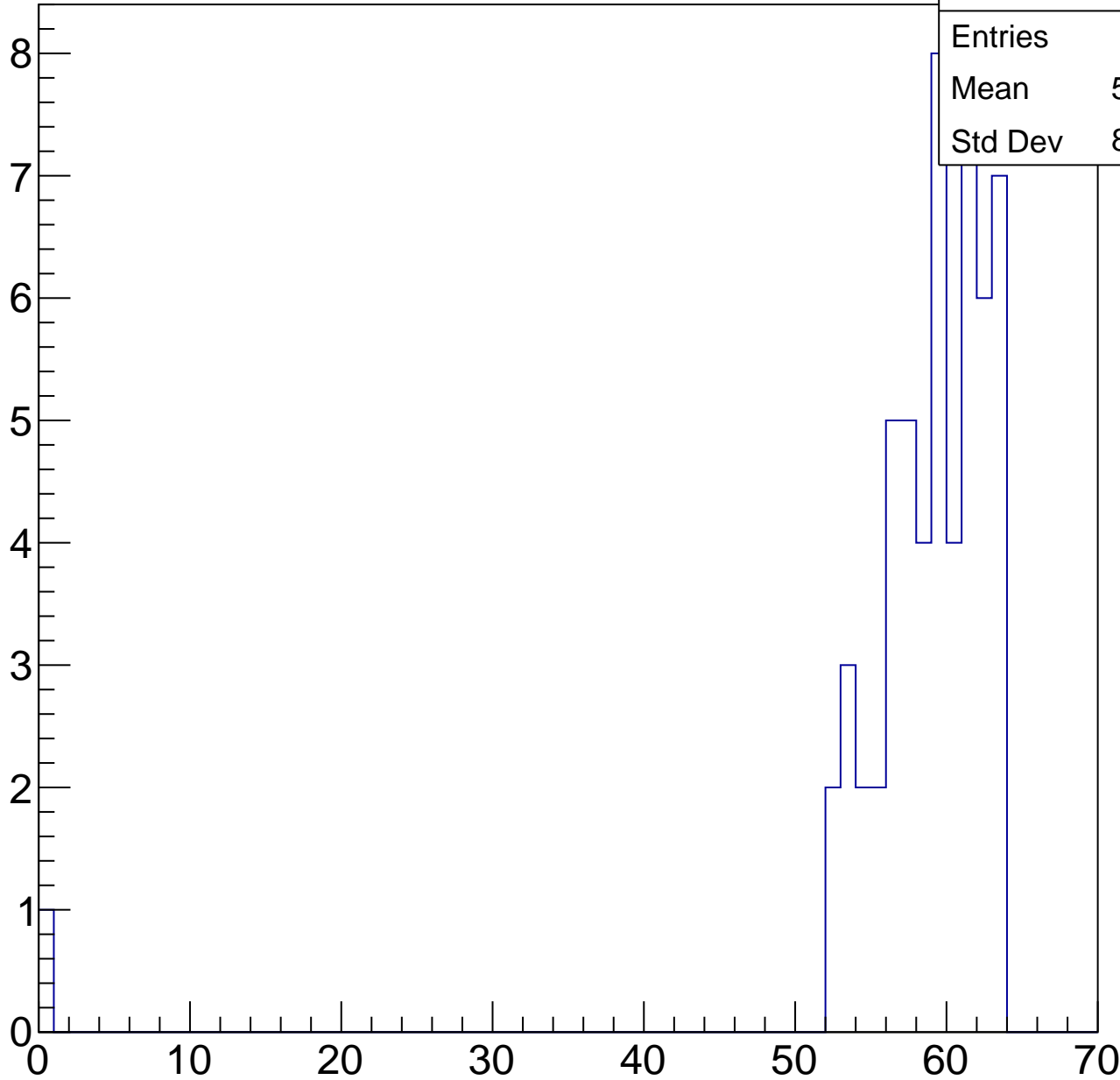
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	57.74
Std Dev	8.324

ampl



B1L003S, U16-ch102, adc6

calib_packv5_042523_0143.root, FC#13, port D2

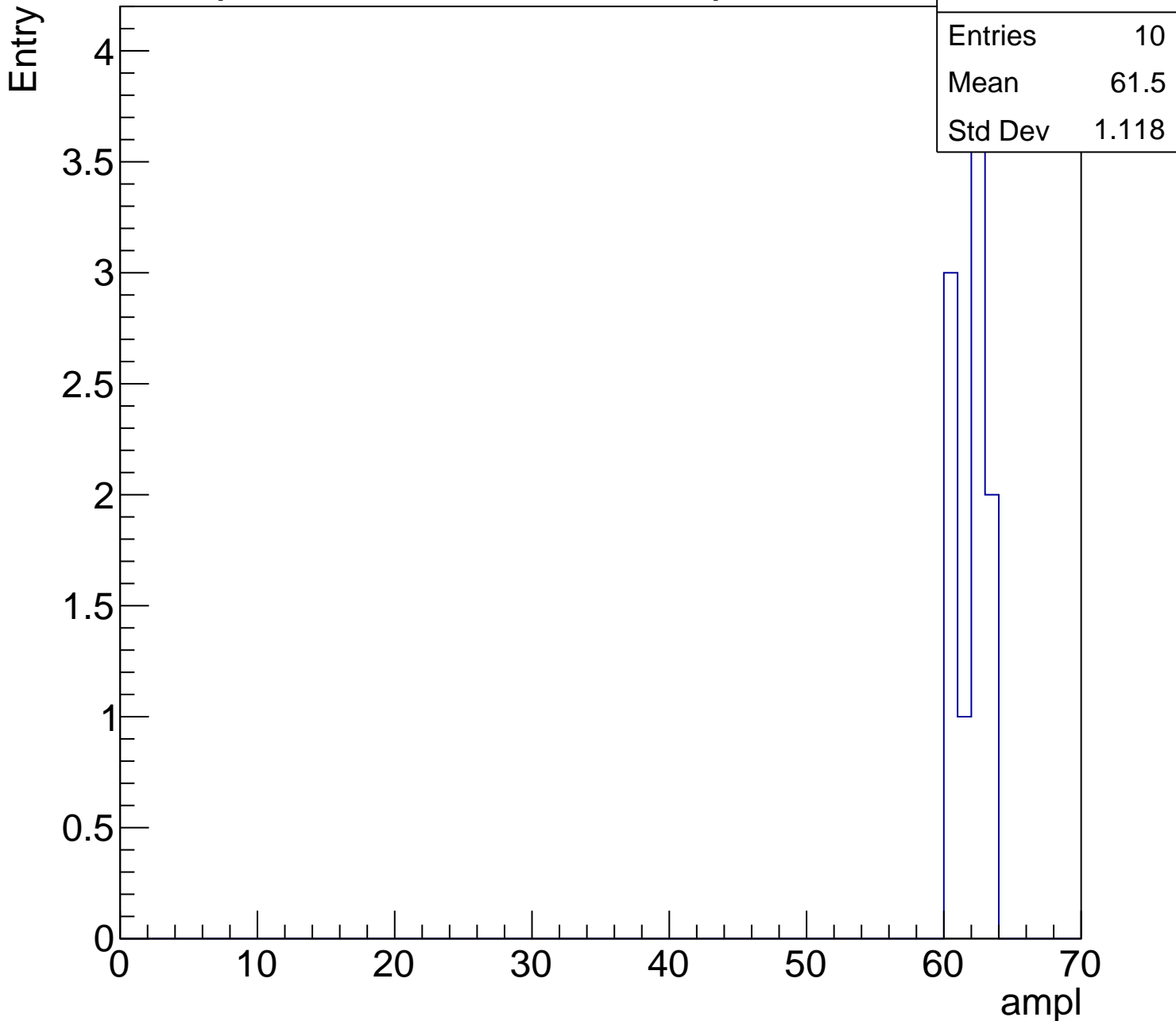
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	61.5
Std Dev	1.118

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch102, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch103, adc0

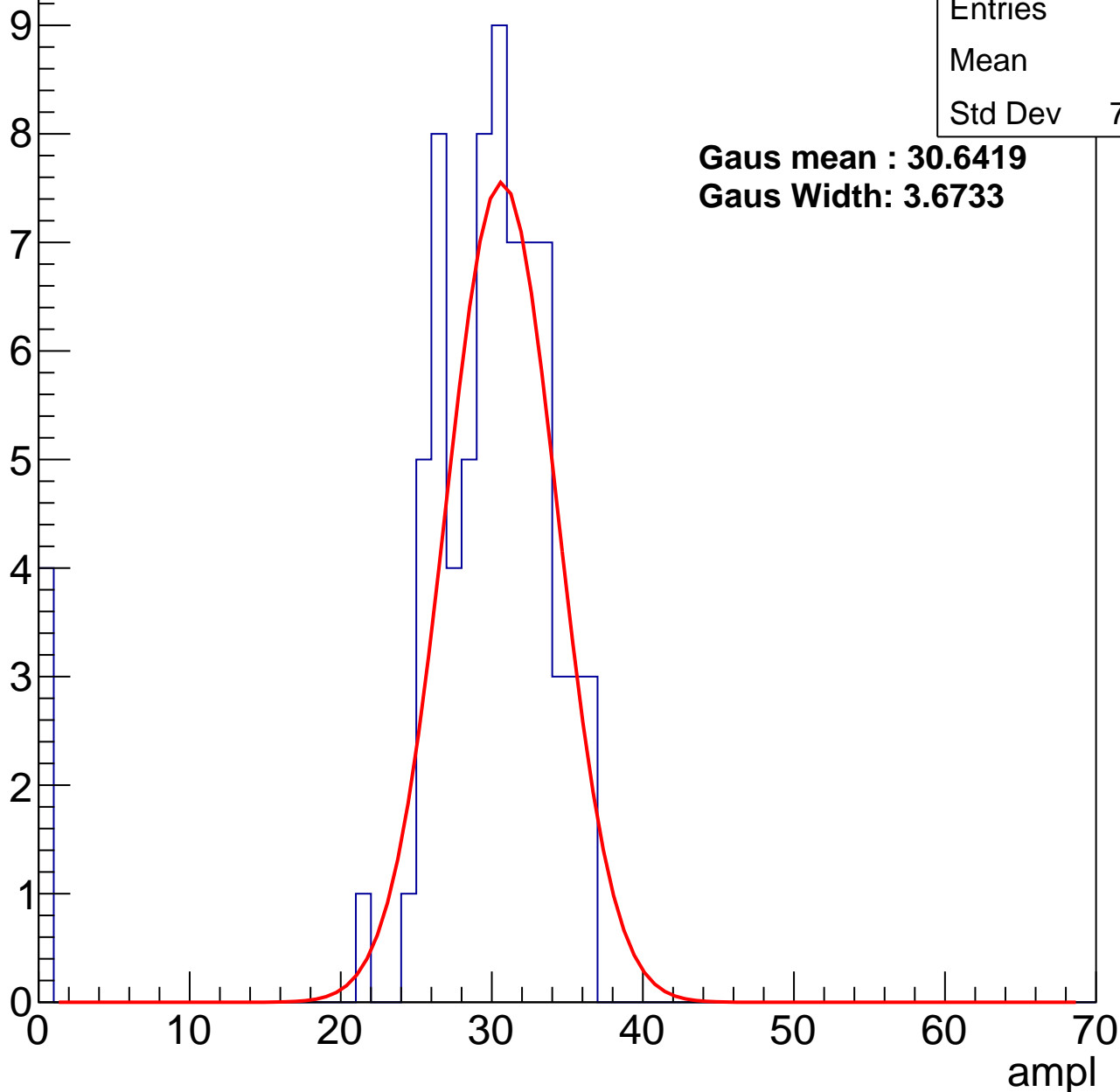
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.2
Std Dev	7.412

Gaus mean : 30.6419

Gaus Width: 3.6733



B1L003S, U16-ch103, adc1

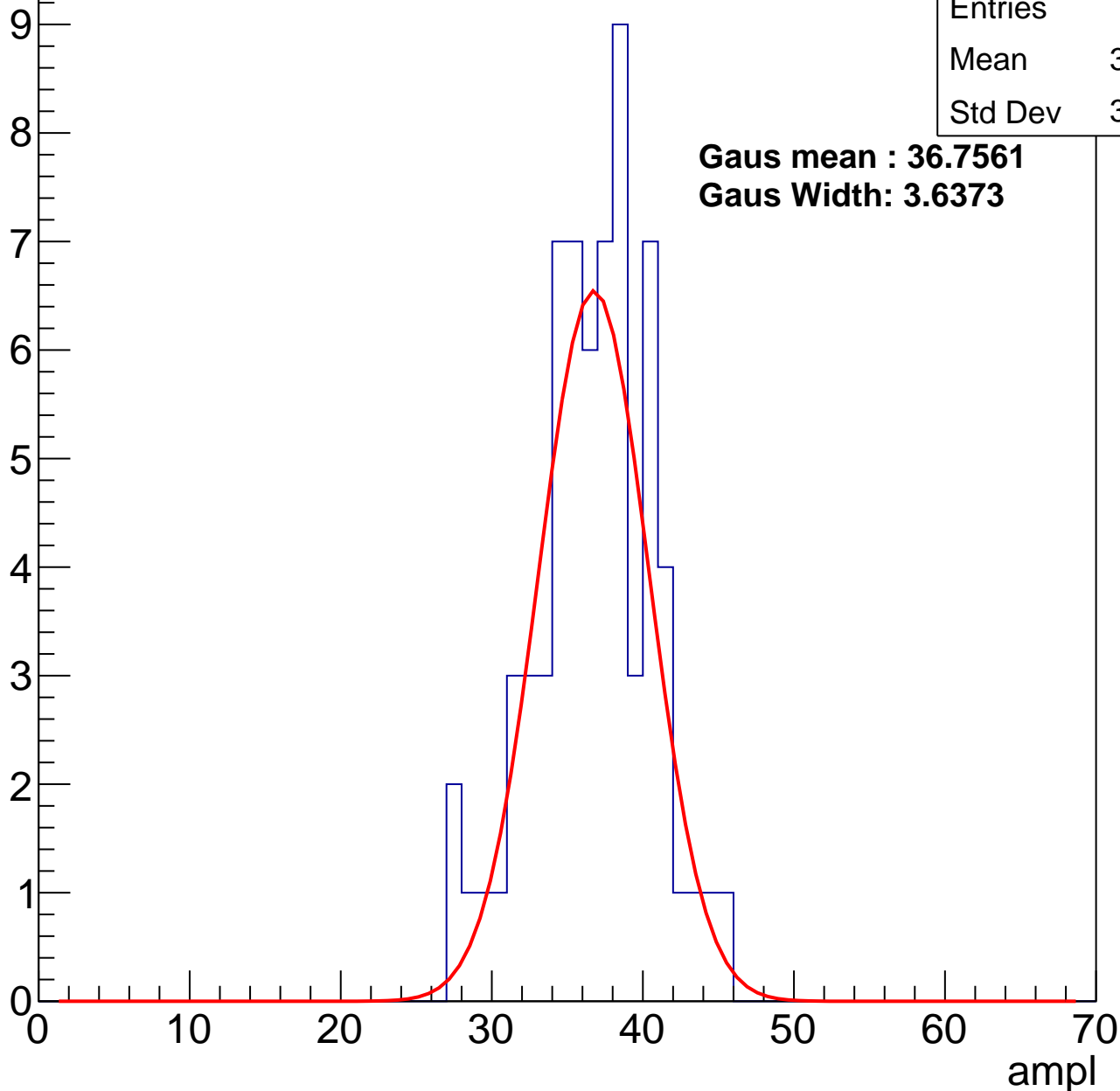
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.24
Std Dev	3.843

Gaus mean : 36.7561

Gaus Width: 3.6373



B1L003S, U16-ch103, adc2

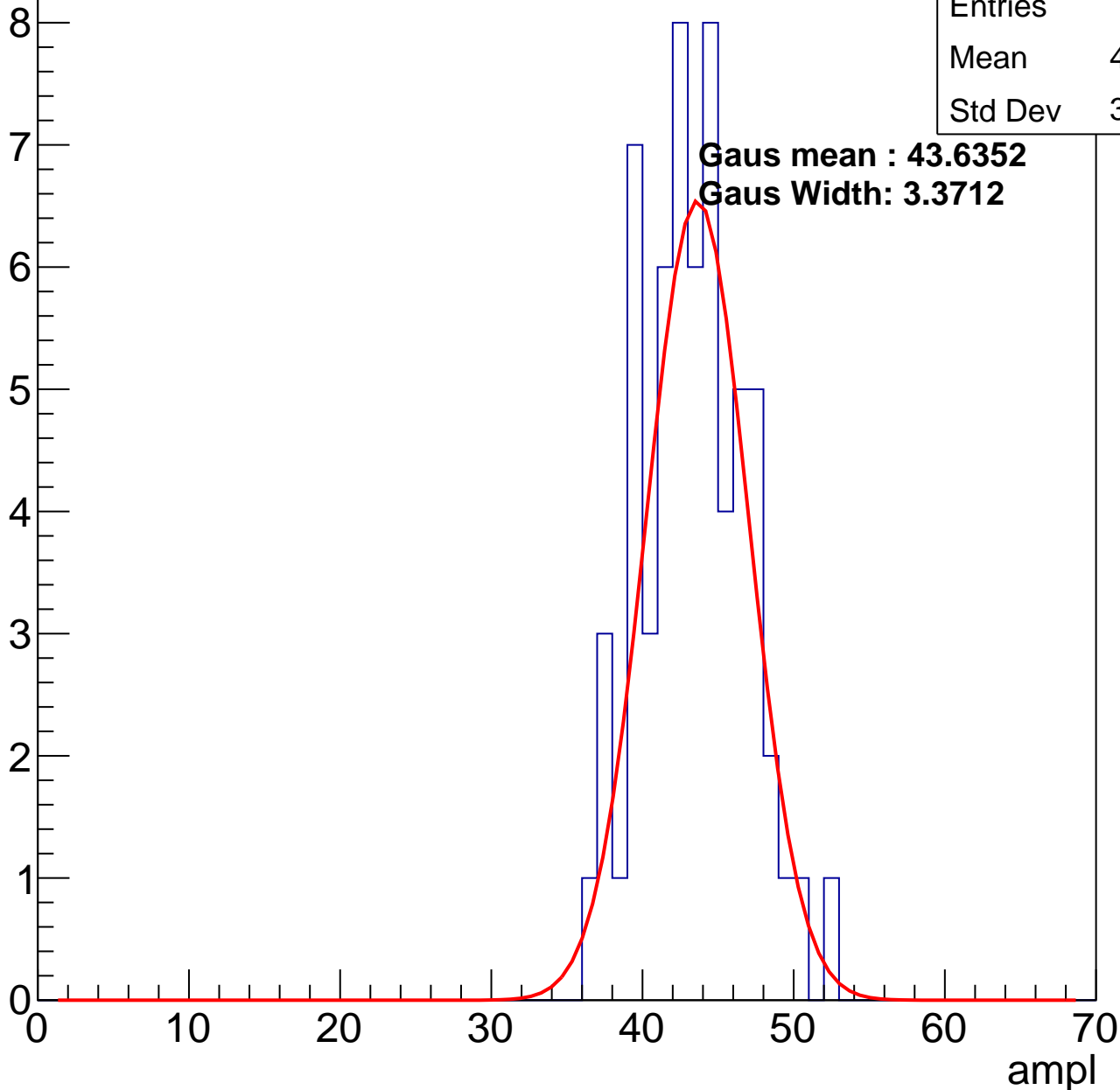
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.94
Std Dev	3.402

Gaus mean : 43.6352

Gaus Width: 3.3712

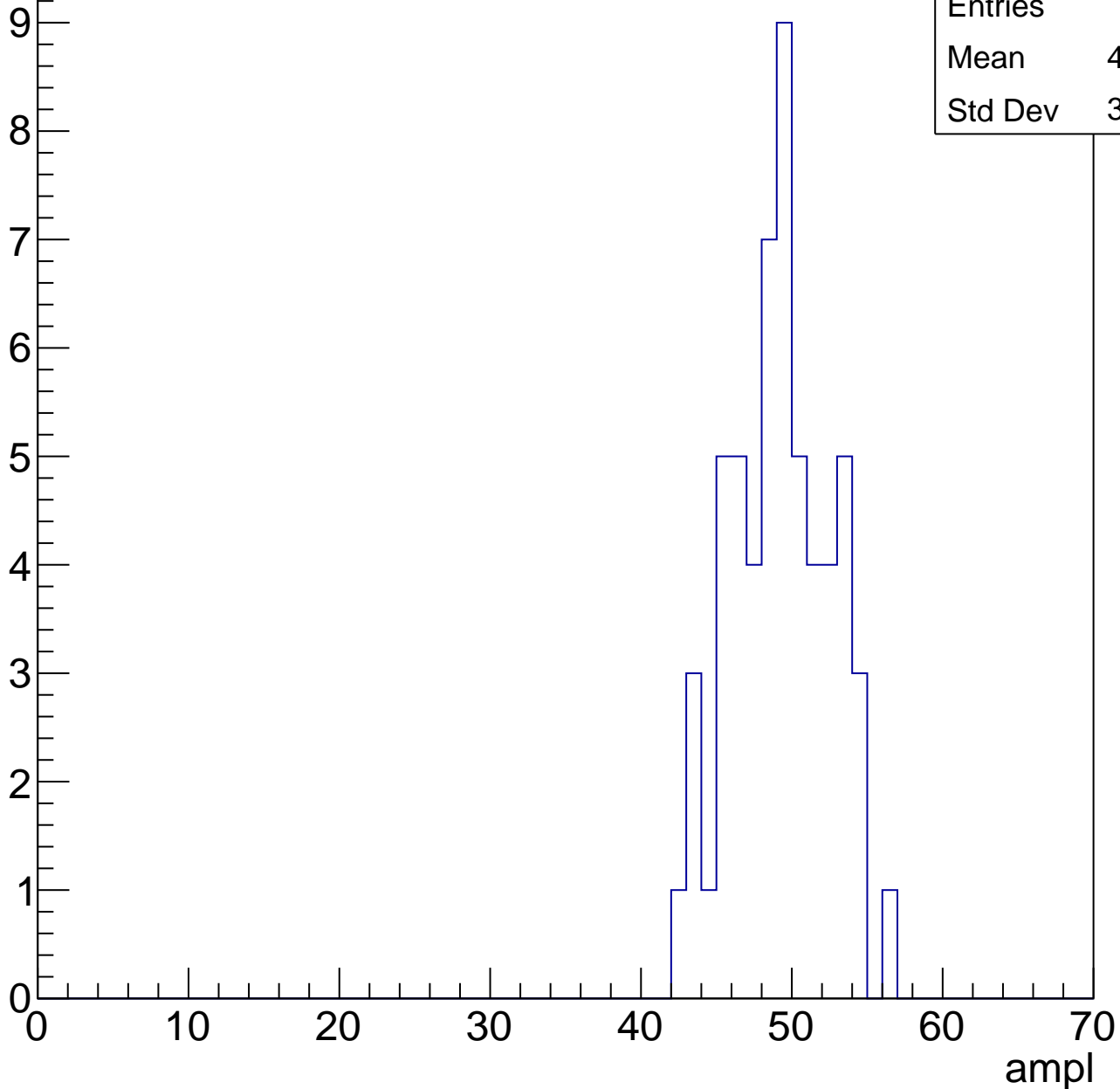


B1L003S, U16-ch103, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

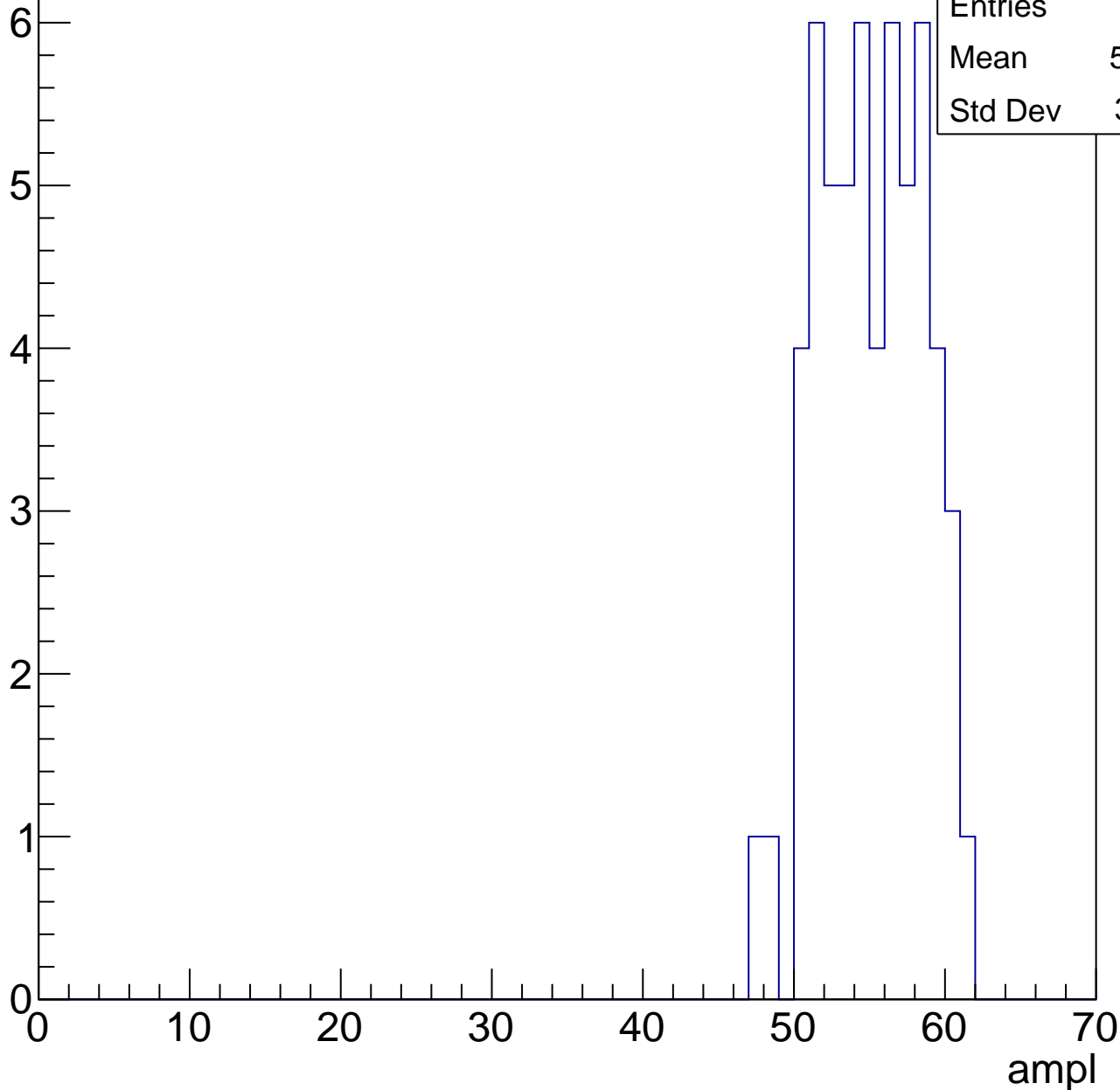
Entries	57
Mean	48.77
Std Dev	3.212



B1L003S, U16-ch103, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



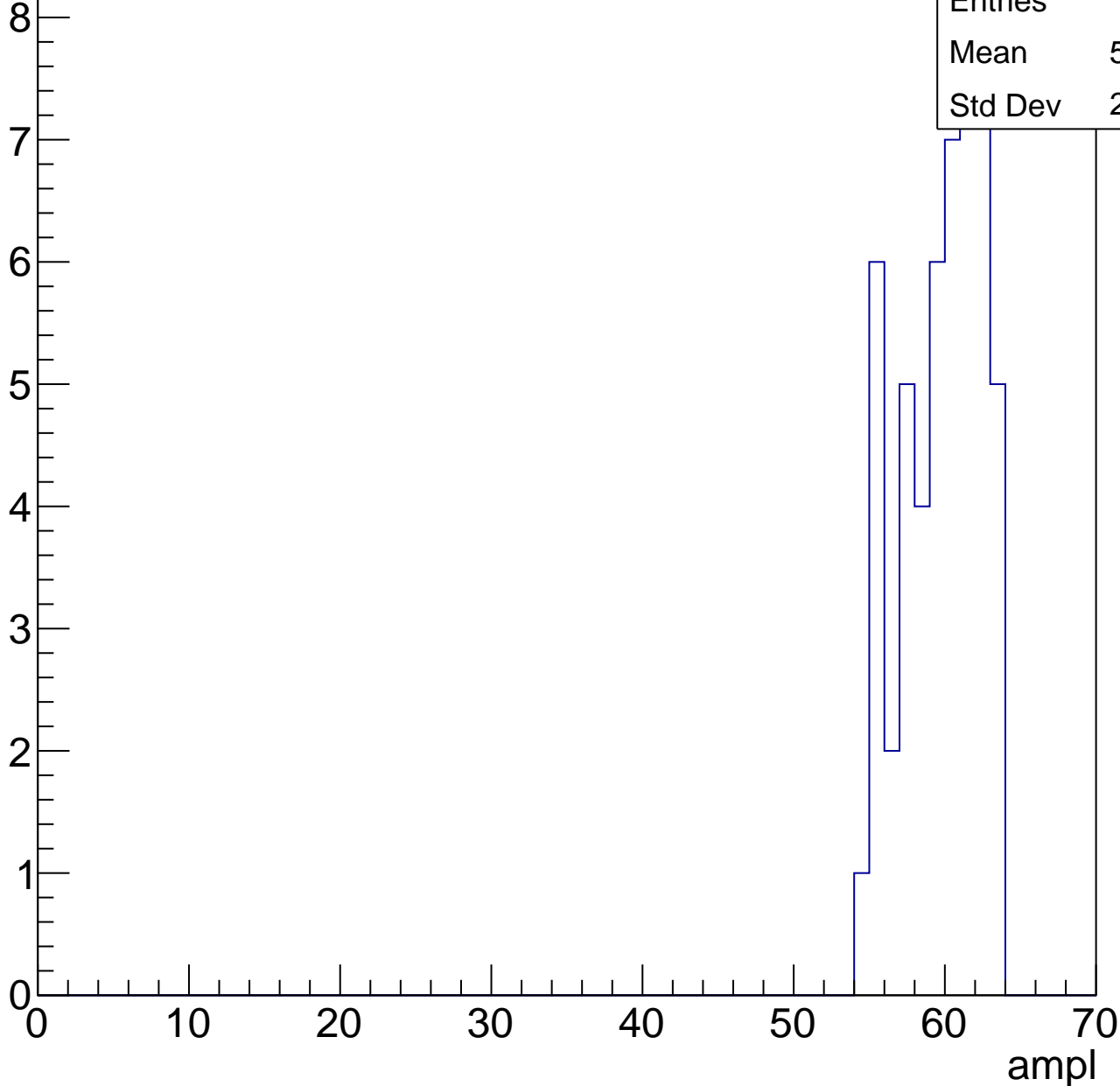
Entries	57
Mean	54.67
Std Dev	3.321

B1L003S, U16-ch103, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	59.35
Std Dev	2.586

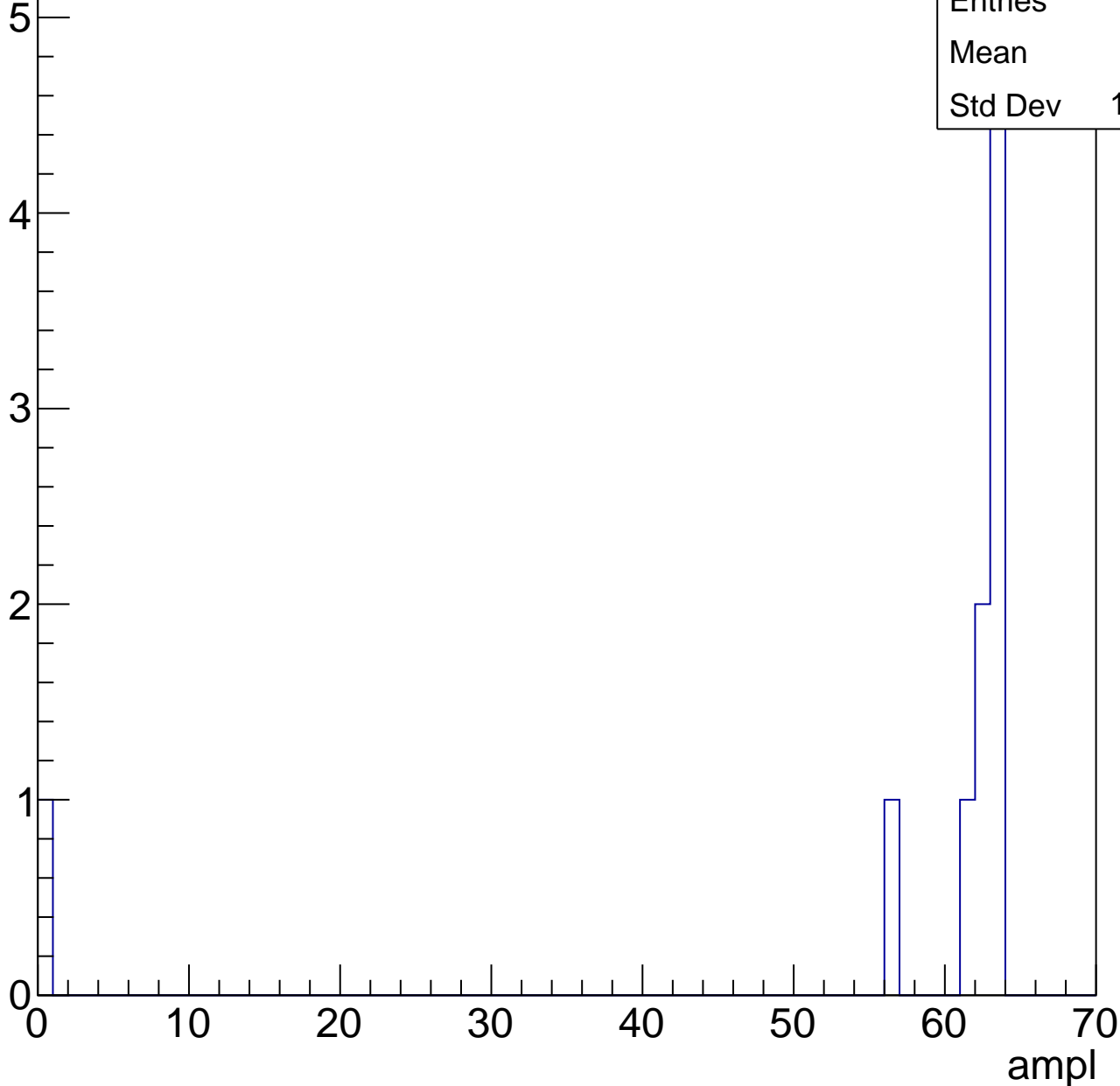


B1L003S, U16-ch103, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.6
Std Dev	18.65



B1L003S, U16-ch103, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch104, adc0

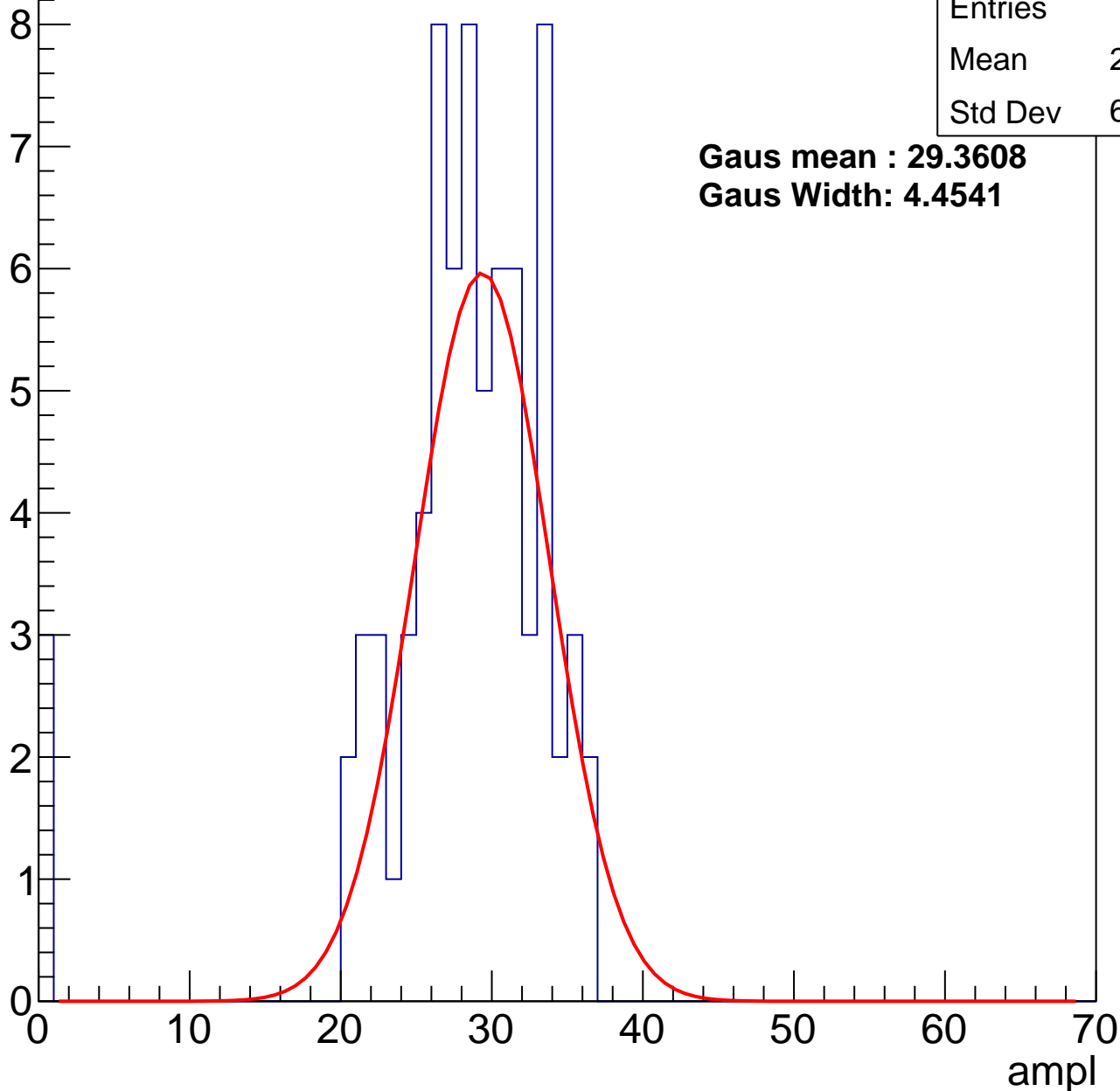
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	27.29
Std Dev	6.807

Gaus mean : 29.3608

Gaus Width: 4.4541



B1L003S, U16-ch104, adc1

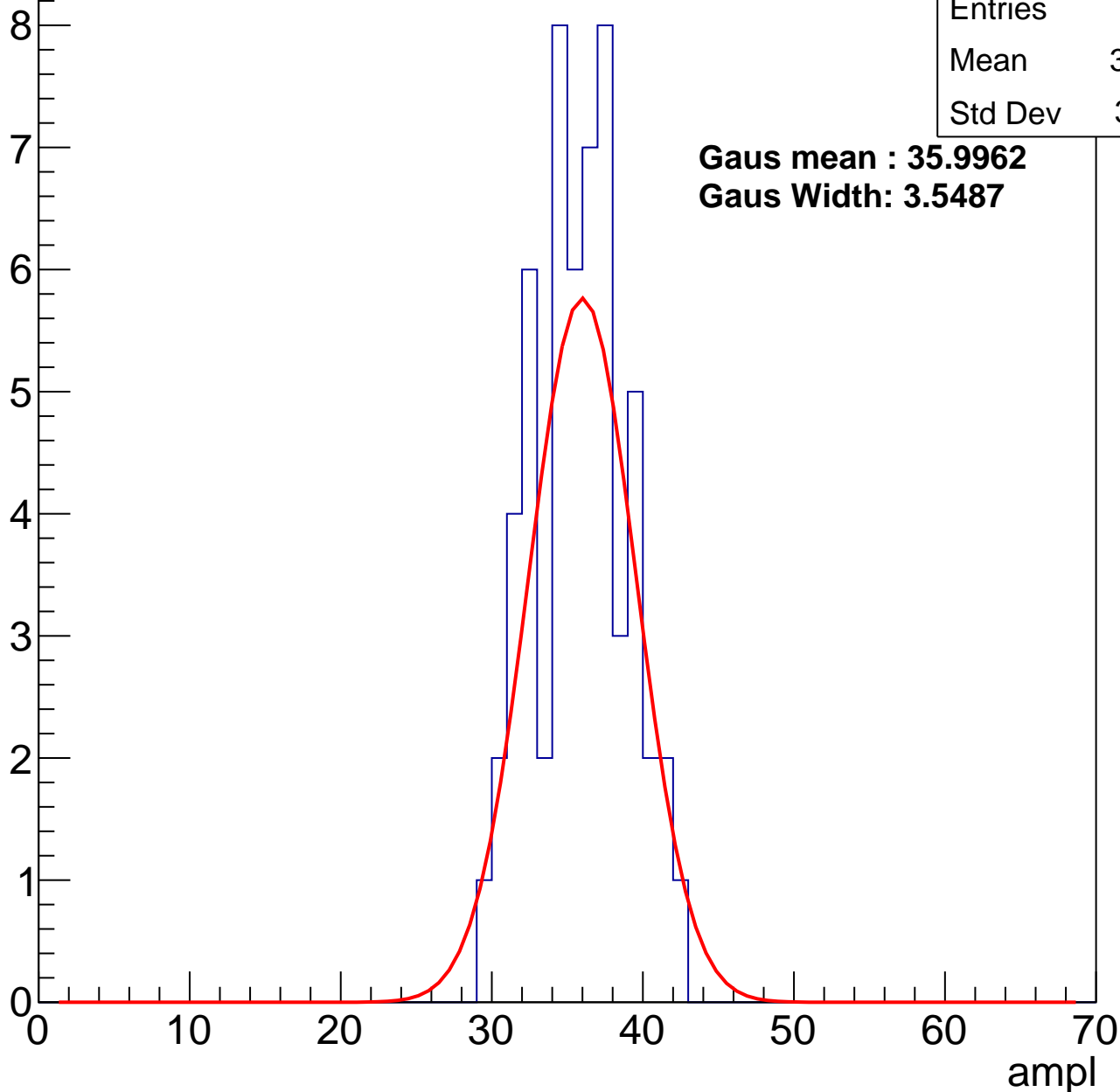
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.33
Std Dev	3.051

Gaus mean : 35.9962

Gaus Width: 3.5487



B1L003S, U16-ch104, adc2

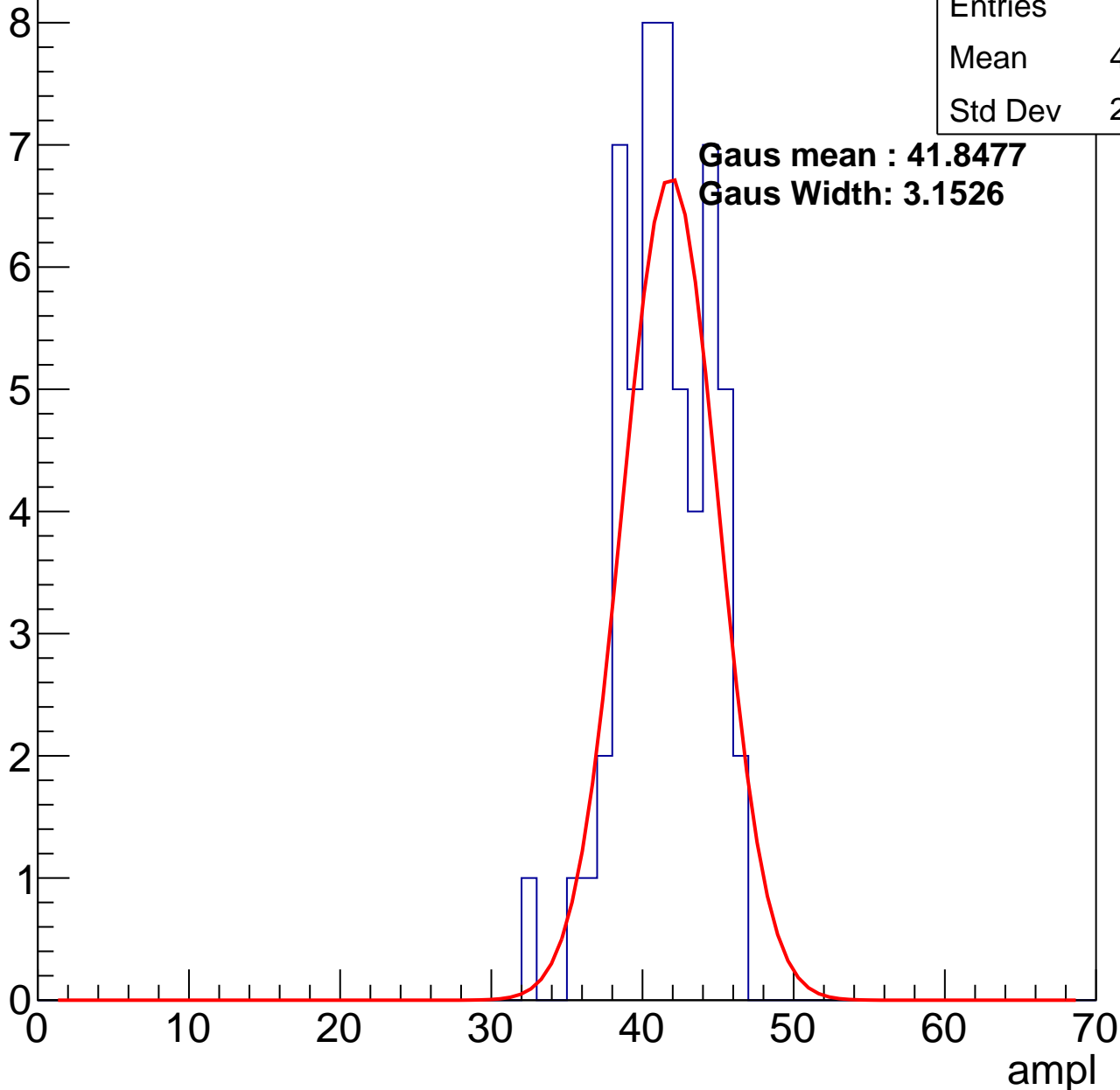
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	40.95
Std Dev	2.924

Gaus mean : 41.8477

Gaus Width: 3.1526

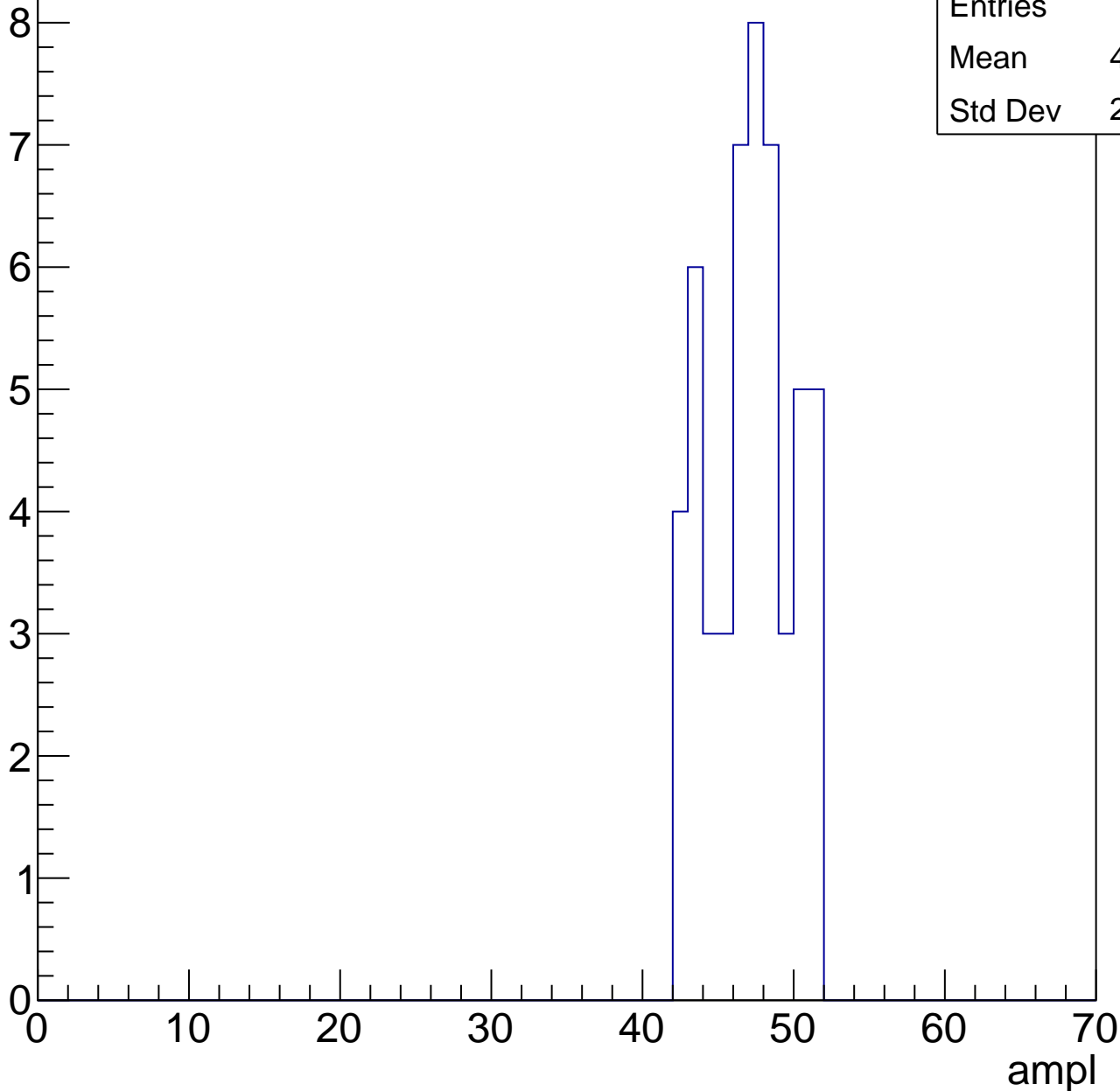


B1L003S, U16-ch104, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	46.65
Std Dev	2.728

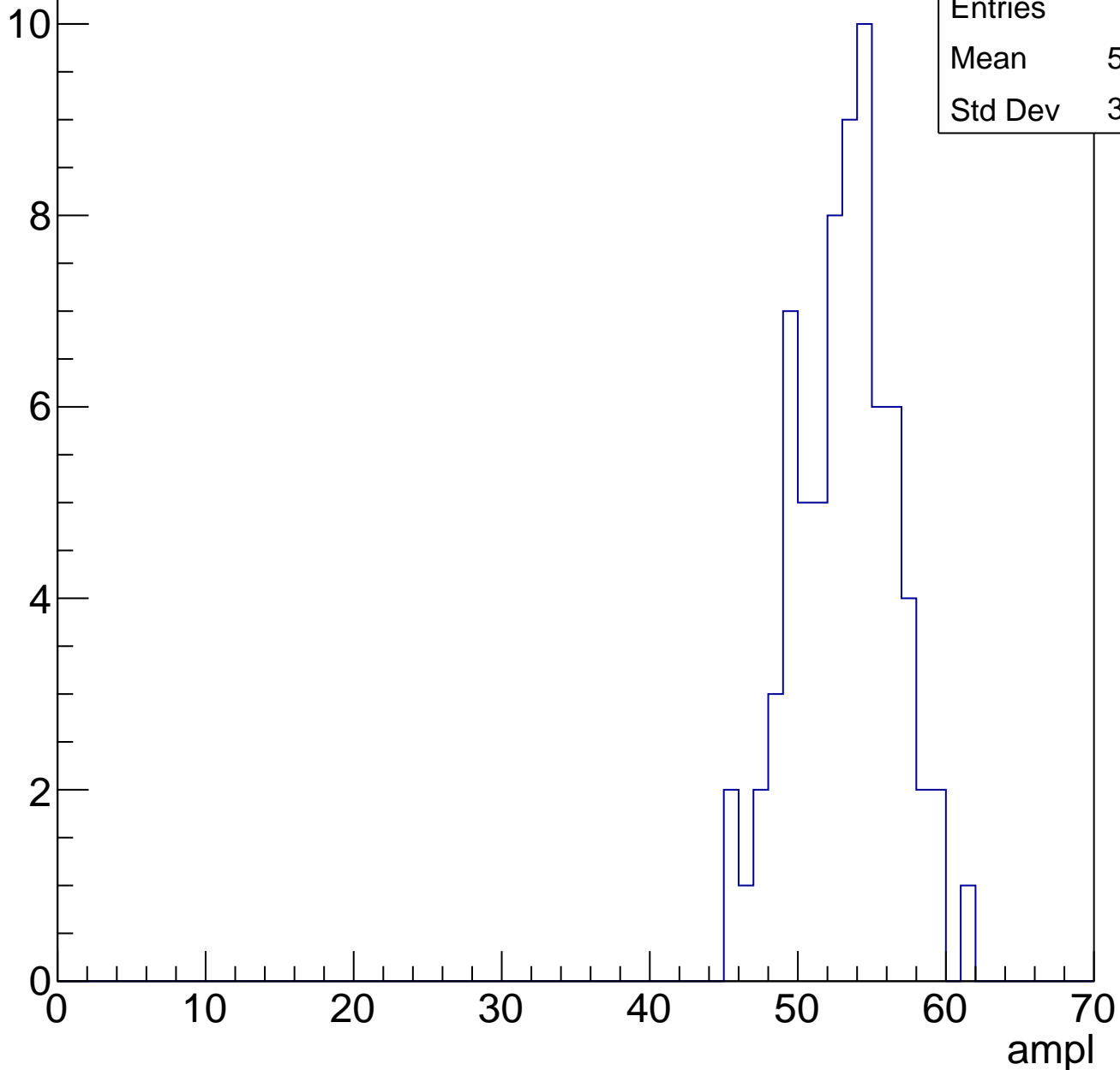


B1L003S, U16-ch104, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	52.66
Std Dev	3.389

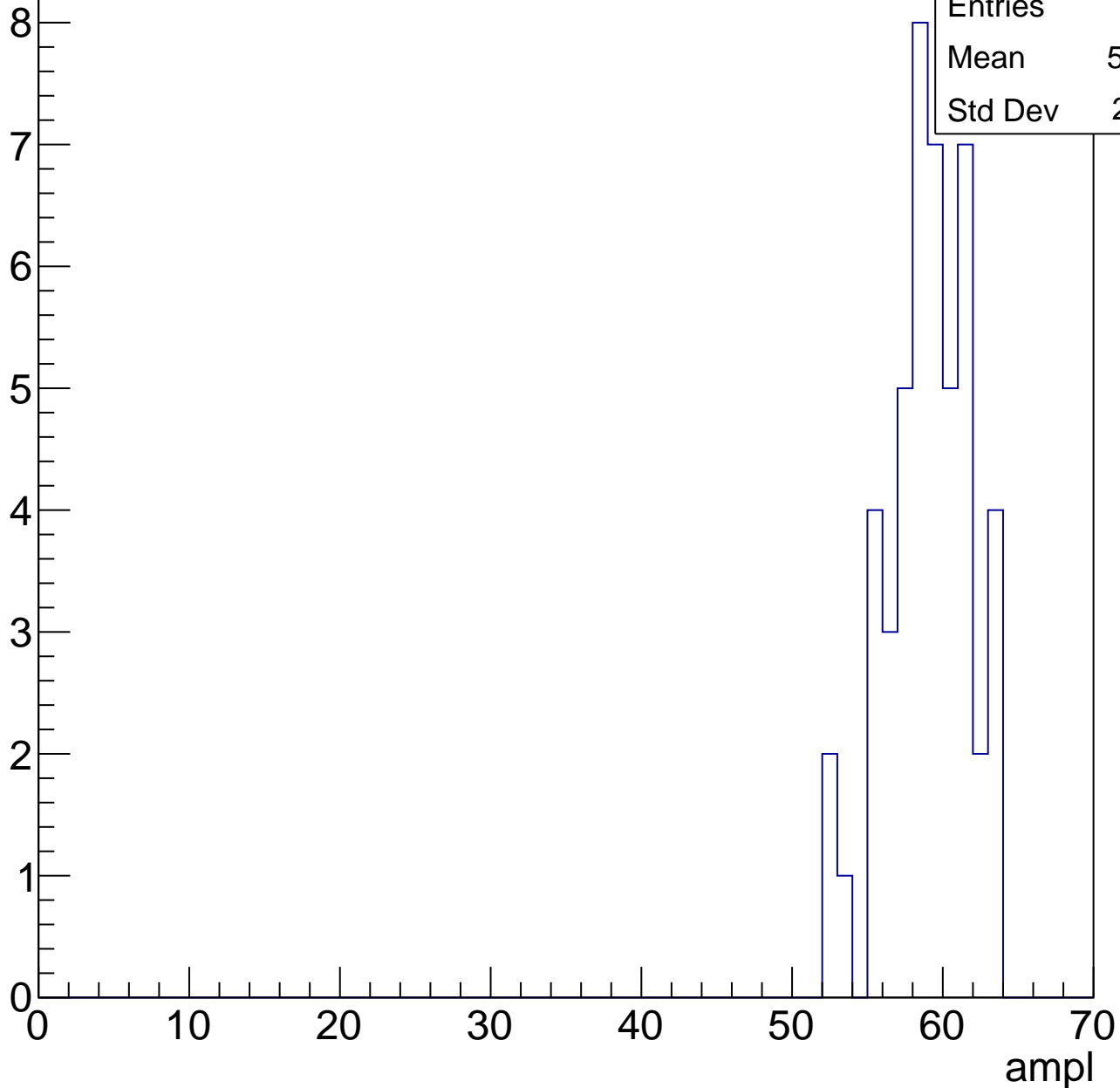
Entry



B1L003S, U16-ch104, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



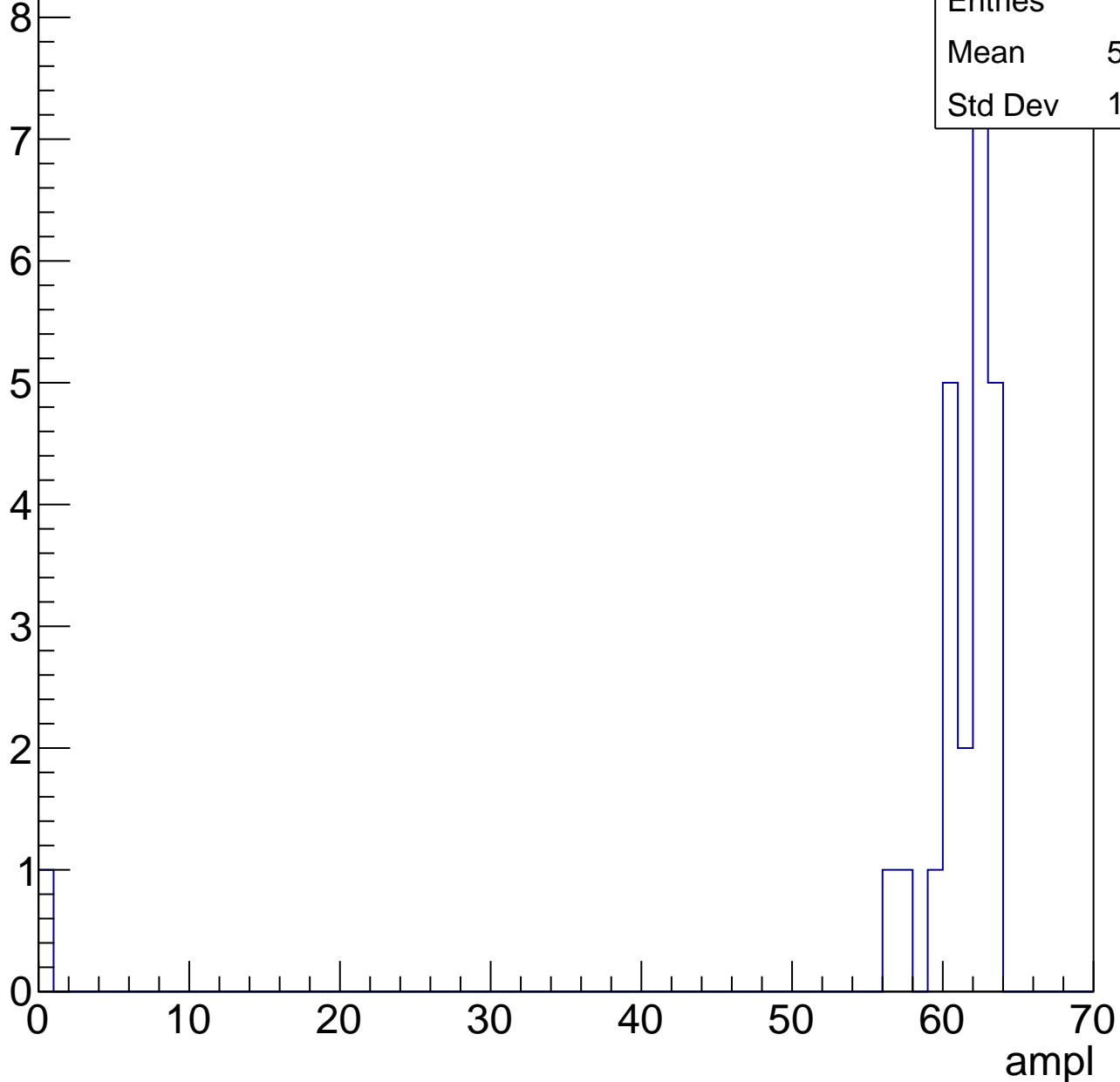
Entries	48
Mean	58.54
Std Dev	2.731

B1L003S, U16-ch104, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	24
Mean	58.54
Std Dev	12.34



B1L003S, U16-ch104, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch105, adc0

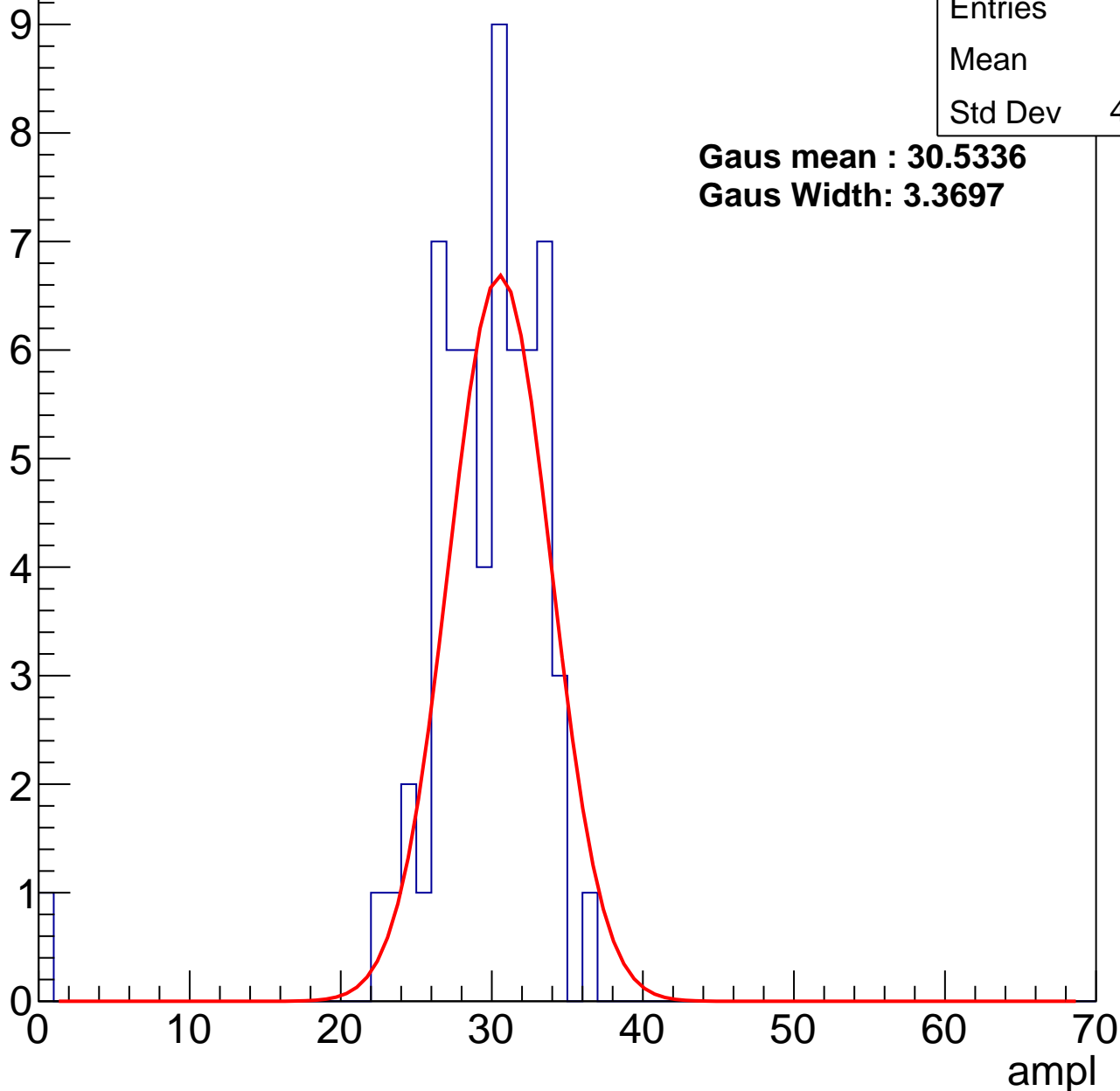
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.9
Std Dev	4.803

Gaus mean : 30.5336

Gaus Width: 3.3697



B1L003S, U16-ch105, adc1

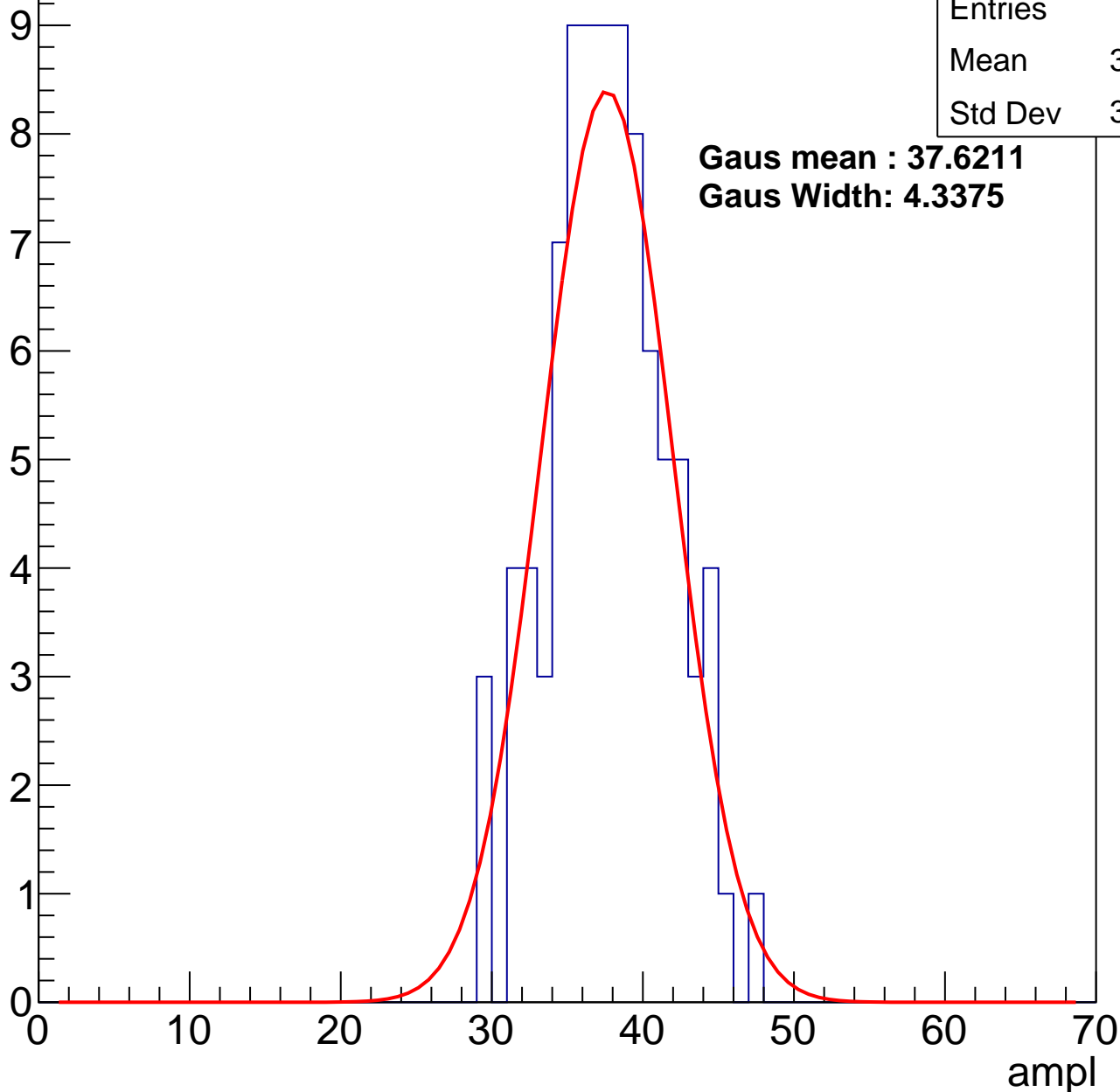
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	90
Mean	37.27
Std Dev	3.878

Gaus mean : 37.6211

Gaus Width: 4.3375



B1L003S, U16-ch105, adc2

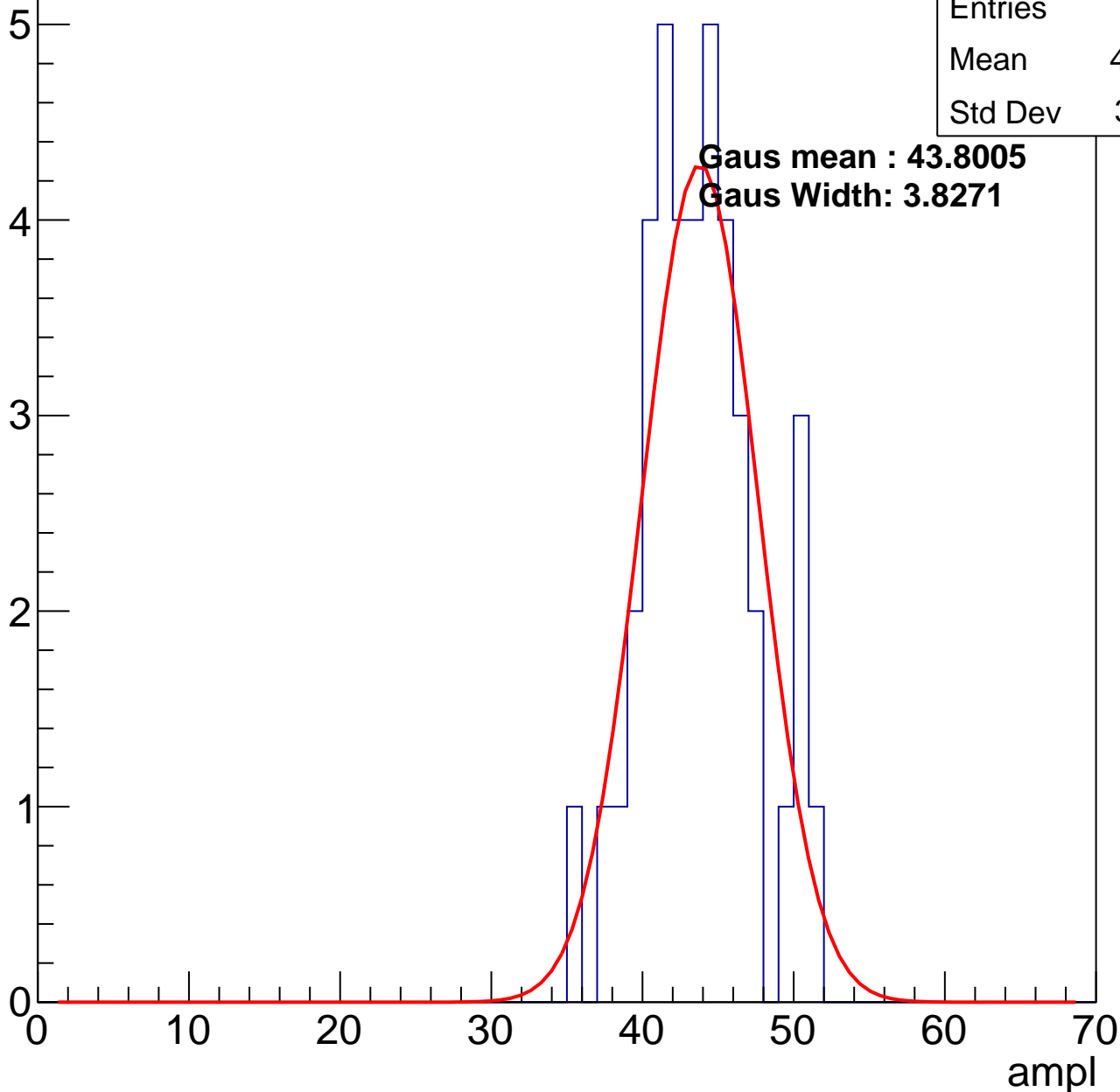
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	43.29
Std Dev	3.631

Gaus mean : 43.8005

Gaus Width: 3.8271

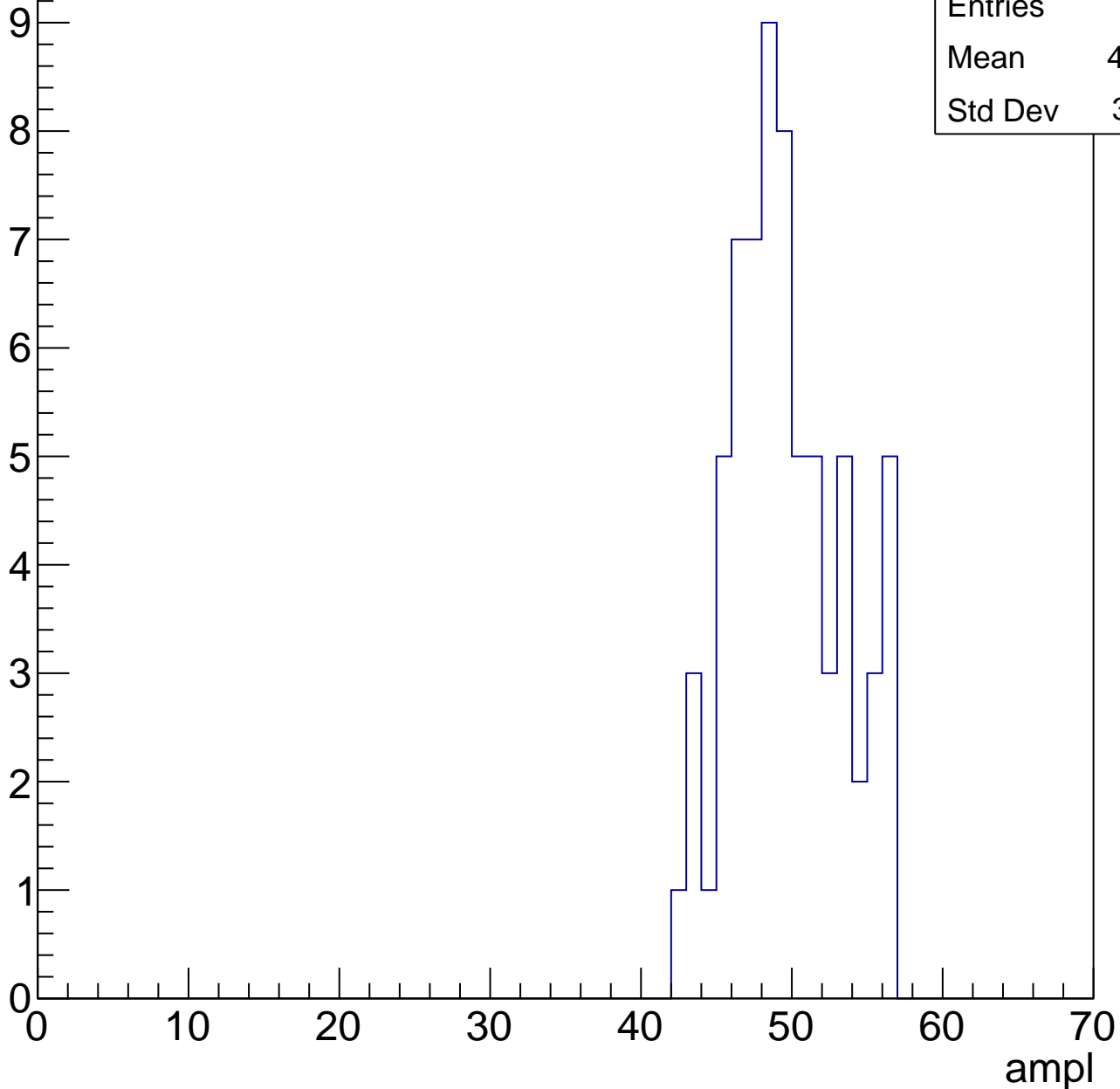


B1L003S, U16-ch105, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.19
Std Dev	3.601

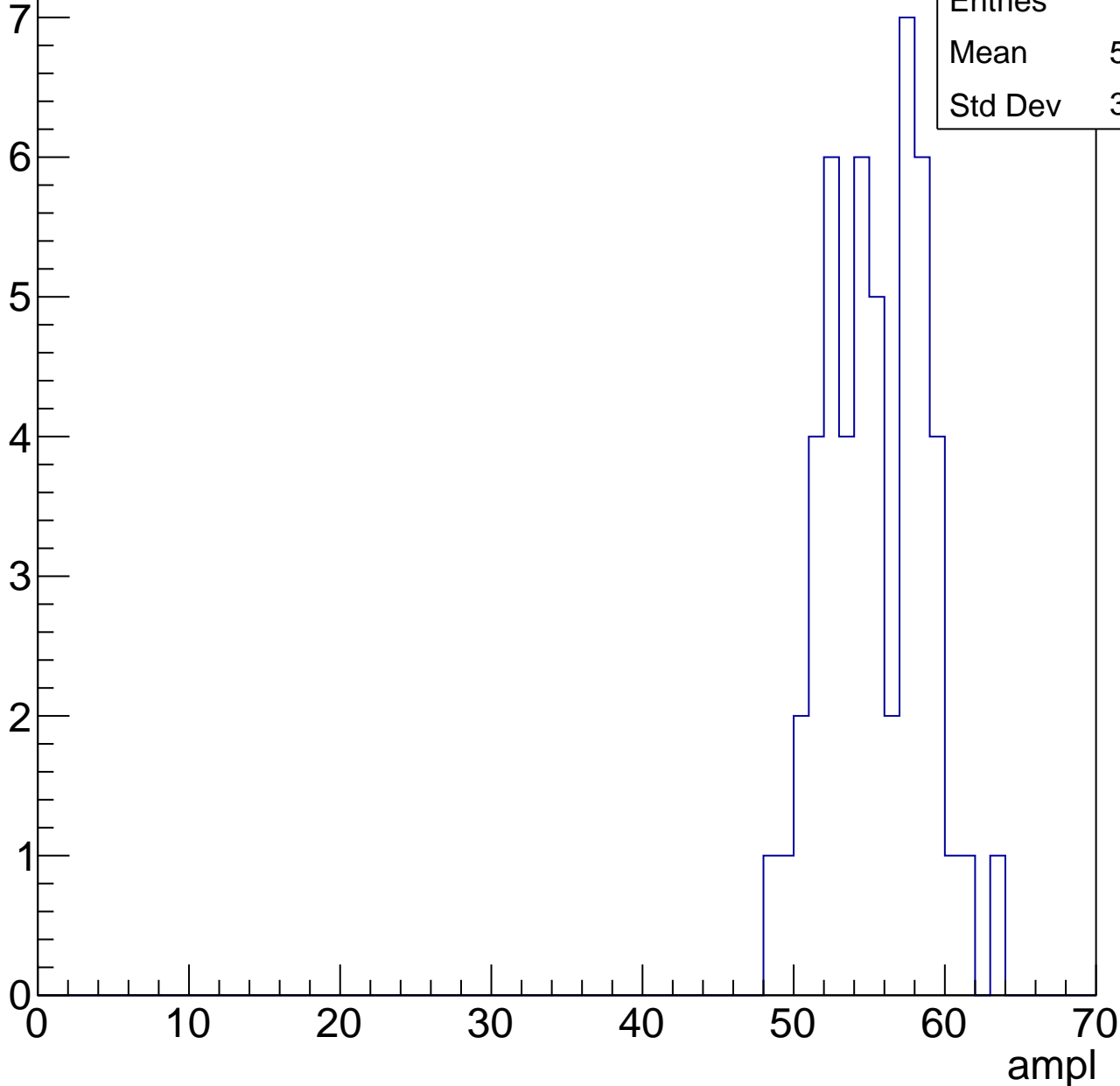


B1L003S, U16-ch105, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.96
Std Dev	3.278

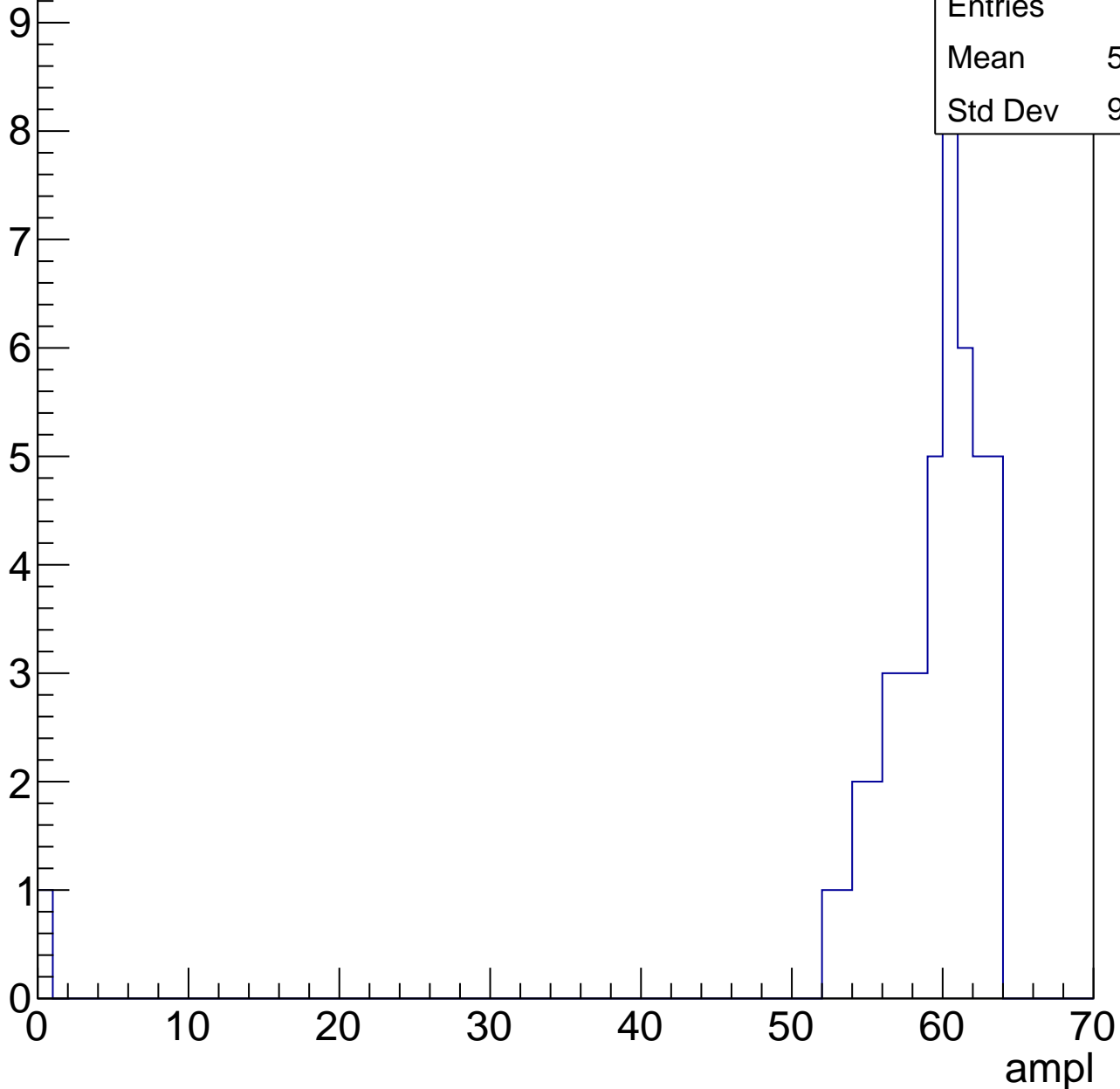


B1L003S, U16-ch105, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	57.87
Std Dev	9.076

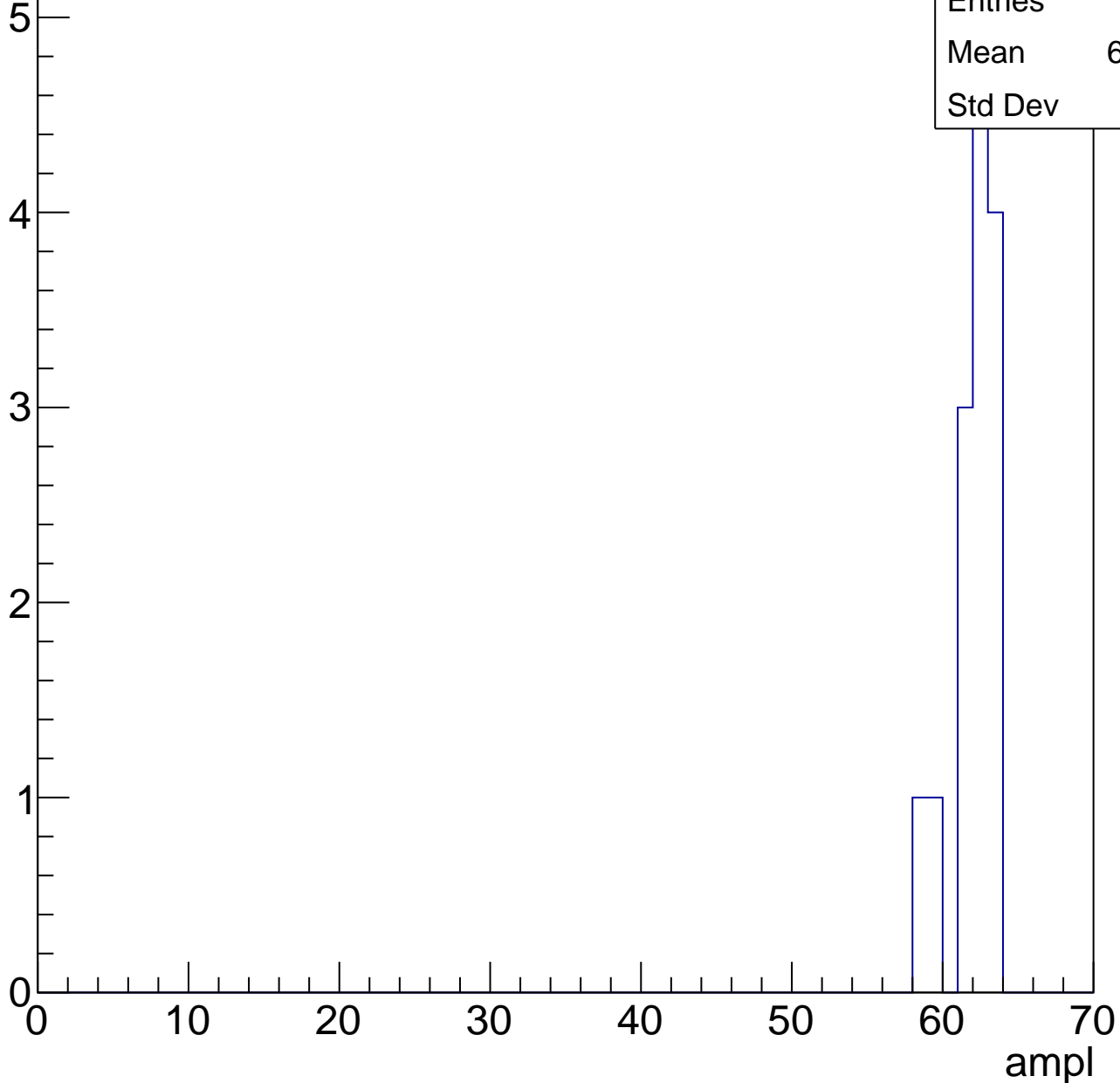


B1L003S, U16-ch105, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.57
Std Dev	1.45



B1L003S, U16-ch105, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch106, adc0

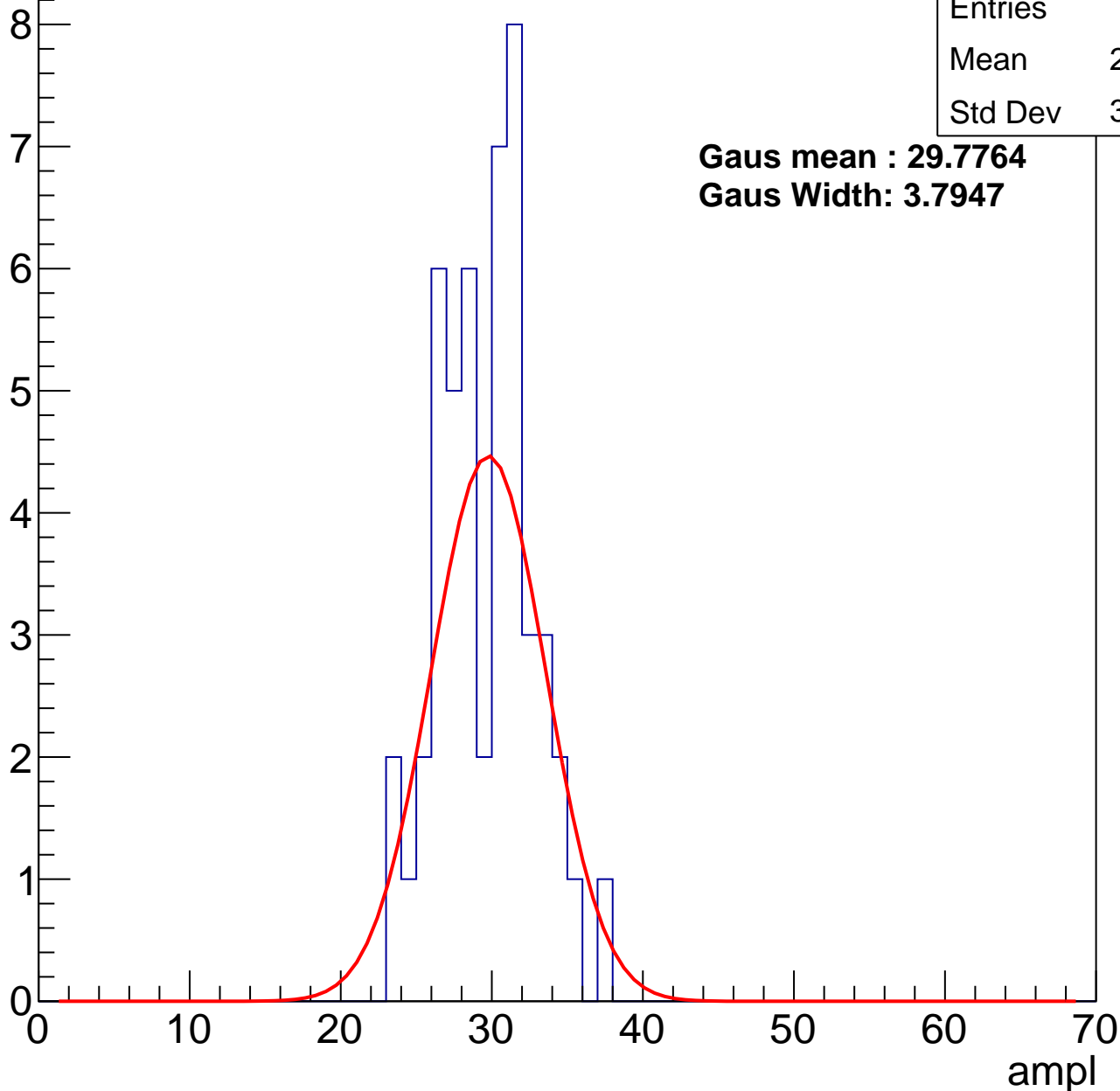
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	29.18
Std Dev	3.102

Gaus mean : 29.7764

Gaus Width: 3.7947



B1L003S, U16-ch106, adc1

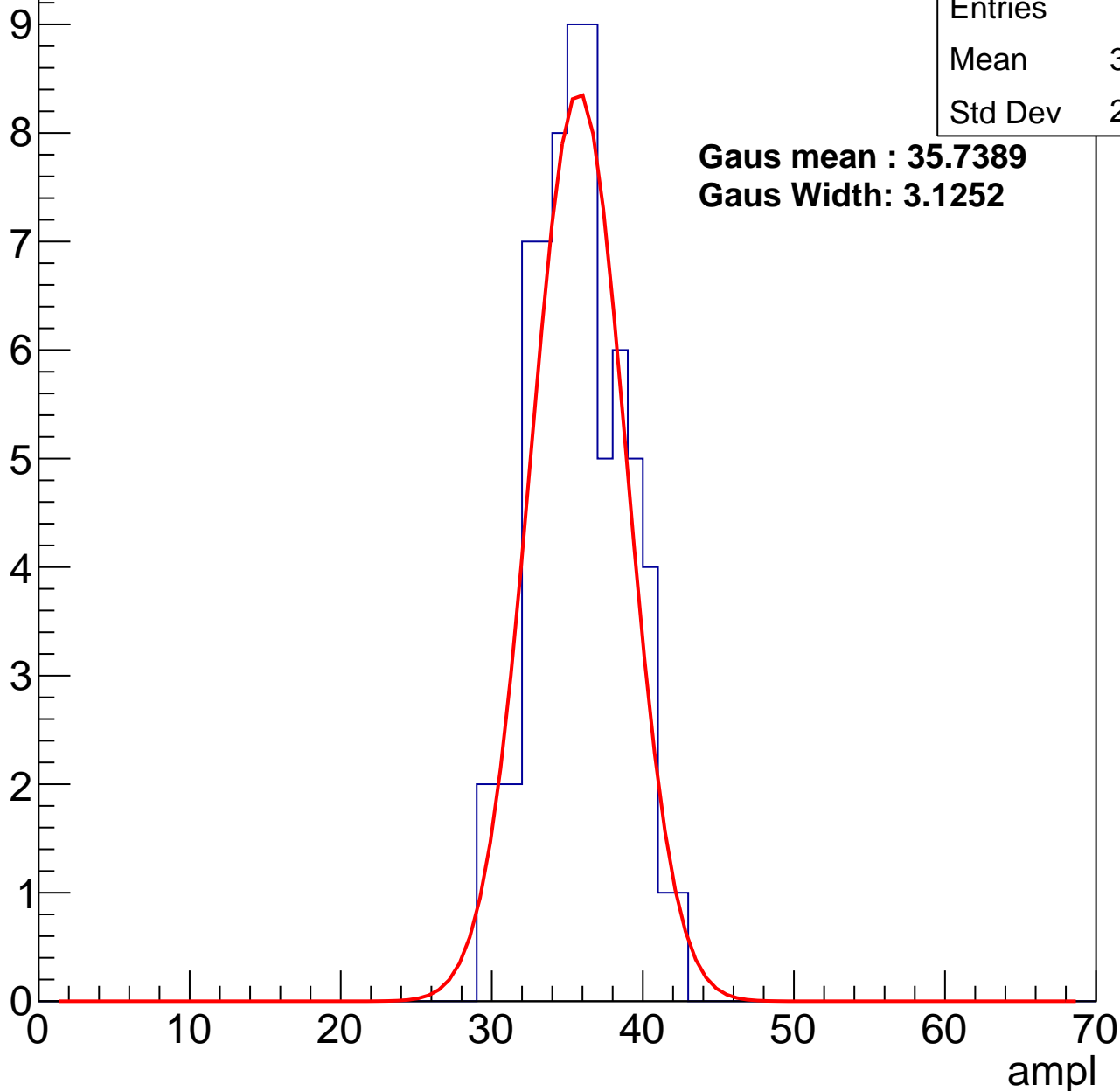
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.25
Std Dev	2.962

Gaus mean : 35.7389

Gaus Width: 3.1252



B1L003S, U16-ch106, adc2

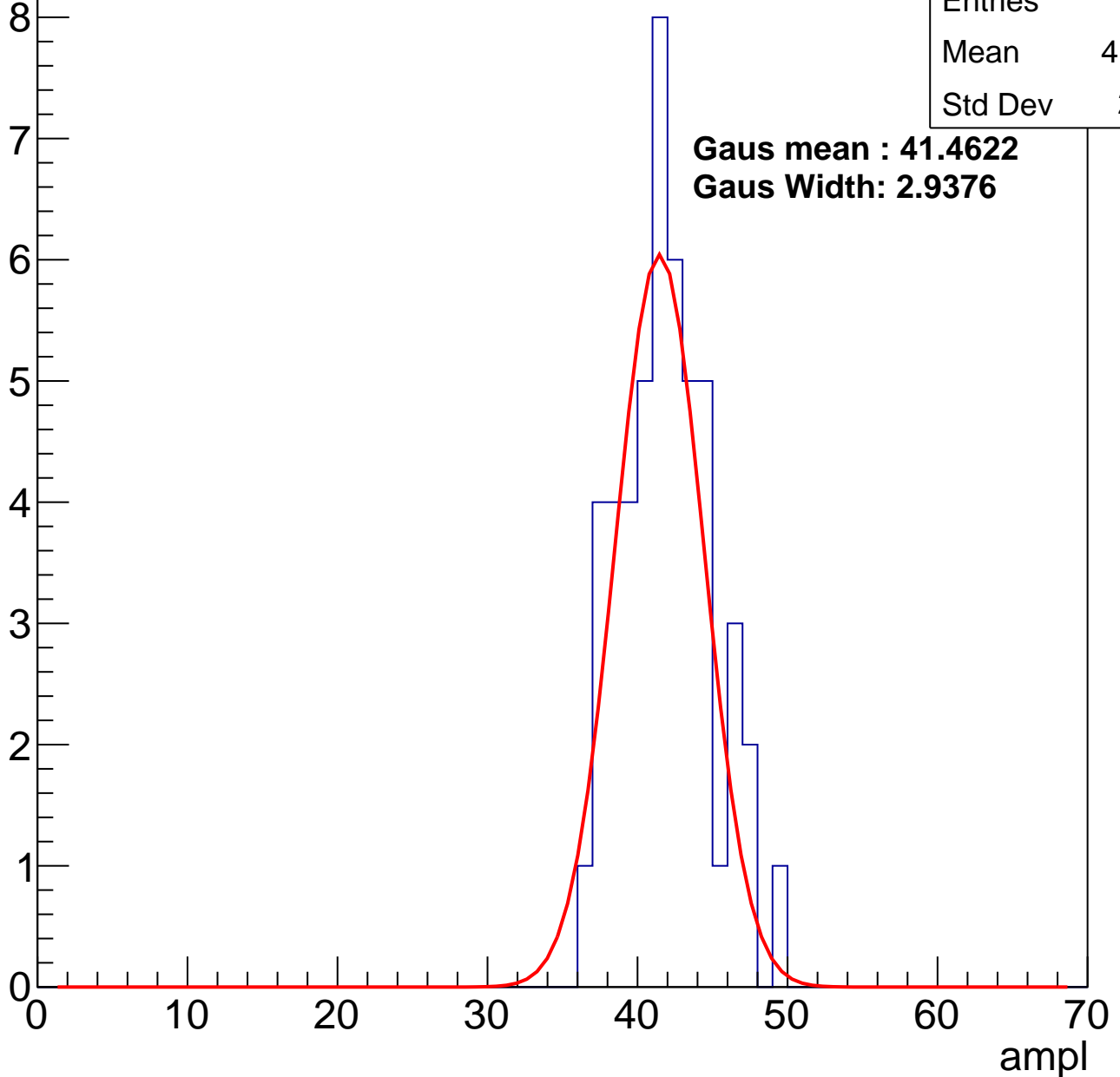
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	41.49
Std Dev	2.97

Gaus mean : 41.4622

Gaus Width: 2.9376

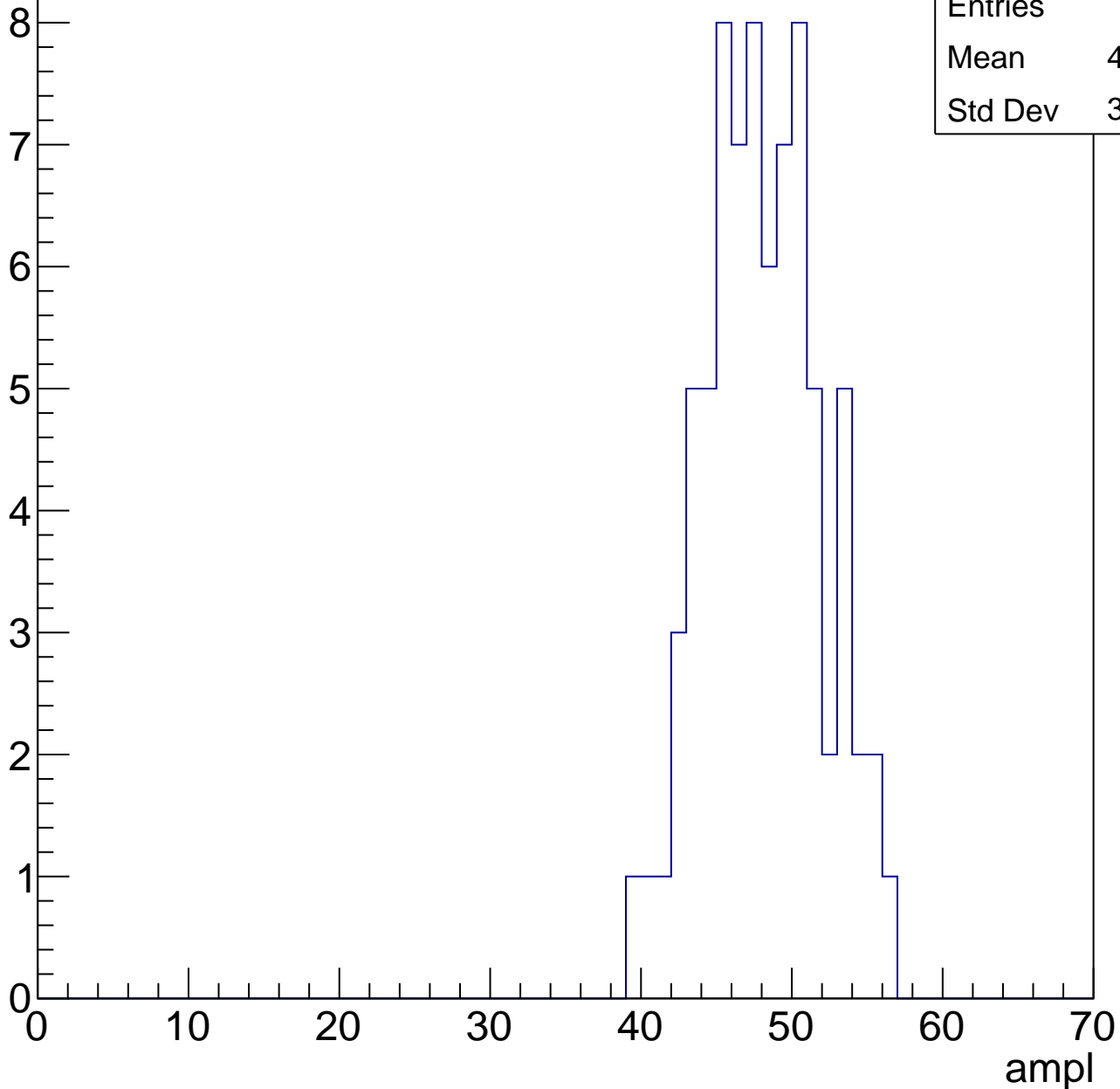


B1L003S, U16-ch106, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	47.64
Std Dev	3.745

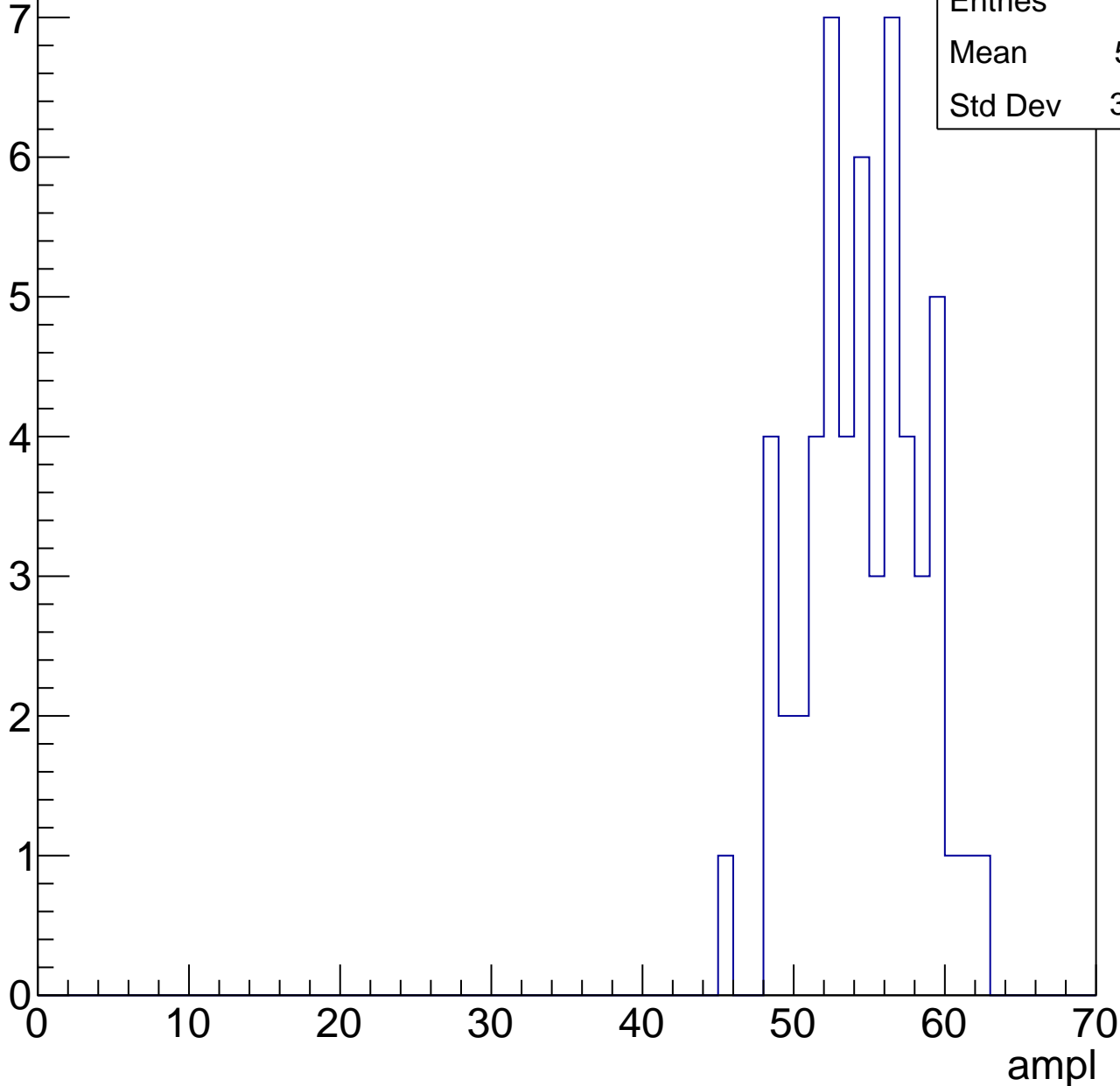


B1L003S, U16-ch106, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.11
Std Dev	3.725

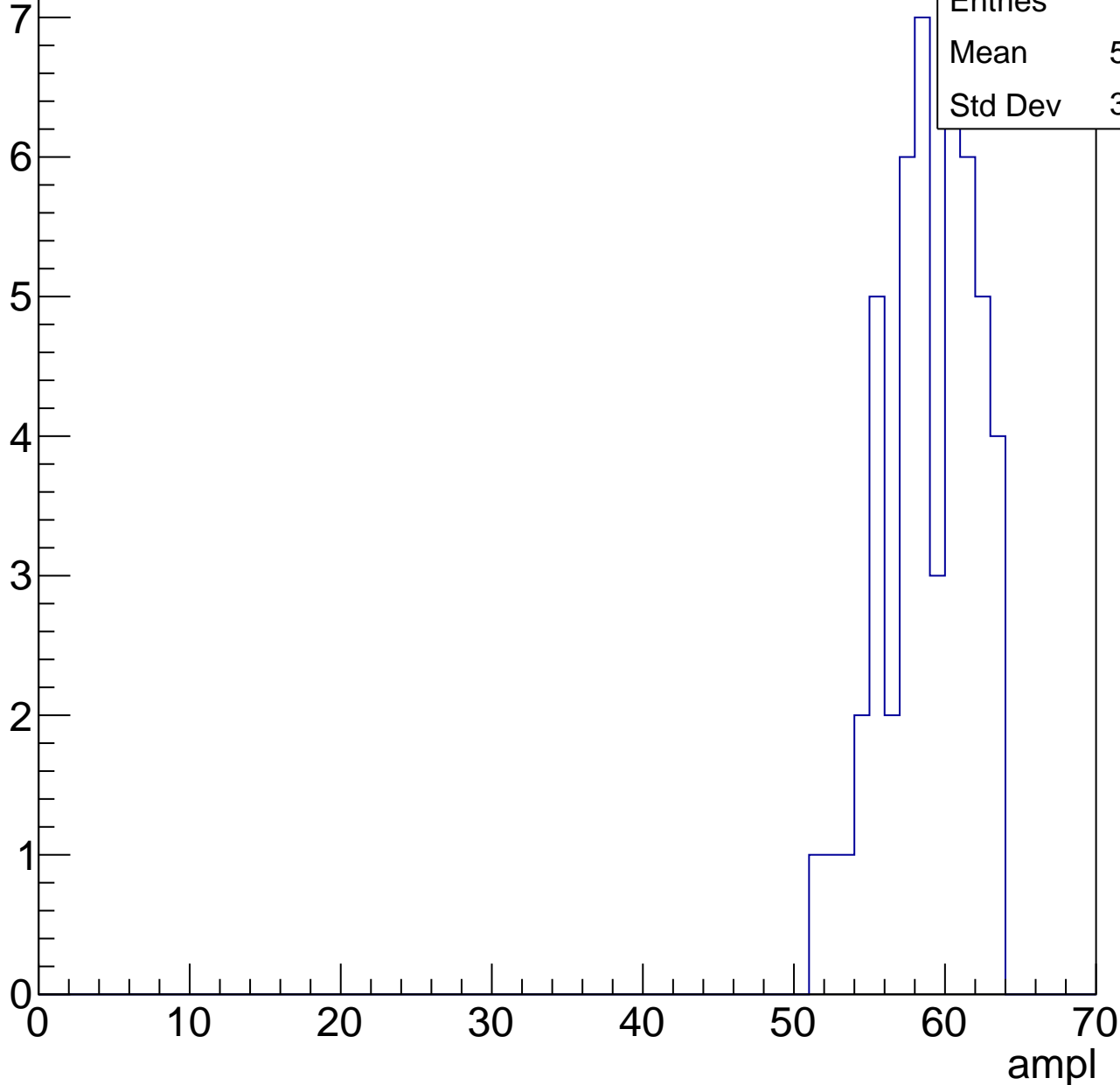


B1L003S, U16-ch106, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	58.48
Std Dev	3.015

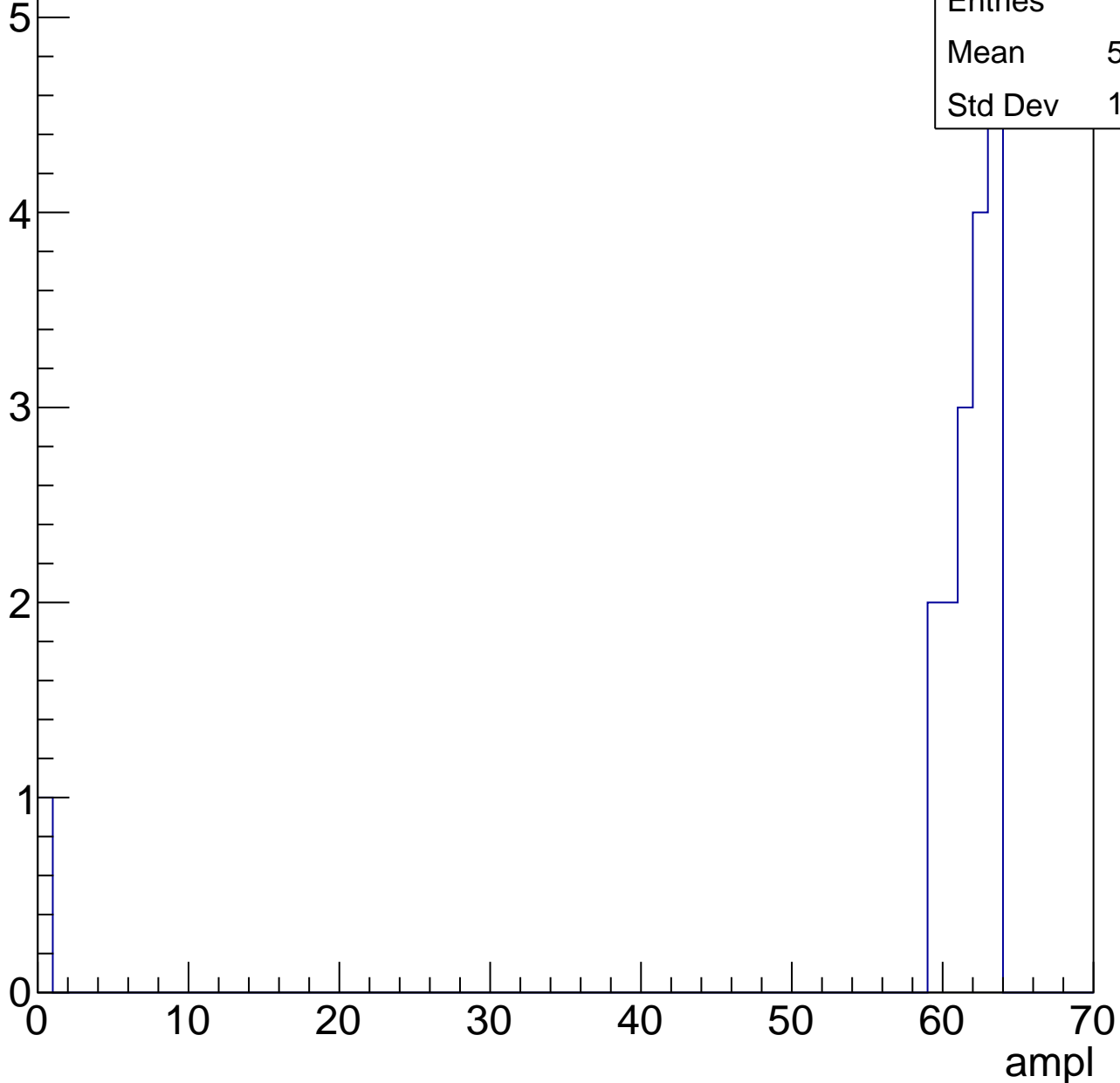


B1L003S, U16-ch106, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	57.88
Std Dev	14.53



B1L003S, U16-ch106, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U16-ch107, adc0

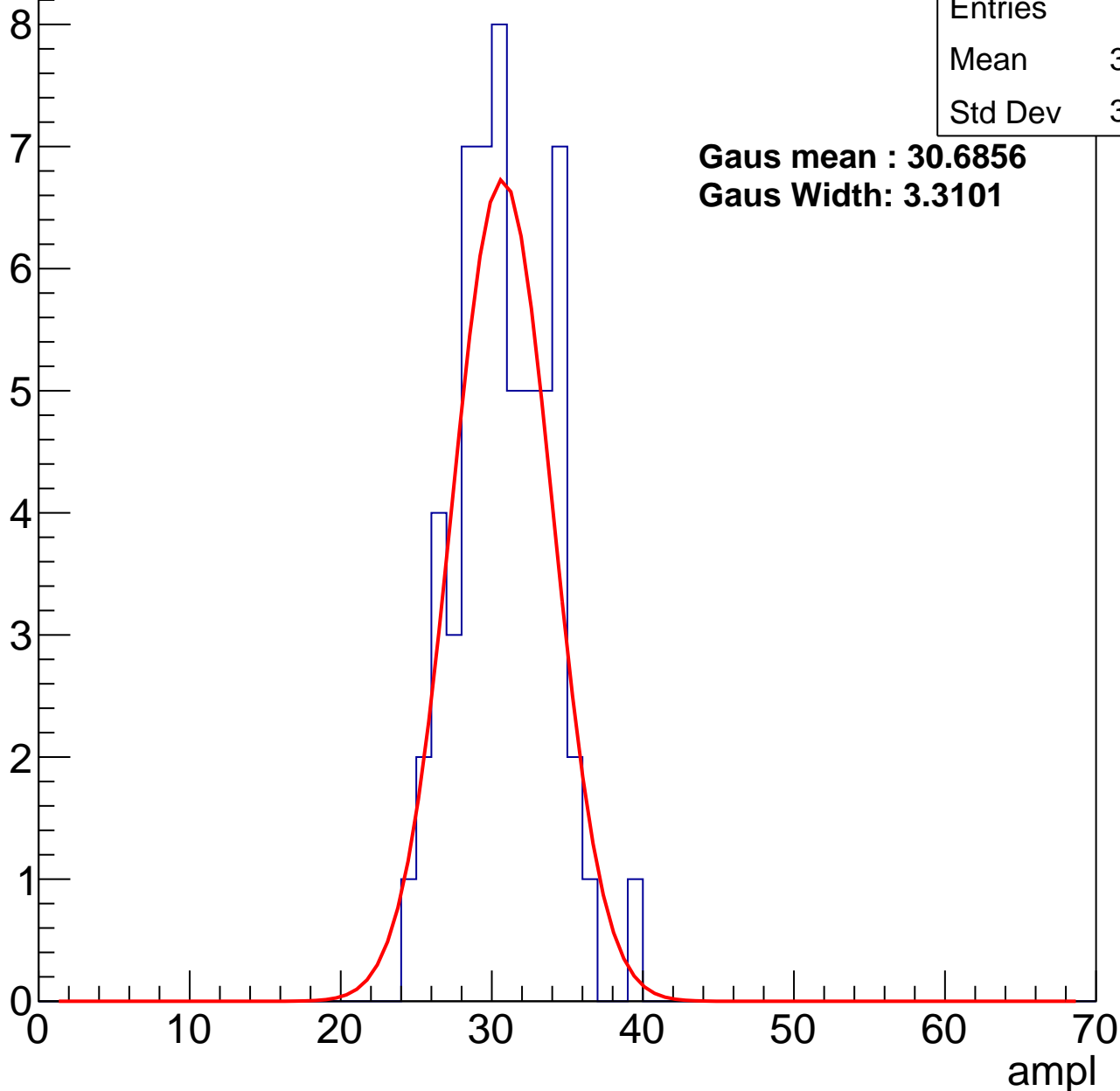
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.36
Std Dev	3.089

Gaus mean : 30.6856

Gaus Width: 3.3101



B1L003S, U16-ch107, adc1

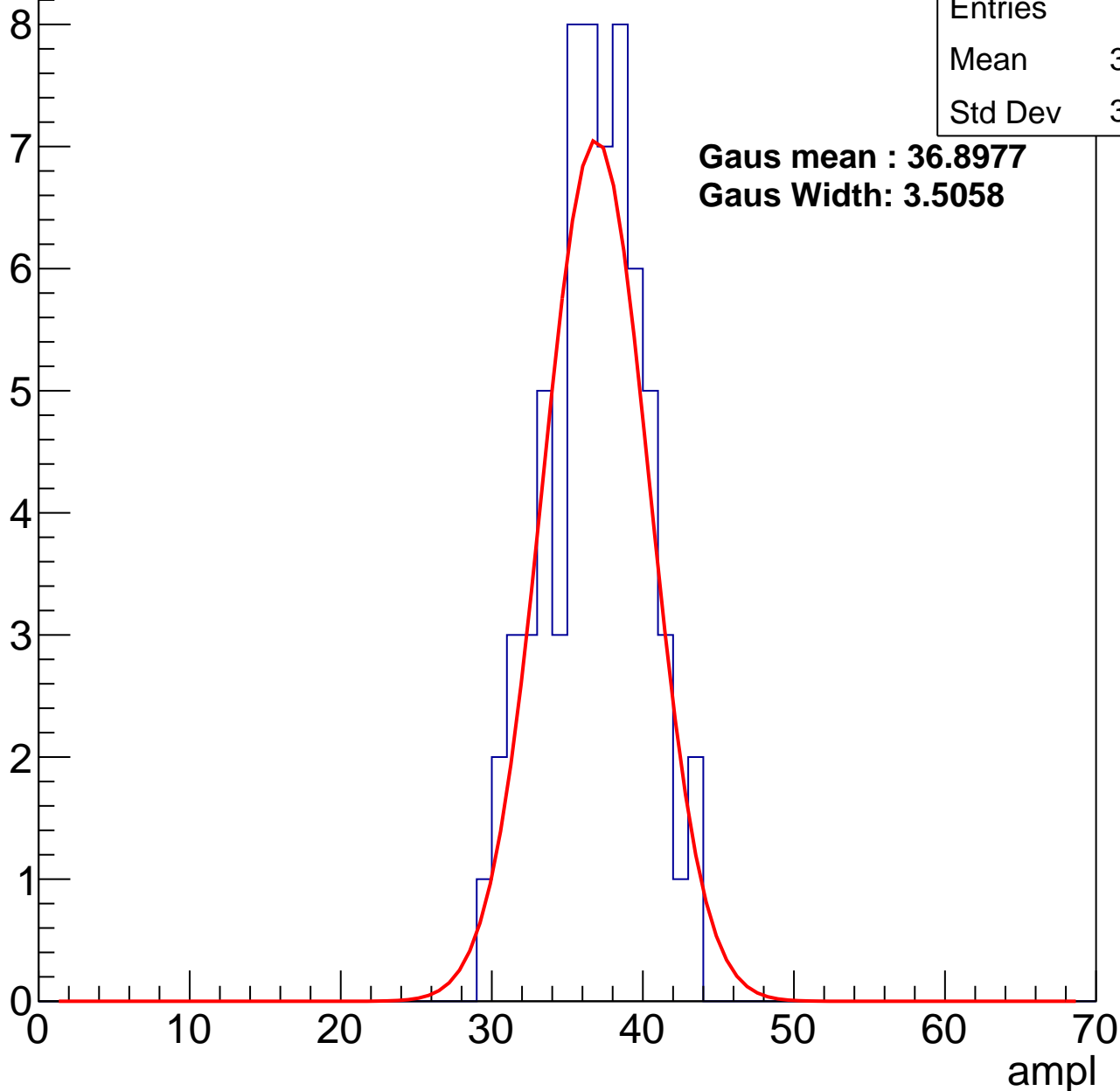
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.32
Std Dev	3.244

Gaus mean : 36.8977

Gaus Width: 3.5058



B1L003S, U16-ch107, adc2

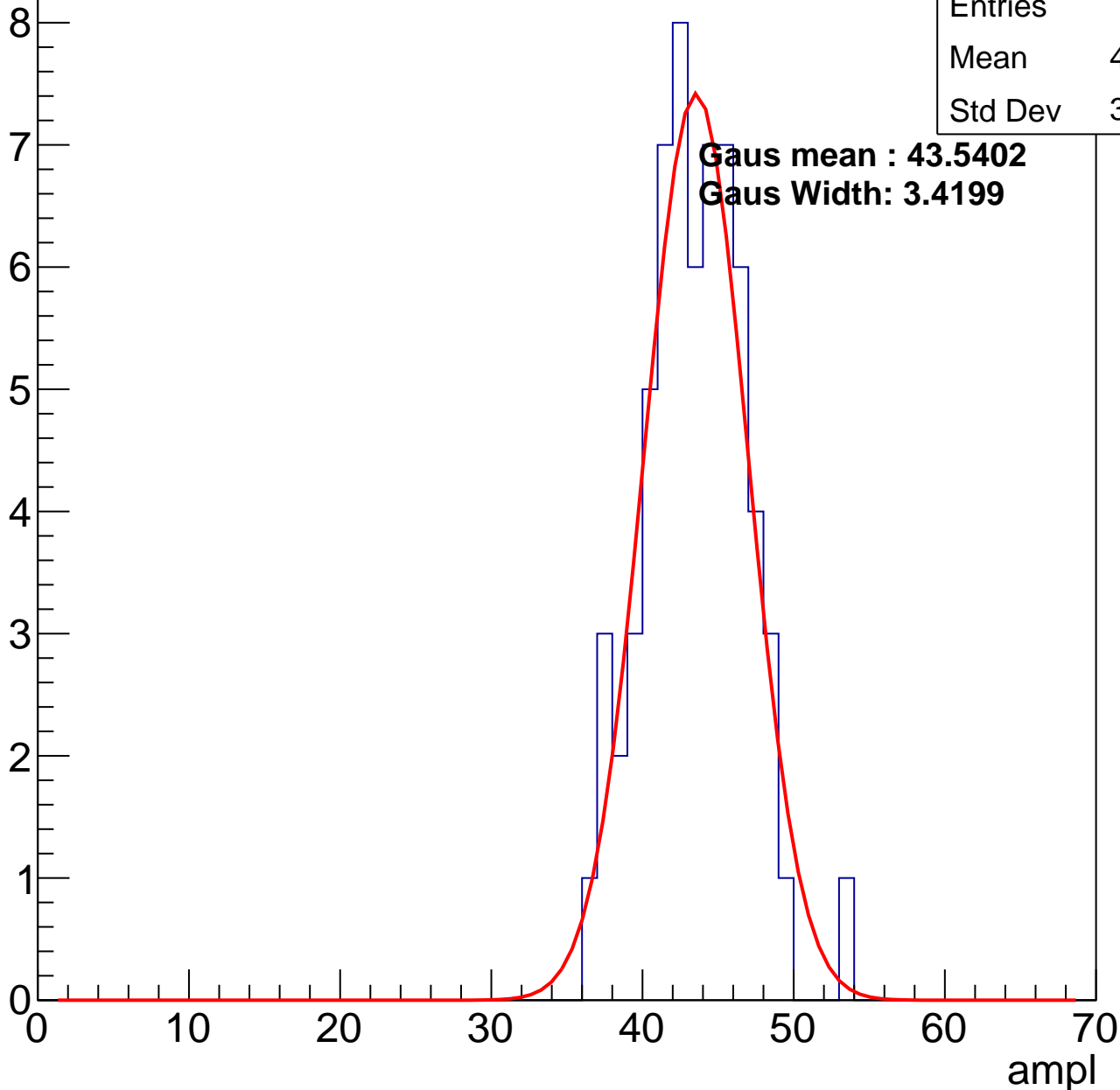
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.03
Std Dev	3.307

Gaus mean : 43.5402

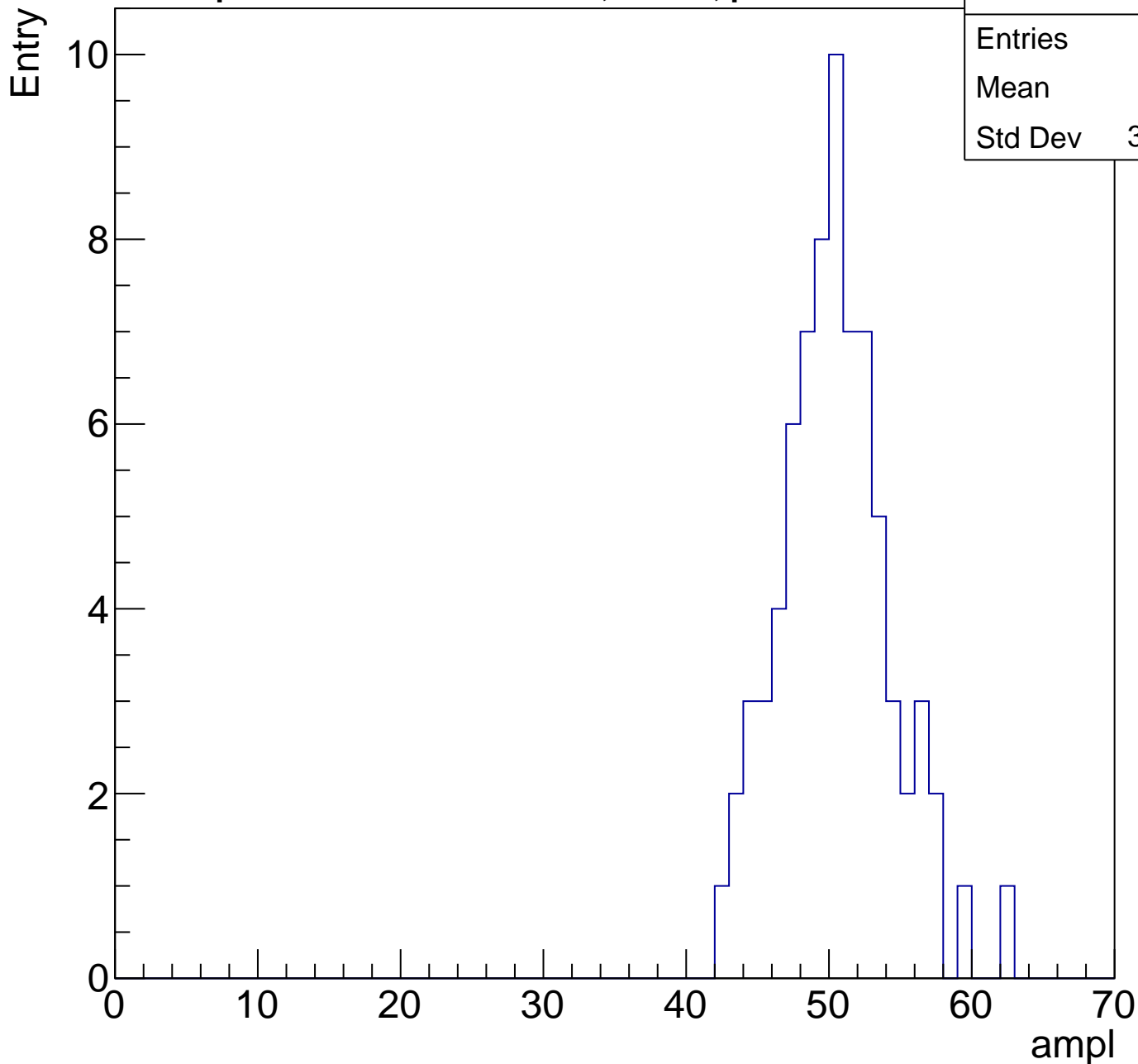
Gaus Width: 3.4199



B1L003S, U16-ch107, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	50
Std Dev	3.844

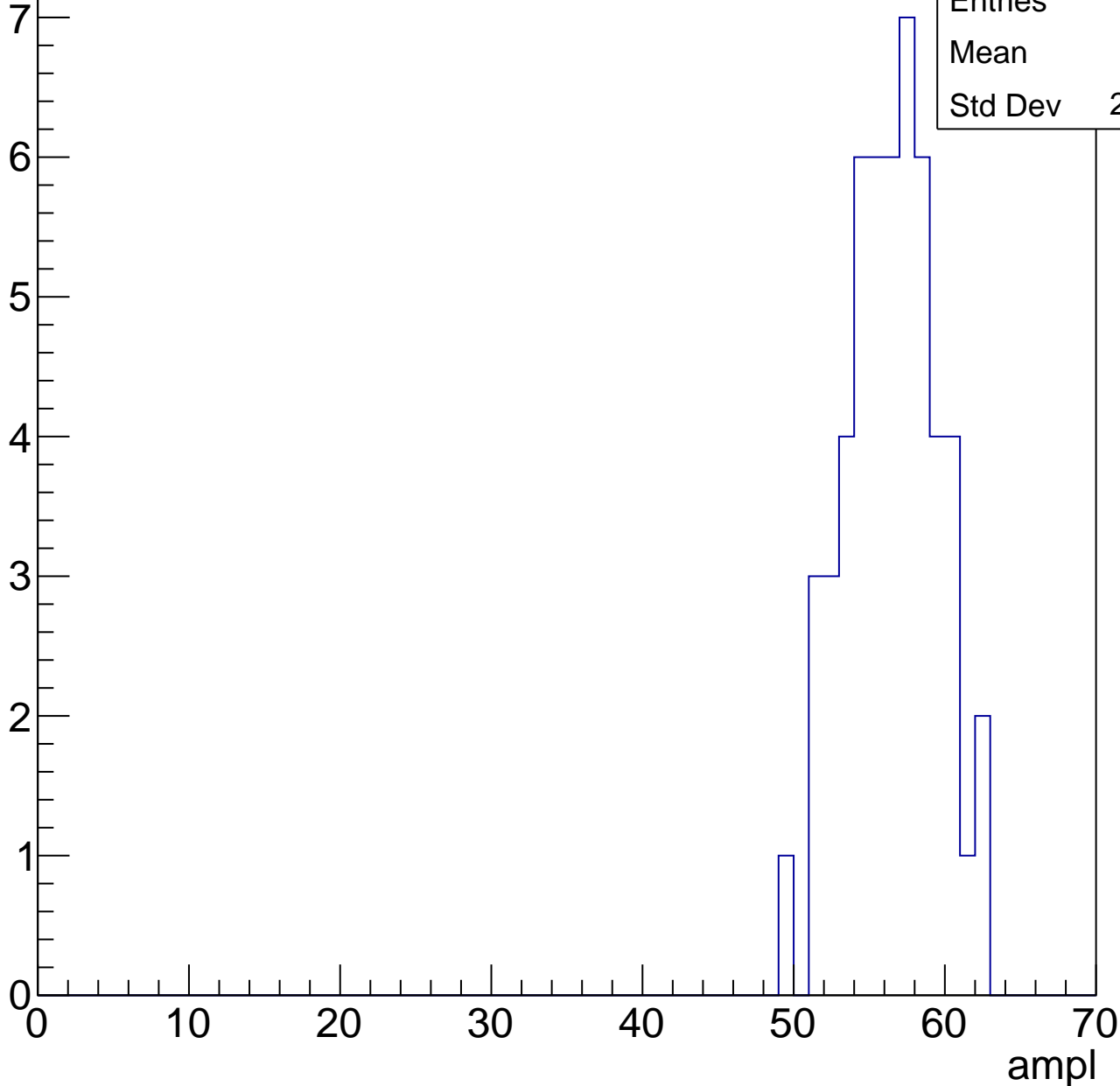


B1L003S, U16-ch107, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56
Std Dev	2.965

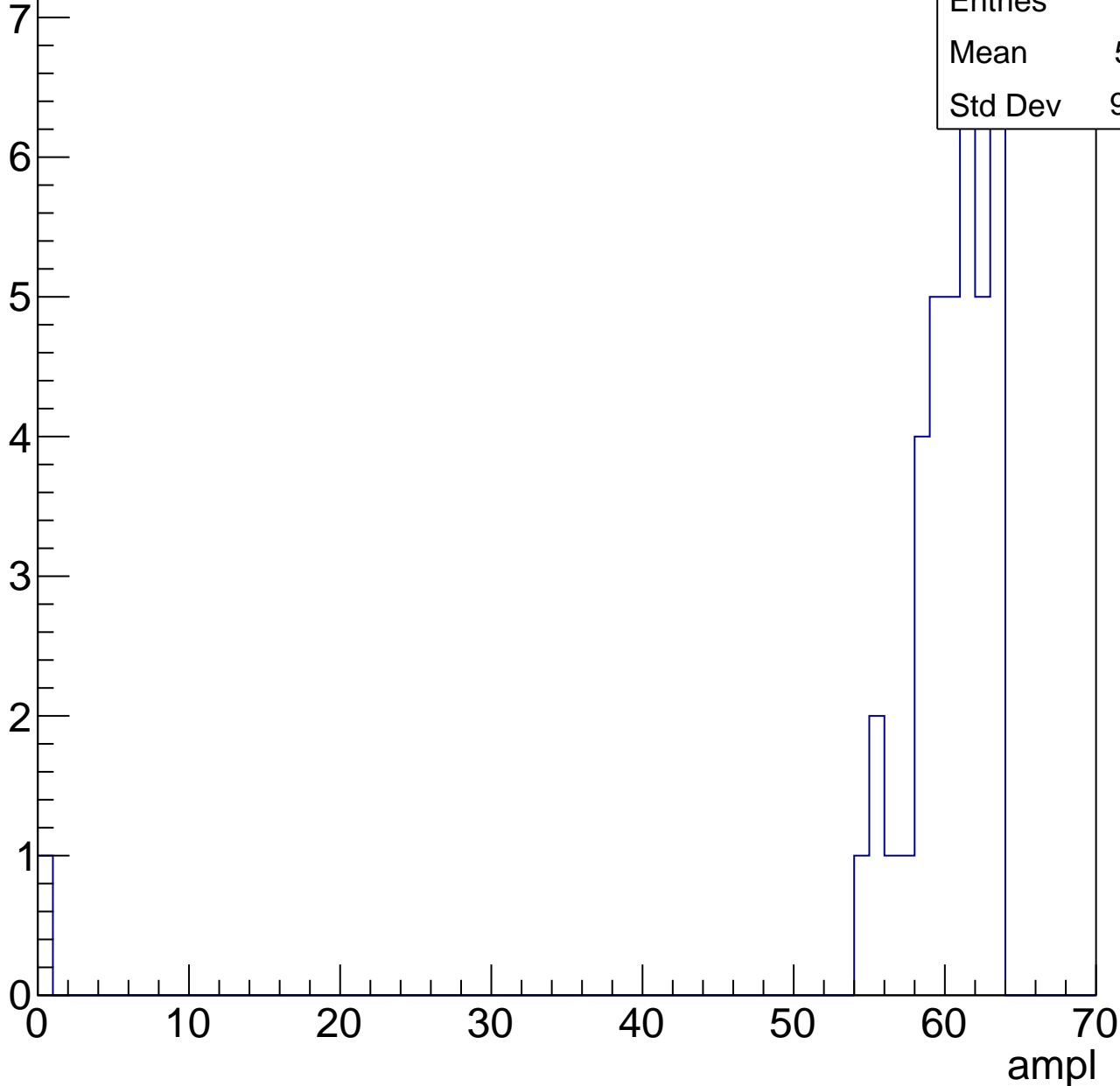


B1L003S, U16-ch107, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.51
Std Dev	9.787



B1L003S, U16-ch107, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	8
Mean	61.88
Std Dev	1.053

B1L003S, U16-ch107, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch108, adc0

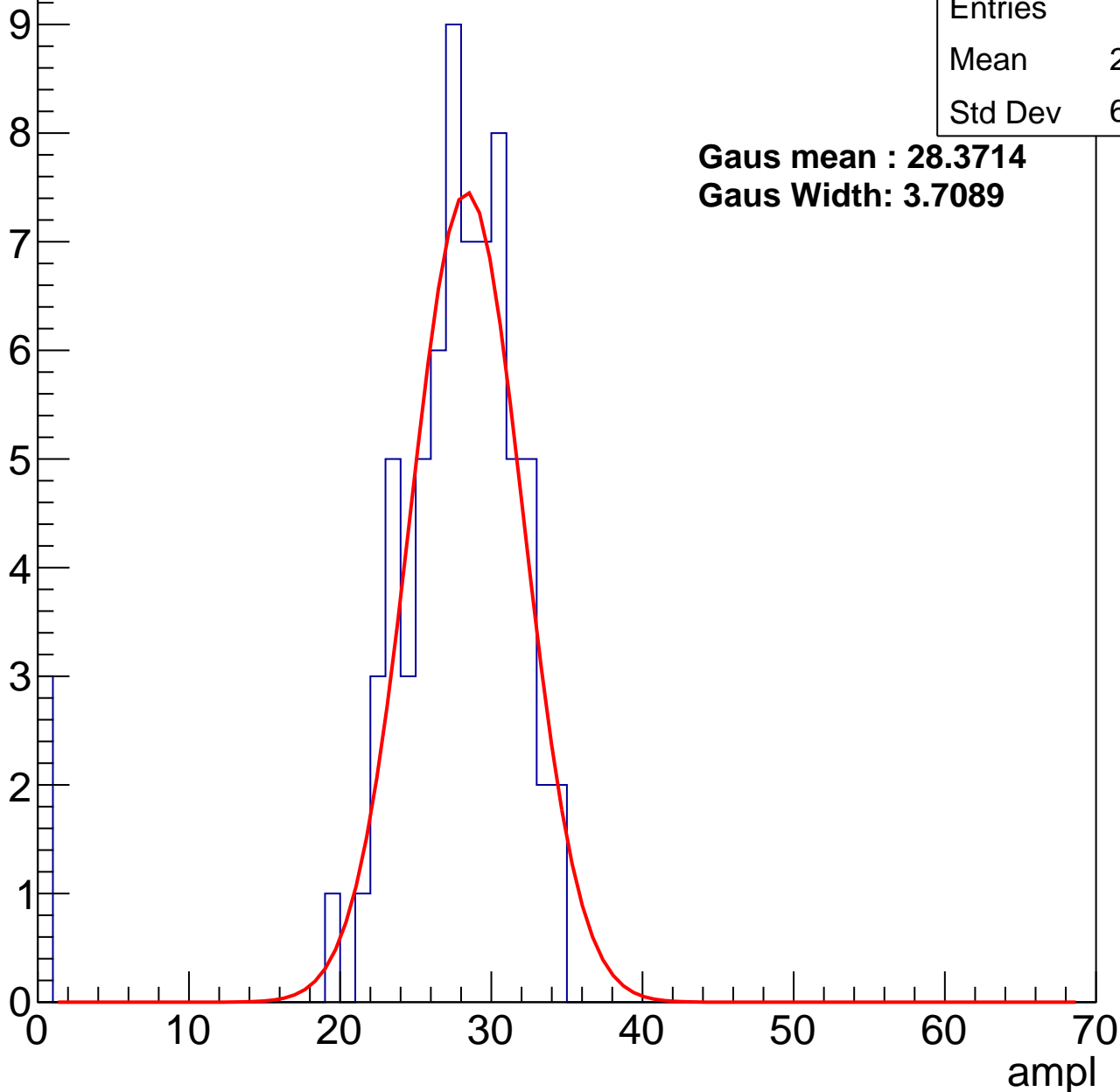
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	26.46
Std Dev	6.407

Gaus mean : 28.3714

Gaus Width: 3.7089



B1L003S, U16-ch108, adc1

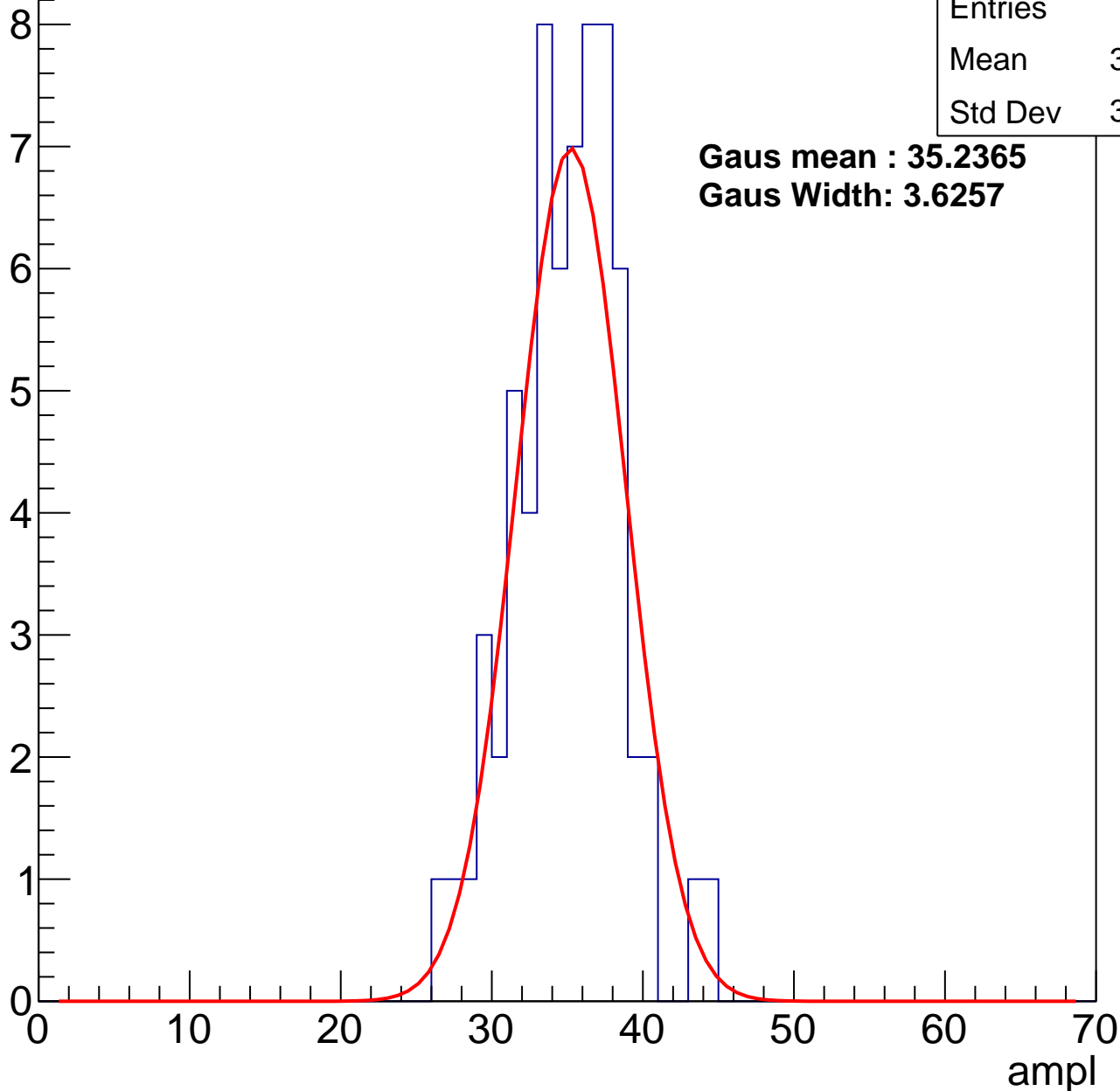
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.56
Std Dev	3.512

Gaus mean : 35.2365

Gaus Width: 3.6257



B1L003S, U16-ch108, adc2

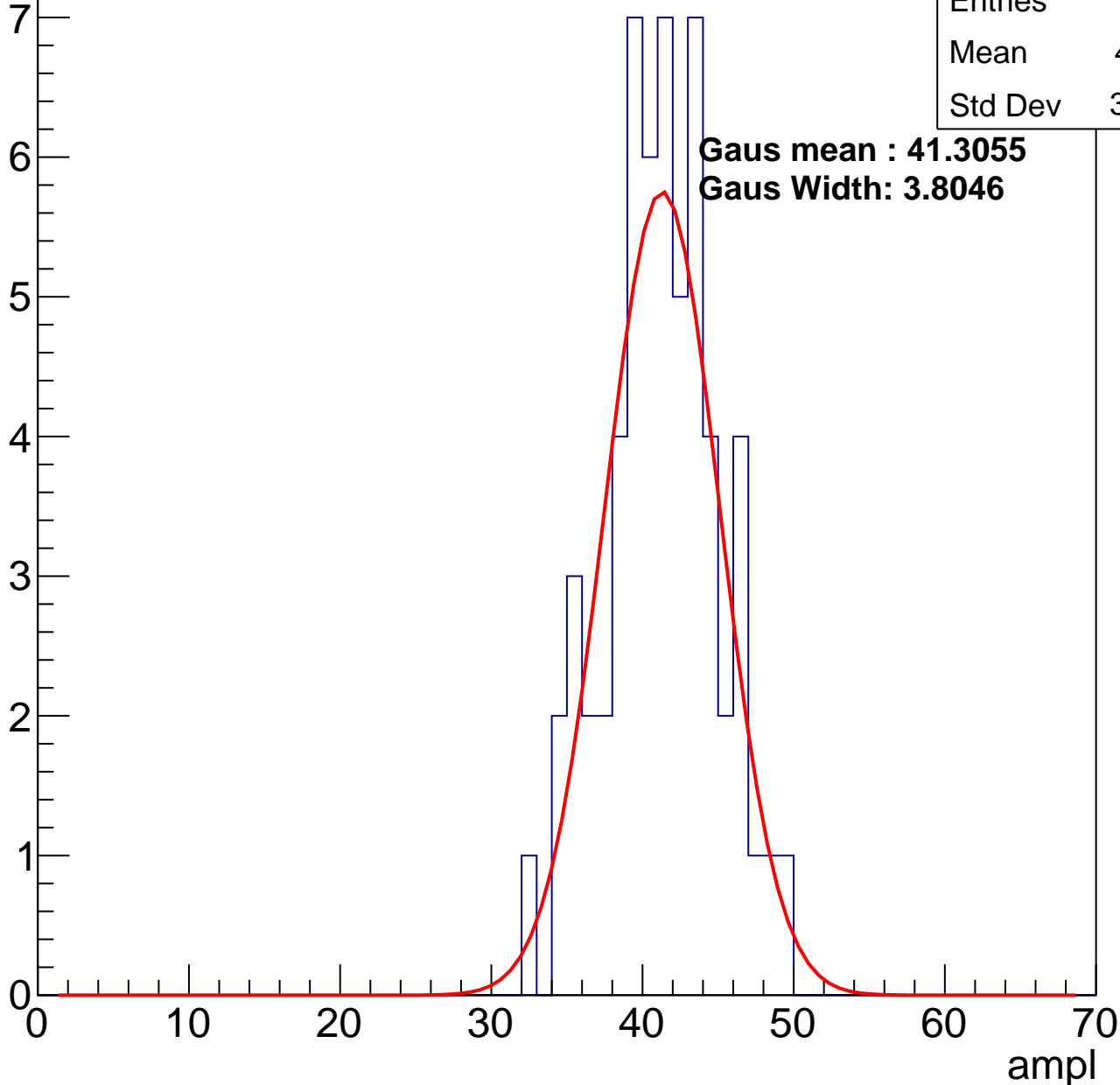
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	40.81
Std Dev	3.652

Gaus mean : 41.3055

Gaus Width: 3.8046

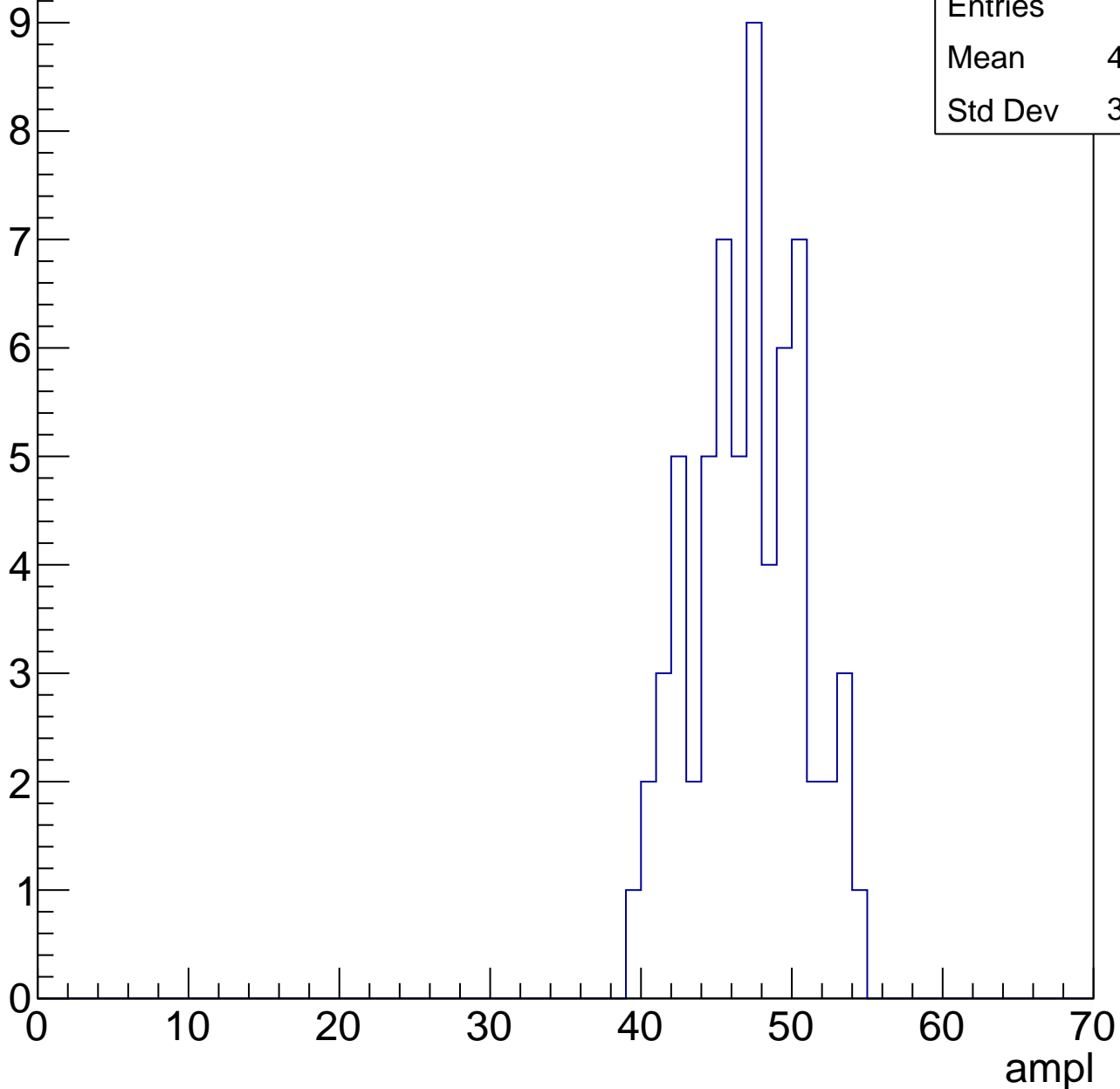


B1L003S, U16-ch108, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	46.58
Std Dev	3.587

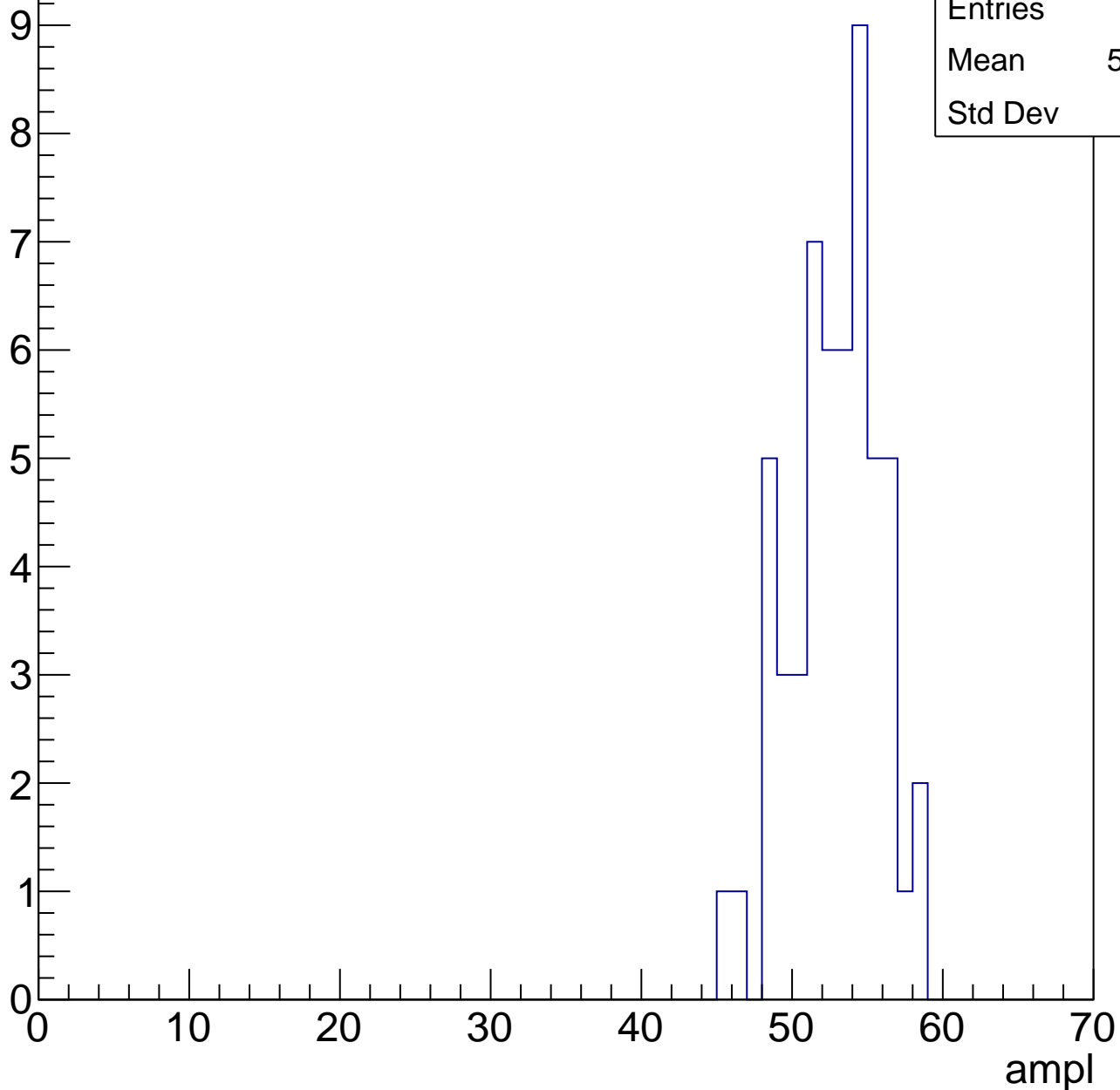


B1L003S, U16-ch108, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	52.39
Std Dev	2.94

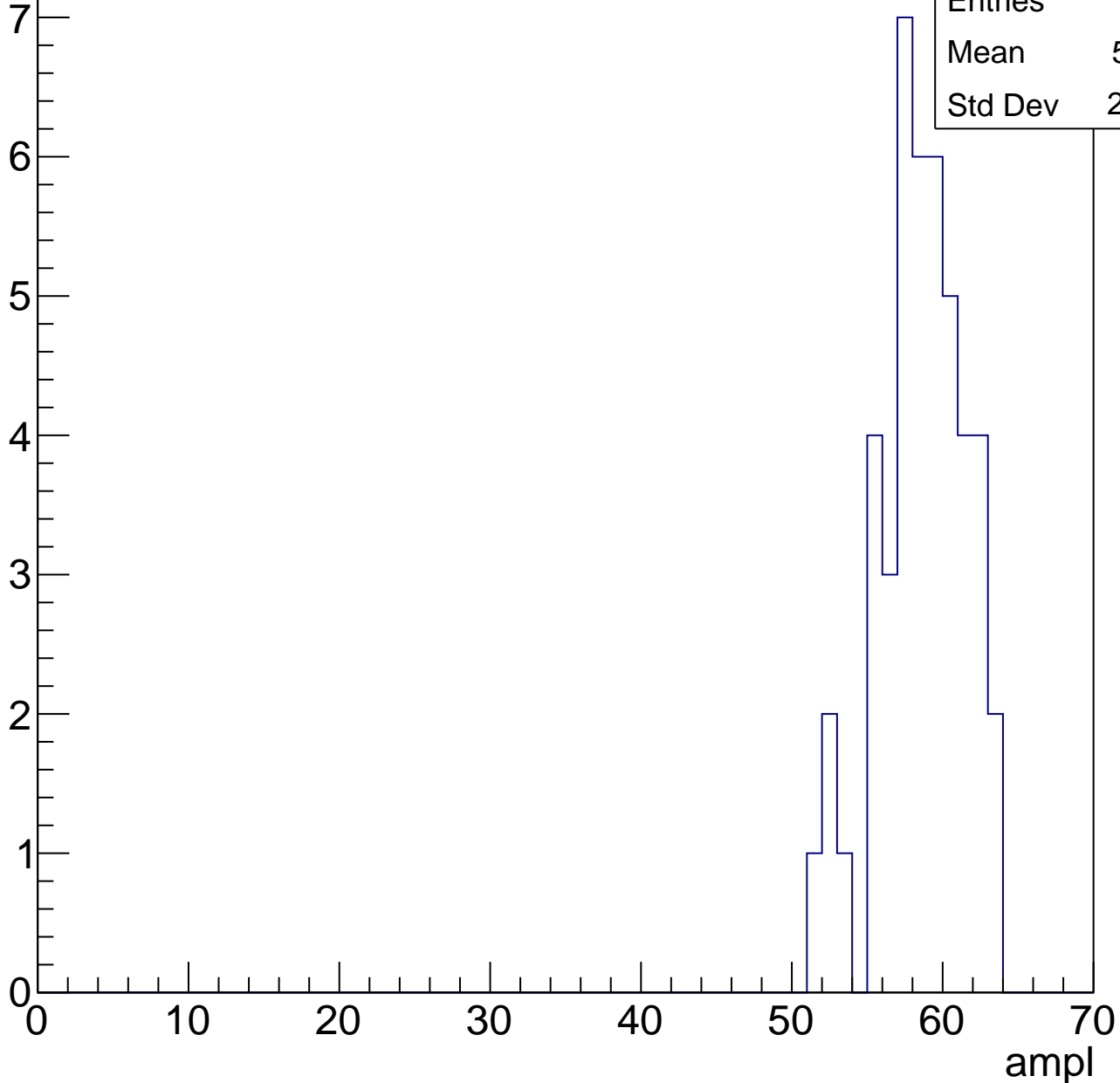


B1L003S, U16-ch108, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.11
Std Dev	2.892

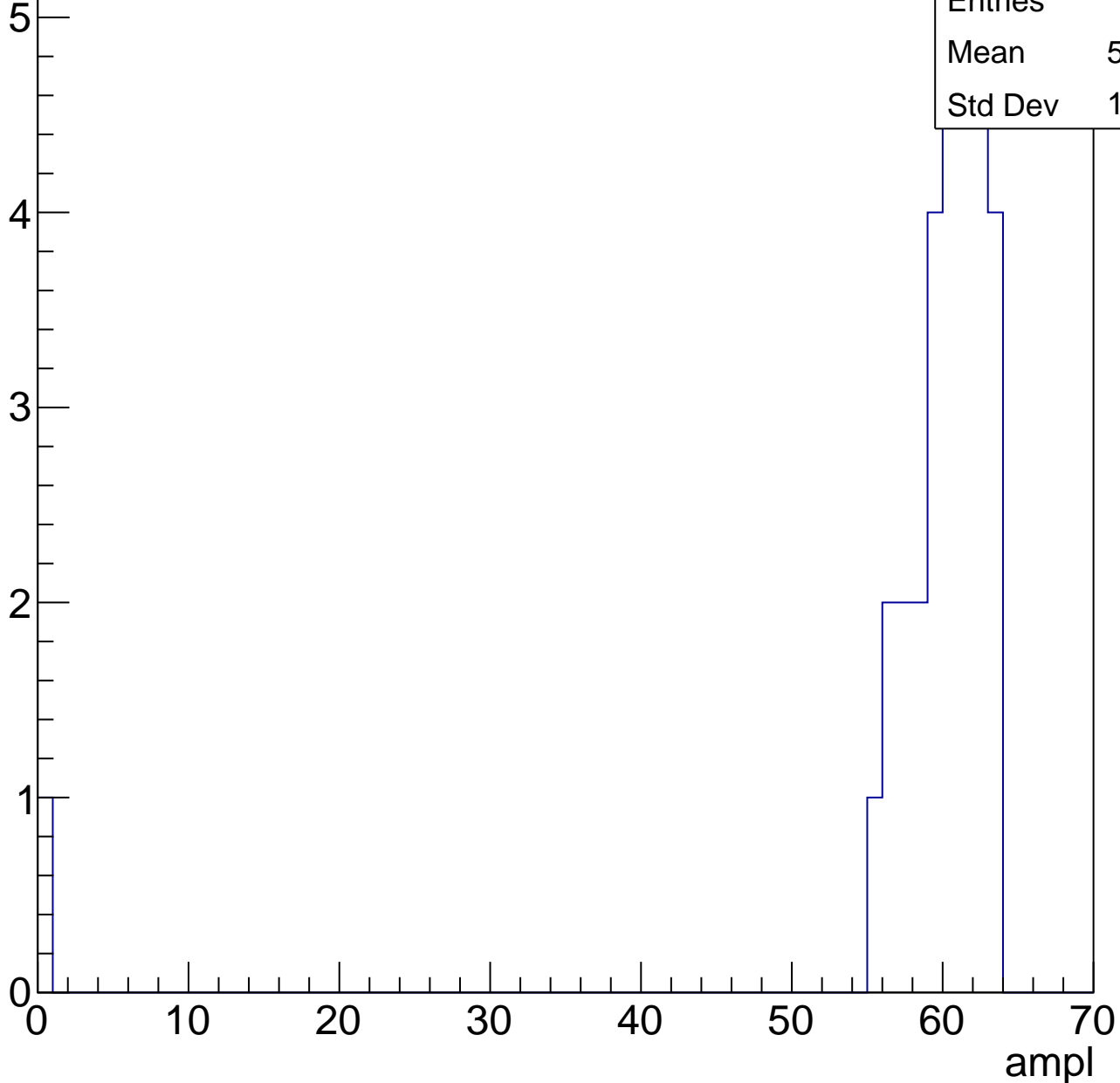


B1L003S, U16-ch108, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

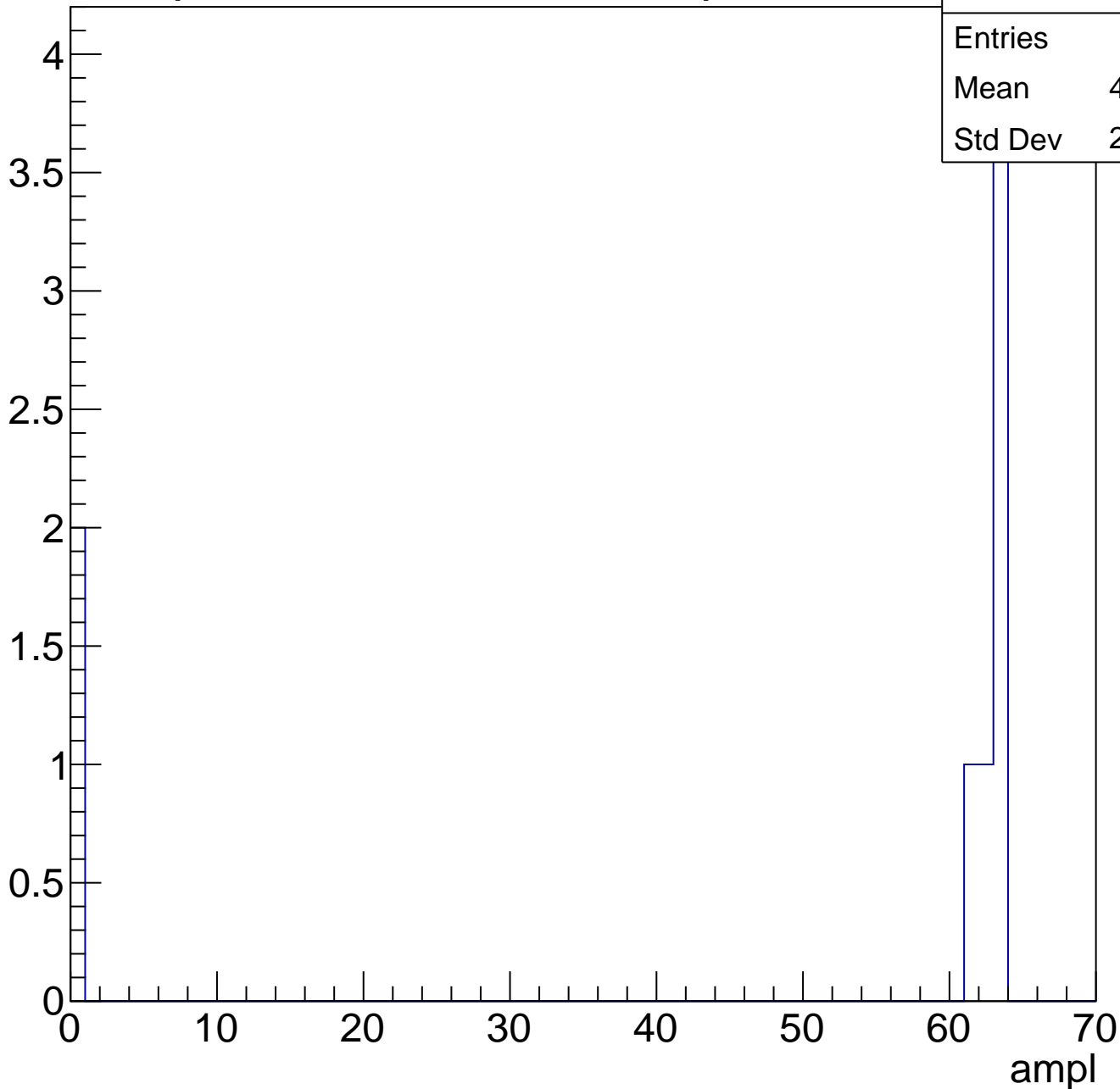
Entries	31
Mean	58.06
Std Dev	10.82



B1L003S, U16-ch108, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch109, adc0

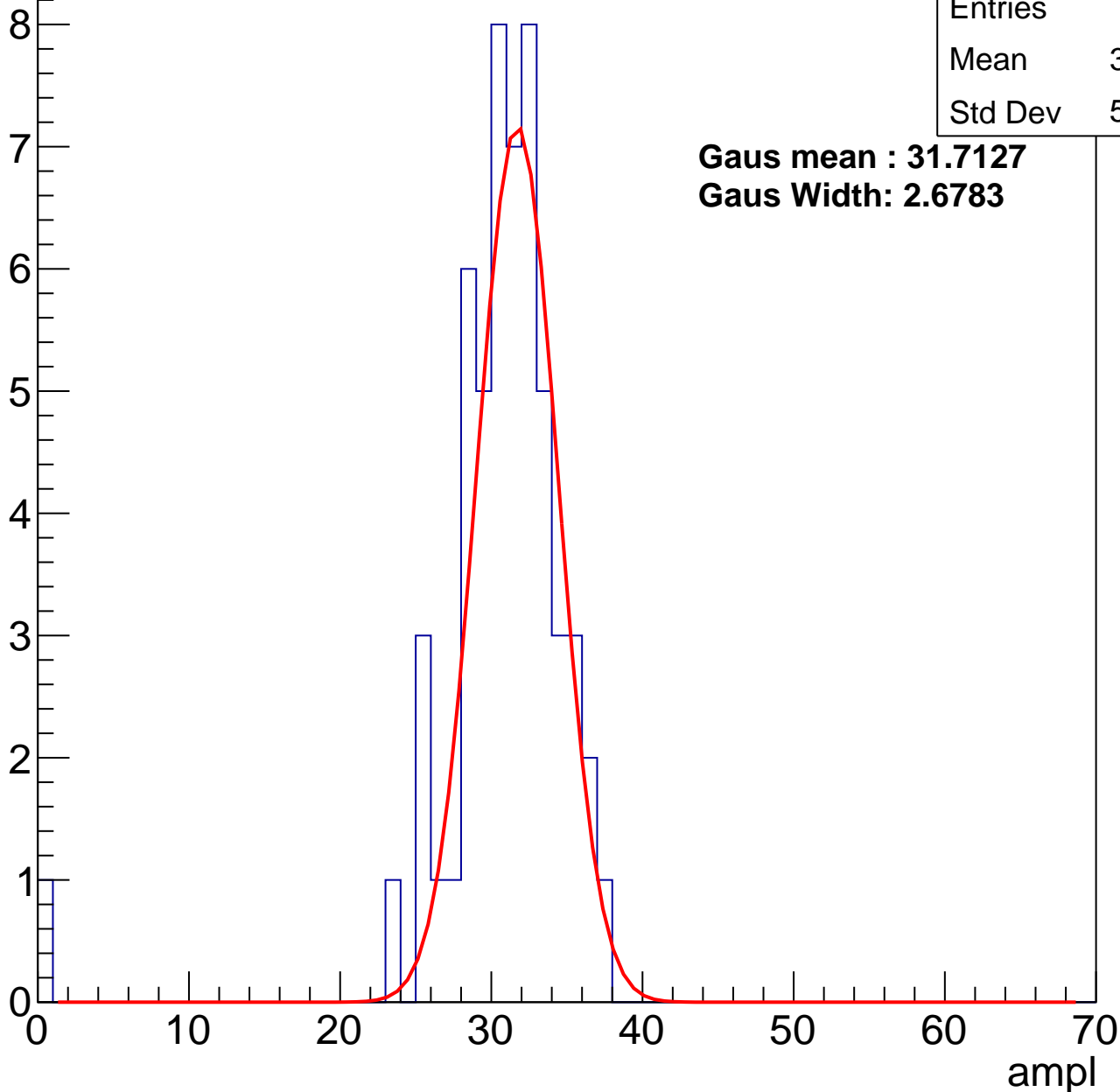
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	30.15
Std Dev	5.054

Gaus mean : 31.7127

Gaus Width: 2.6783



B1L003S, U16-ch109, adc1

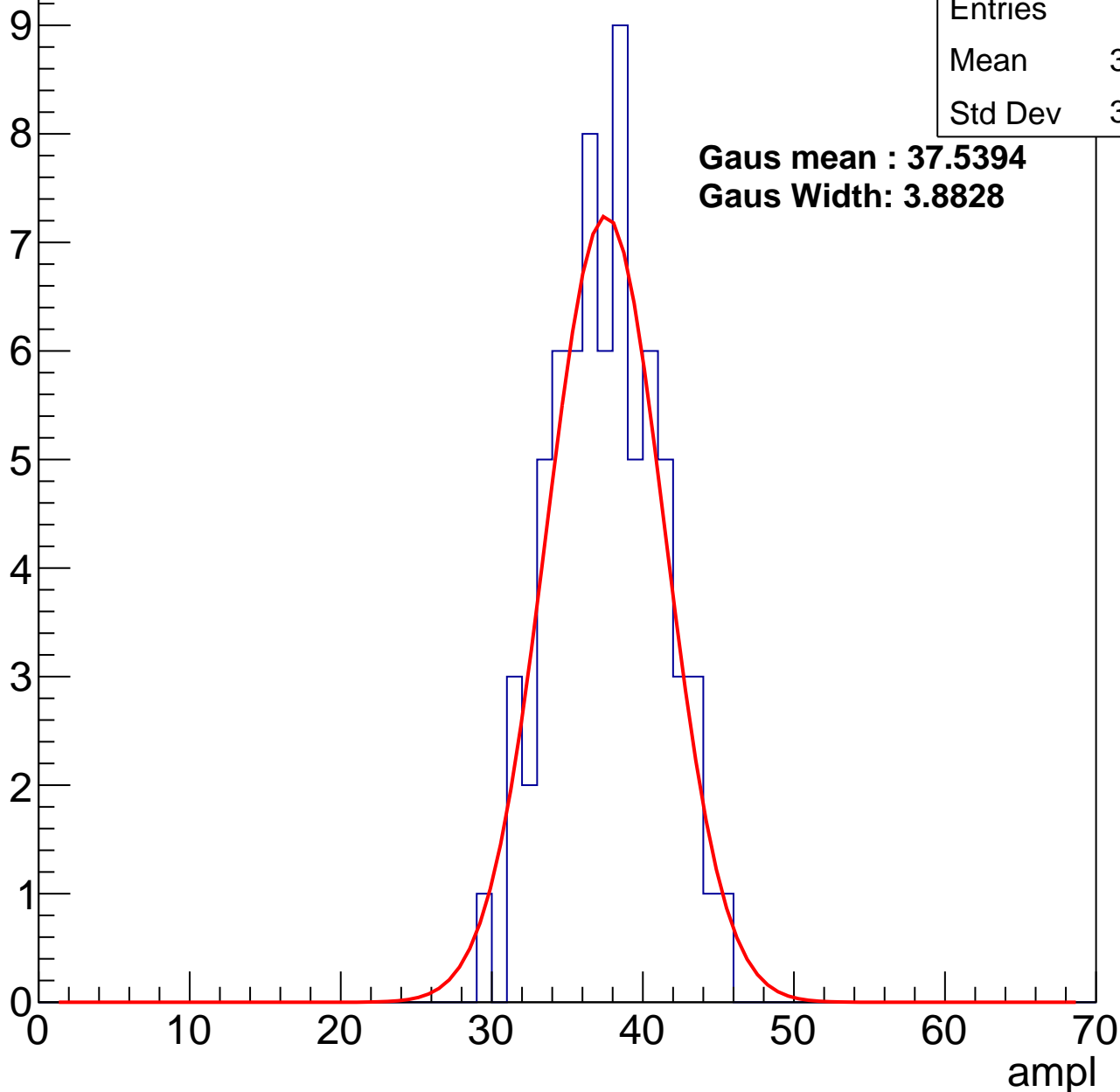
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.16
Std Dev	3.475

Gaus mean : 37.5394

Gaus Width: 3.8828

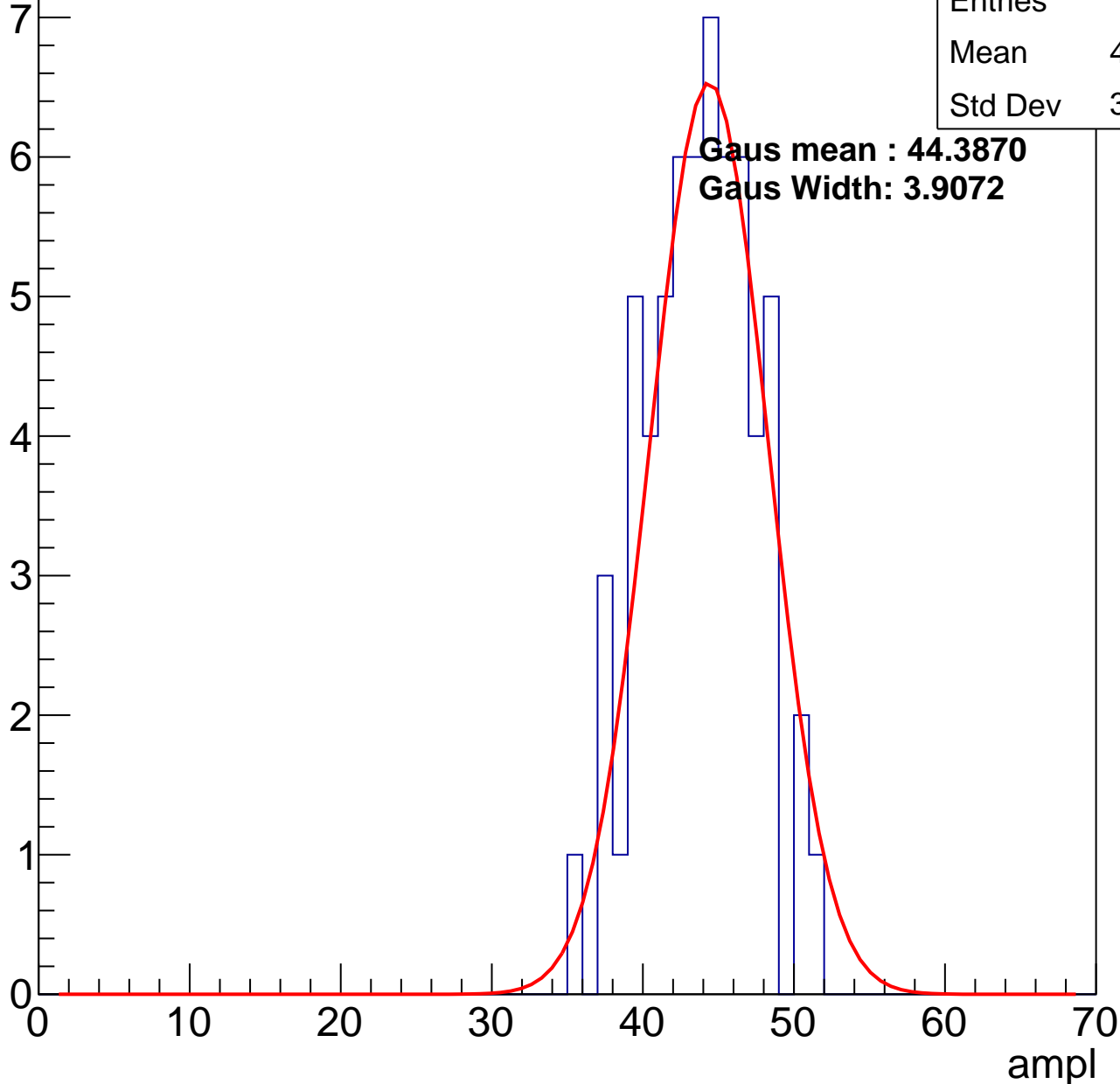


B1L003S, U16-ch109, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.34
Std Dev	3.519

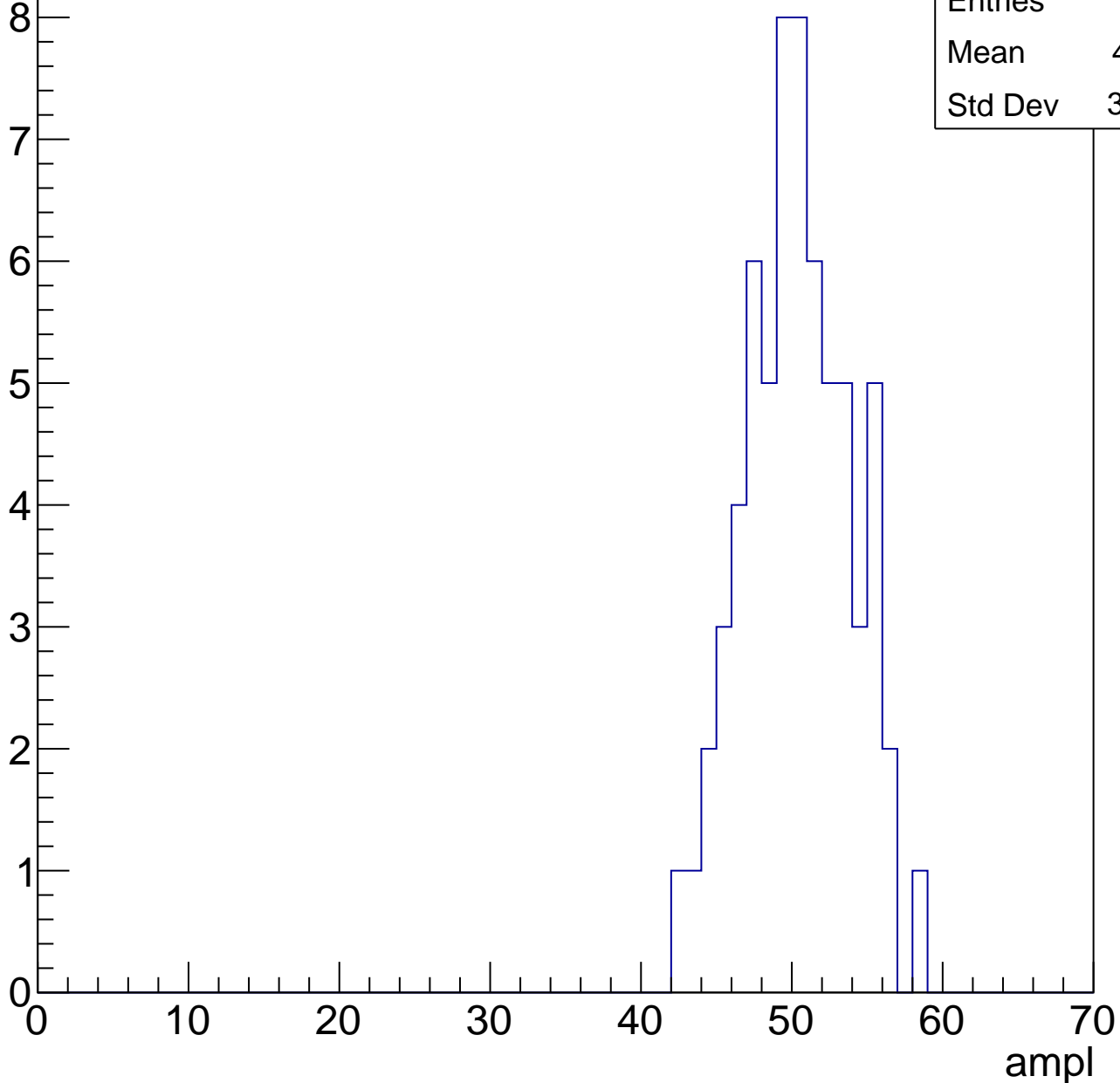


B1L003S, U16-ch109, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.91
Std Dev	3.476

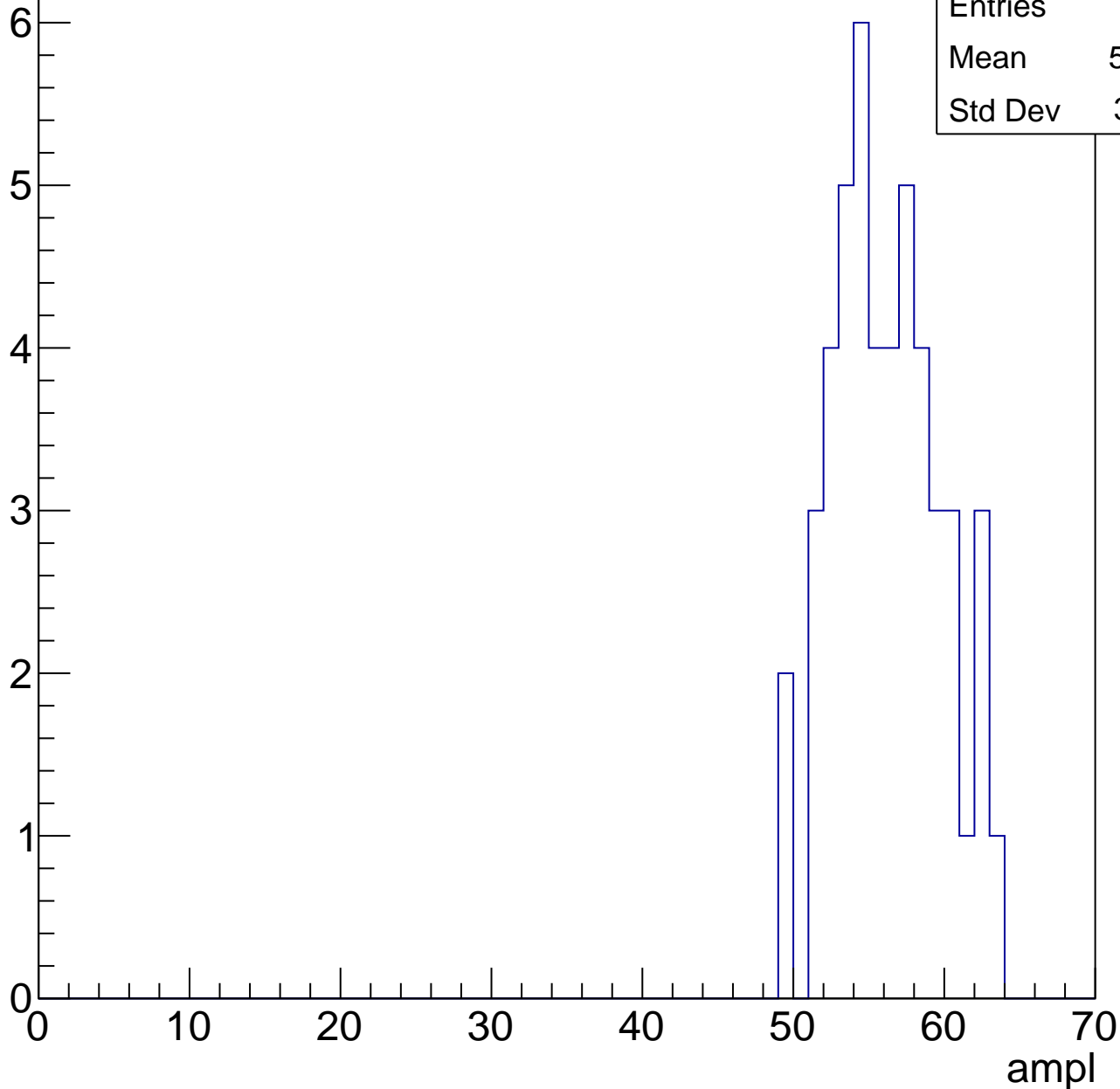


B1L003S, U16-ch109, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	55.75
Std Dev	3.491

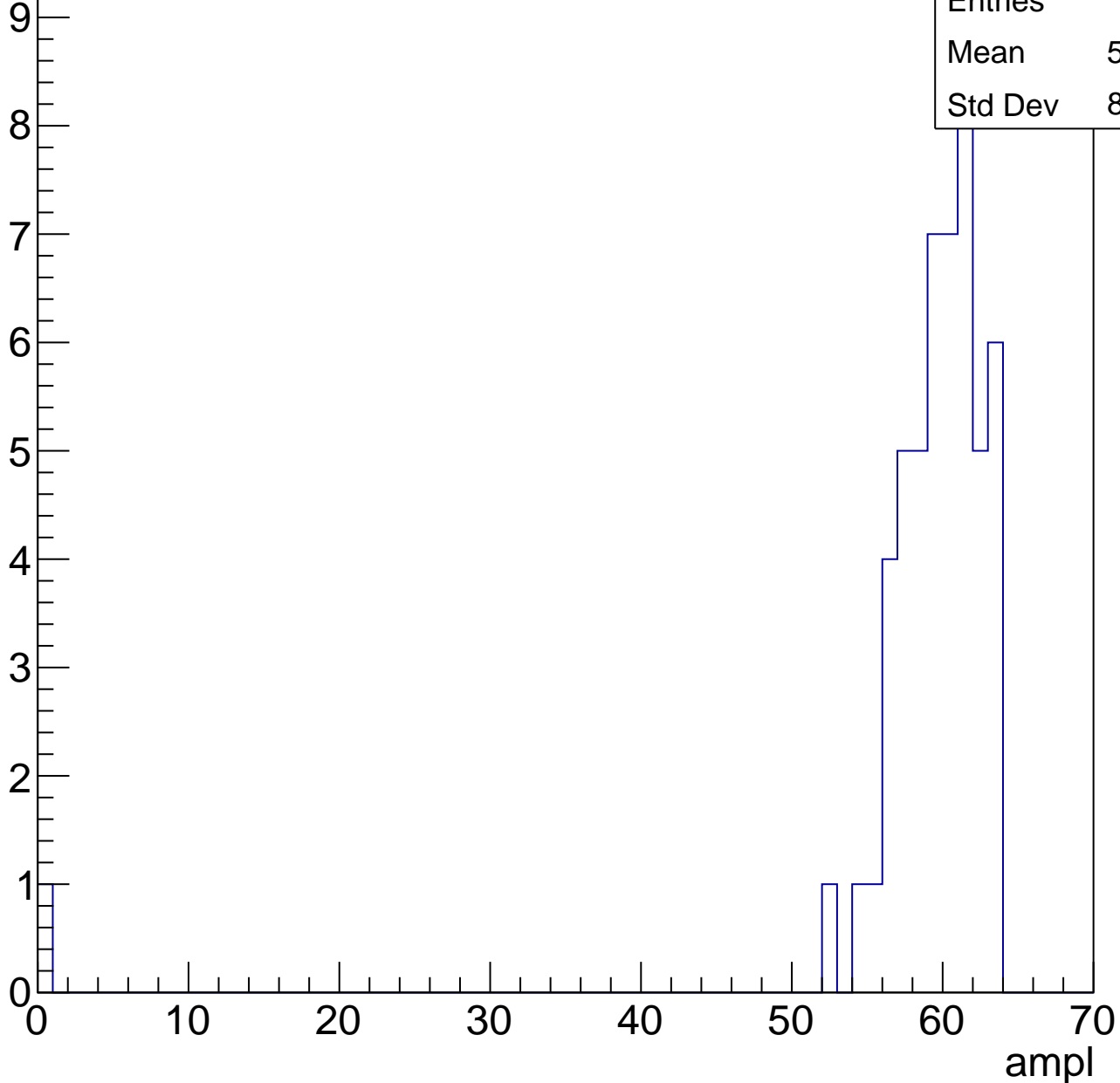


B1L003S, U16-ch109, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	58.27
Std Dev	8.535



B1L003S, U16-ch109, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	62.6
Std Dev	0.4899

B1L003S, U16-ch109, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch110, adc0

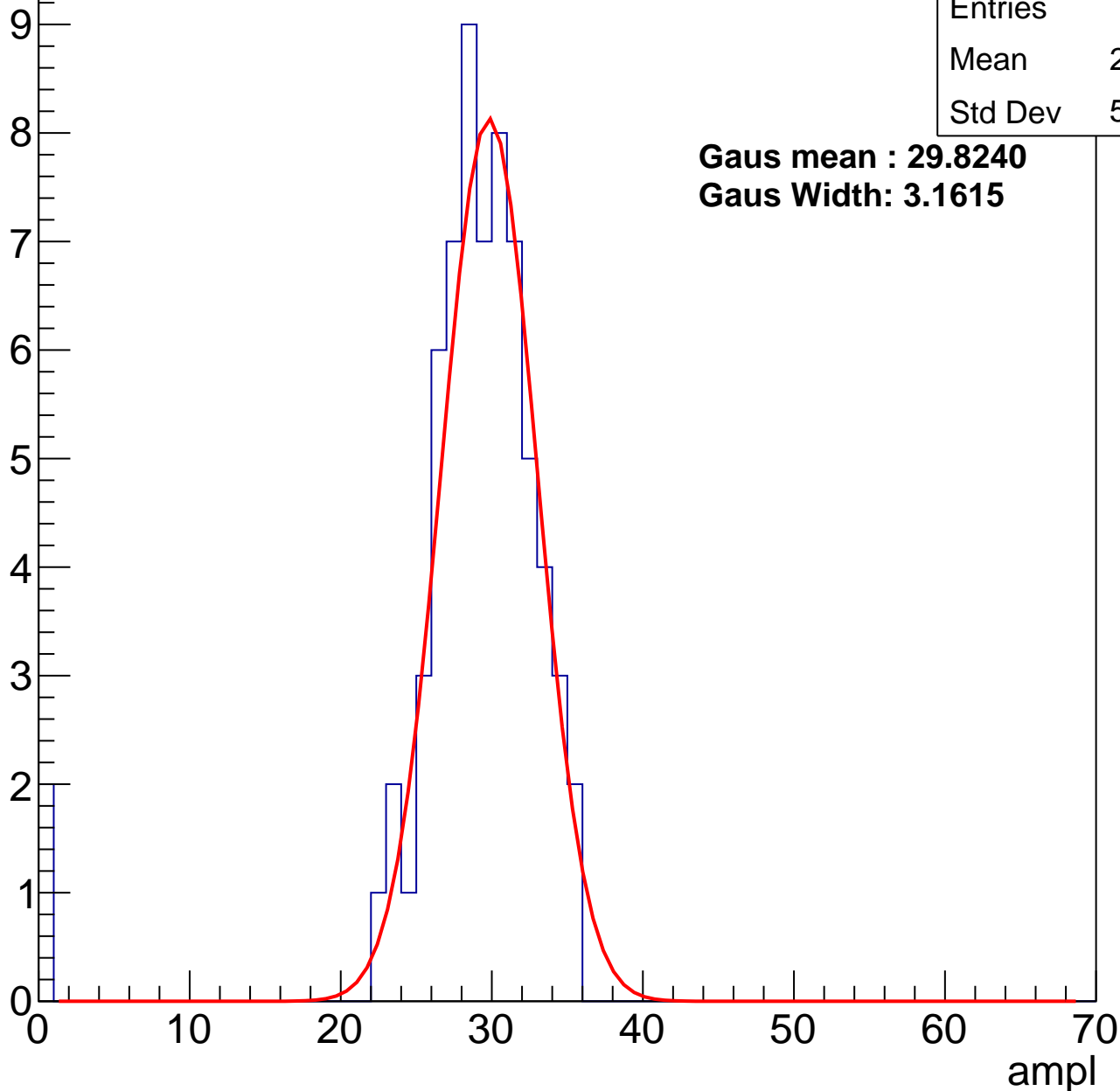
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.18
Std Dev	5.748

Gaus mean : 29.8240

Gaus Width: 3.1615



B1L003S, U16-ch110, adc1

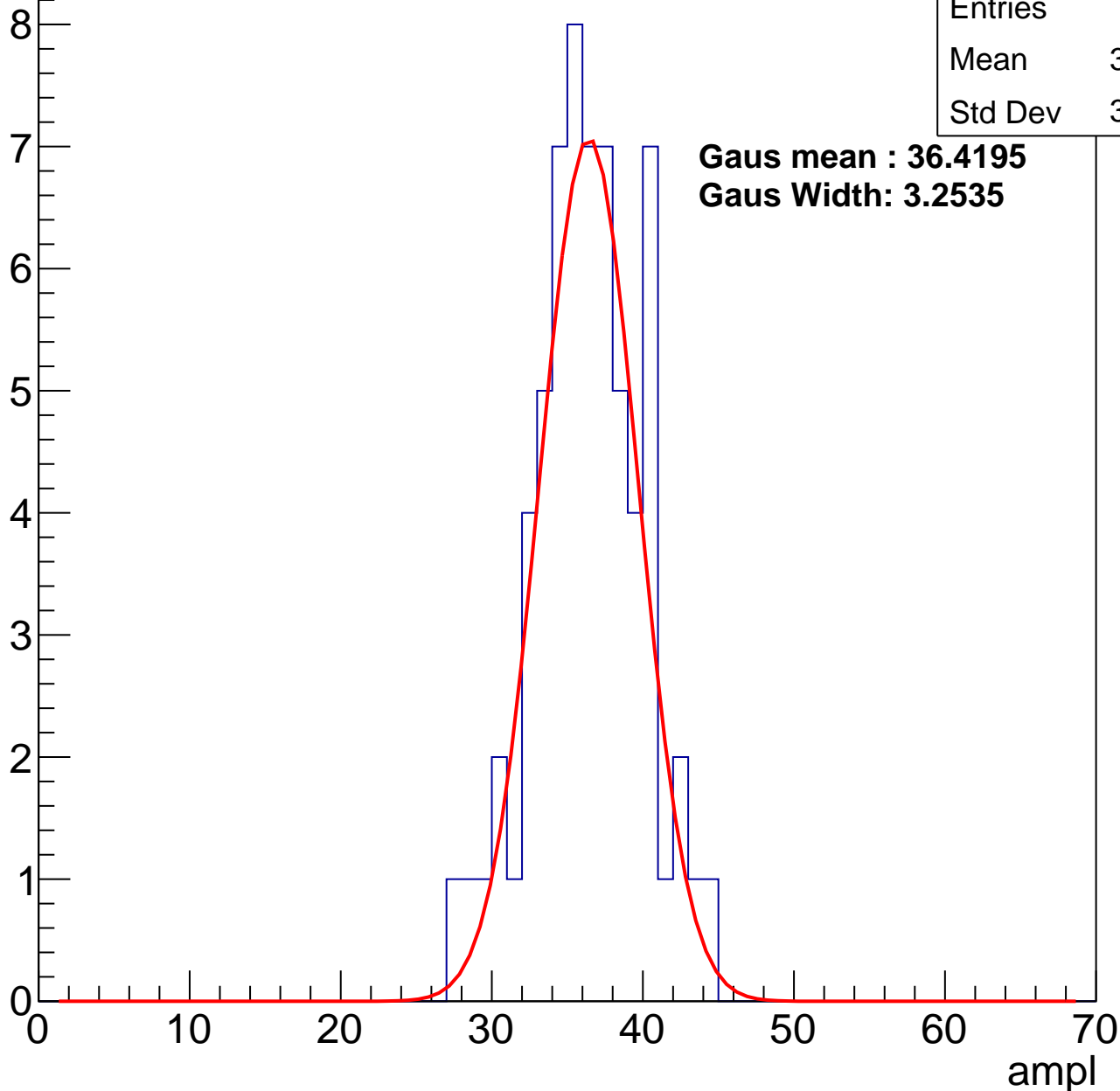
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.92
Std Dev	3.553

Gaus mean : 36.4195

Gaus Width: 3.2535



B1L003S, U16-ch110, adc2

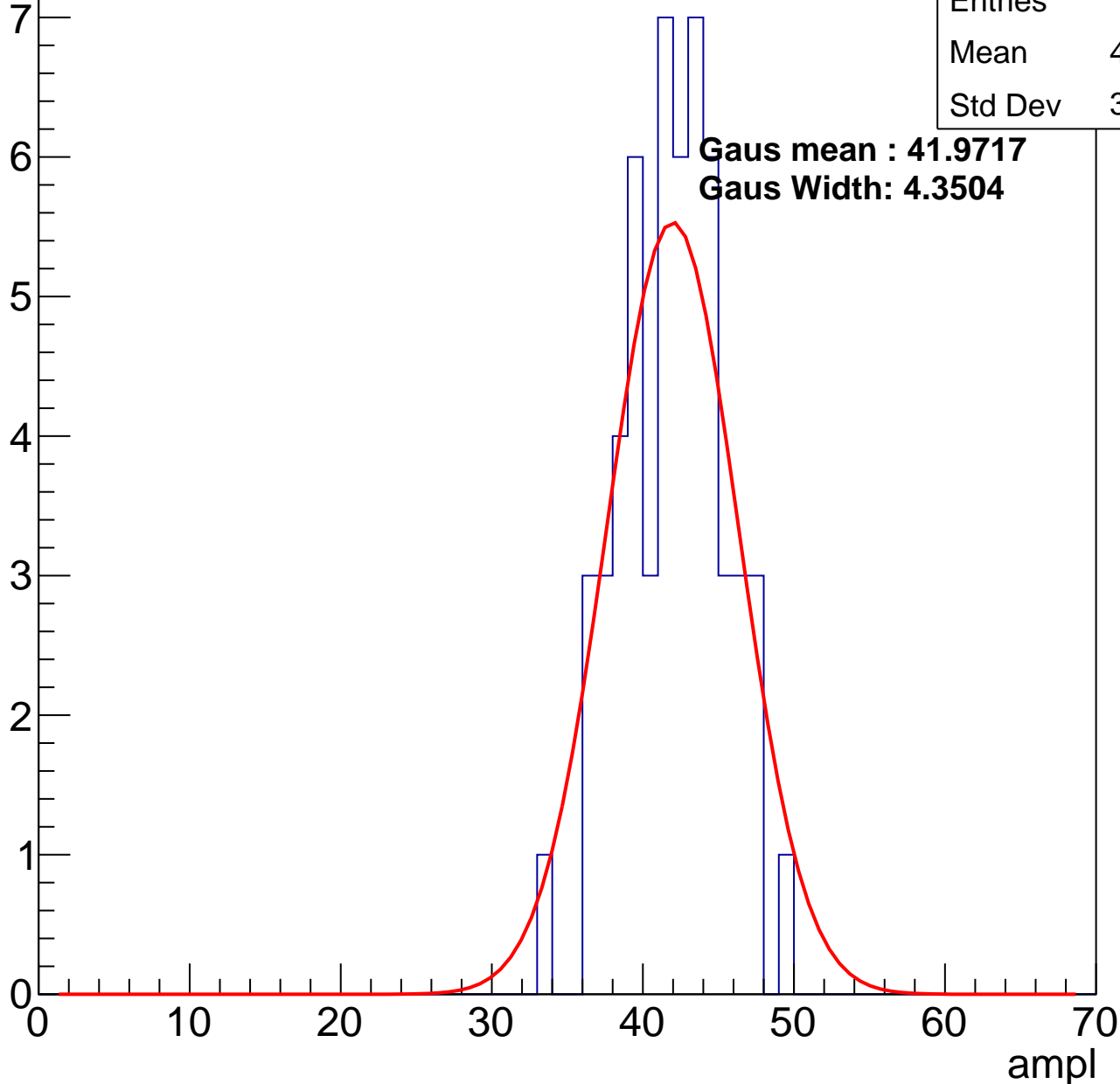
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.52
Std Dev	3.322

Gaus mean : 41.9717

Gaus Width: 4.3504

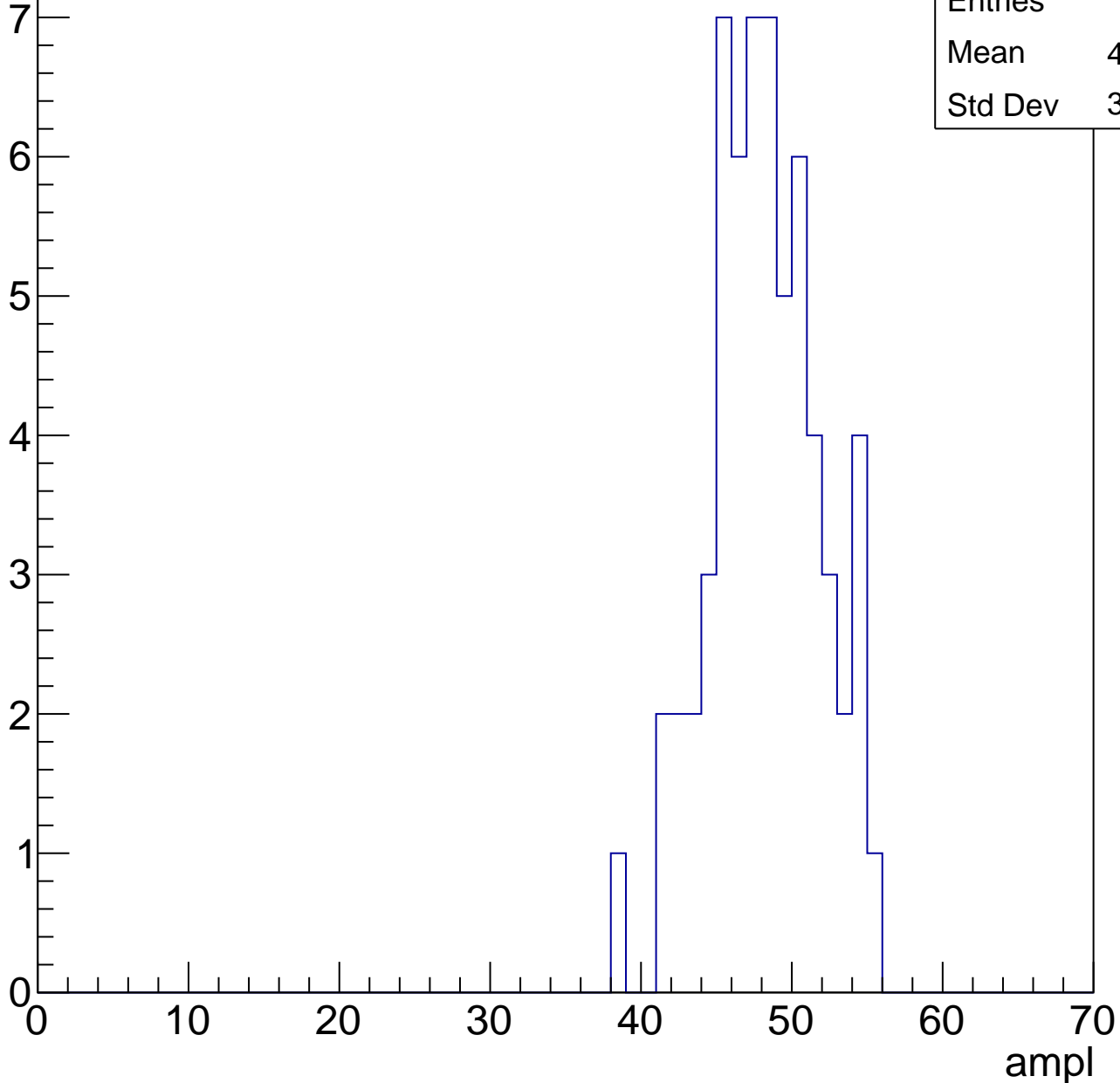


B1L003S, U16-ch110, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	47.74
Std Dev	3.623

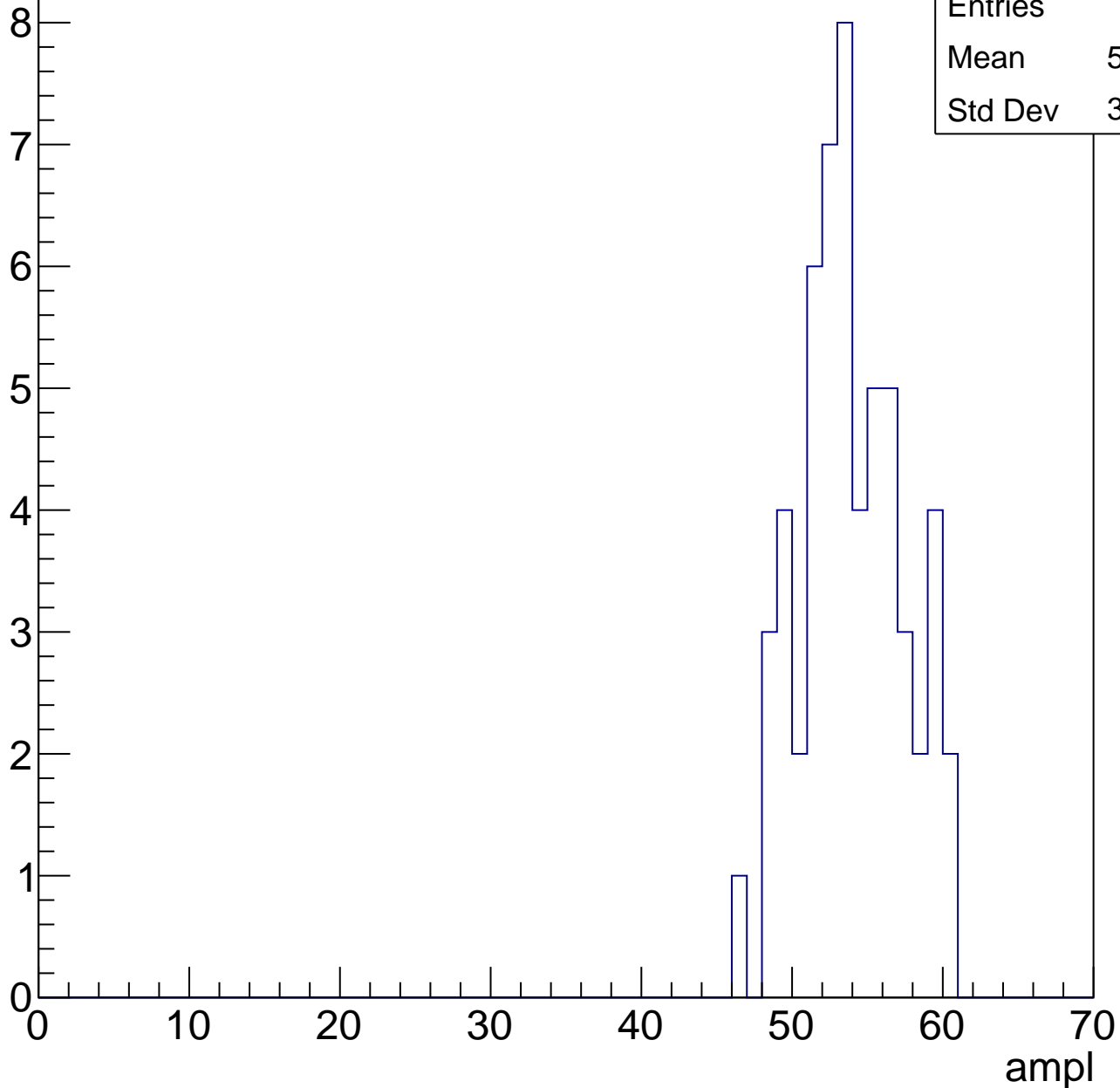


B1L003S, U16-ch110, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

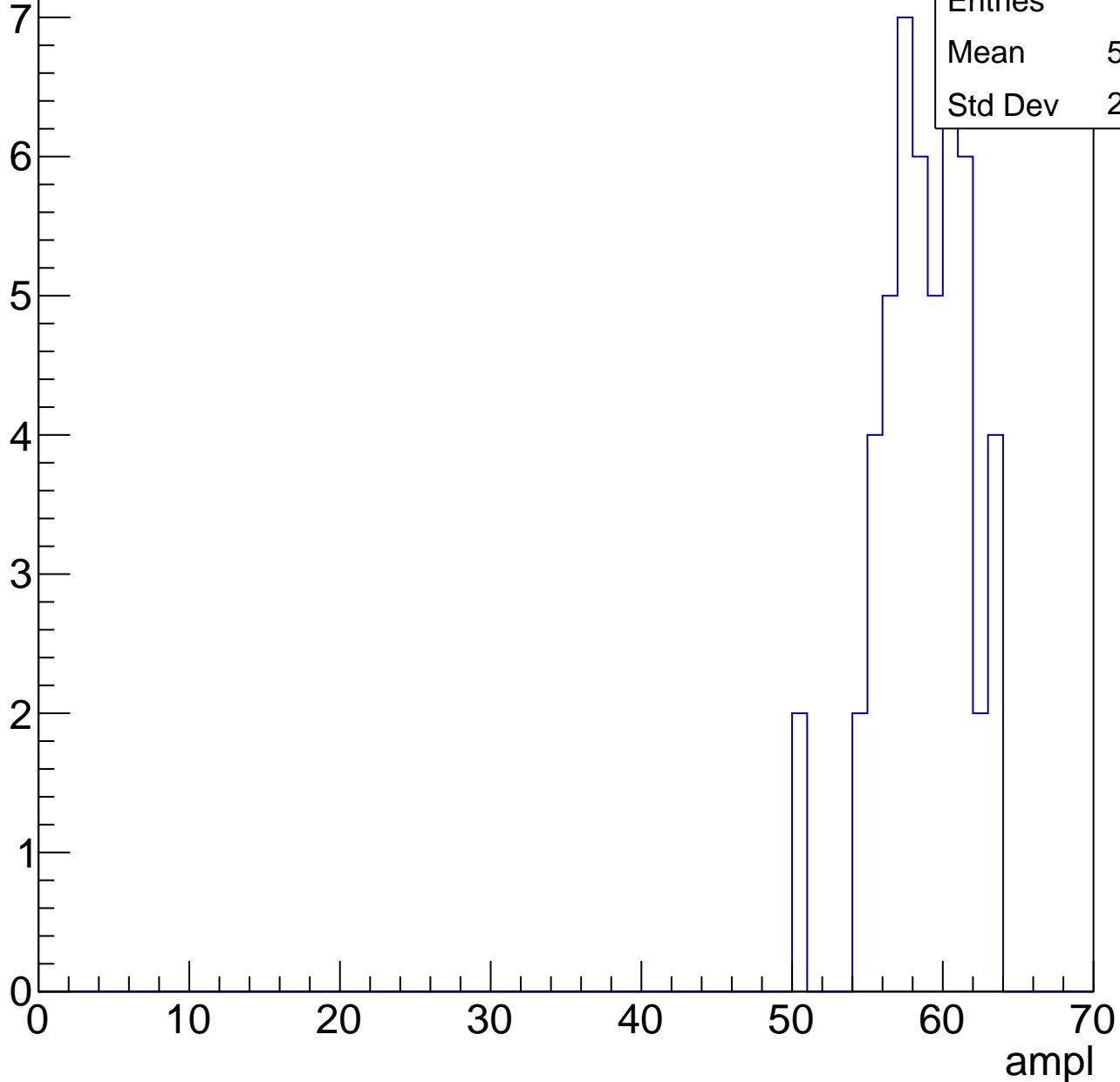
Entries	56
Mean	53.46
Std Dev	3.365



B1L003S, U16-ch110, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



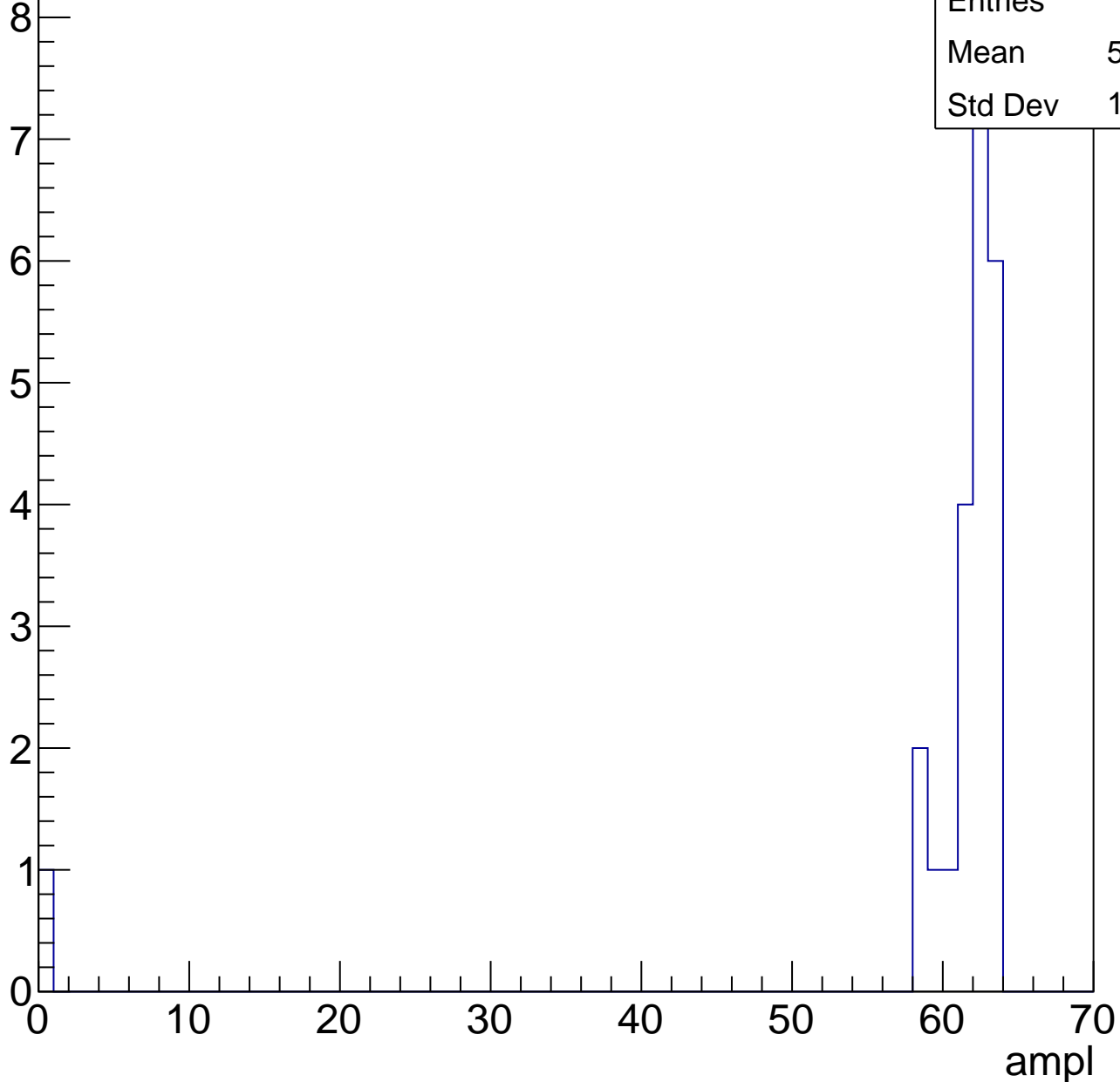
Entries	50
Mean	58.24
Std Dev	2.964

B1L003S, U16-ch110, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.83
Std Dev	12.63



B1L003S, U16-ch110, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch111, adc0

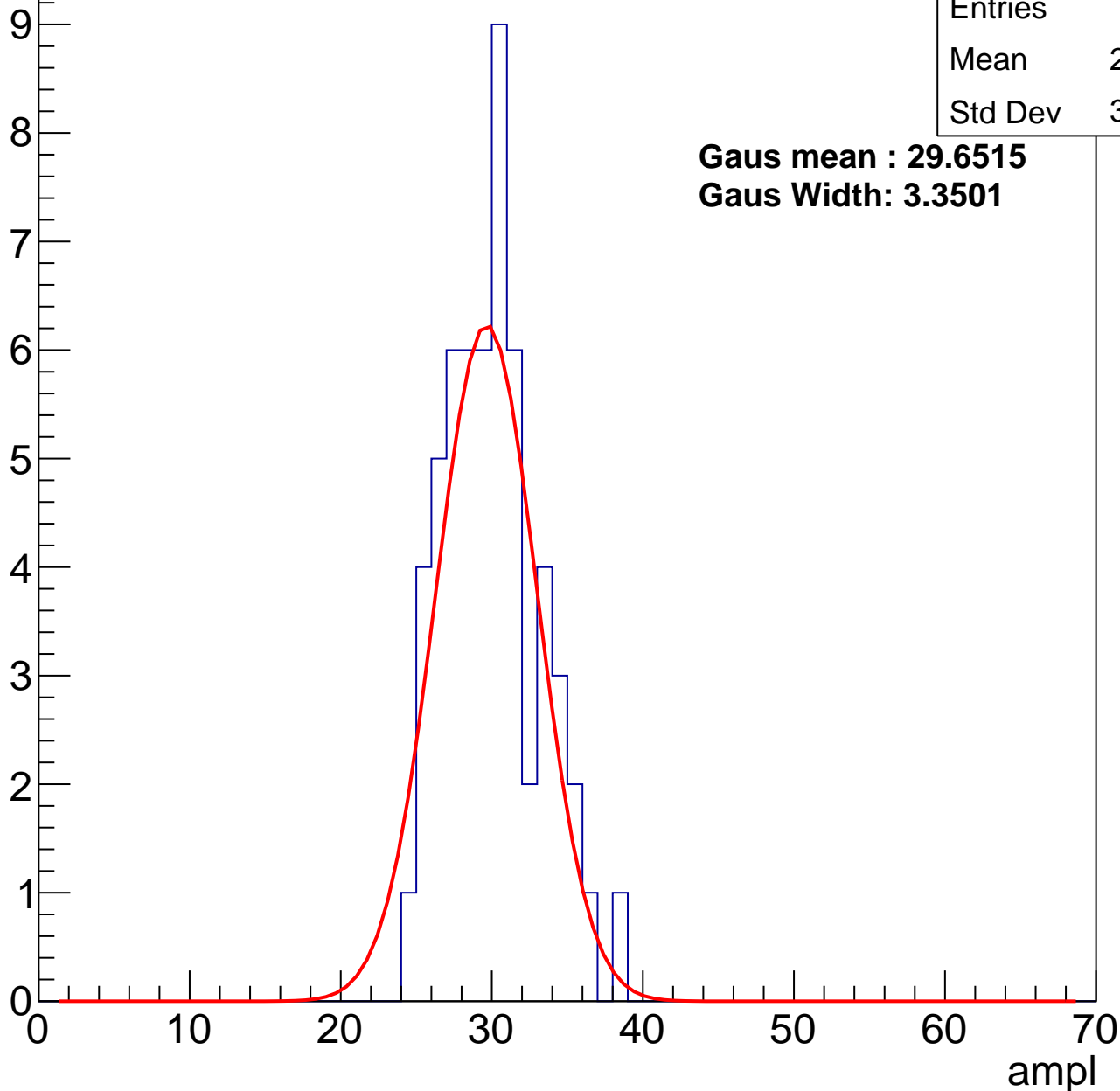
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	29.57
Std Dev	3.099

Gaus mean : 29.6515

Gaus Width: 3.3501



B1L003S, U16-ch111, adc1

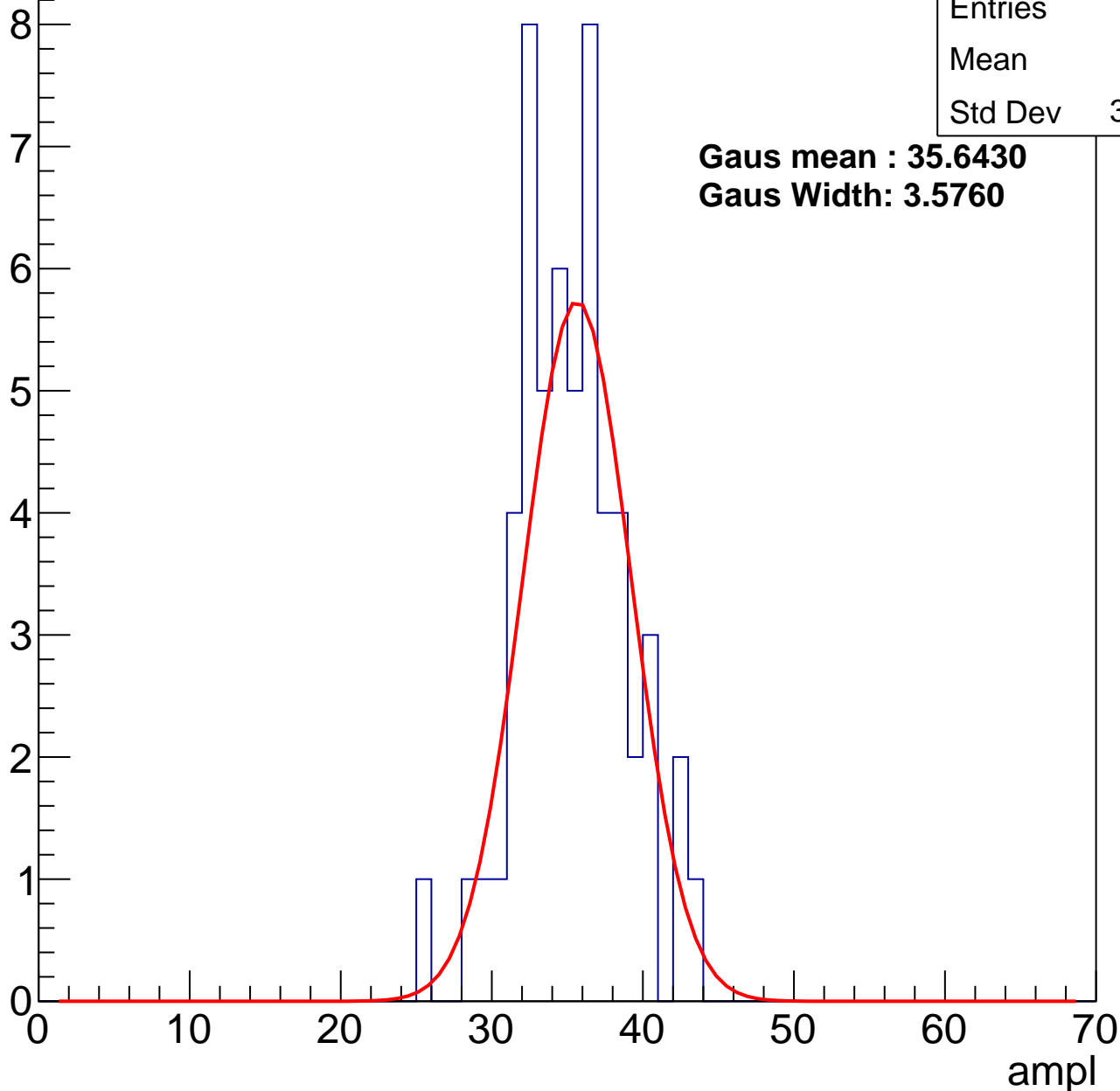
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	34.8
Std Dev	3.533

Gaus mean : 35.6430

Gaus Width: 3.5760



B1L003S, U16-ch111, adc2

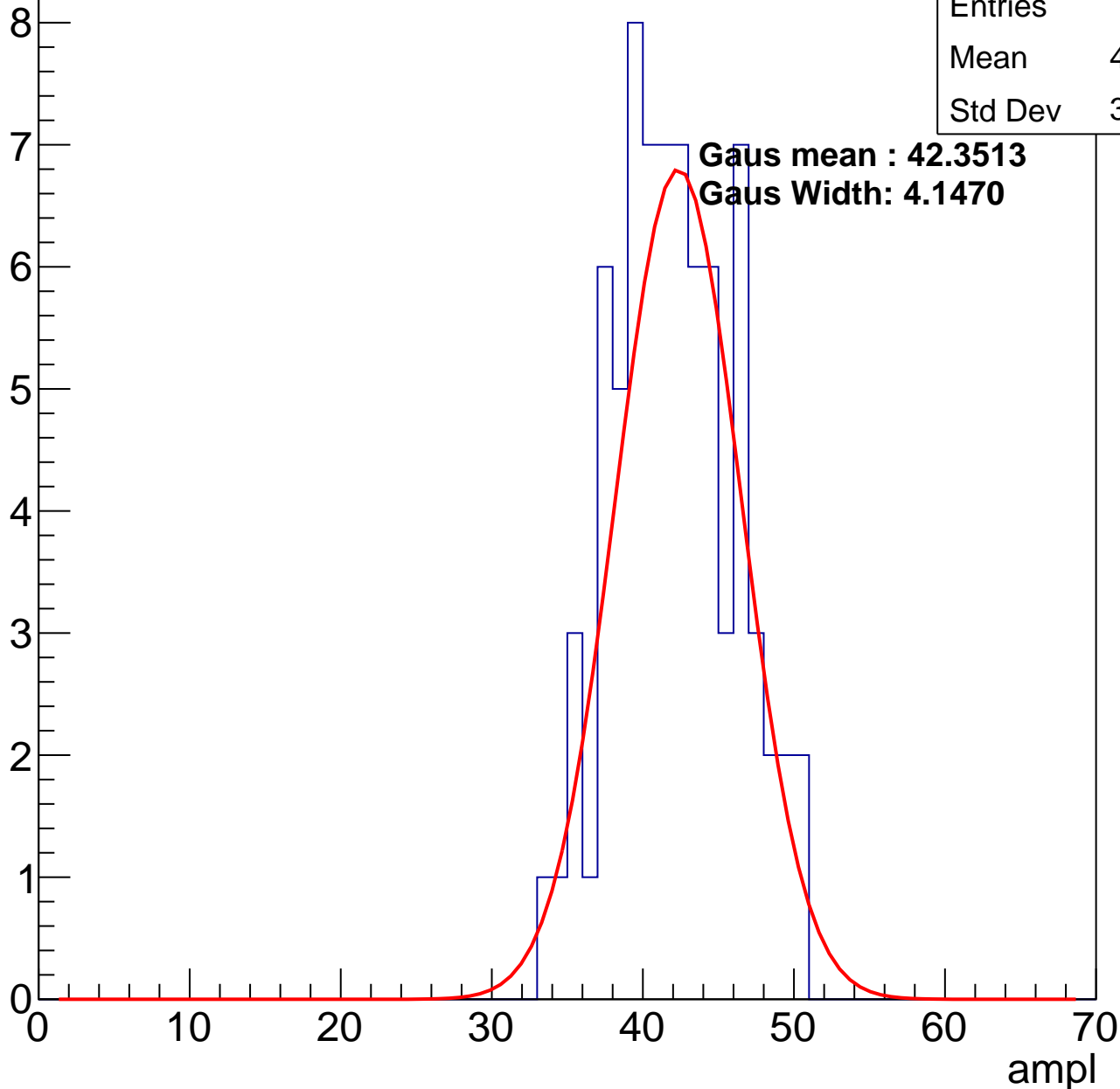
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	41.65
Std Dev	3.944

Gaus mean : 42.3513

Gaus Width: 4.1470

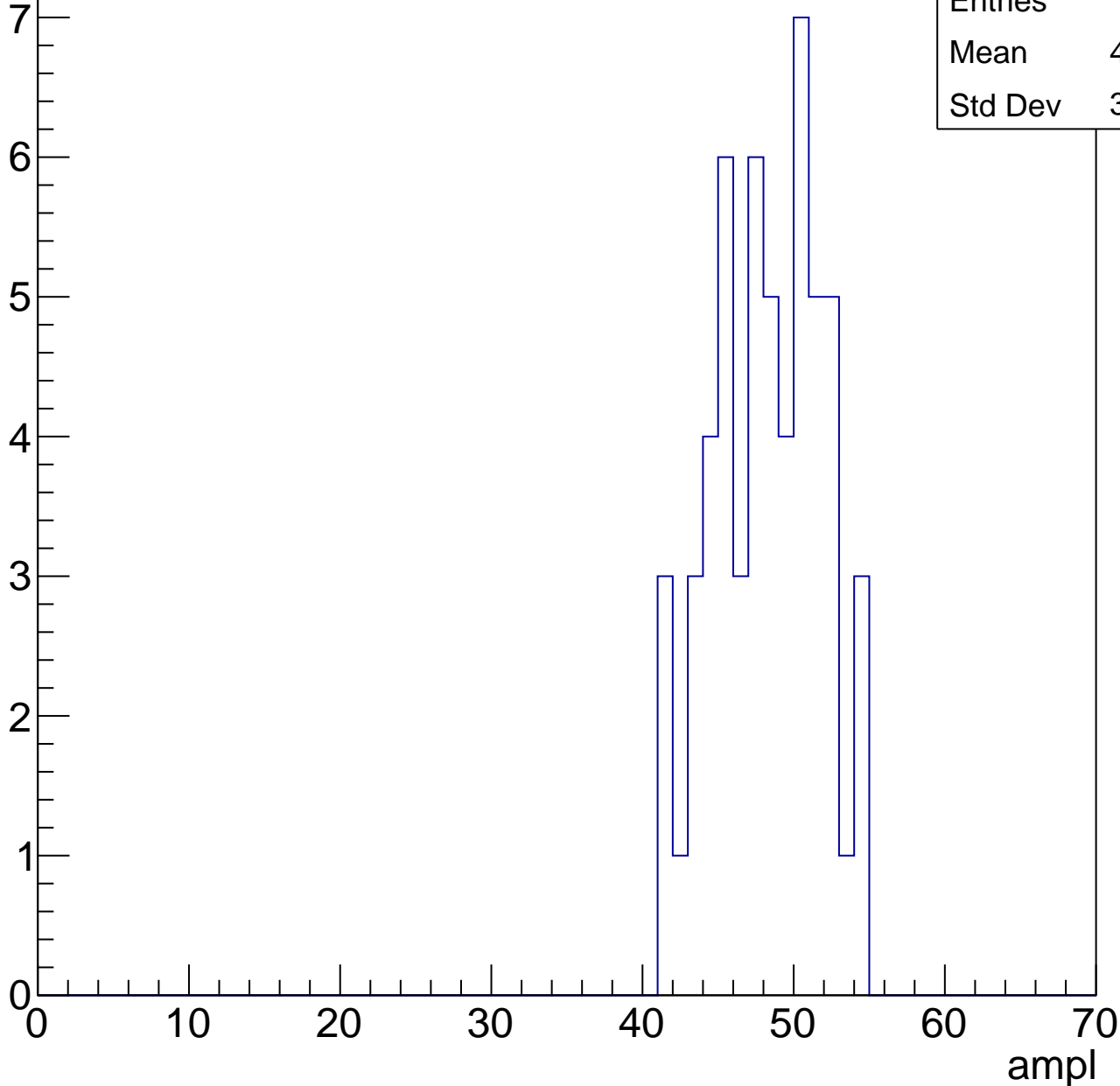


B1L003S, U16-ch111, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	47.79
Std Dev	3.488

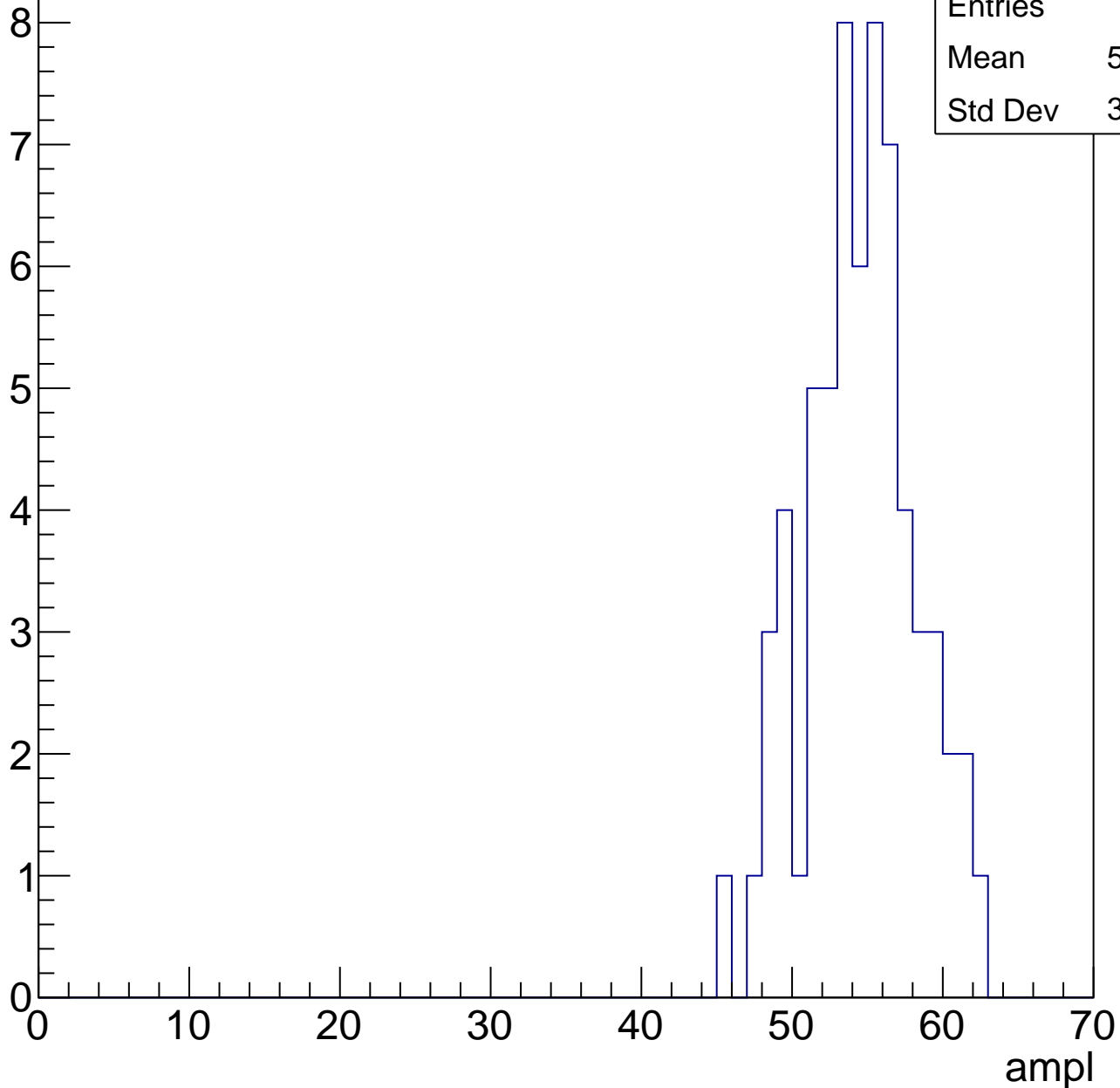


B1L003S, U16-ch111, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	54.06
Std Dev	3.657

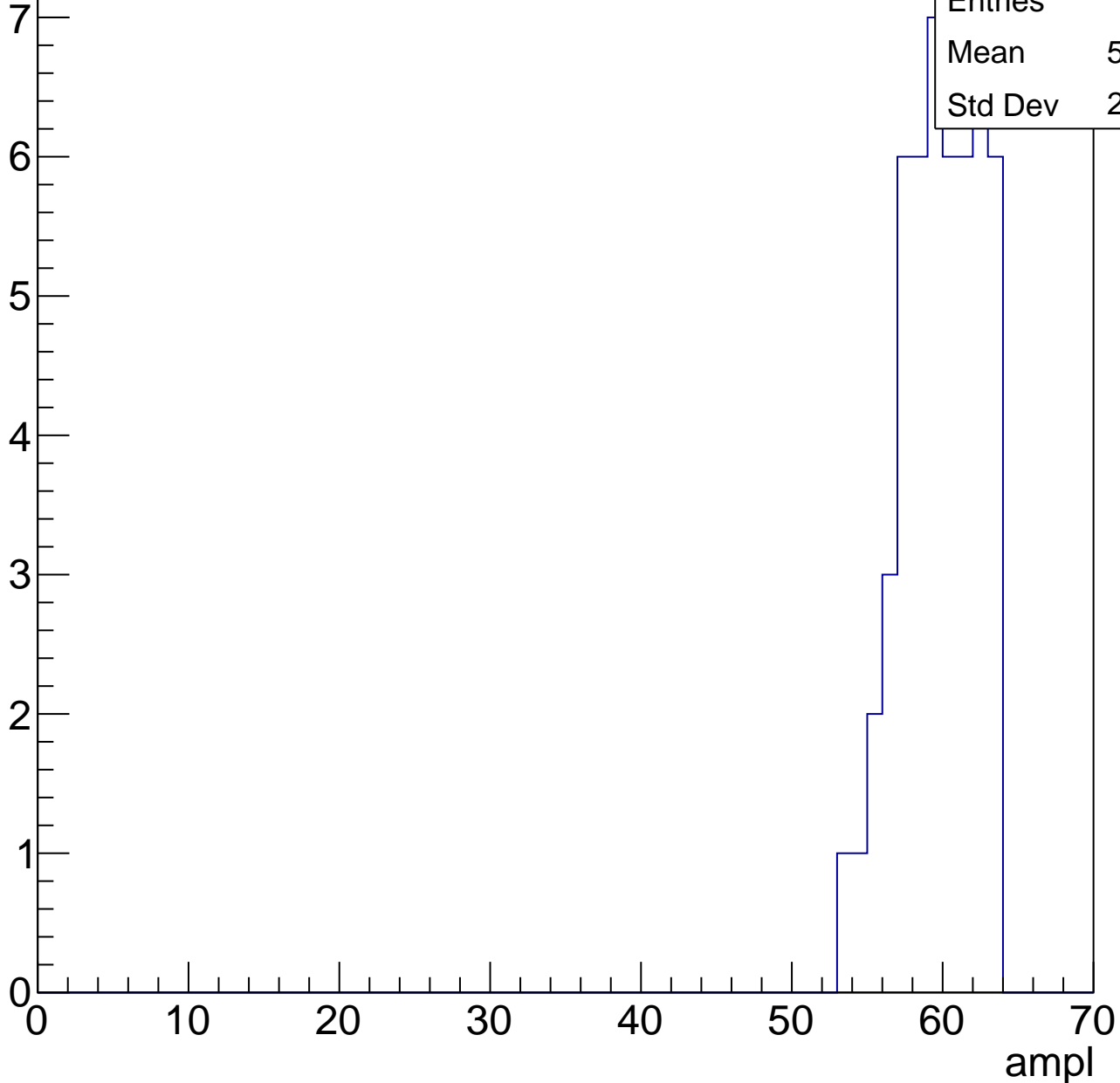


B1L003S, U16-ch111, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

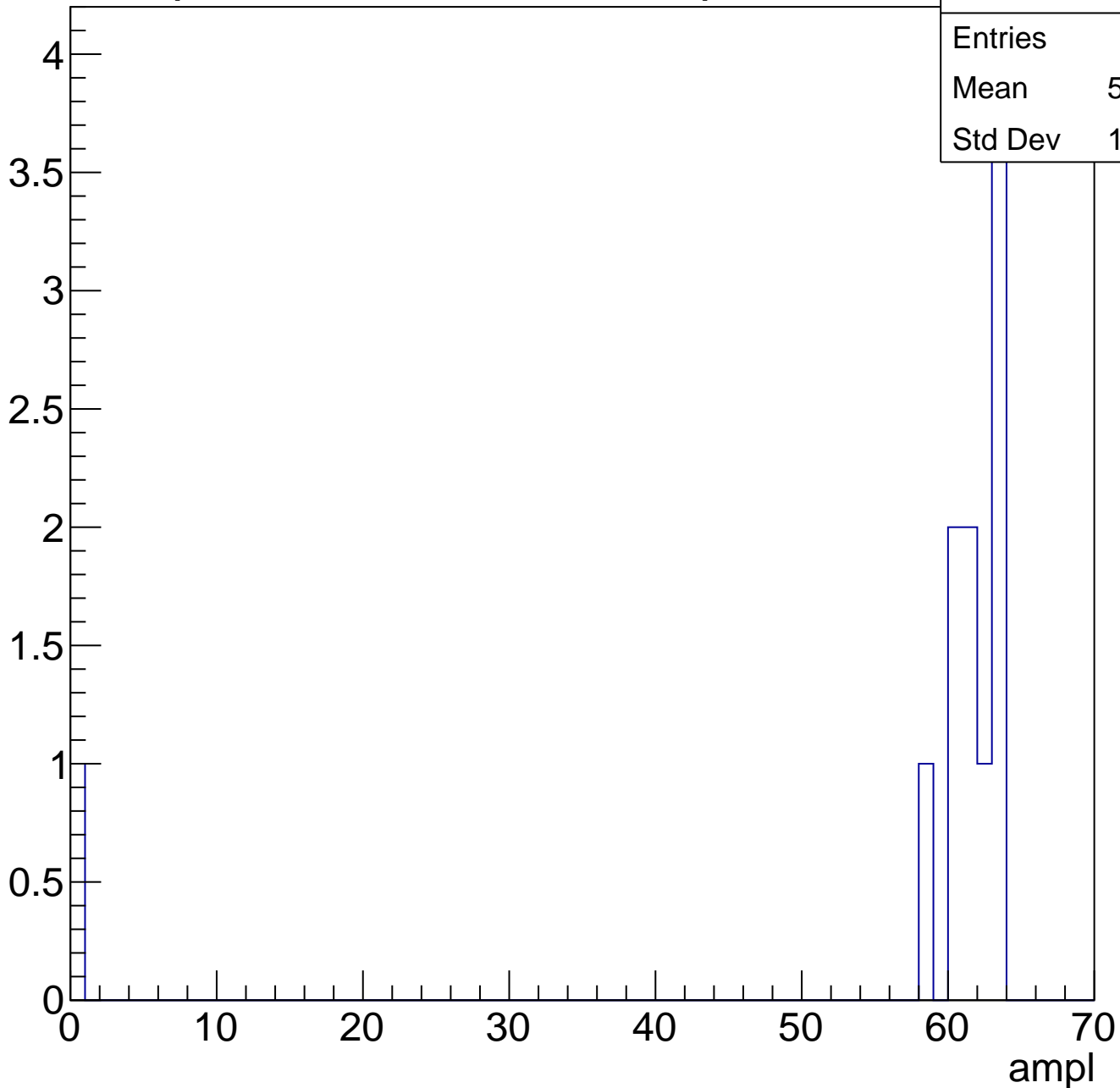
Entries	51
Mean	59.33
Std Dev	2.557



B1L003S, U16-ch111, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	11
Mean	55.82
Std Dev	17.72

B1L003S, U16-ch111, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch112, adc0

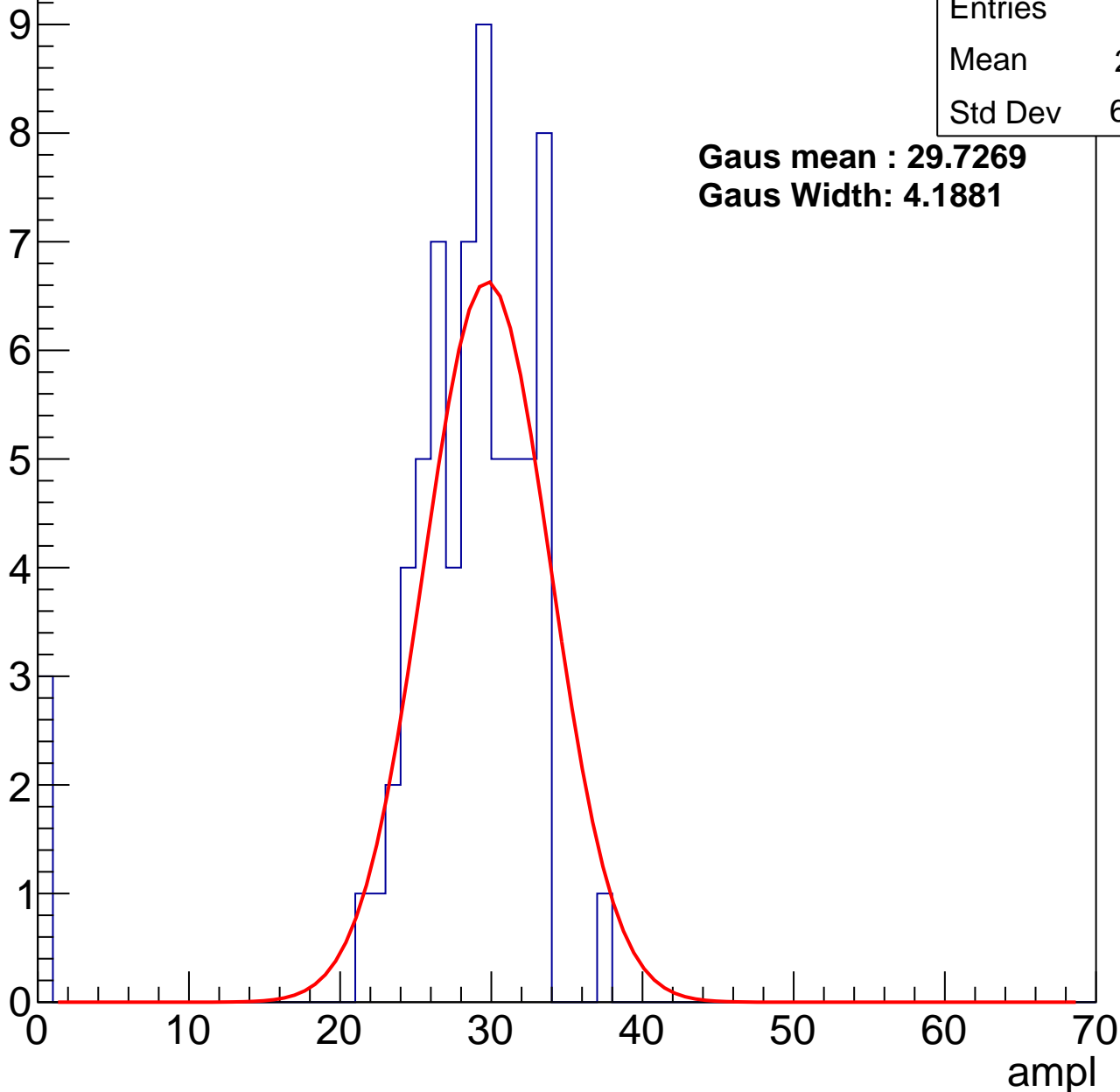
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	27.21
Std Dev	6.715

Gaus mean : 29.7269

Gaus Width: 4.1881



B1L003S, U16-ch112, adc1

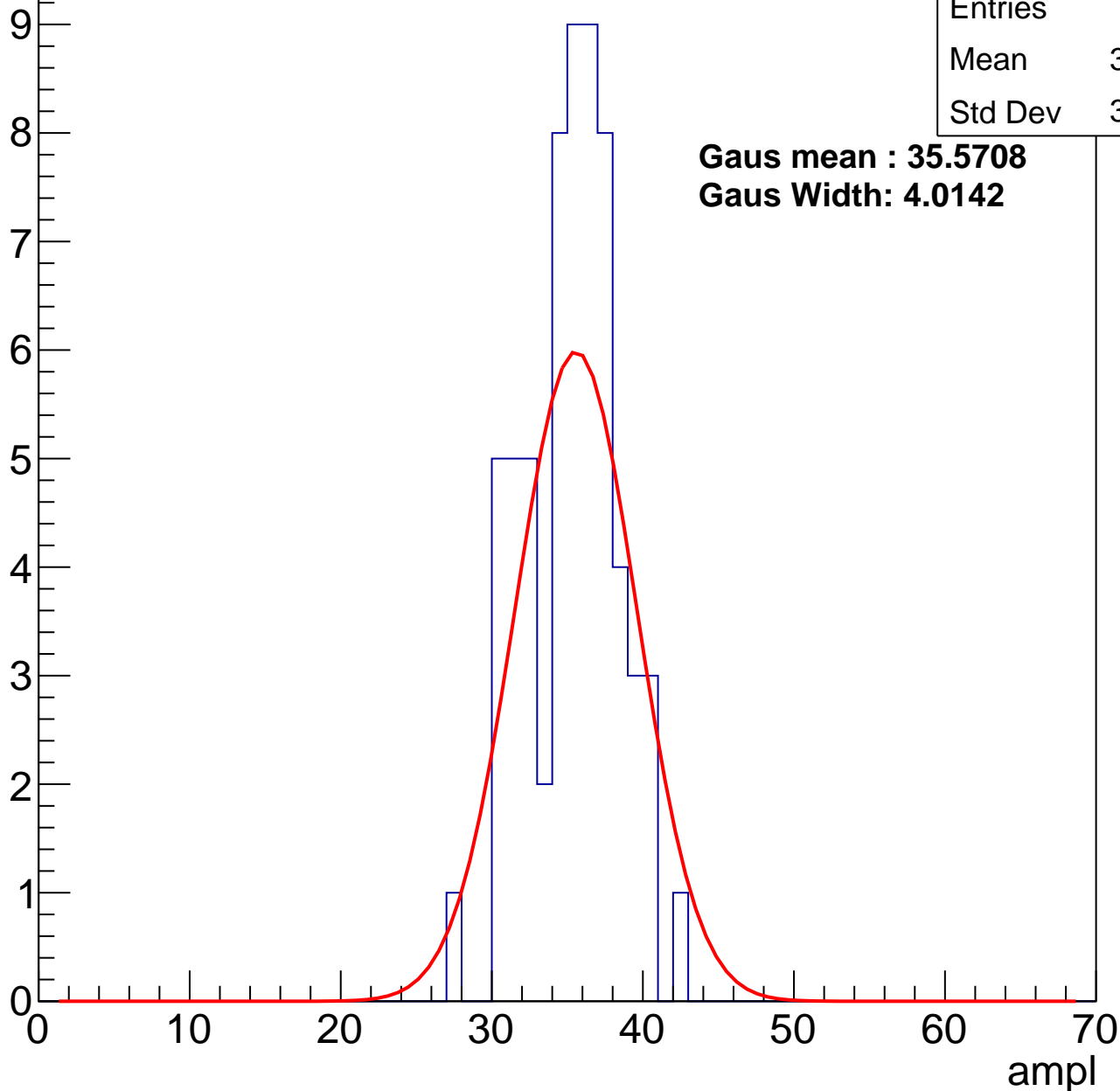
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	34.86
Std Dev	3.028

Gaus mean : 35.5708

Gaus Width: 4.0142



B1L003S, U16-ch112, adc2

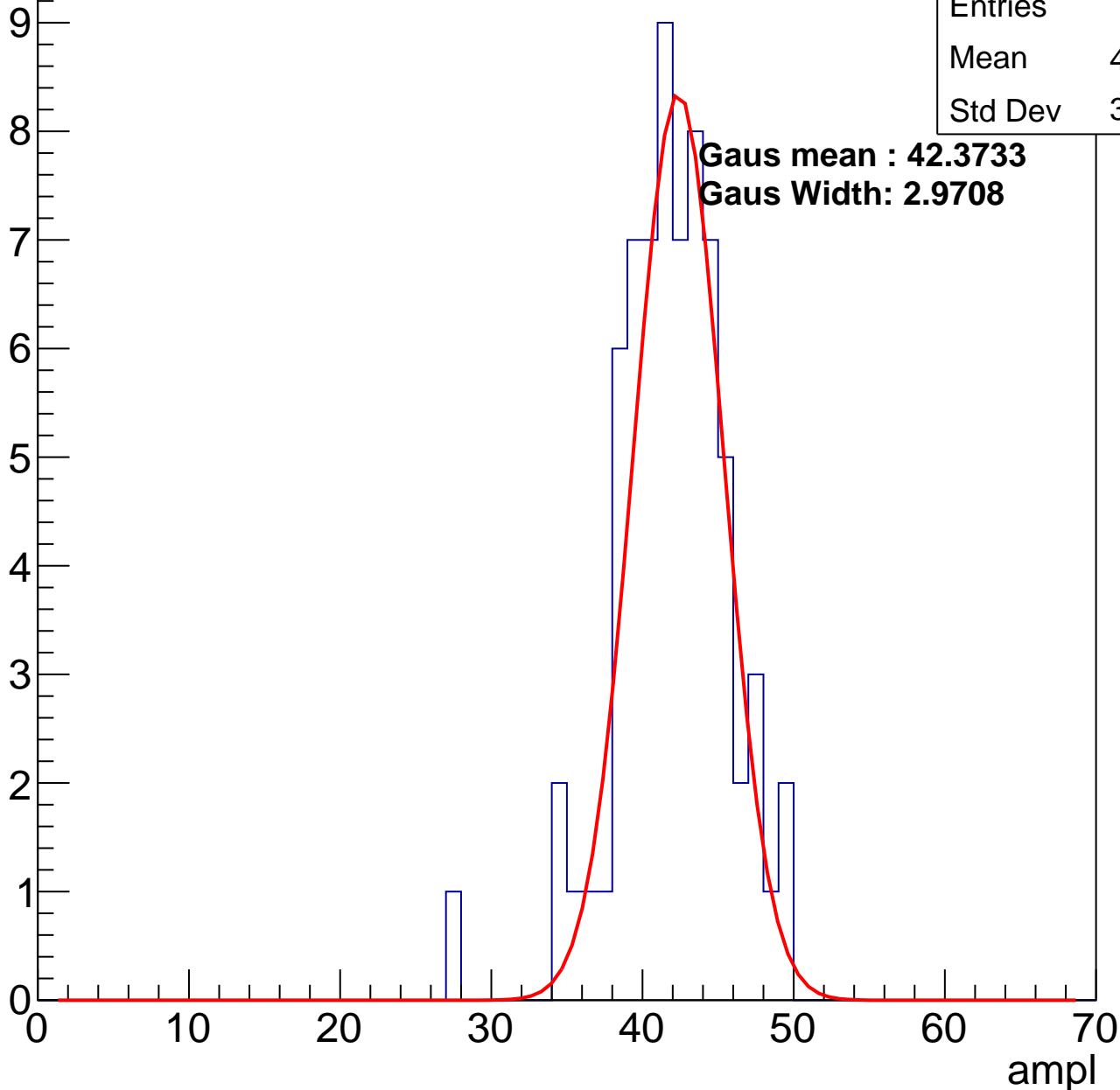
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	41.47
Std Dev	3.706

Gaus mean : 42.3733

Gaus Width: 2.9708

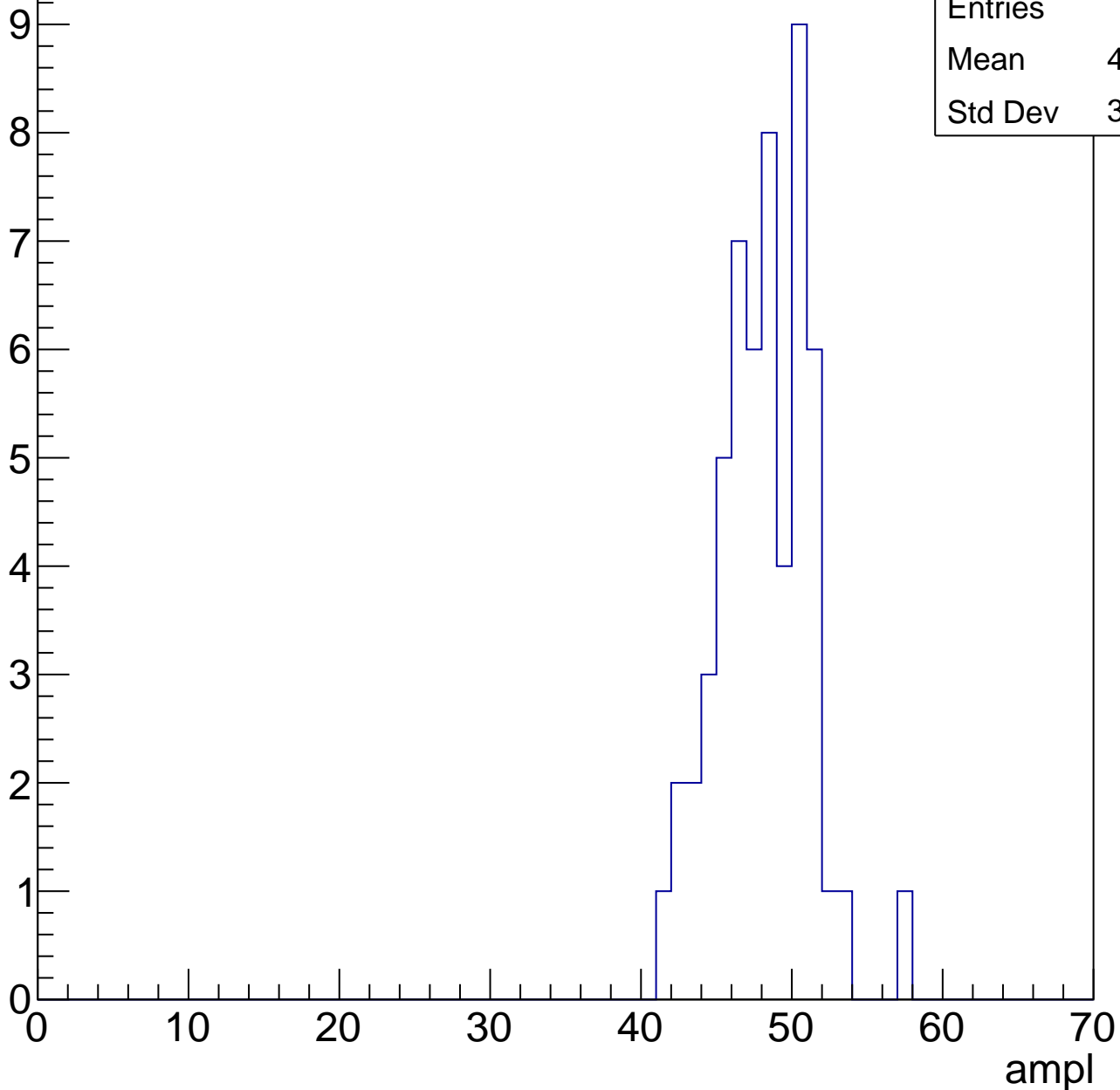


B1L003S, U16-ch112, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	47.68
Std Dev	3.013

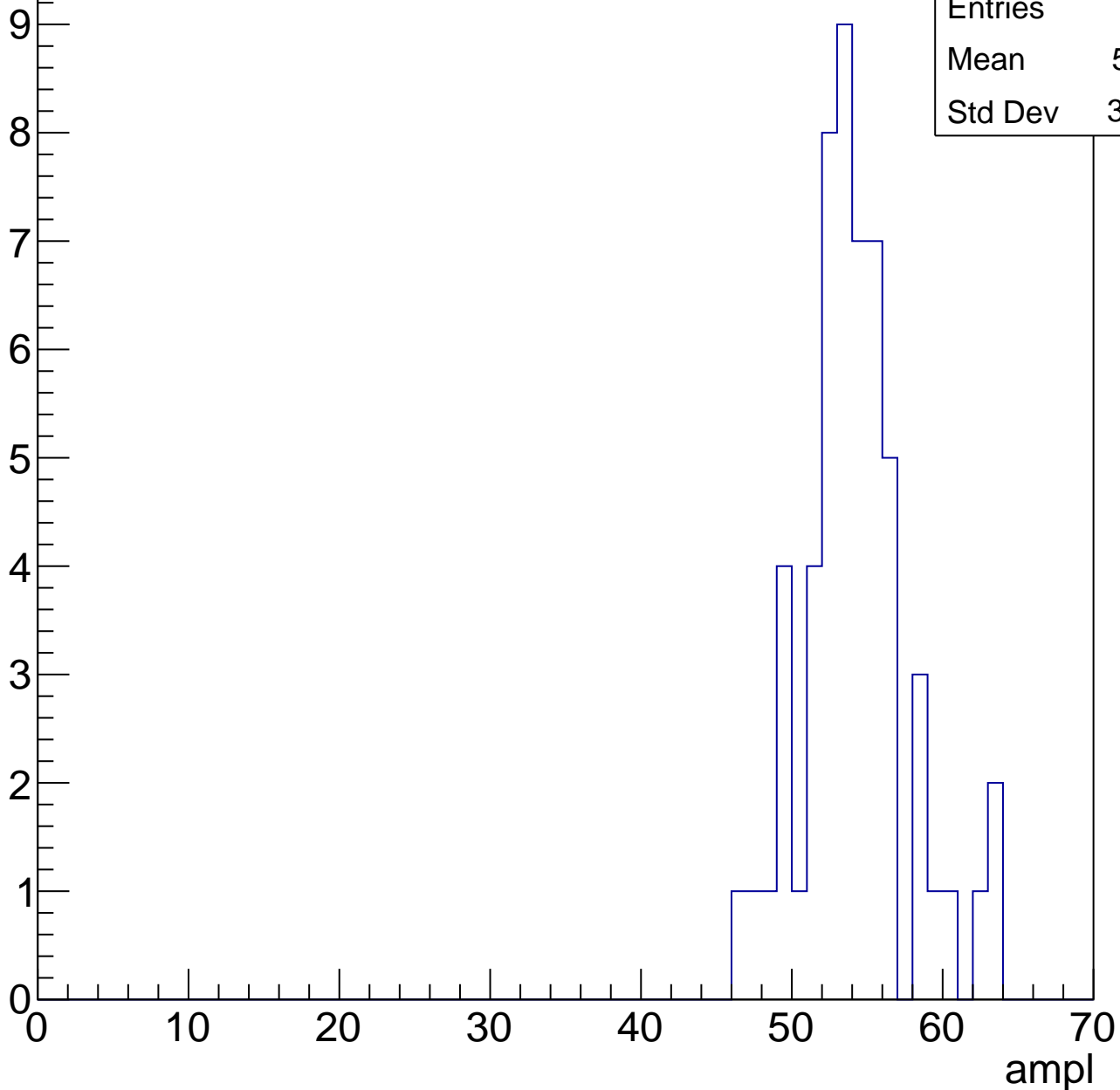


B1L003S, U16-ch112, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	53.71
Std Dev	3.534

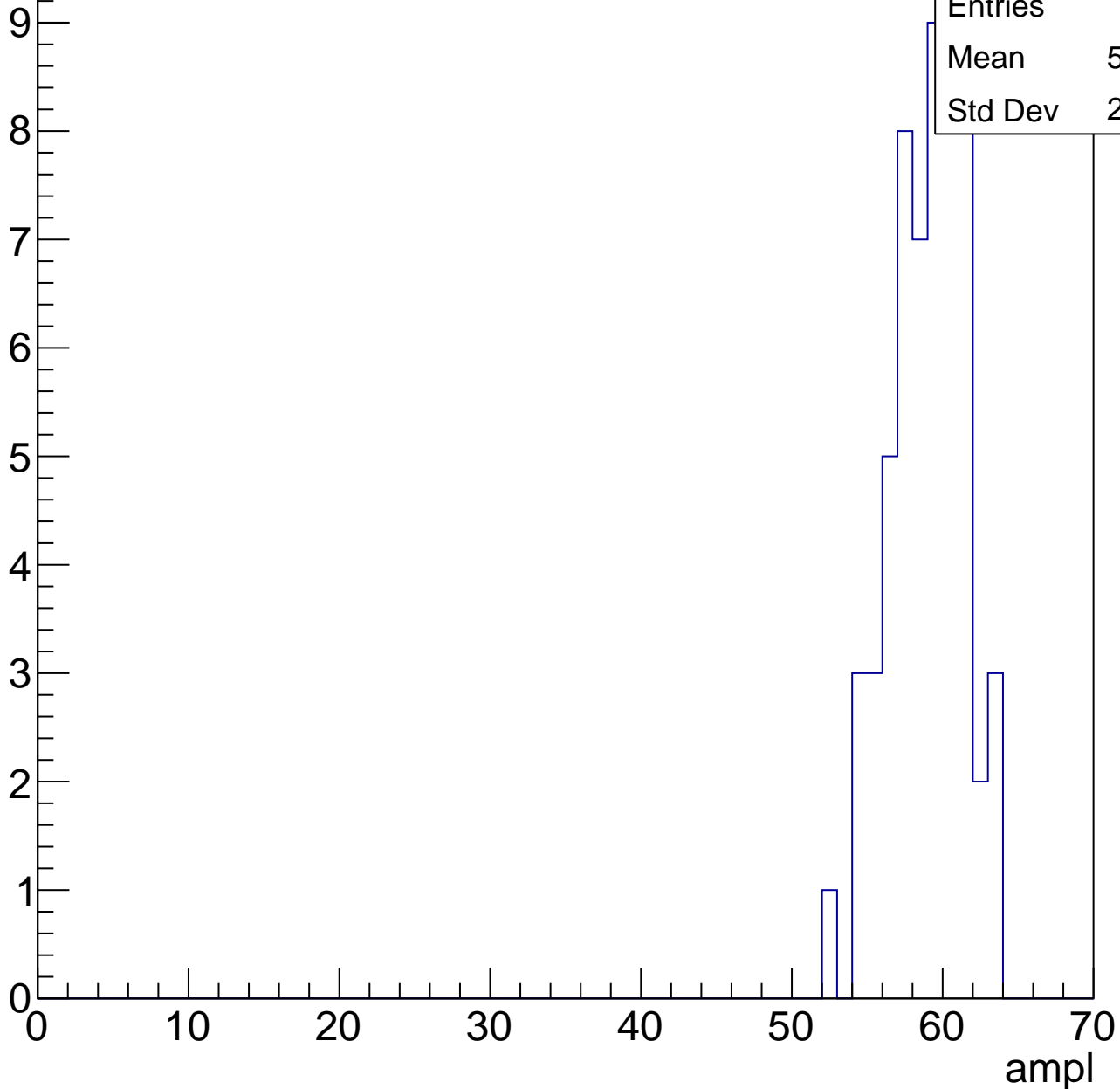


B1L003S, U16-ch112, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.47
Std Dev	2.465

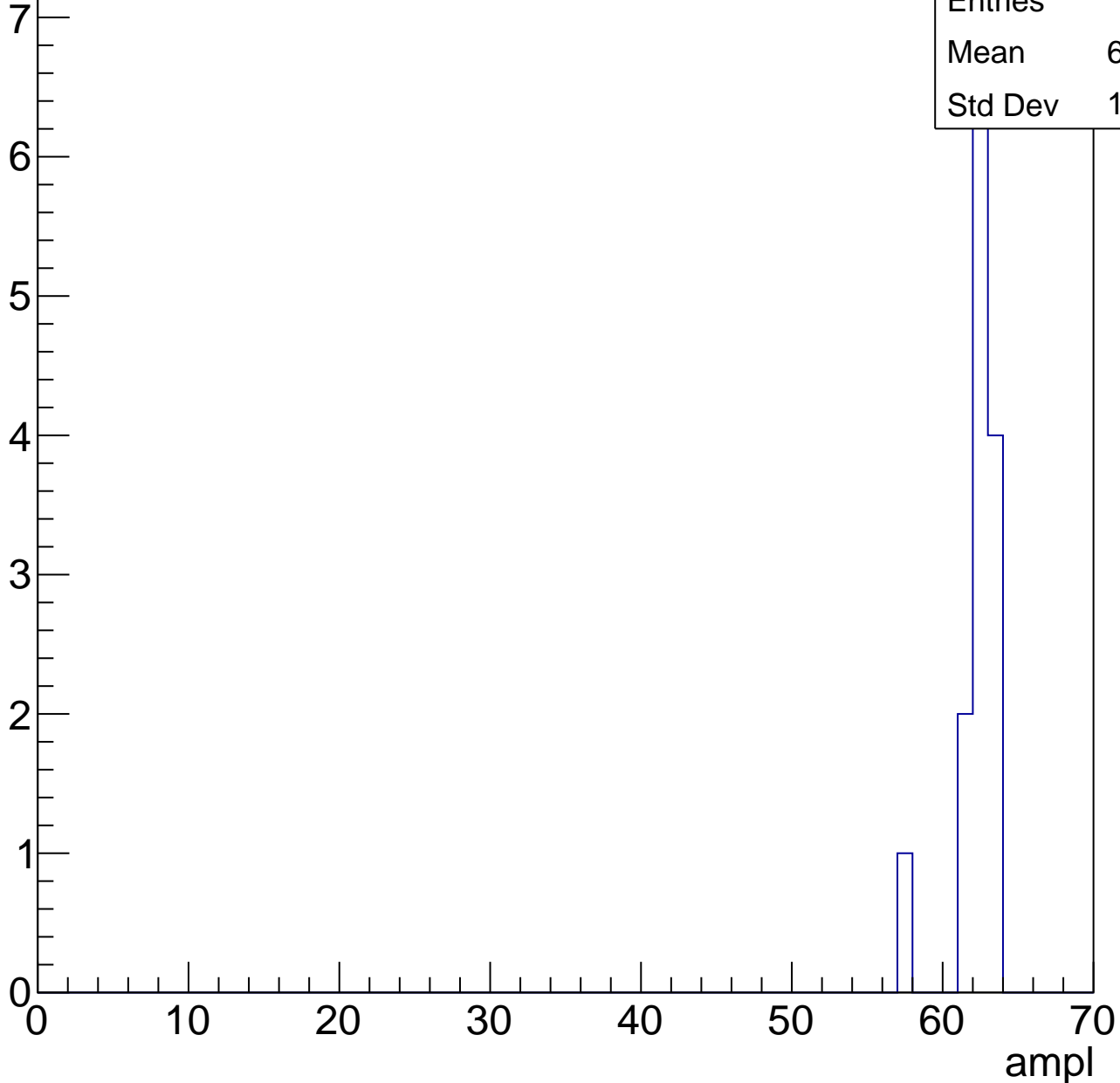


B1L003S, U16-ch112, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

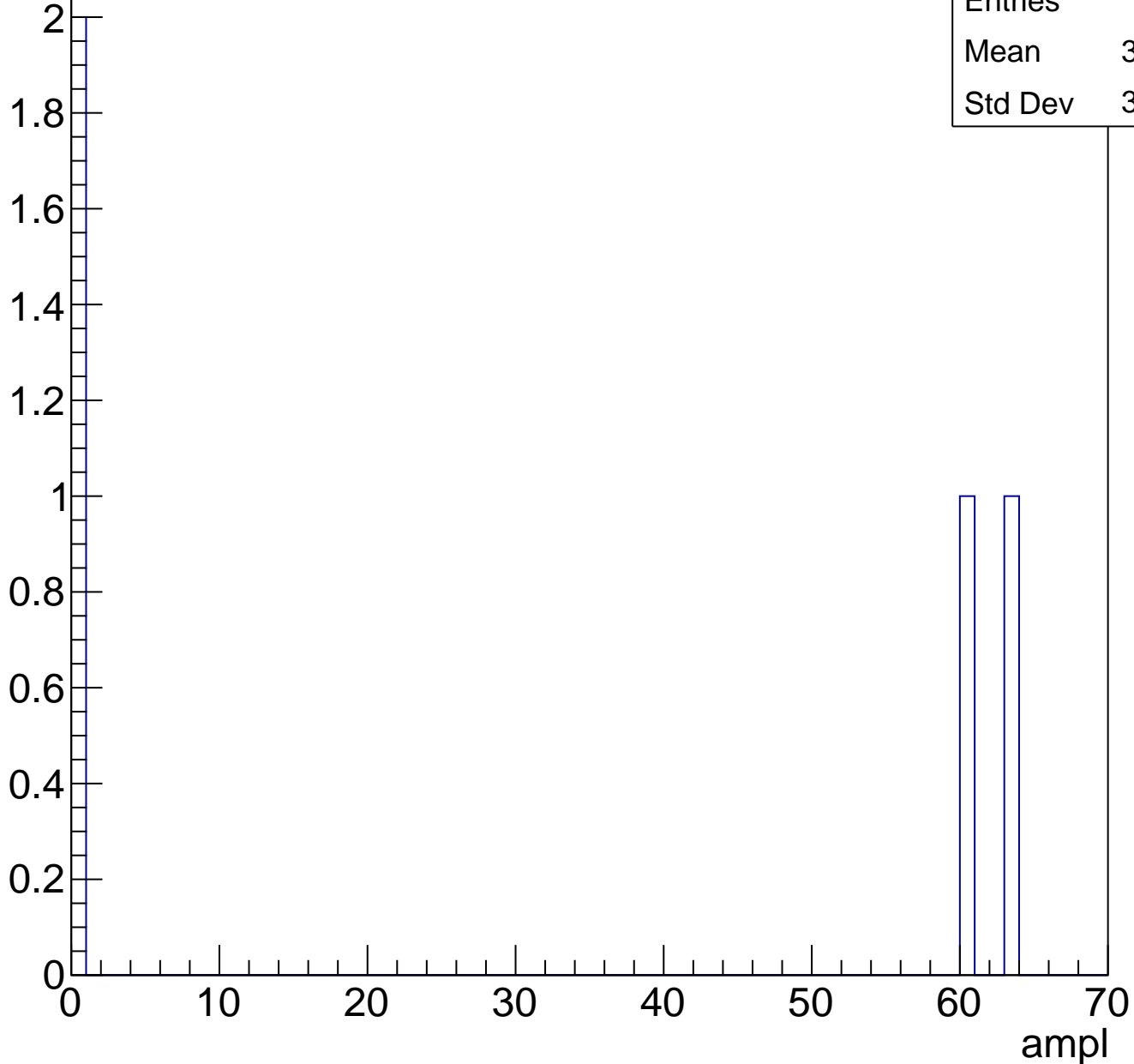
Entries	14
Mean	61.79
Std Dev	1.473



B1L003S, U16-ch112, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	30.75
Std Dev	30.77

B1L003S, U16-ch113, adc0

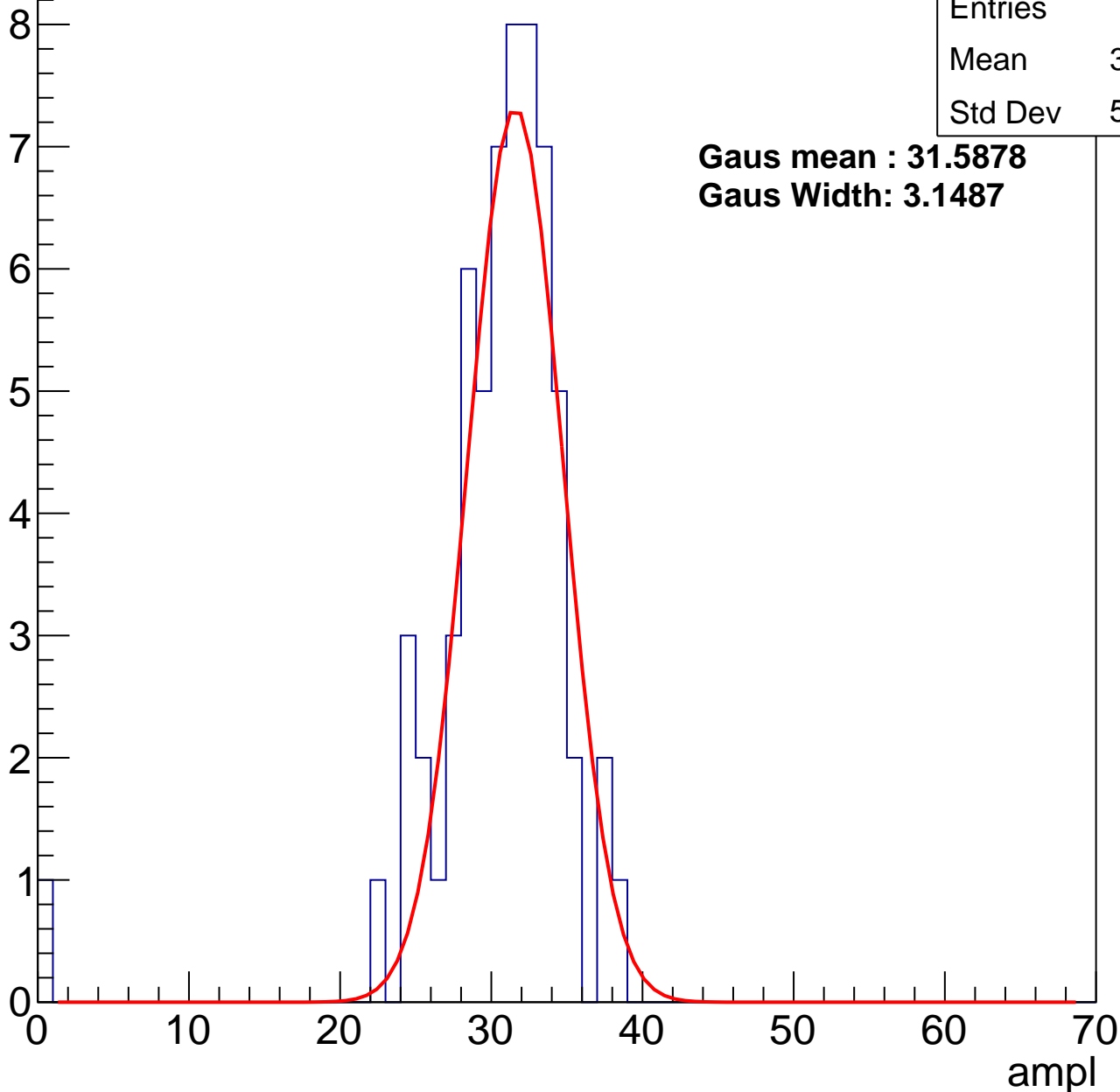
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.02
Std Dev	5.062

Gaus mean : 31.5878

Gaus Width: 3.1487



B1L003S, U16-ch113, adc1

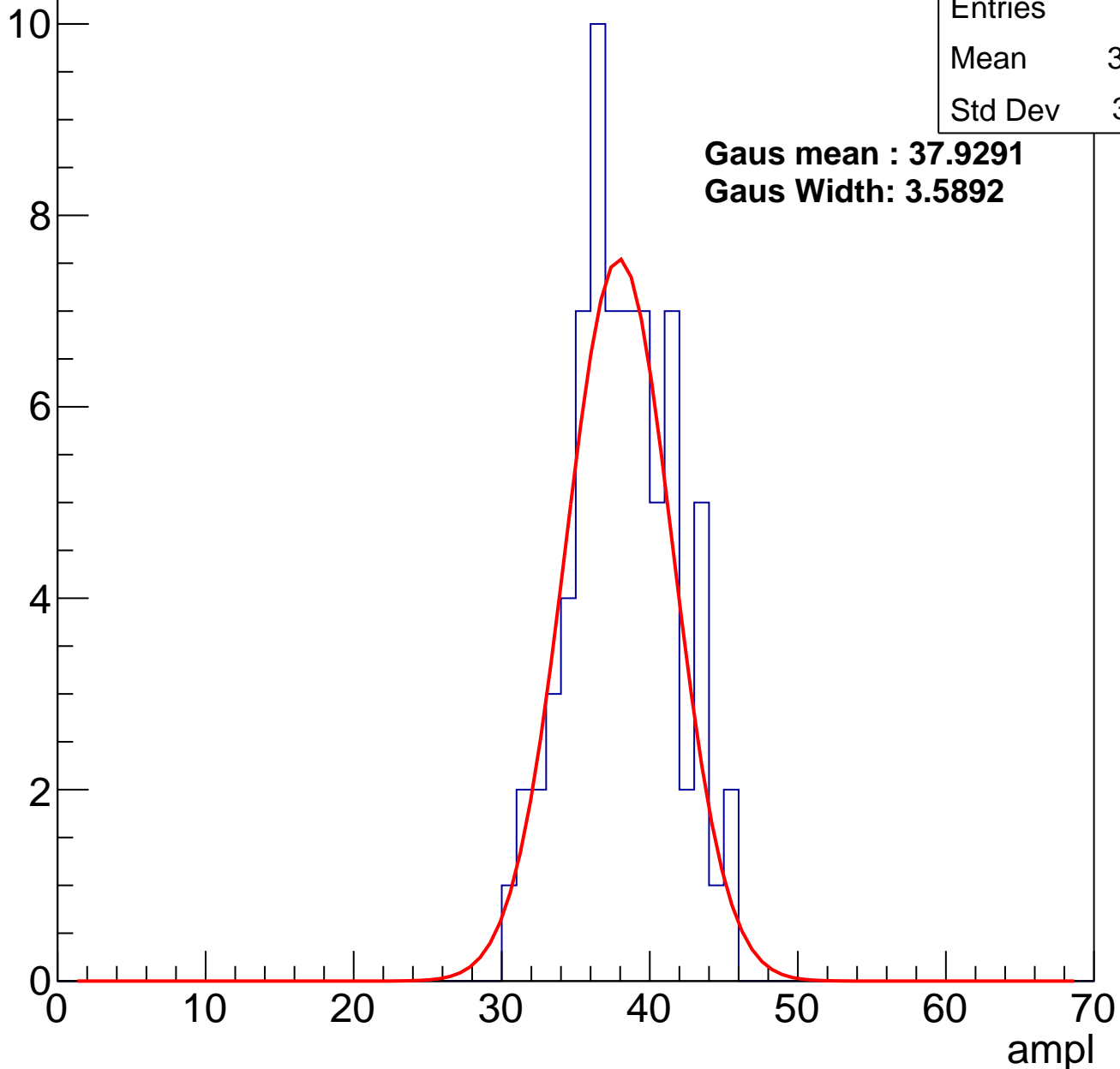
calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	37.69
Std Dev	3.451

Gaus mean : 37.9291

Gaus Width: 3.5892

Entry



B1L003S, U16-ch113, adc2

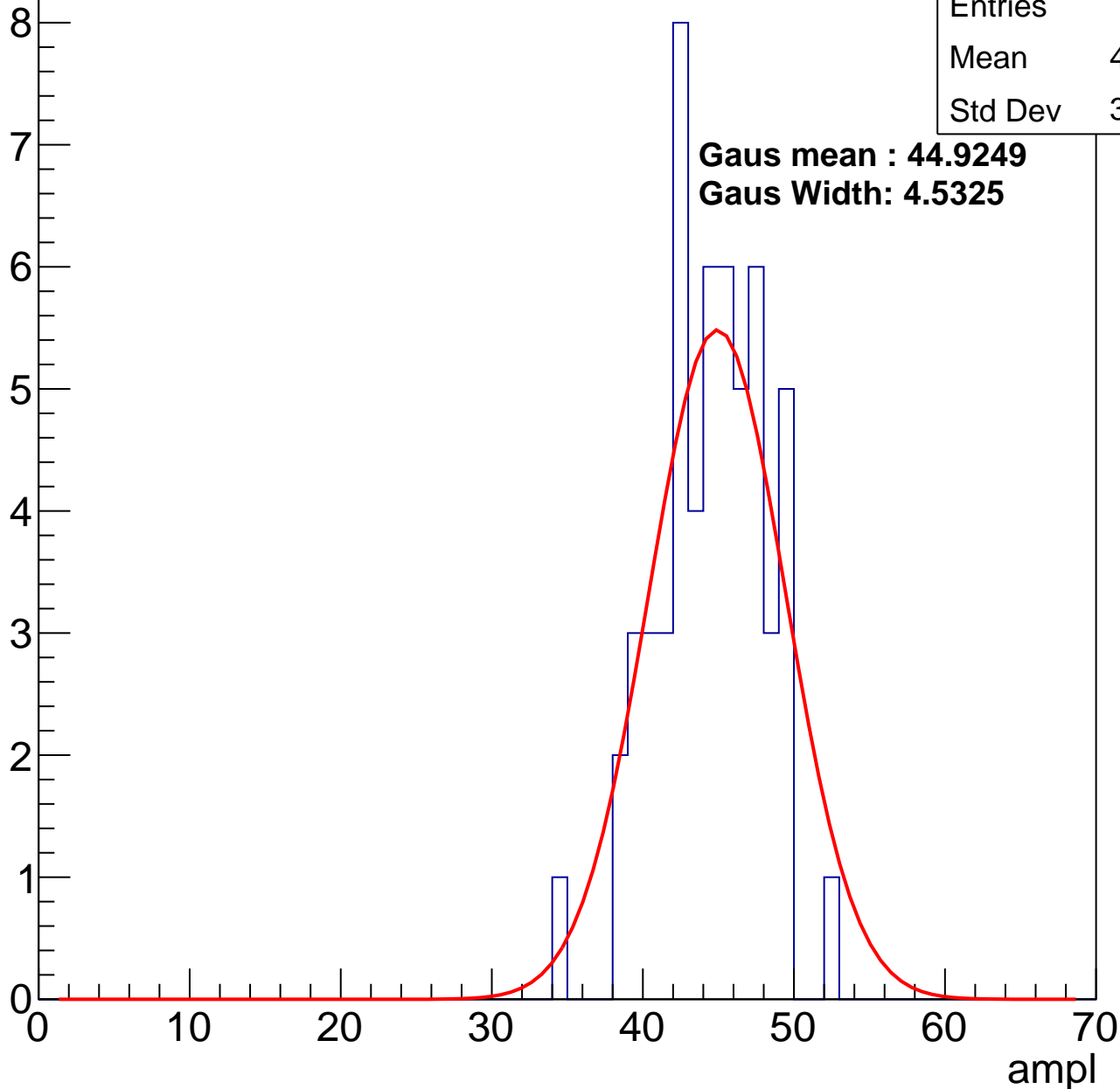
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	44.02
Std Dev	3.472

Gaus mean : 44.9249

Gaus Width: 4.5325



B1L003S, U16-ch113, adc3

calib_packv5_042523_0143.root, FC#13, port D2

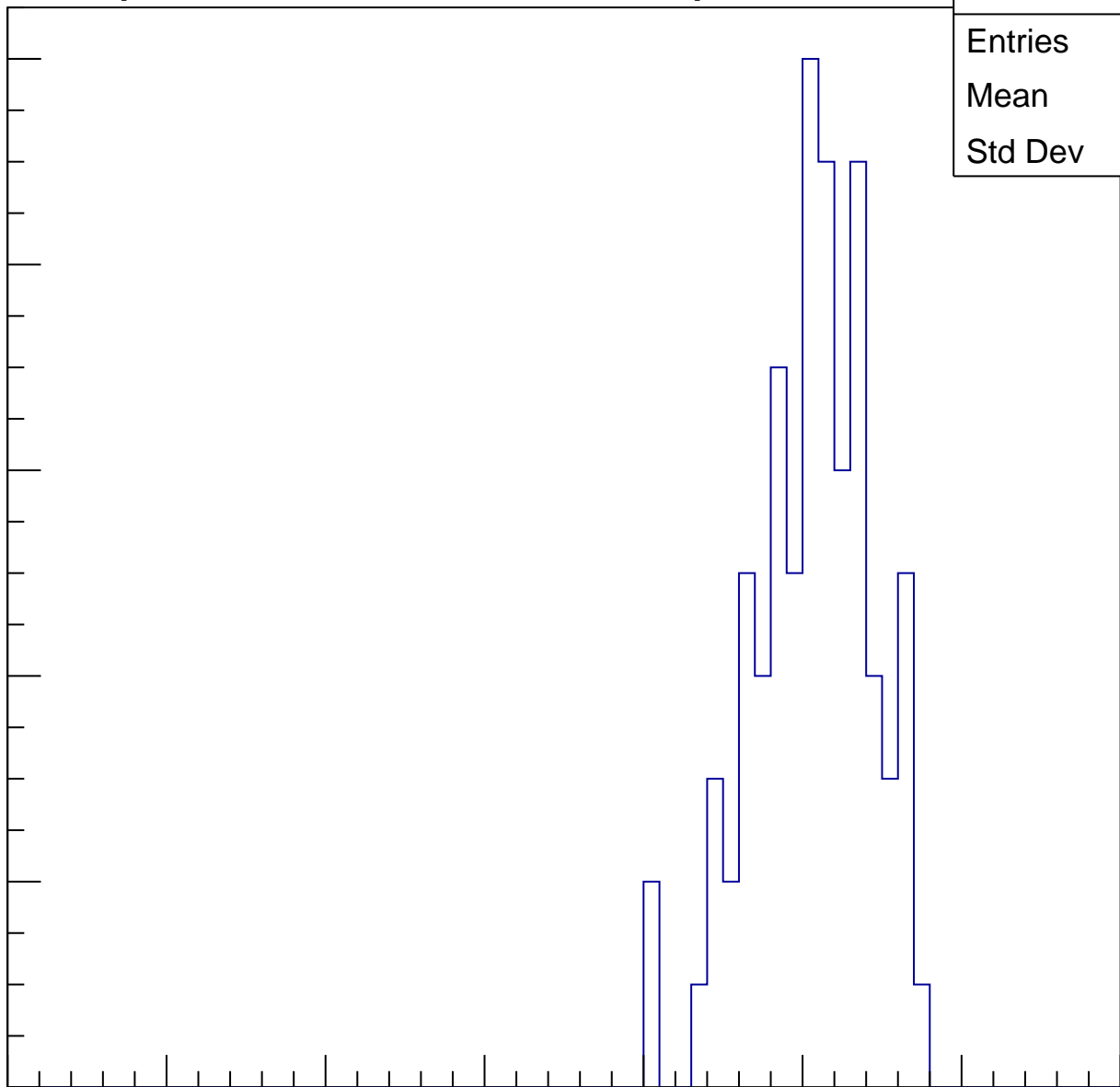
Entries	76
Mean	50.13
Std Dev	3.693

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

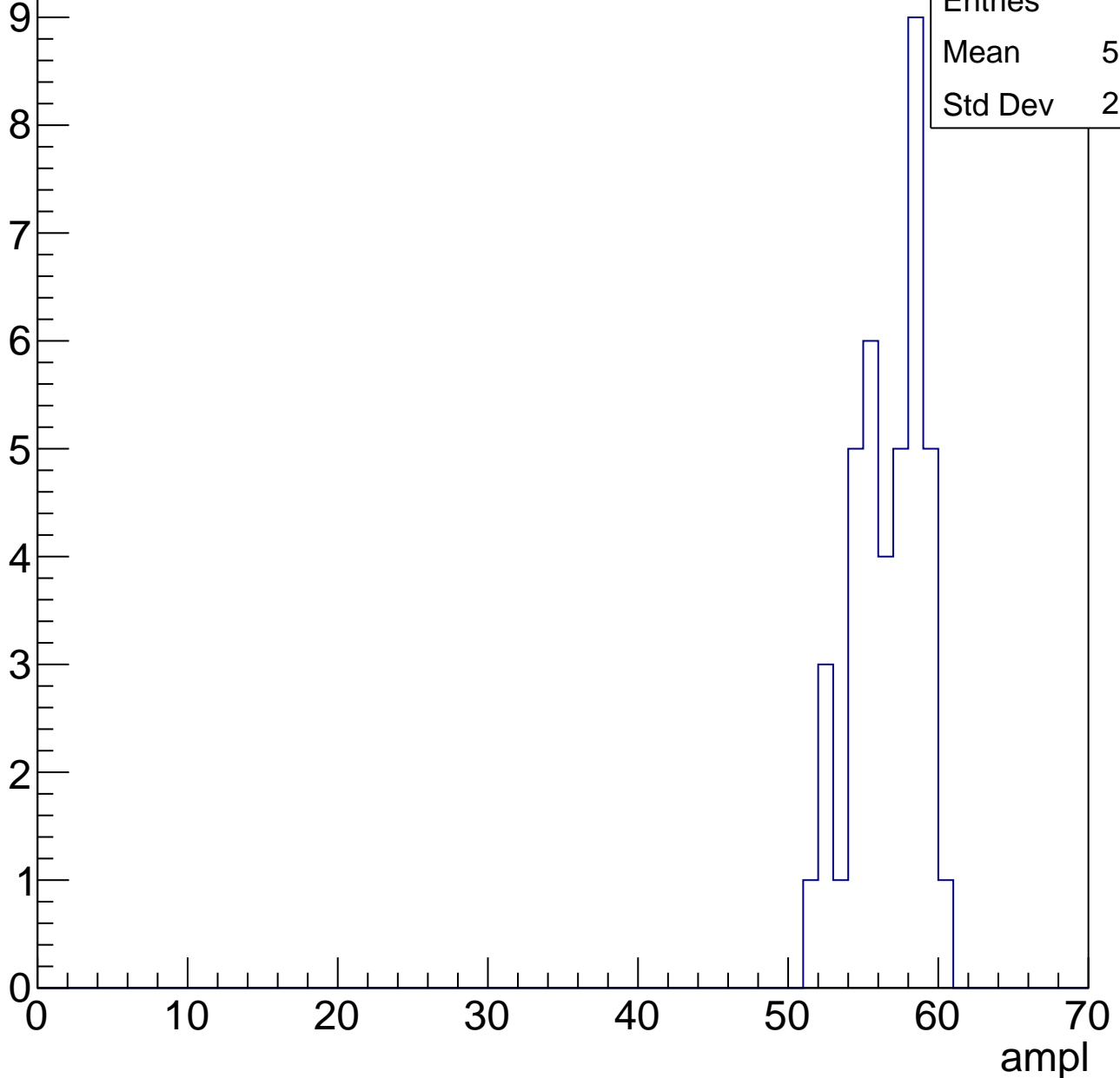


B1L003S, U16-ch113, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	56.15
Std Dev	2.286



B1L003S, U16-ch113, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	47
Mean	59.04
Std Dev	8.977

Entry

10

8

6

4

2

0

0

10

20

30

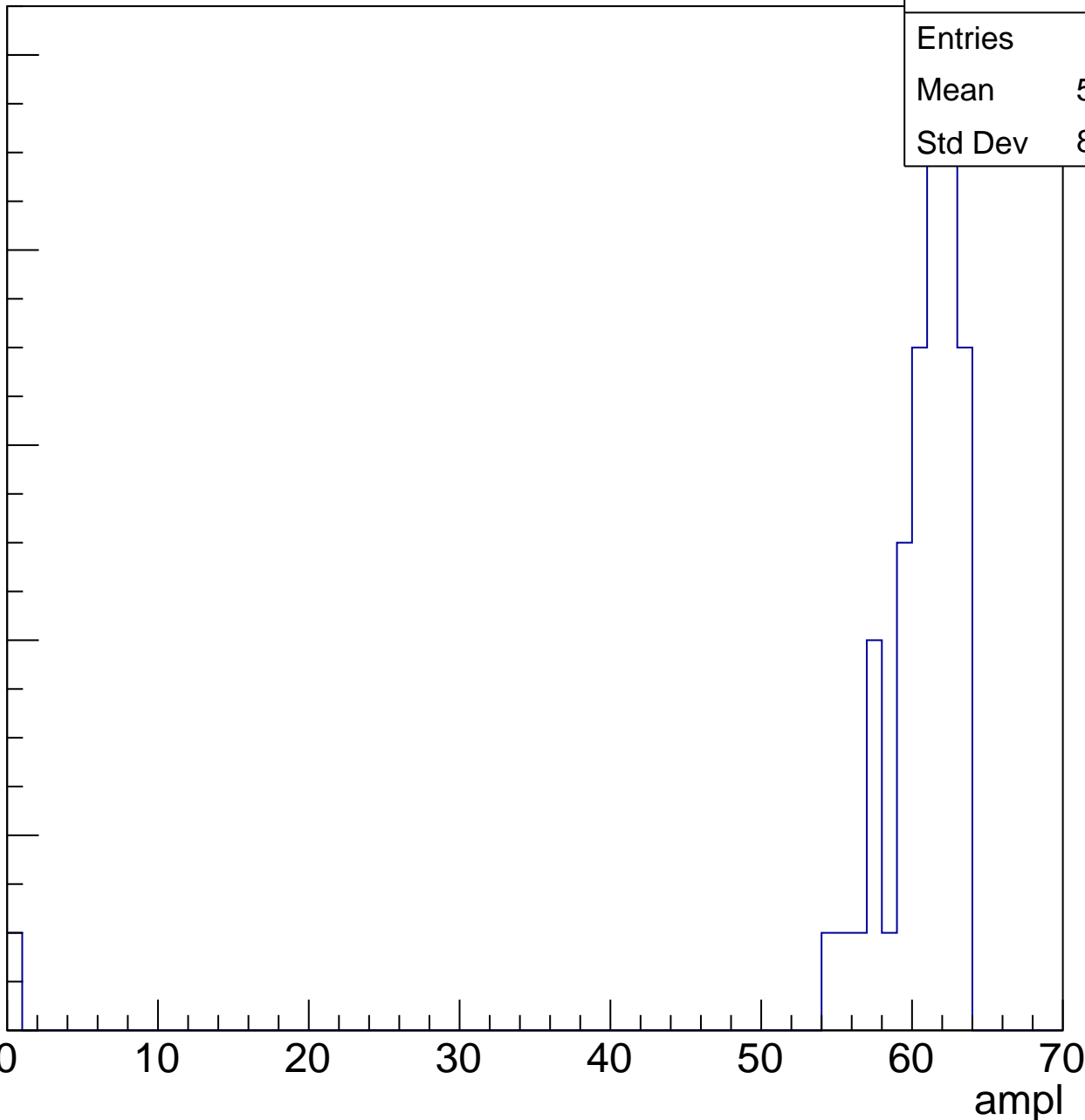
40

50

60

70

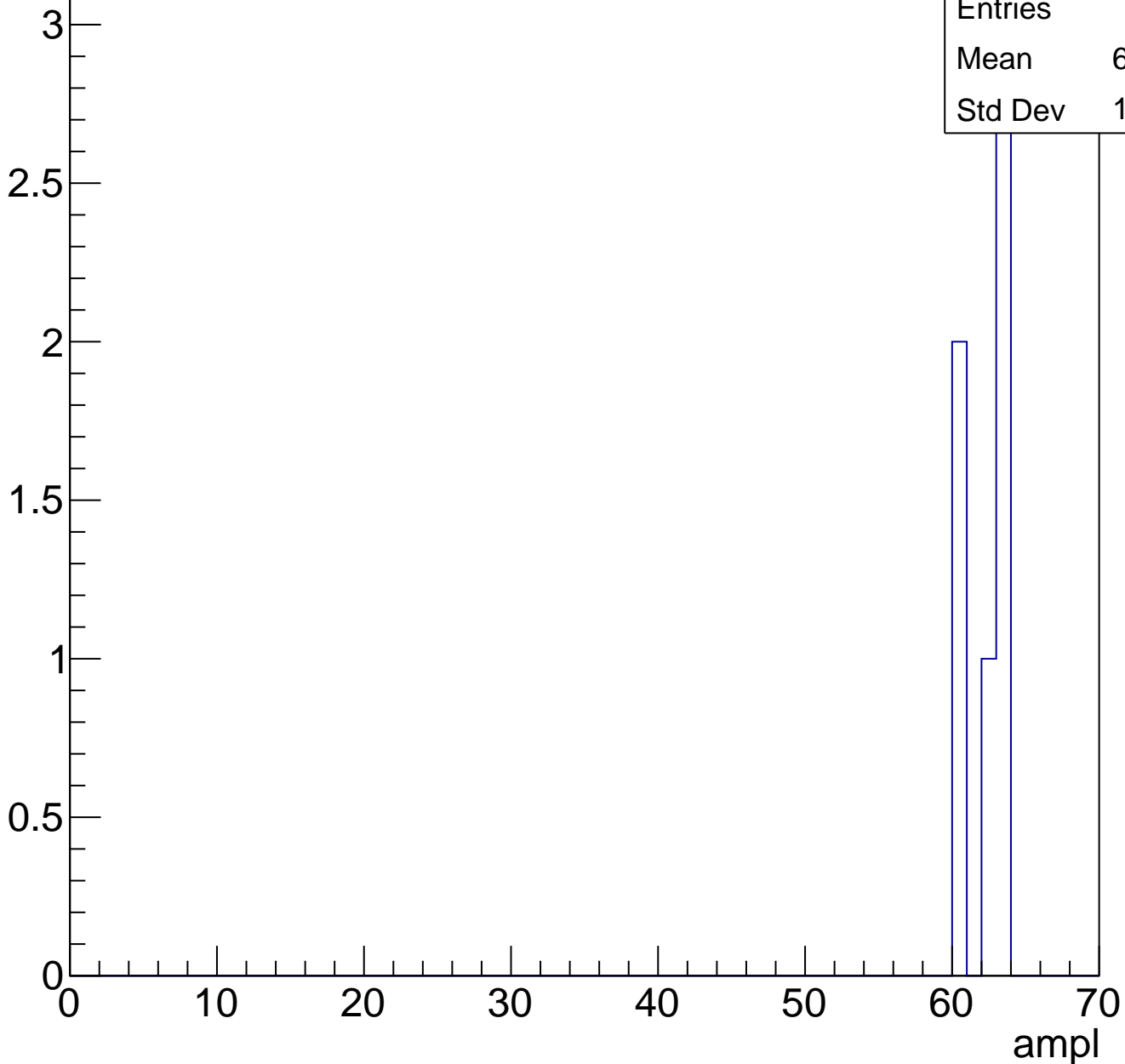
ampl



B1L003S, U16-ch113, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch113, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch114, adc0

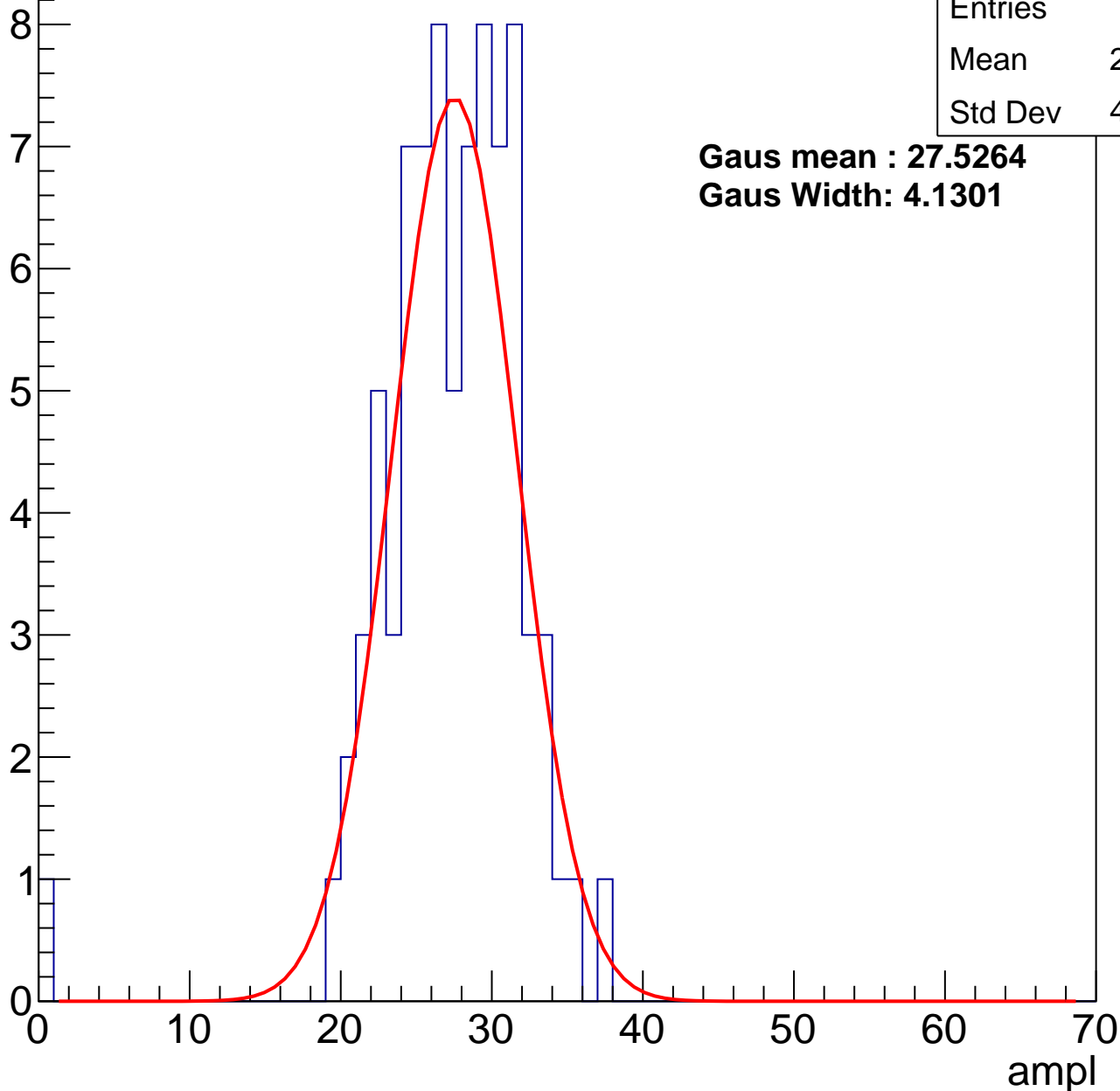
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	26.84
Std Dev	4.837

Gaus mean : 27.5264

Gaus Width: 4.1301



B1L003S, U16-ch114, adc1

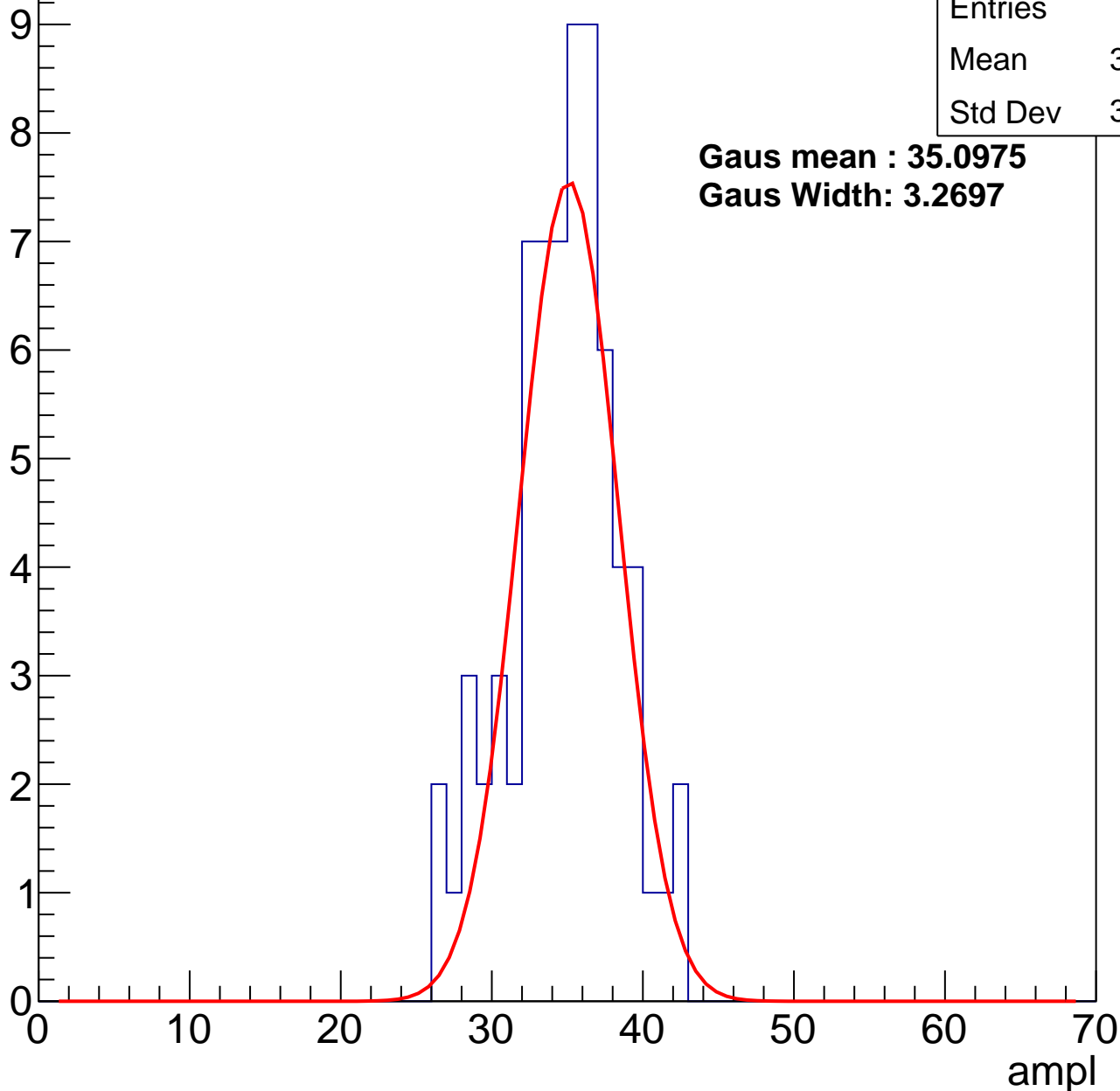
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	34.29
Std Dev	3.598

Gaus mean : 35.0975

Gaus Width: 3.2697



B1L003S, U16-ch114, adc2

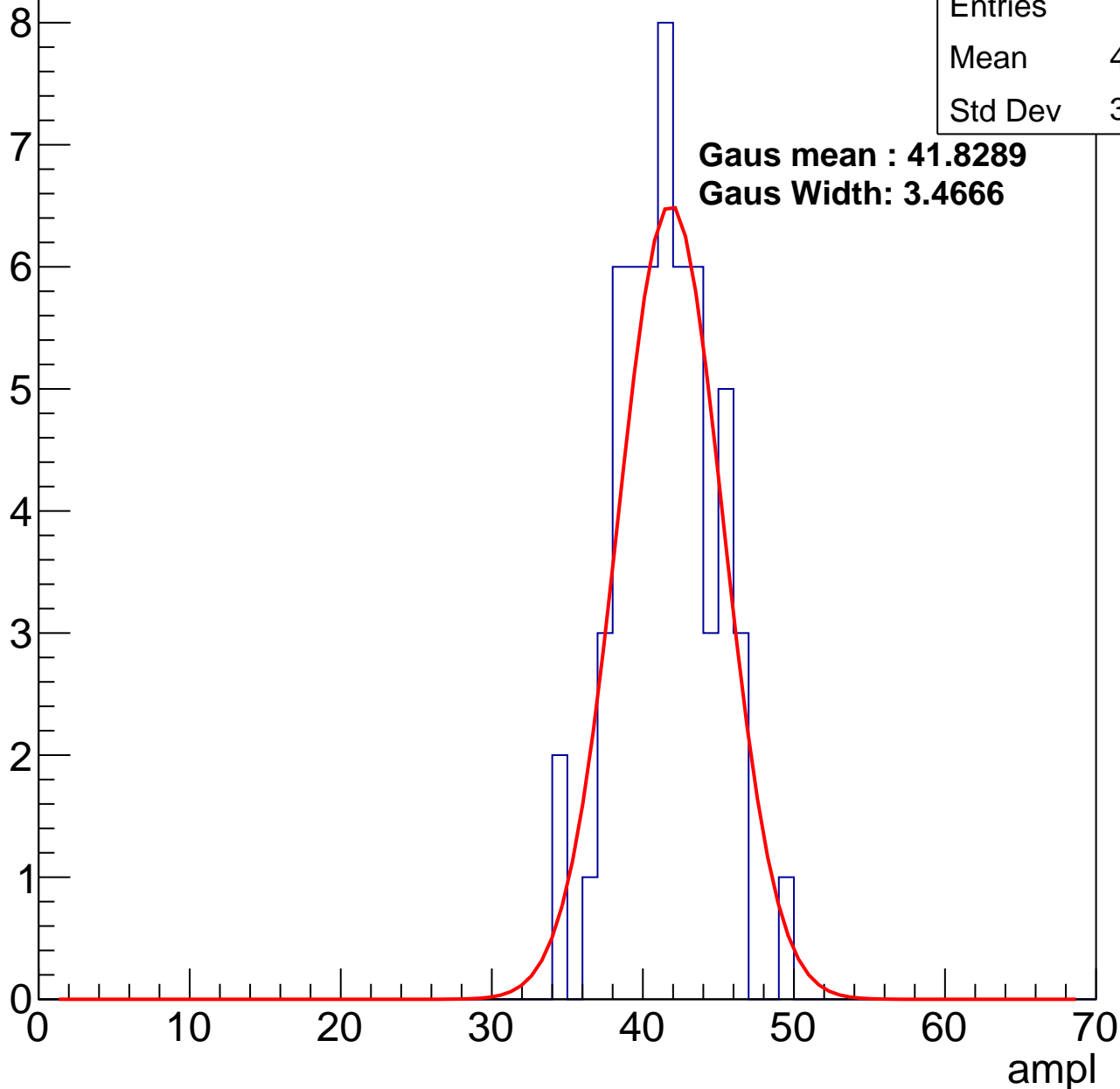
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.05
Std Dev	3.079

Gaus mean : 41.8289

Gaus Width: 3.4666

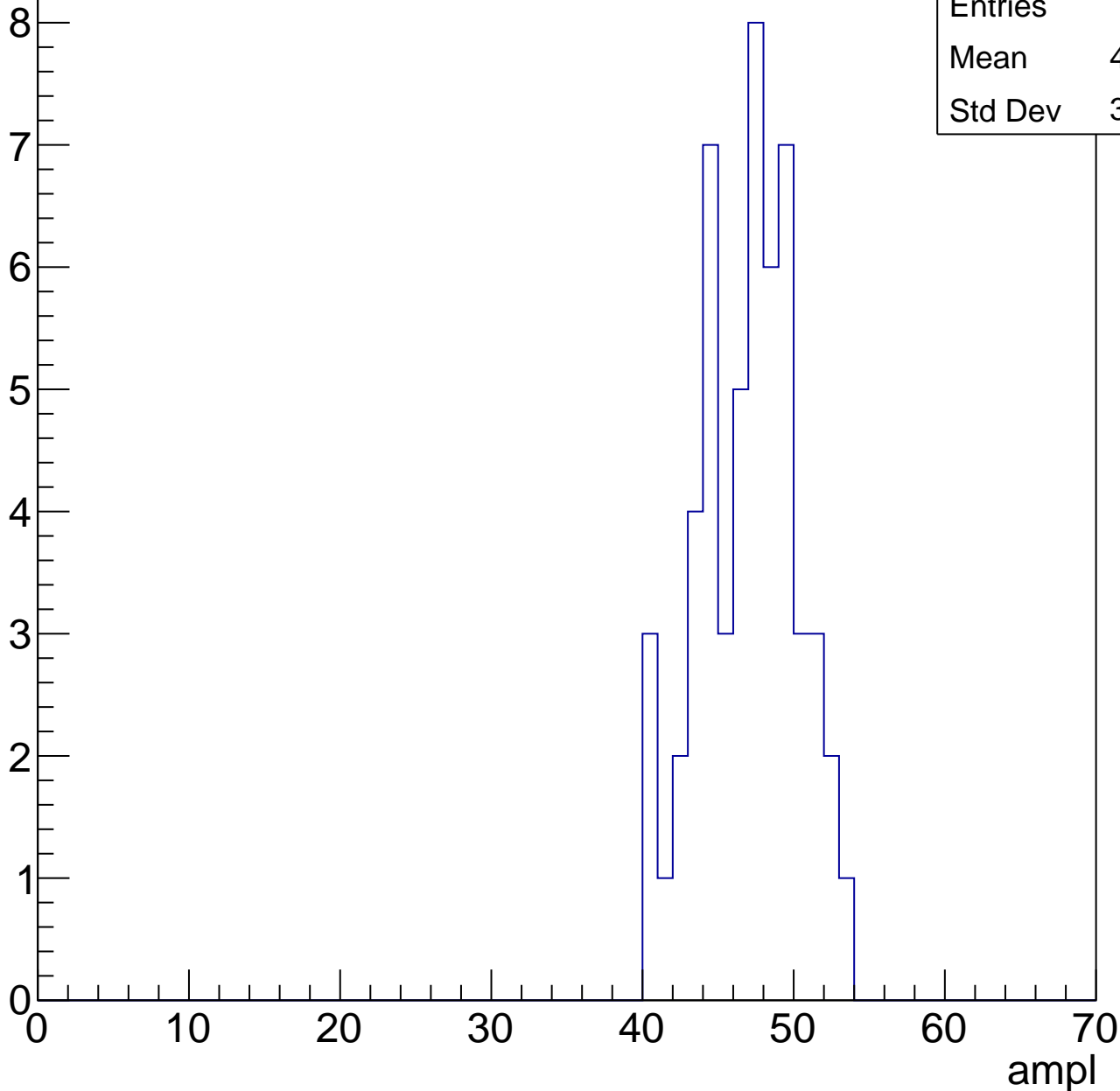


B1L003S, U16-ch114, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

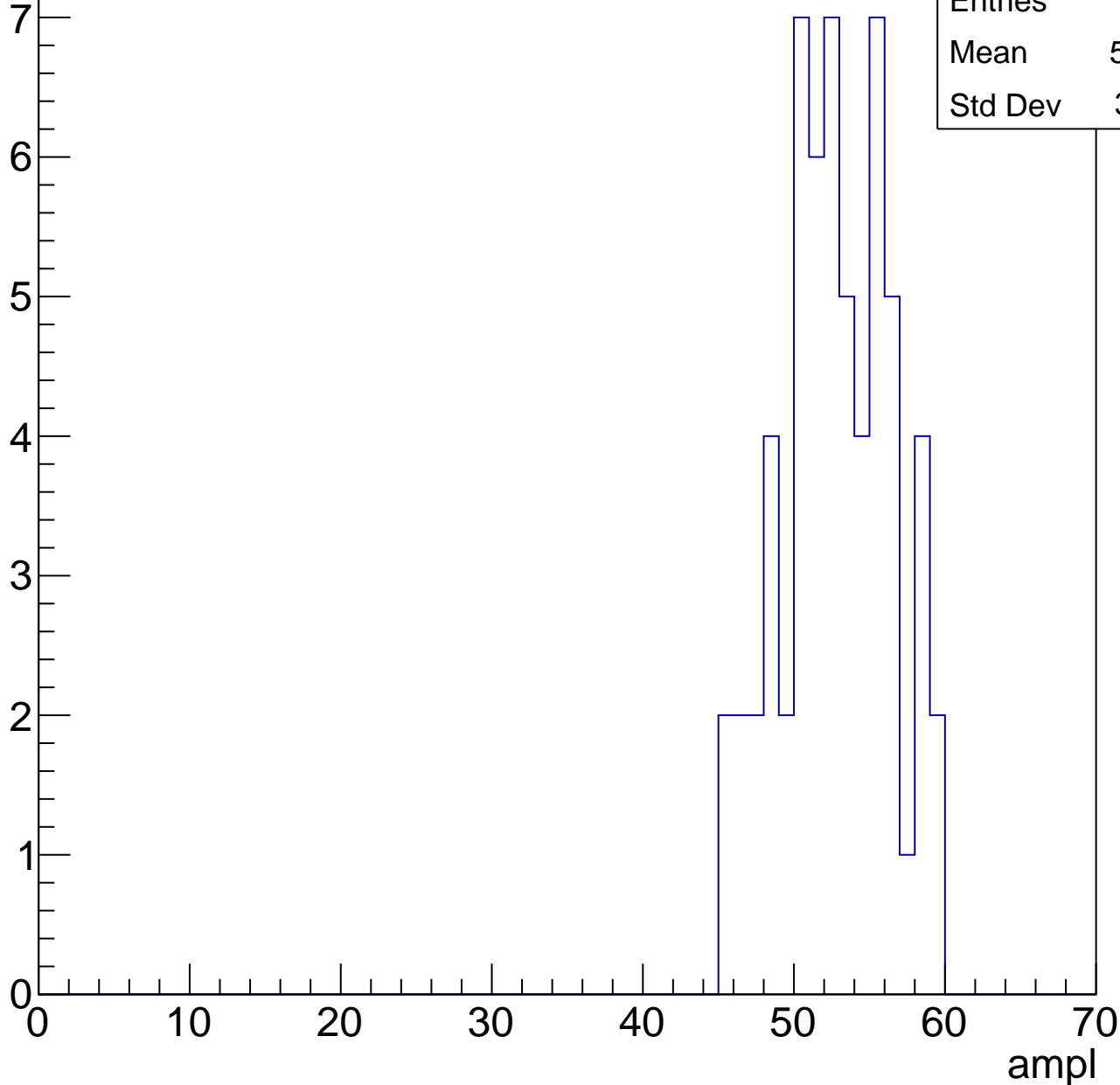
Entries	55
Mean	46.49
Std Dev	3.184



B1L003S, U16-ch114, adc4

calib_packv5_042523_0143.root, FC#13, port D2

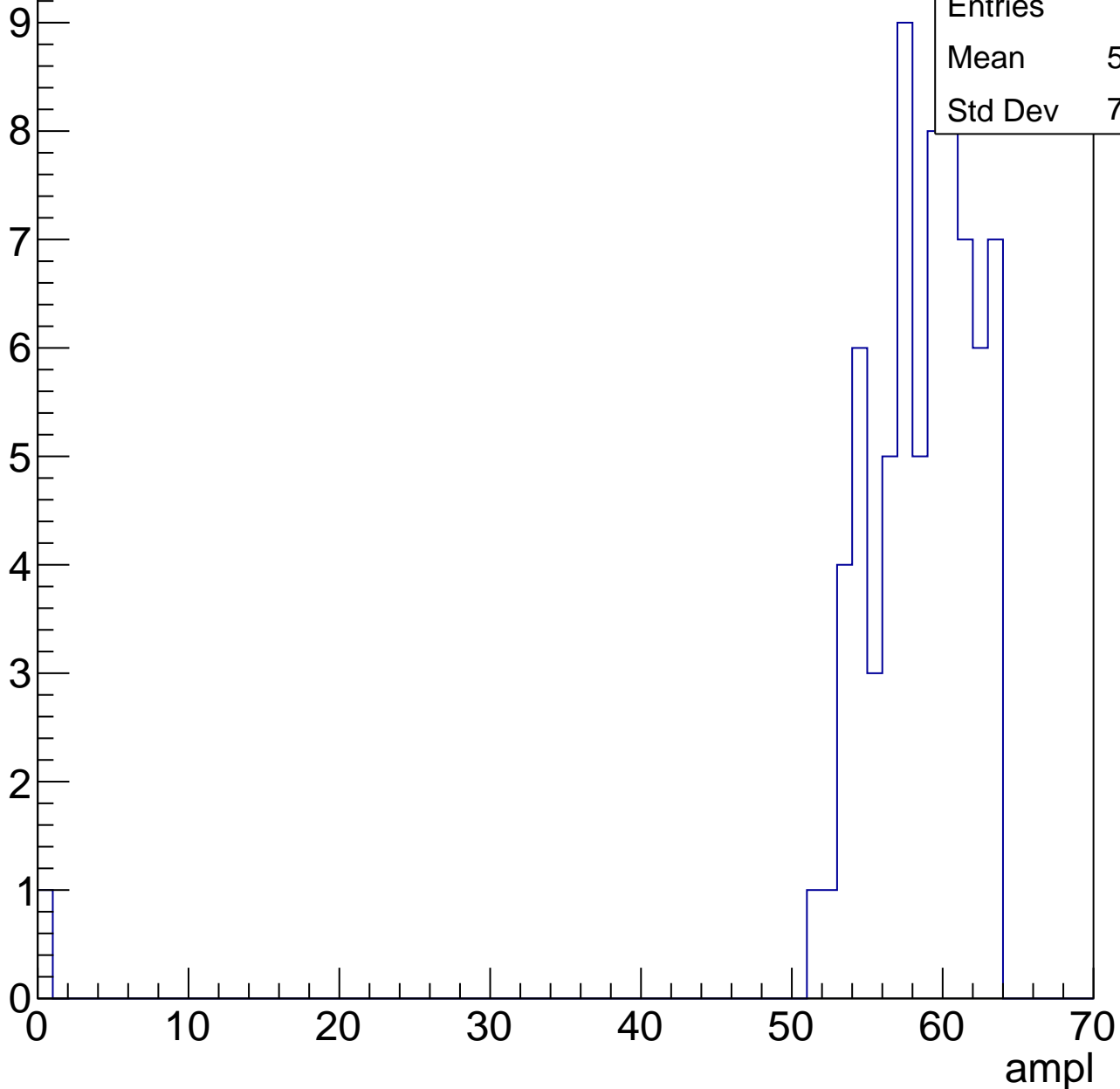
Entry



B1L003S, U16-ch114, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

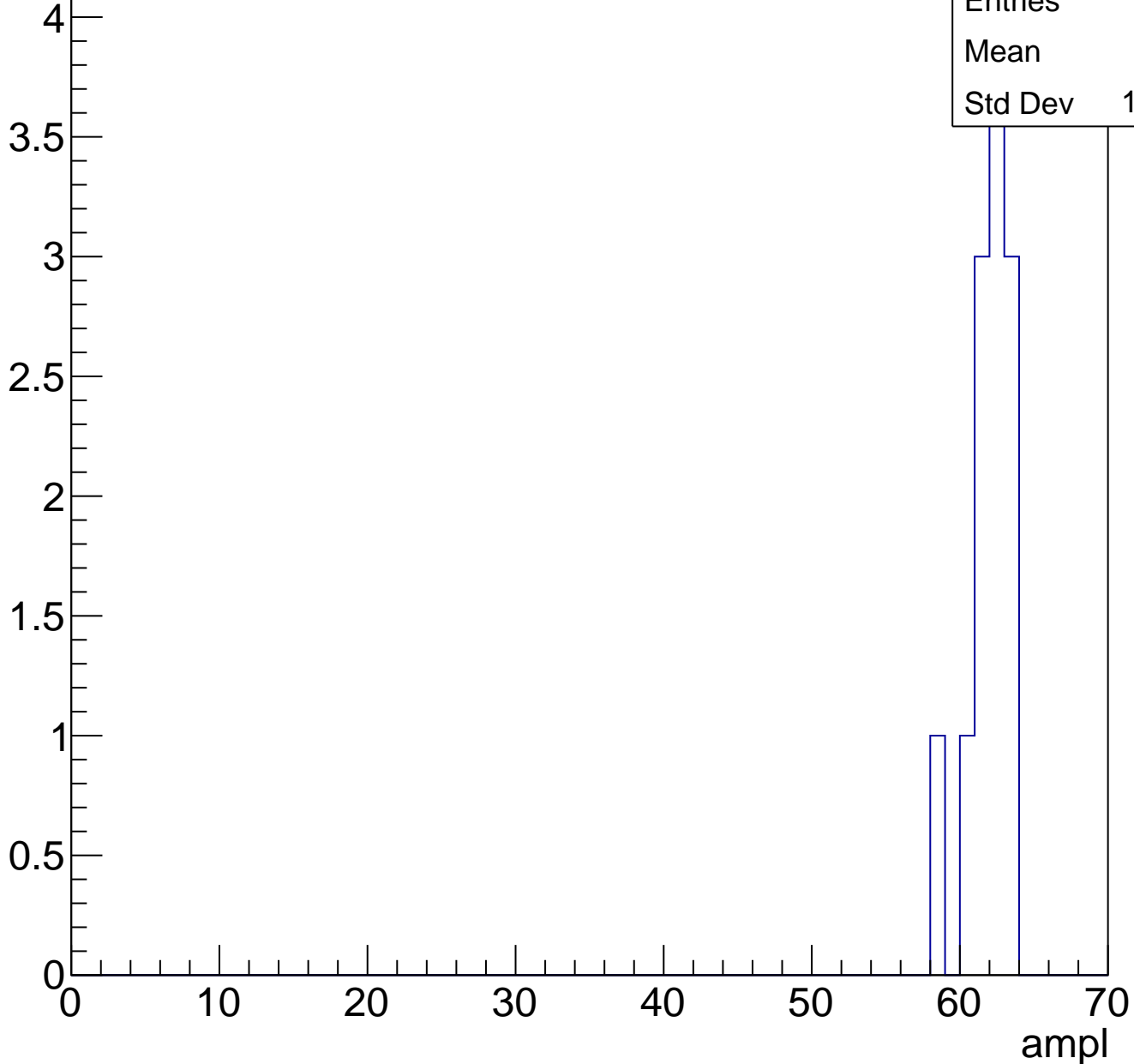


Entries	72
Mean	57.49
Std Dev	7.509

B1L003S, U16-ch114, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	12
Mean	61.5
Std Dev	1.384

B1L003S, U16-ch114, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch115, adc0

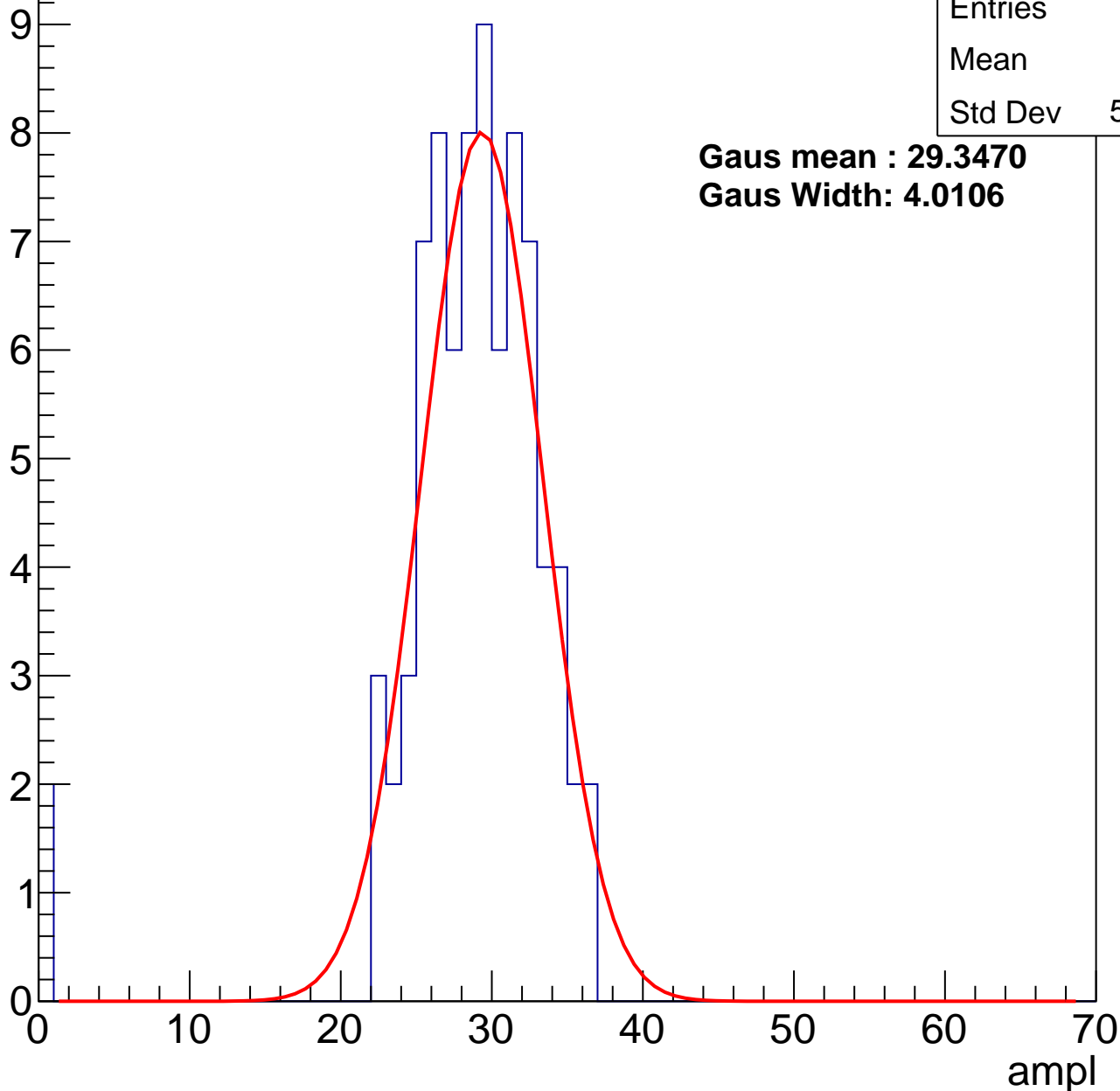
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	28.1
Std Dev	5.623

Gaus mean : 29.3470

Gaus Width: 4.0106



B1L003S, U16-ch115, adc1

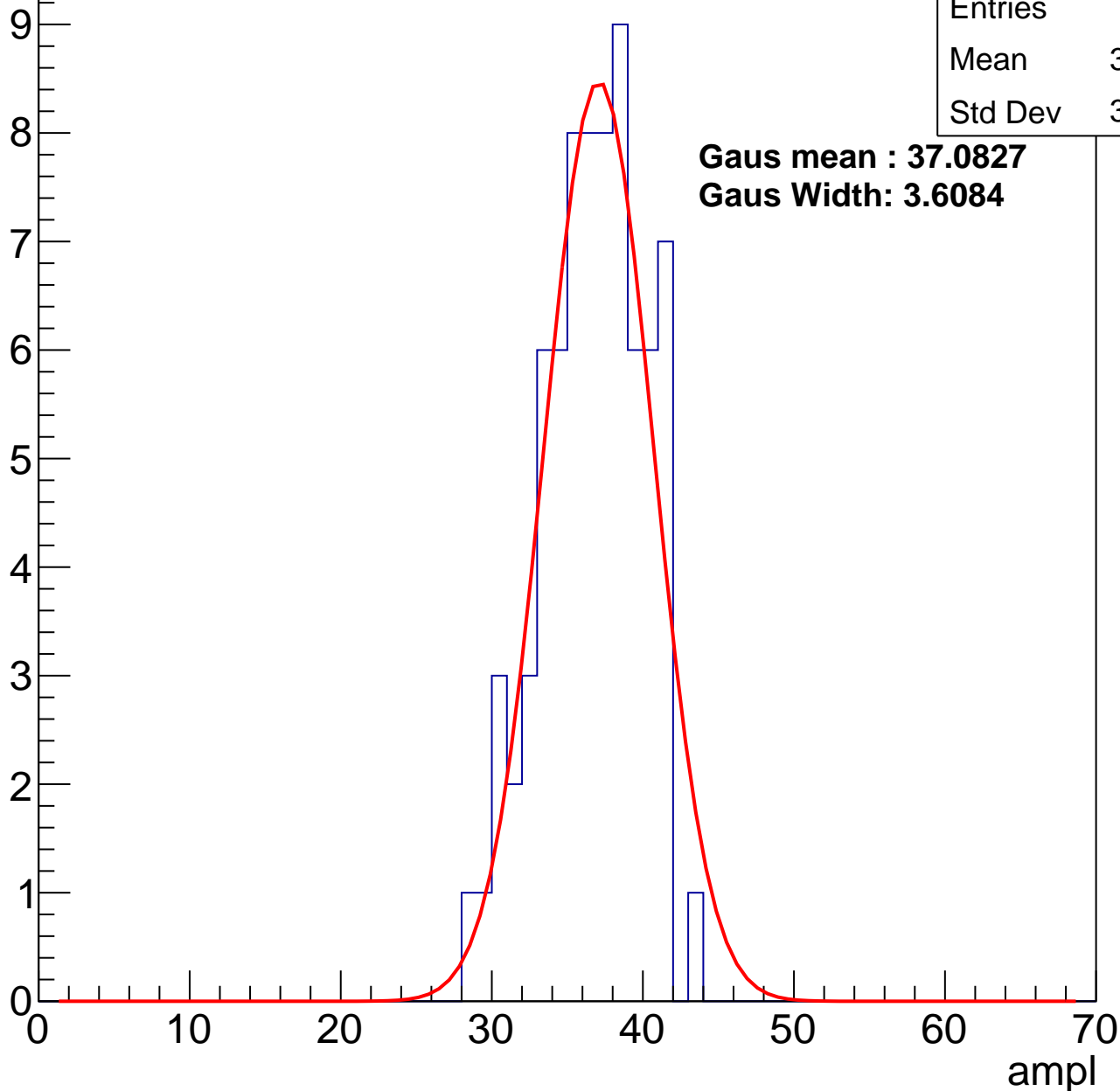
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.23
Std Dev	3.305

Gaus mean : 37.0827

Gaus Width: 3.6084



B1L003S, U16-ch115, adc2

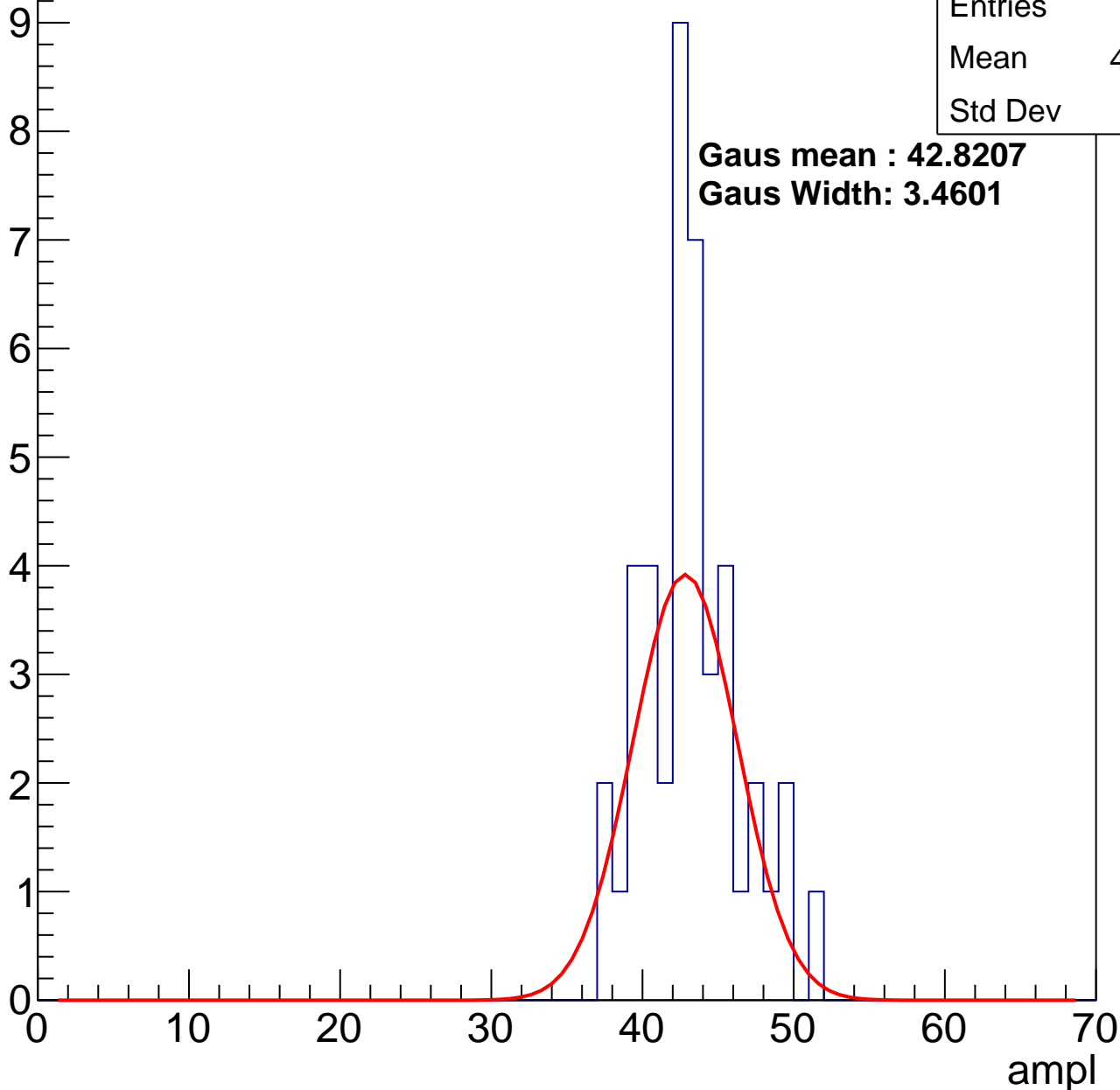
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	42.74
Std Dev	3.17

Gaus mean : 42.8207

Gaus Width: 3.4601

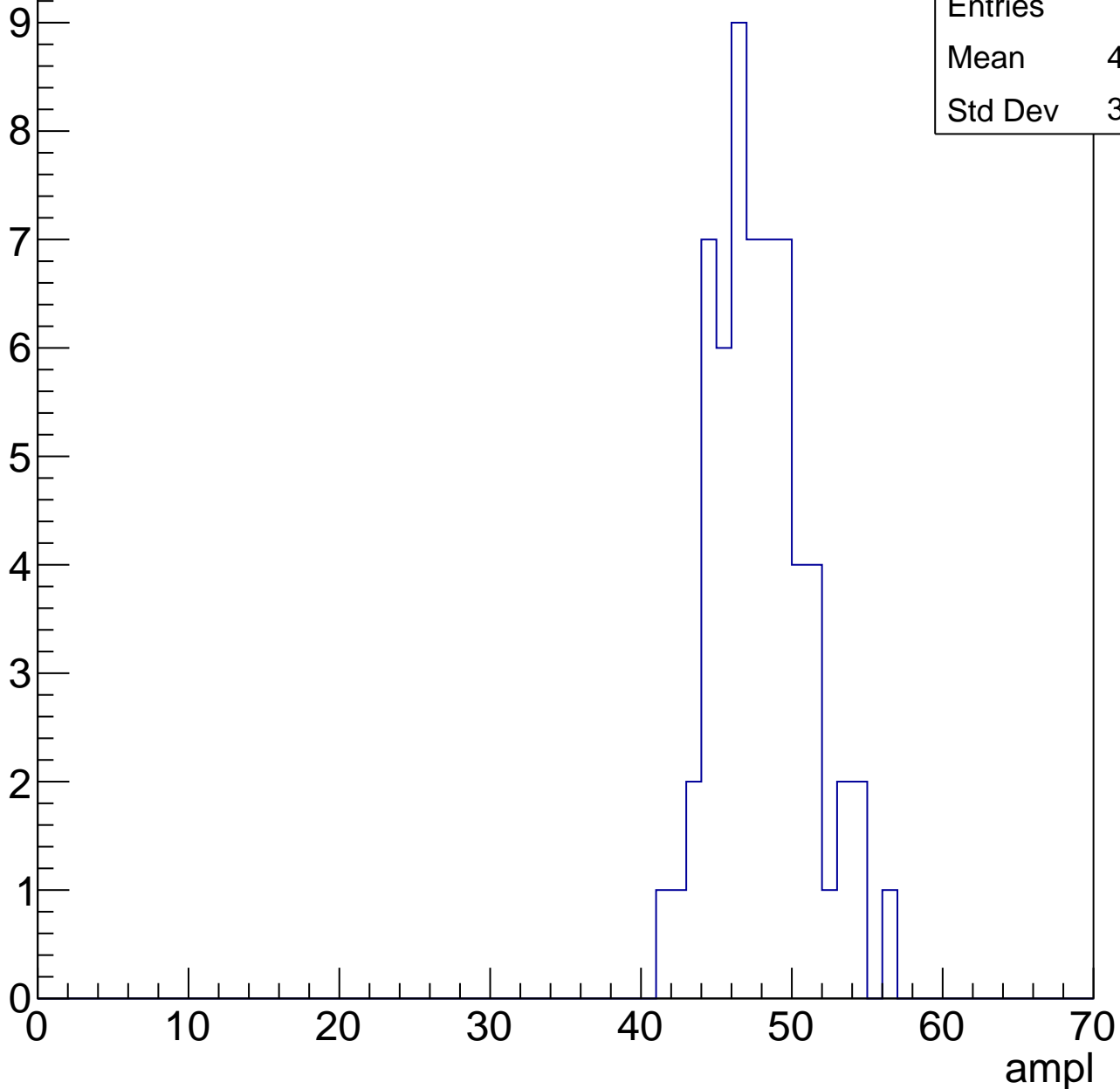


B1L003S, U16-ch115, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	47.46
Std Dev	3.102

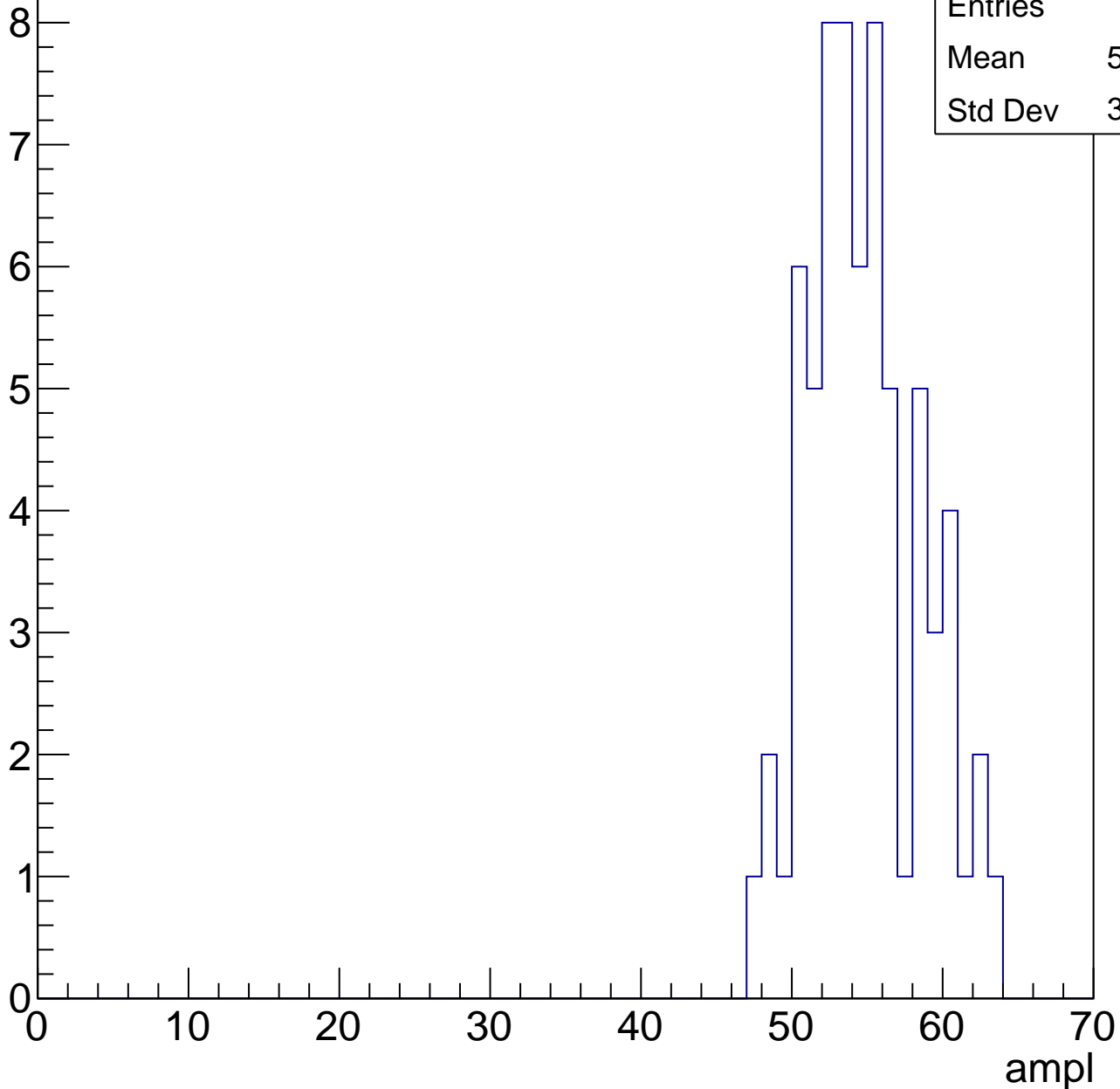


B1L003S, U16-ch115, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	54.37
Std Dev	3.689

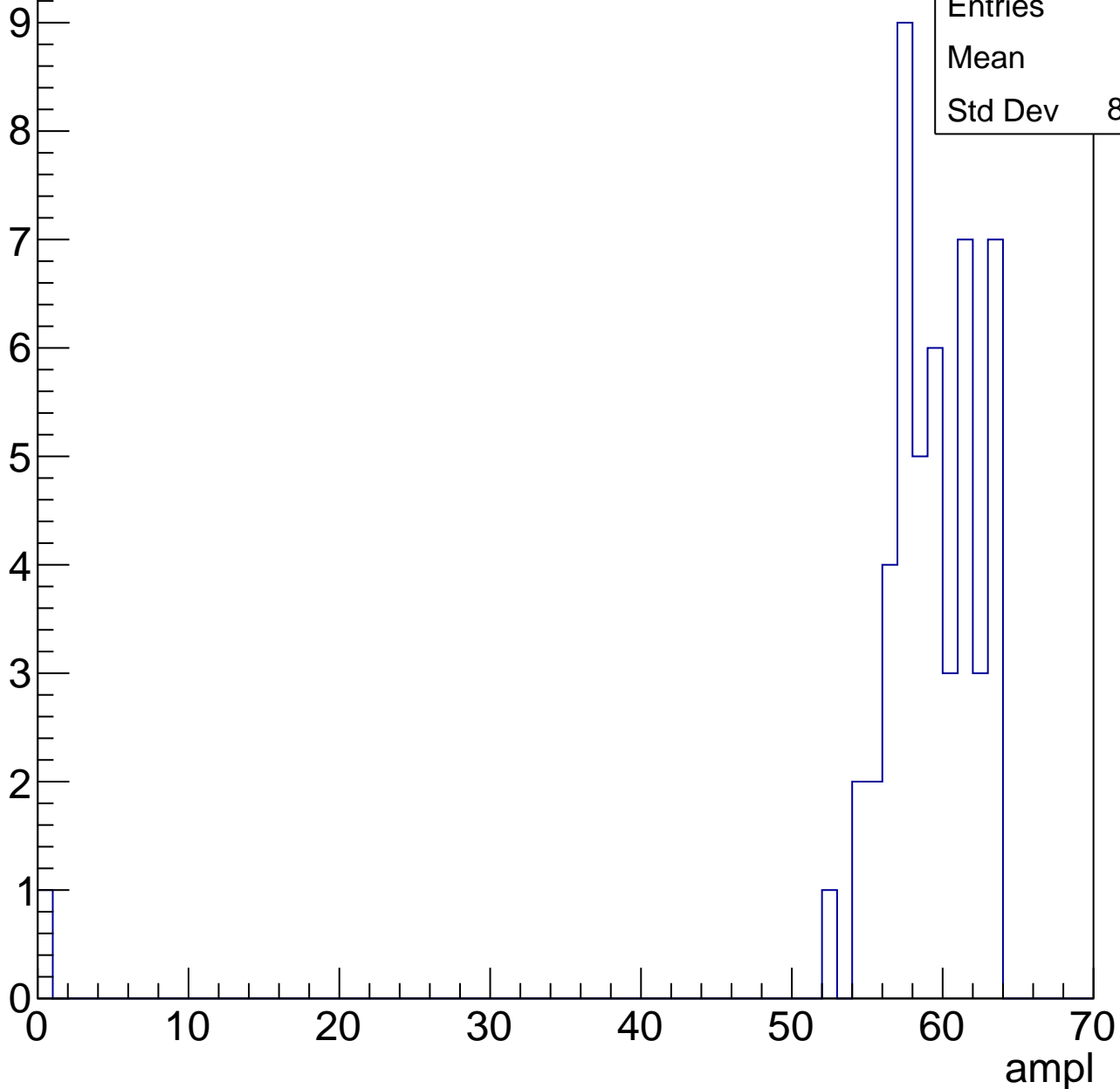


B1L003S, U16-ch115, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	57.7
Std Dev	8.688

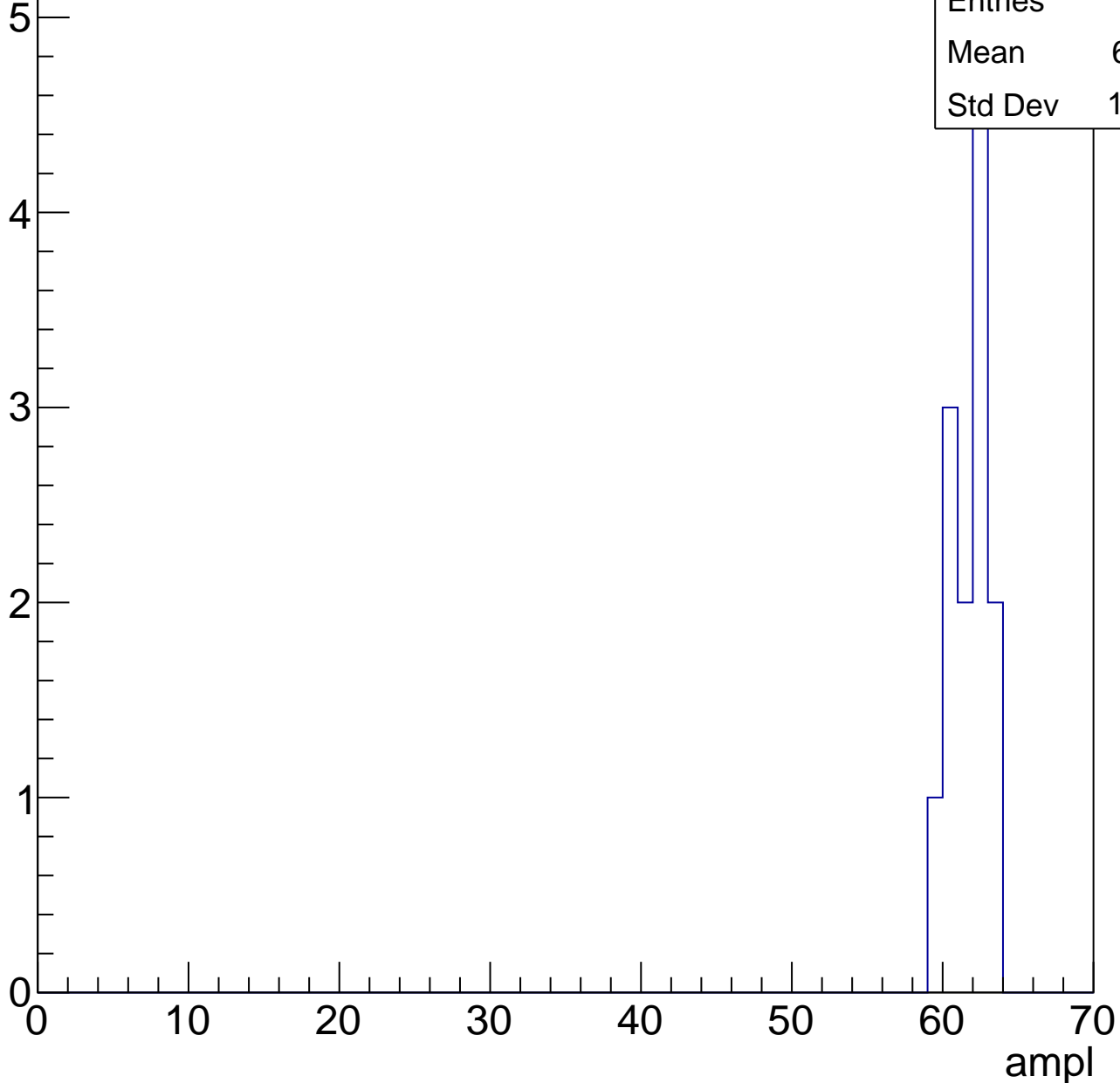


B1L003S, U16-ch115, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.31
Std Dev	1.202



B1L003S, U16-ch115, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch116, adc0

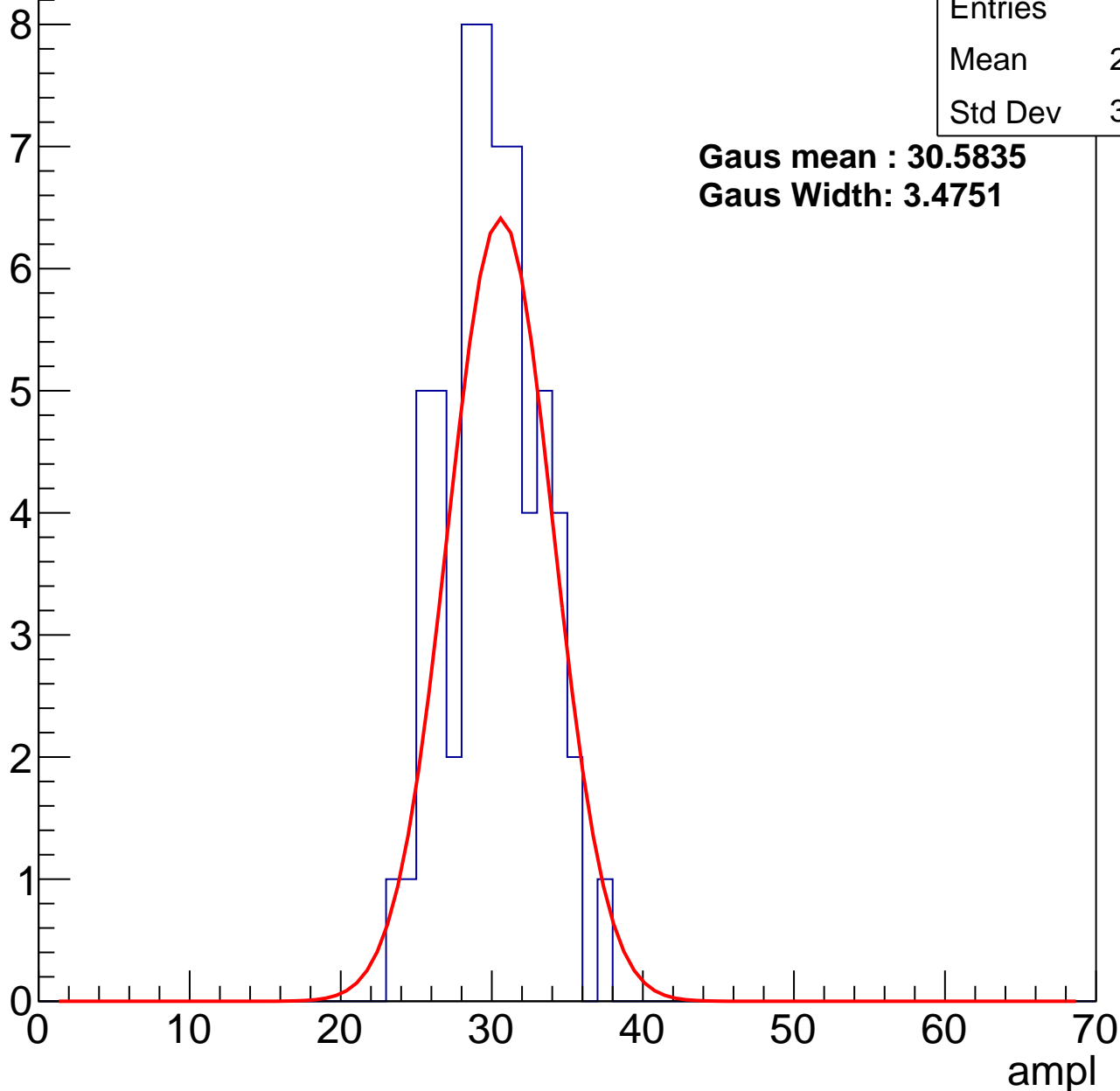
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.58
Std Dev	3.078

Gaus mean : 30.5835

Gaus Width: 3.4751



B1L003S, U16-ch116, adc1

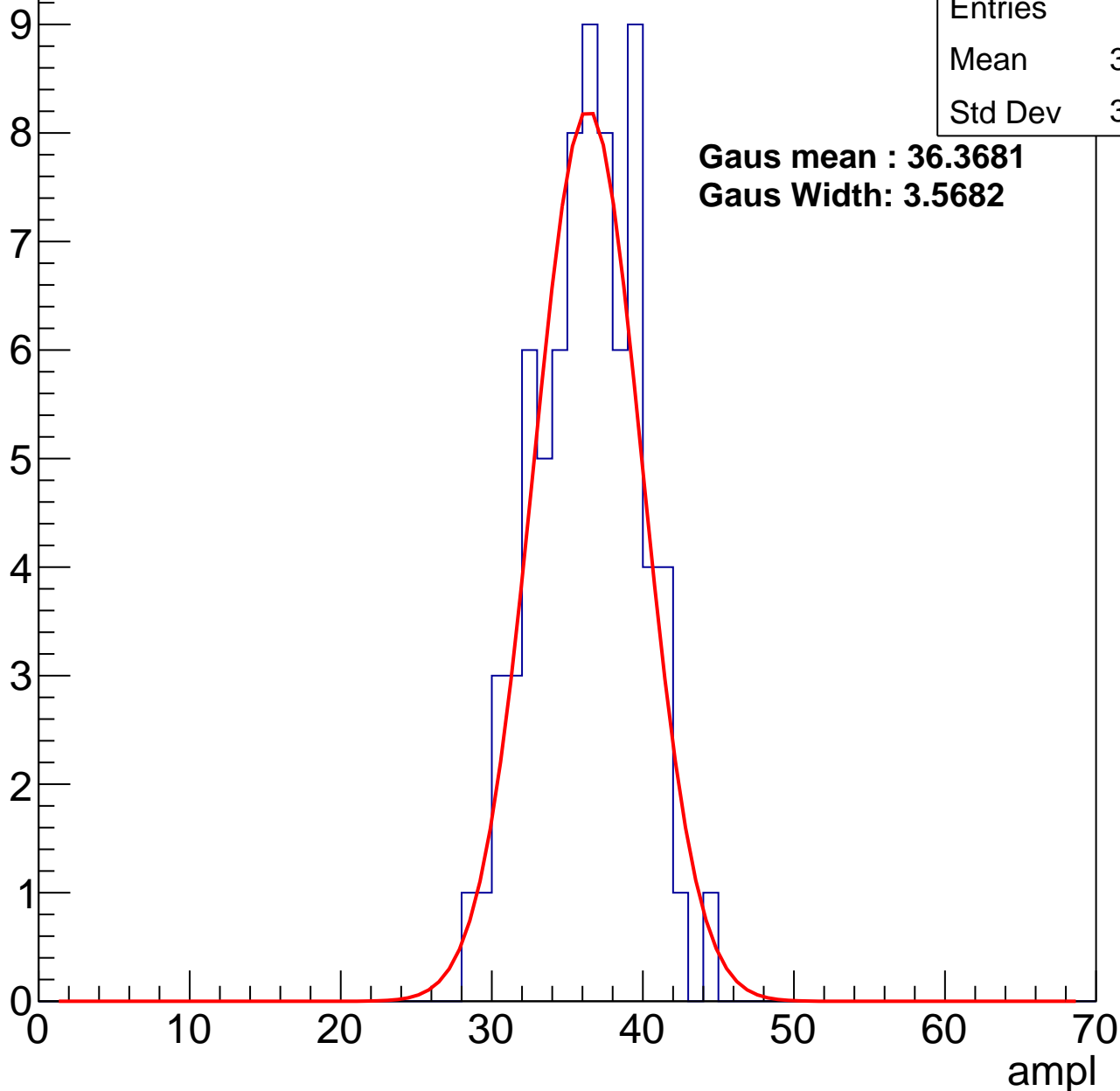
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	35.87
Std Dev	3.356

Gaus mean : 36.3681

Gaus Width: 3.5682



B1L003S, U16-ch116, adc2

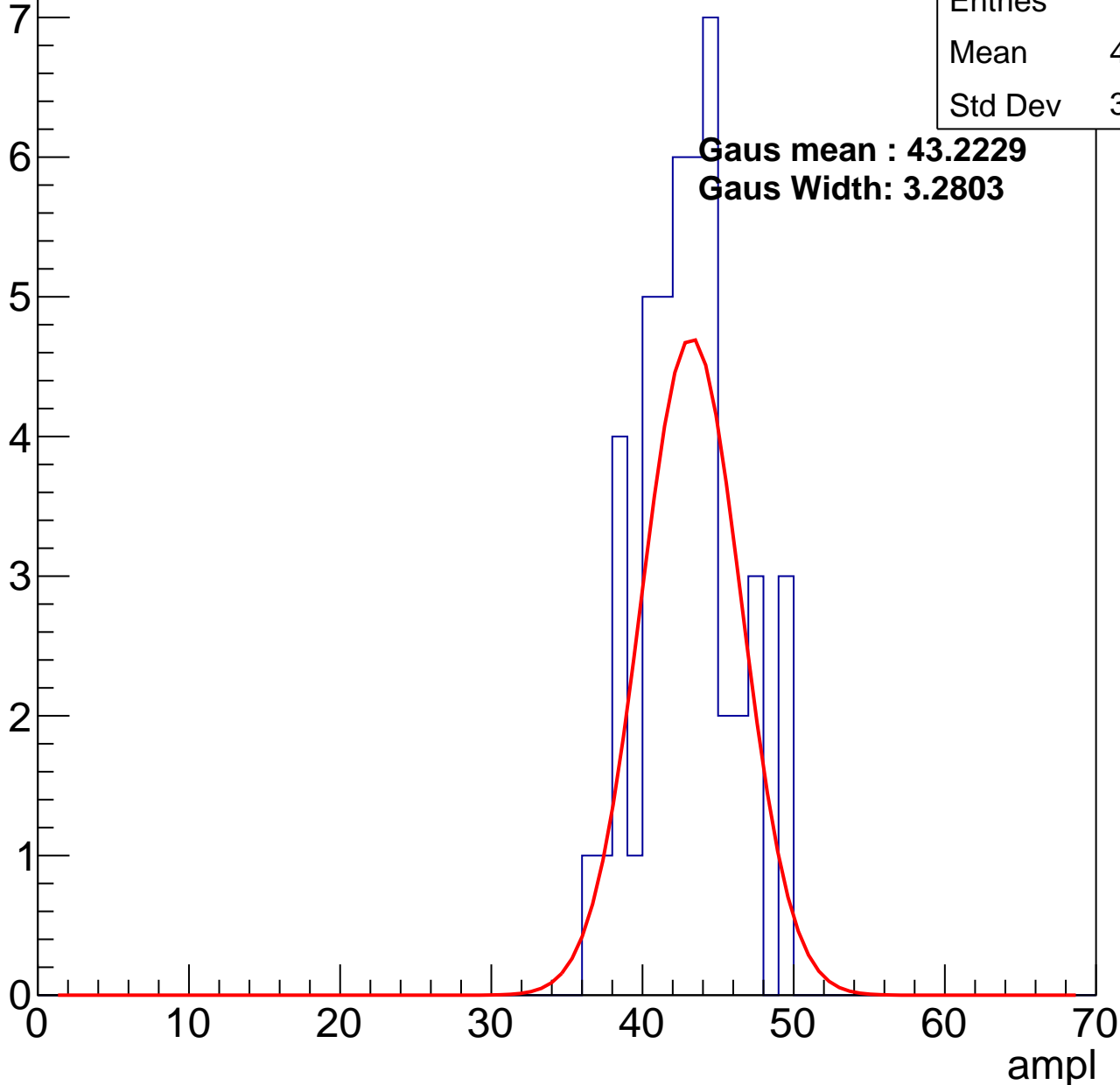
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	42.54
Std Dev	3.133

Gaus mean : 43.2229

Gaus Width: 3.2803

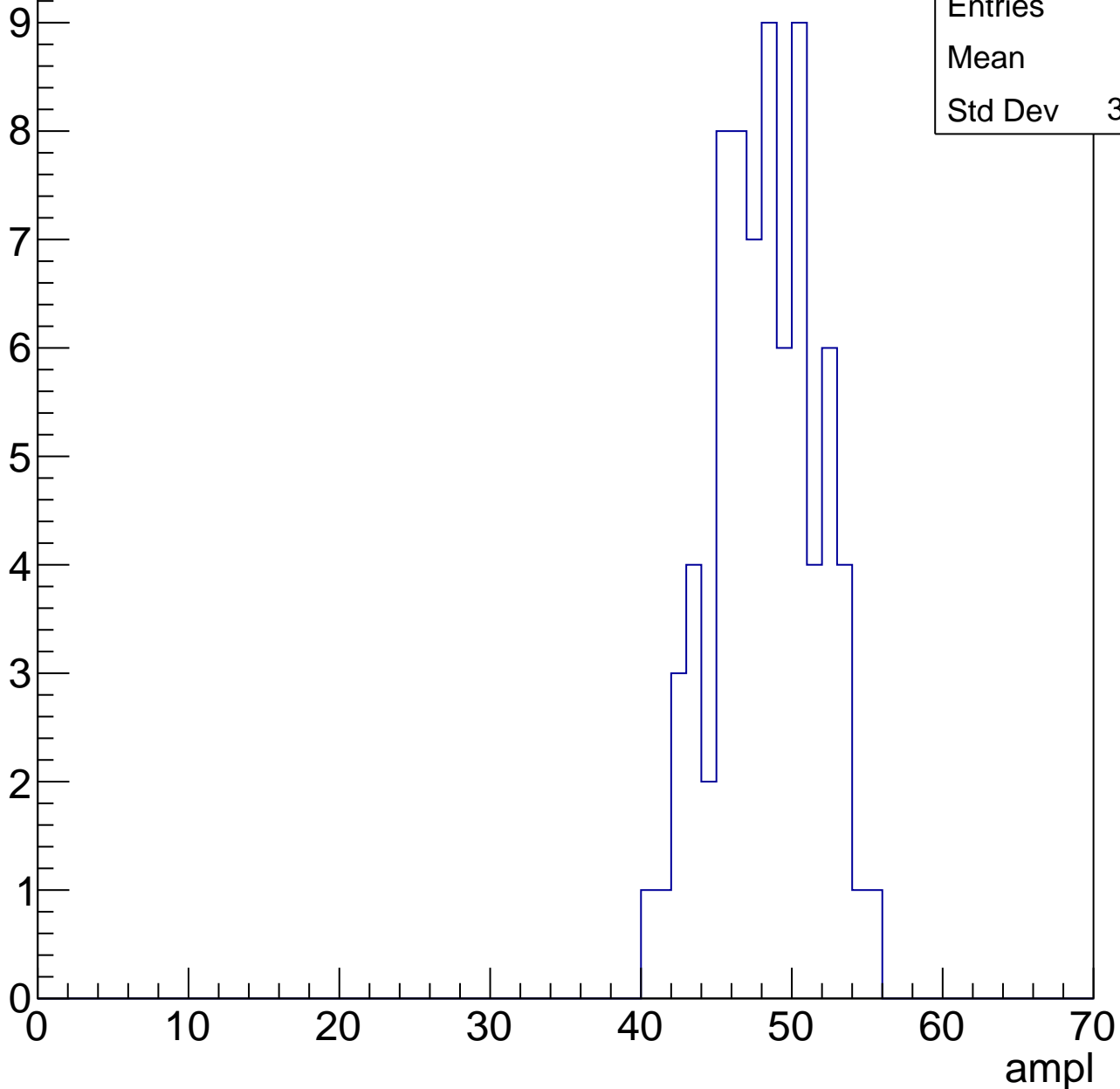


B1L003S, U16-ch116, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	47.8
Std Dev	3.329

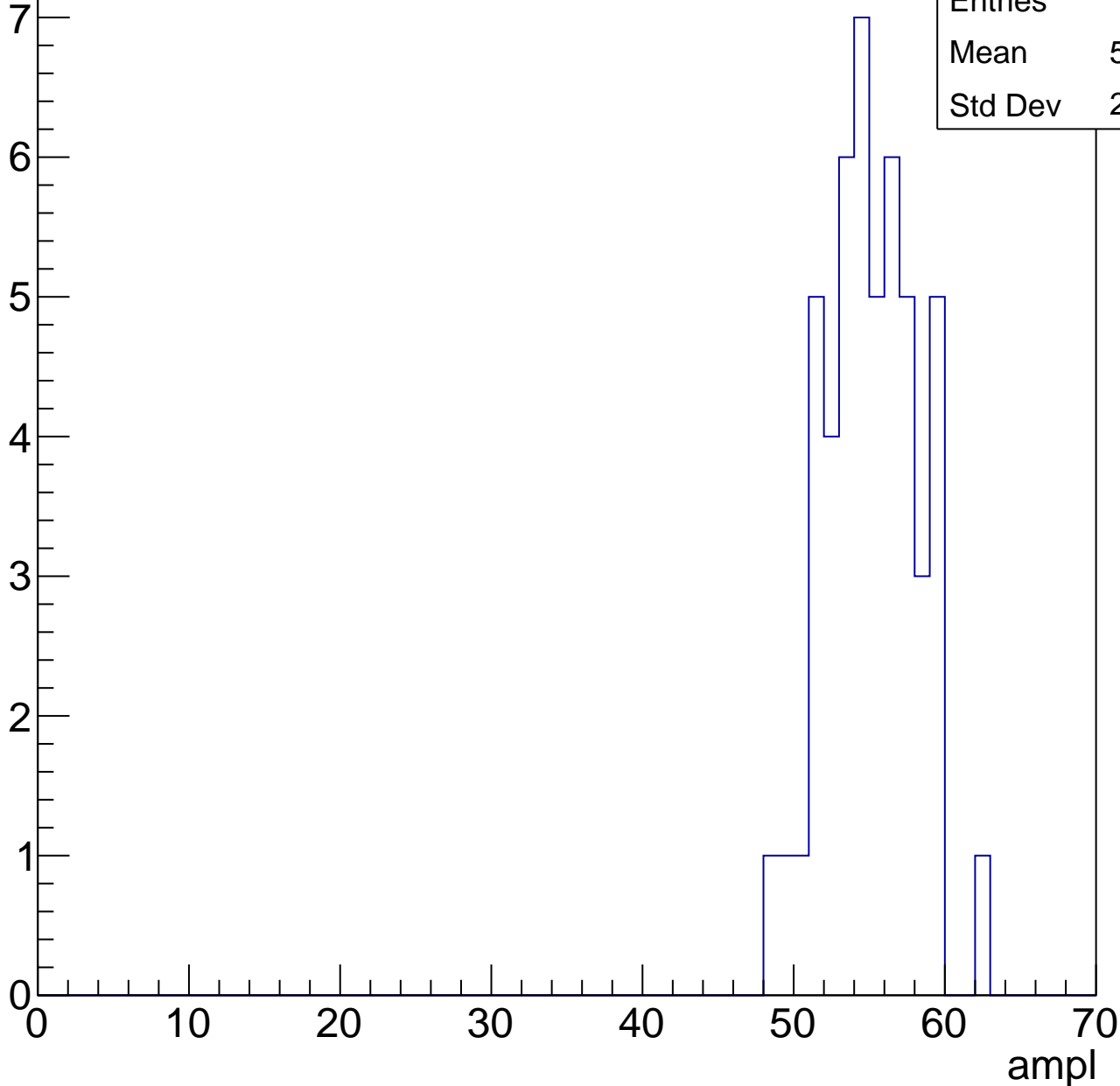


B1L003S, U16-ch116, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	54.66
Std Dev	2.944

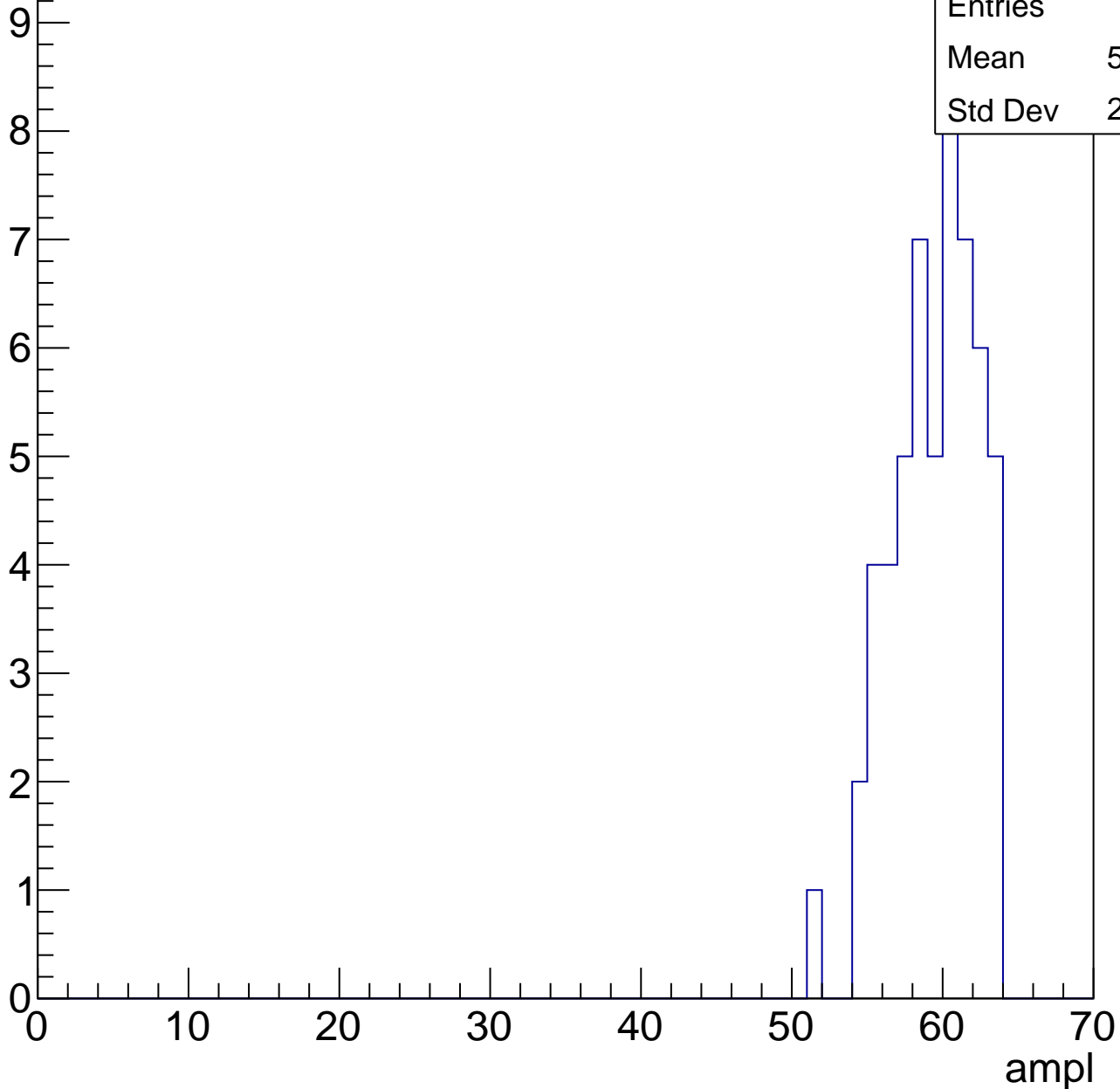


B1L003S, U16-ch116, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	58.96
Std Dev	2.737

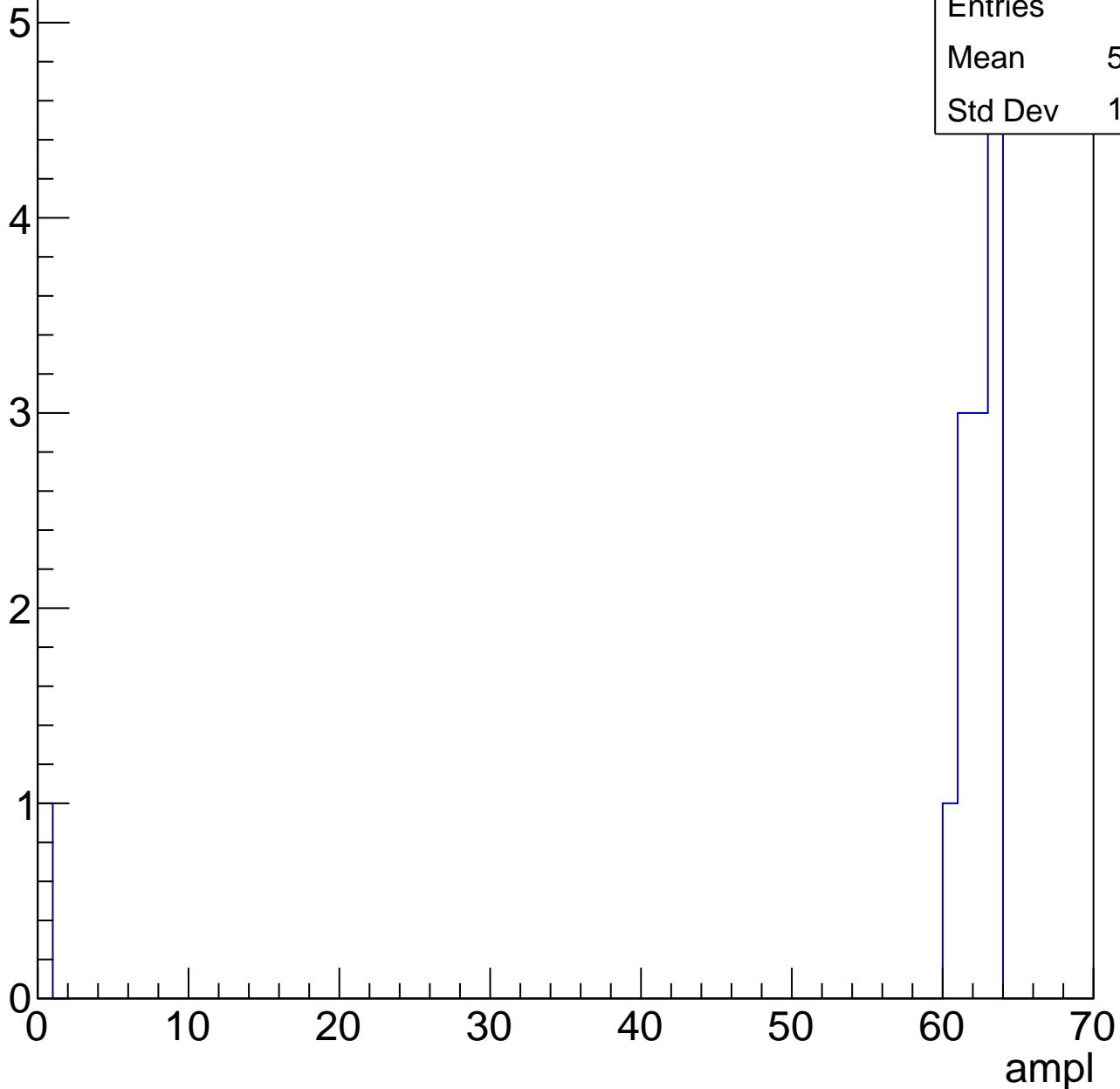


B1L003S, U16-ch116, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B1L003S, U16-ch116, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch117, adc0

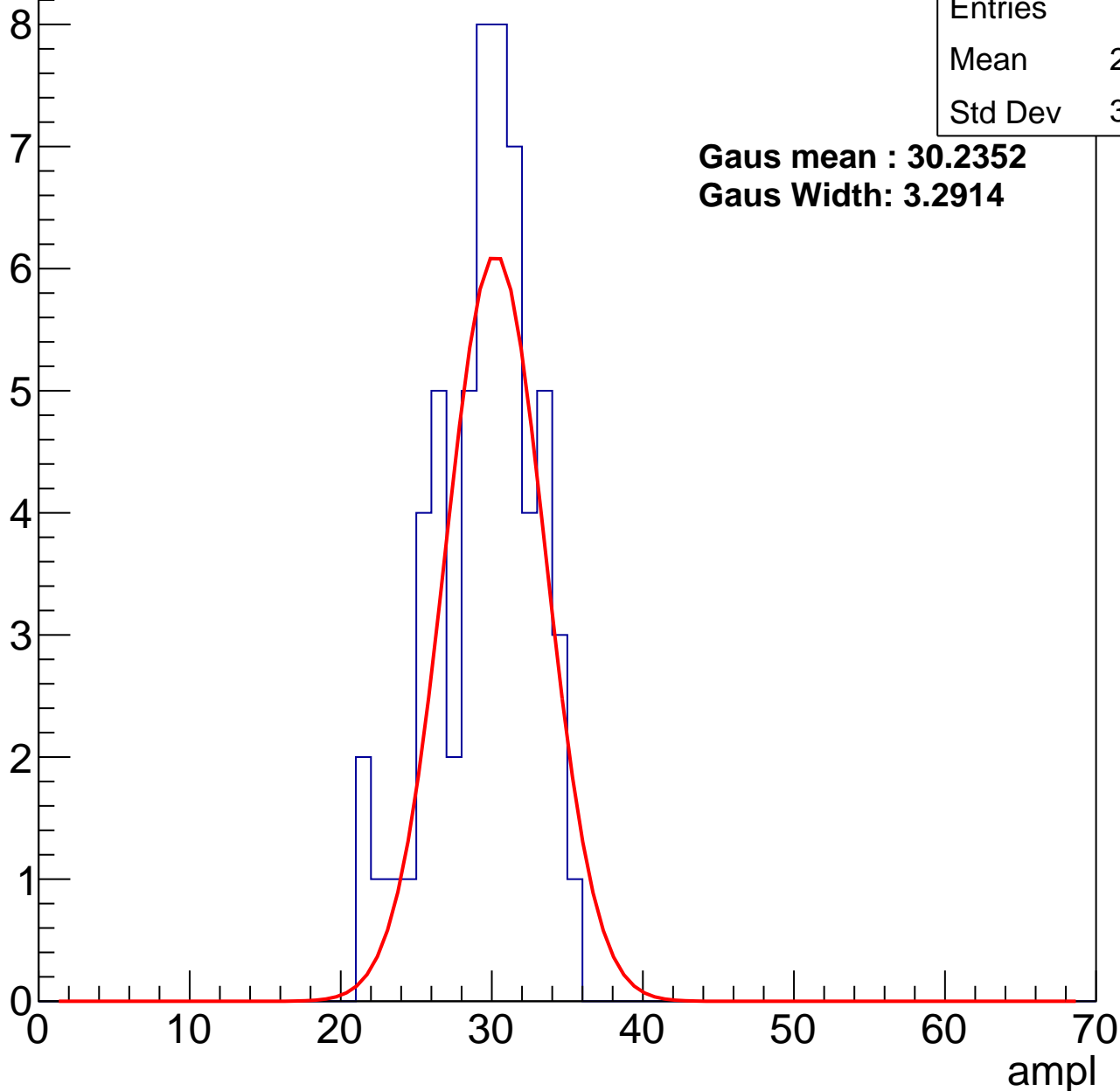
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.02
Std Dev	3.306

Gaus mean : 30.2352

Gaus Width: 3.2914



B1L003S, U16-ch117, adc1

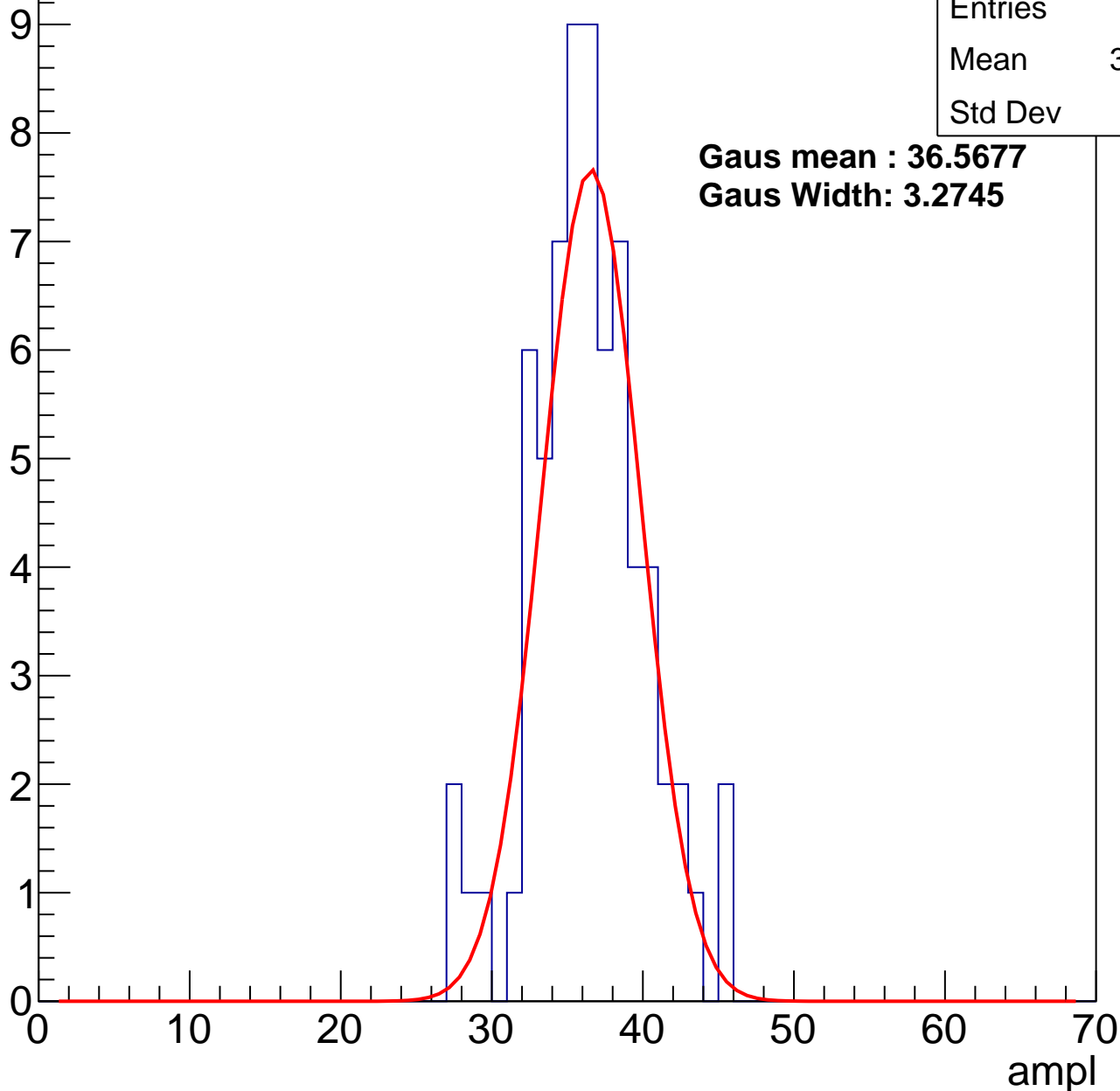
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.93
Std Dev	3.7

Gaus mean : 36.5677

Gaus Width: 3.2745



B1L003S, U16-ch117, adc2

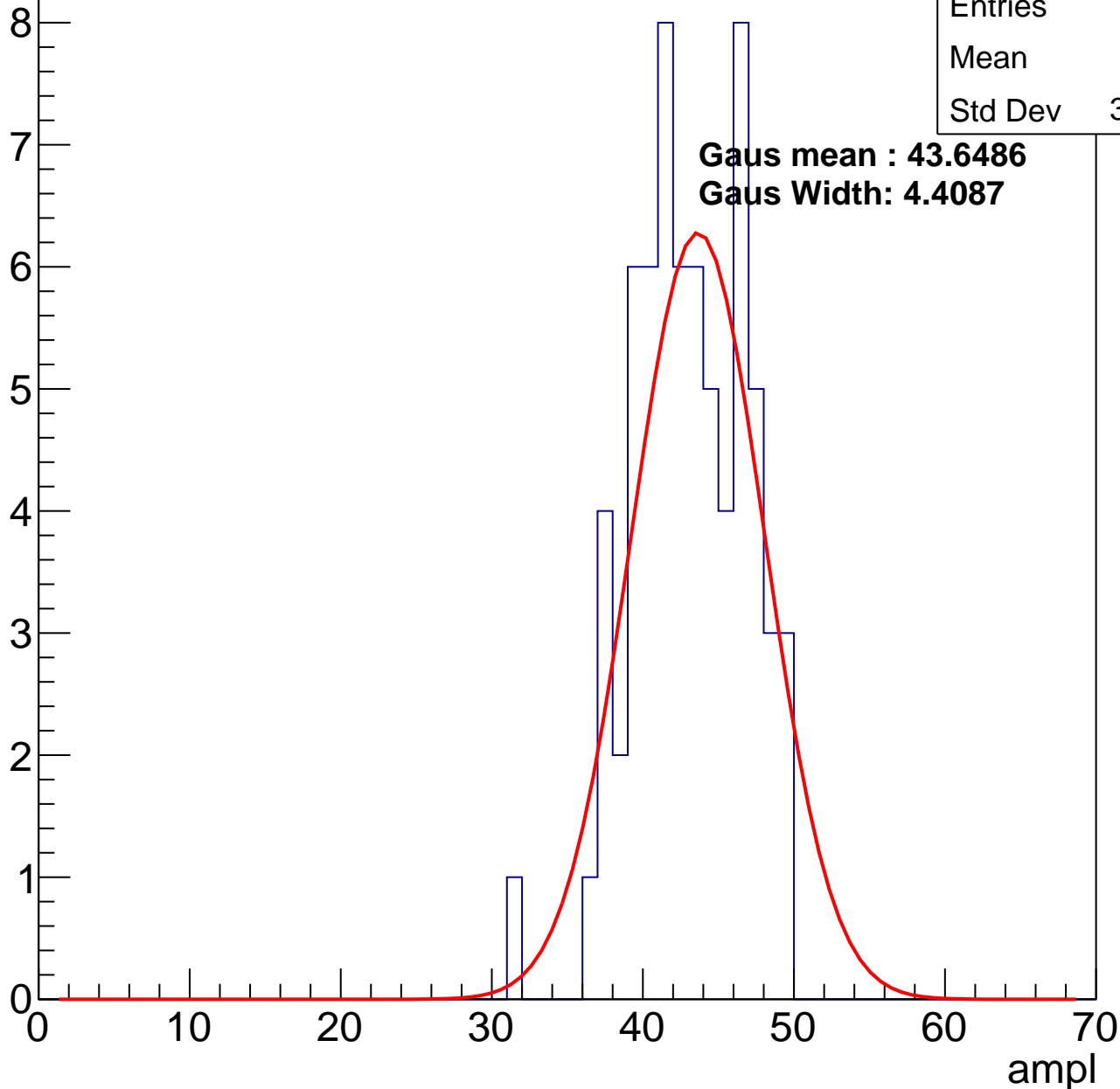
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.6
Std Dev	3.679

Gaus mean : 43.6486

Gaus Width: 4.4087

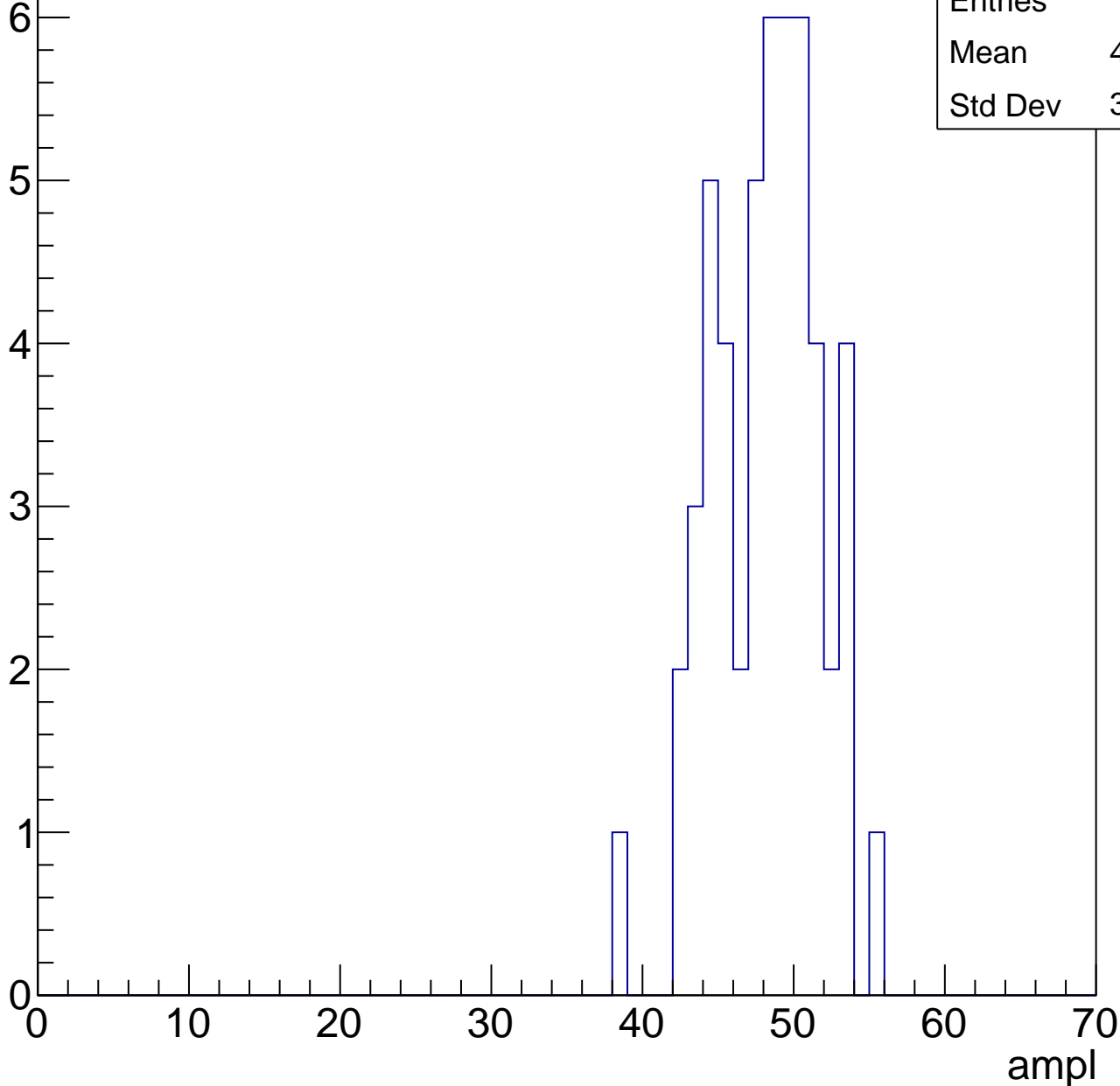


B1L003S, U16-ch117, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

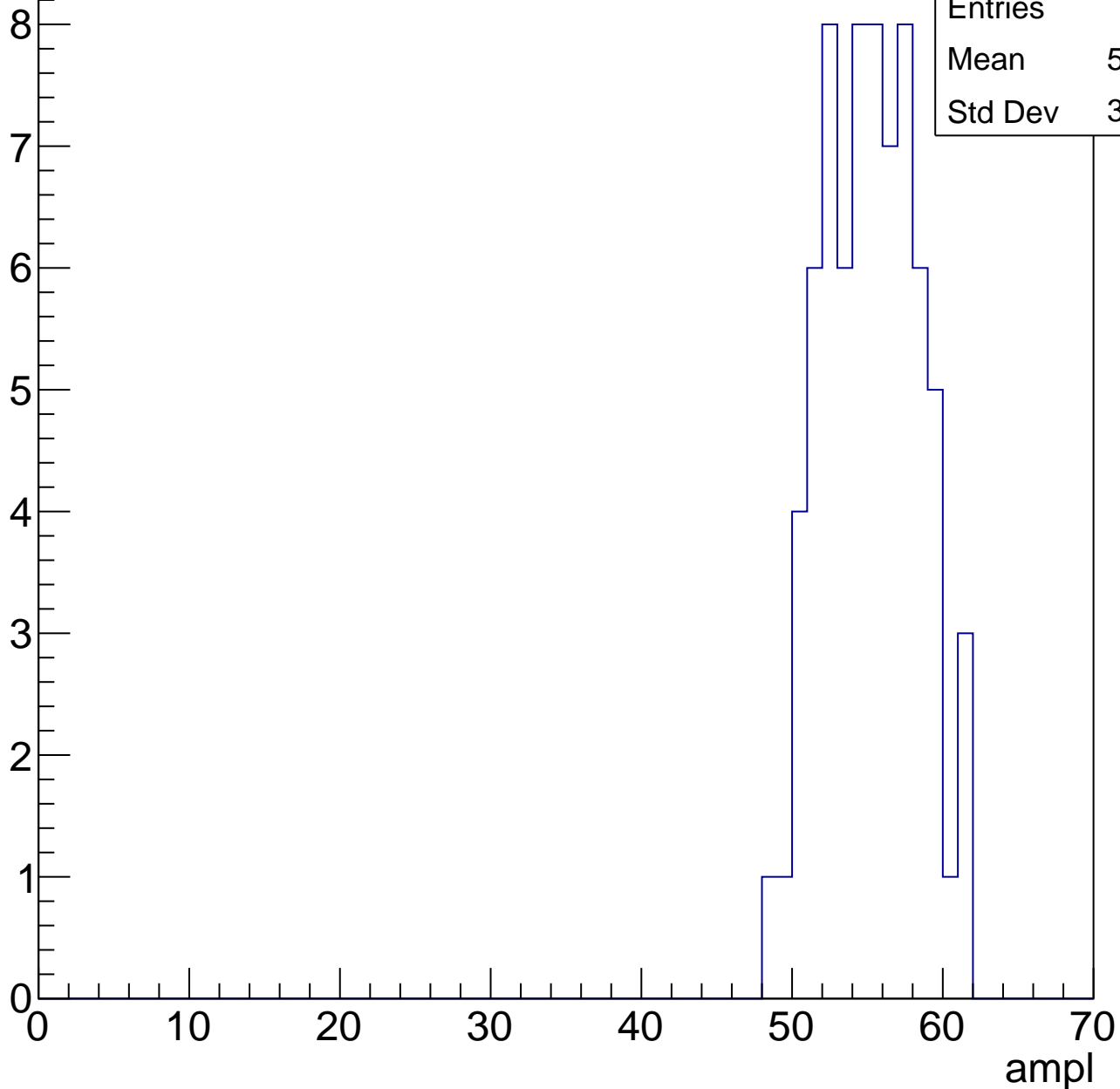
Entries	51
Mean	47.75
Std Dev	3.486



B1L003S, U16-ch117, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

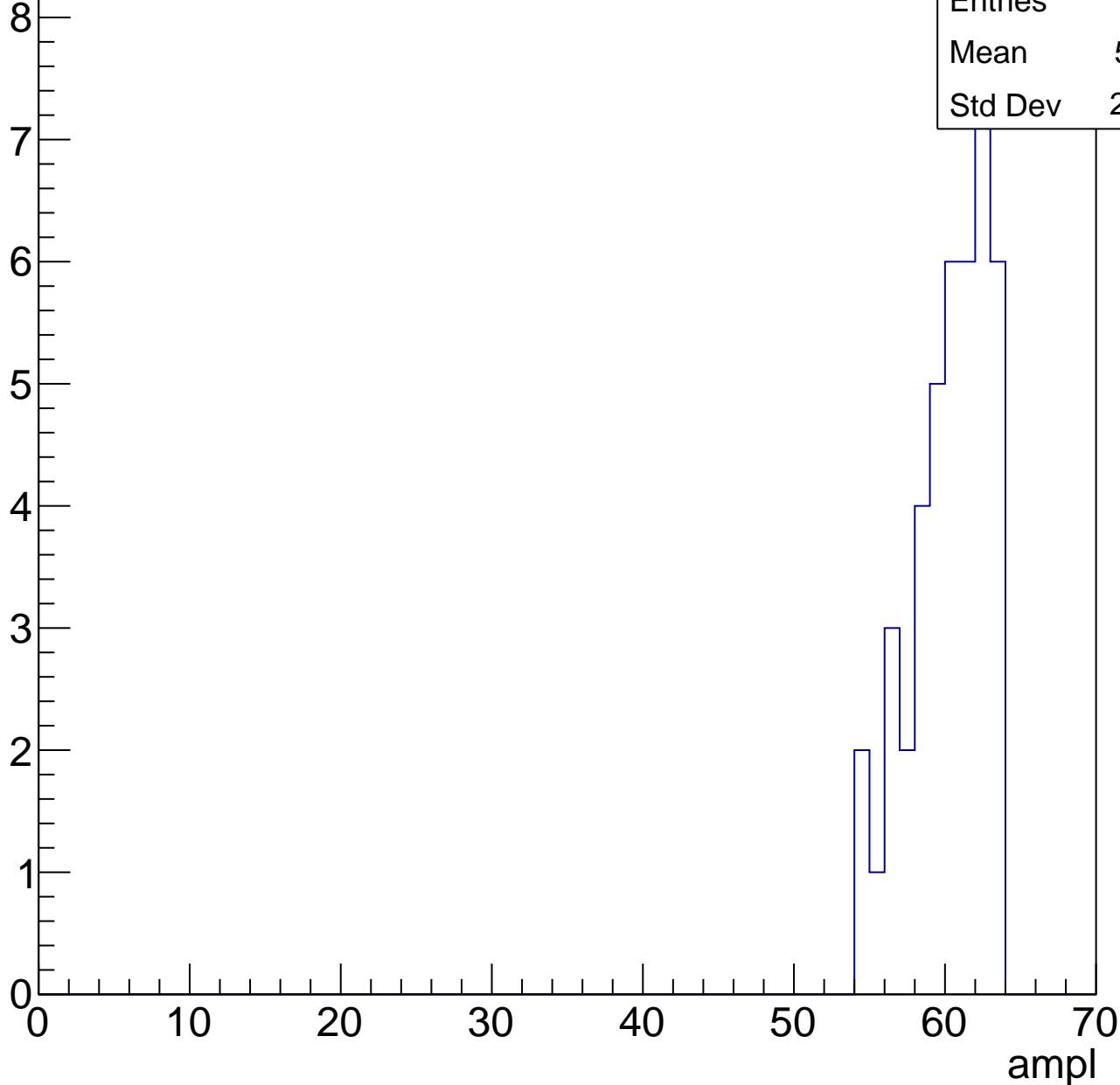


B1L003S, U16-ch117, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

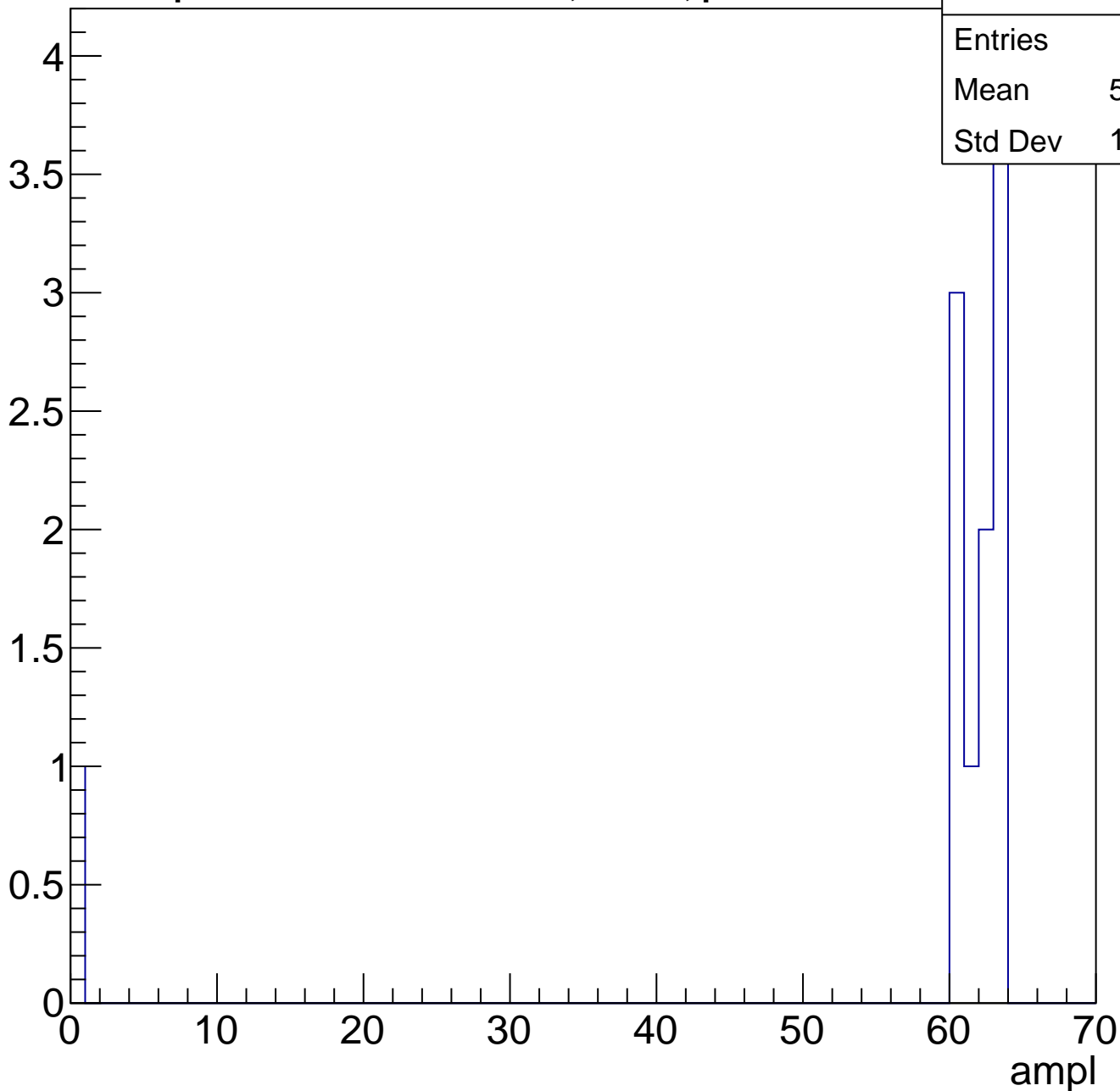
Entries	43
Mean	59.81
Std Dev	2.527



B1L003S, U16-ch117, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch117, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch118, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	28.25
Std Dev	3.526

Gaus mean : 28.9252

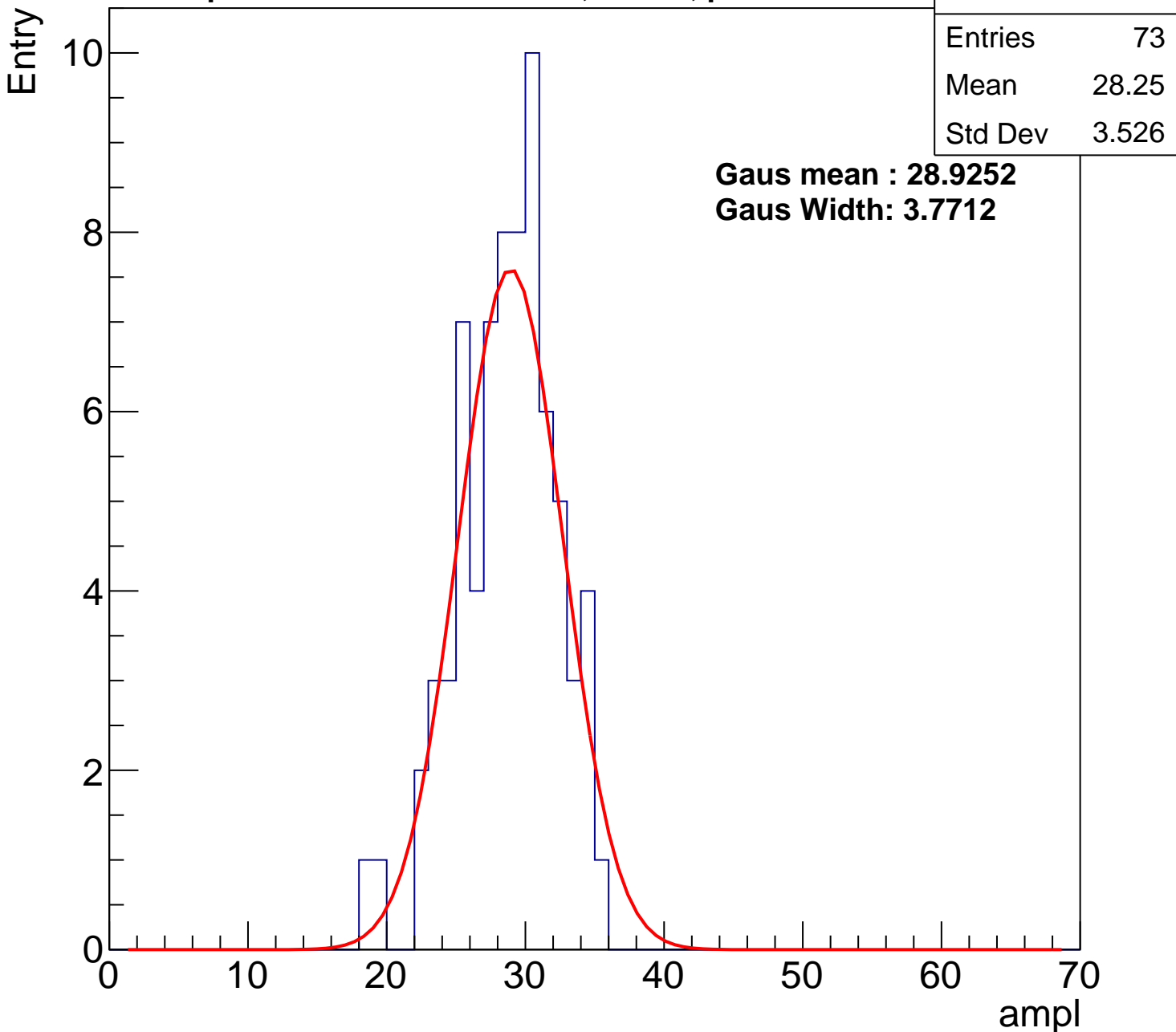
Gaus Width: 3.7712

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch118, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	36.26
Std Dev	3.771

Gaus mean : 36.3139

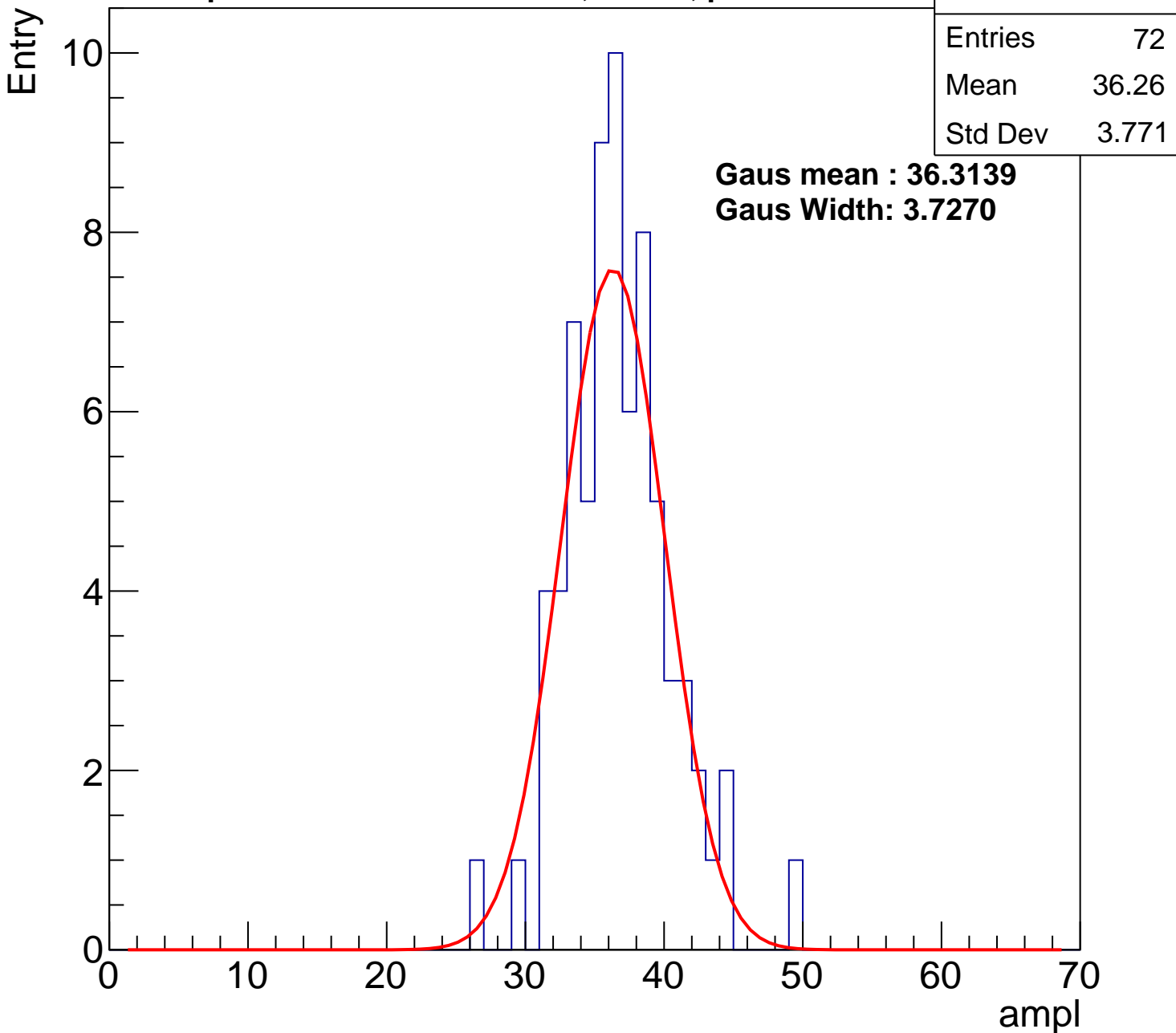
Gaus Width: 3.7270

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

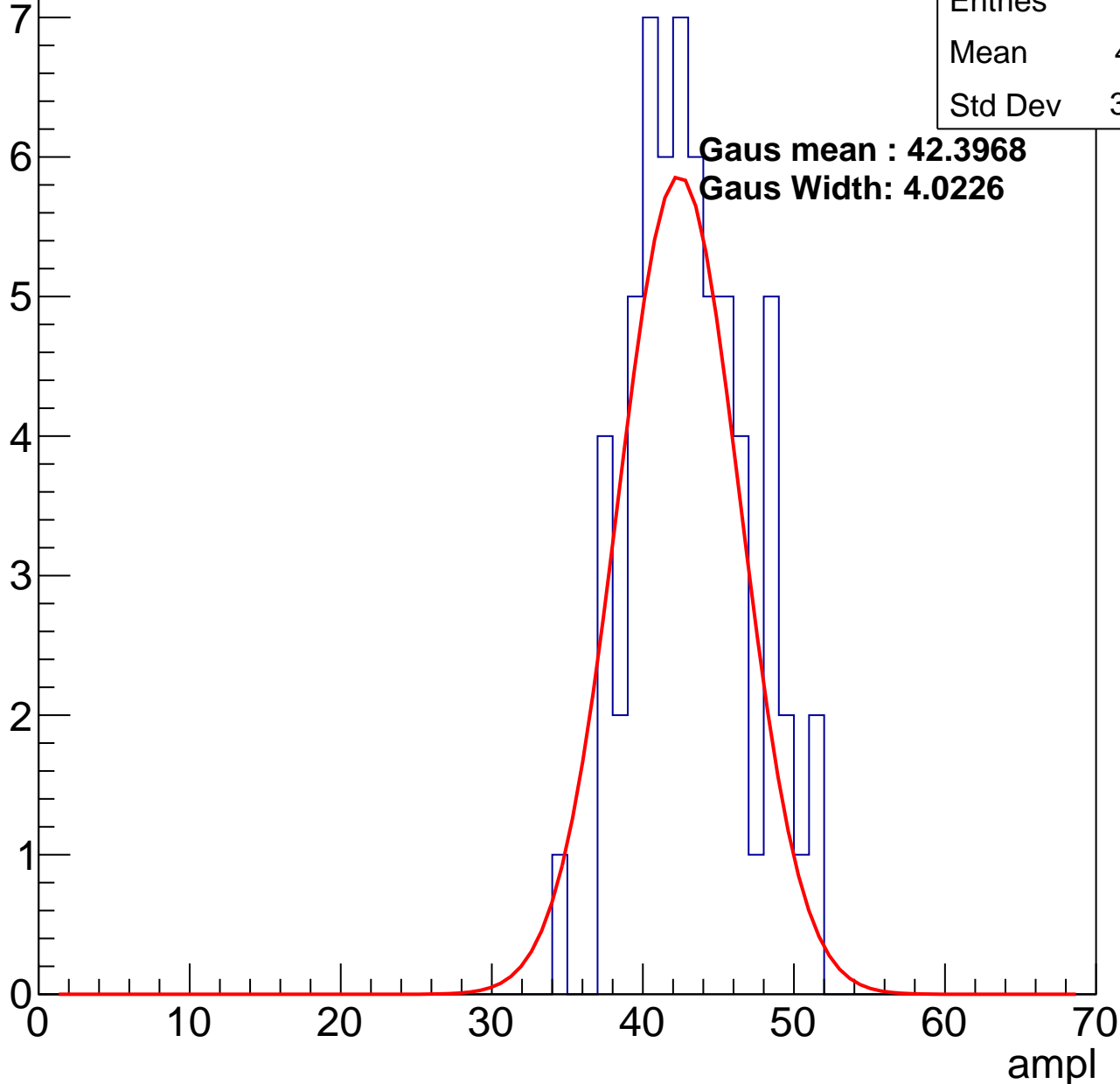


B1L003S, U16-ch118, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.81
Std Dev	3.775

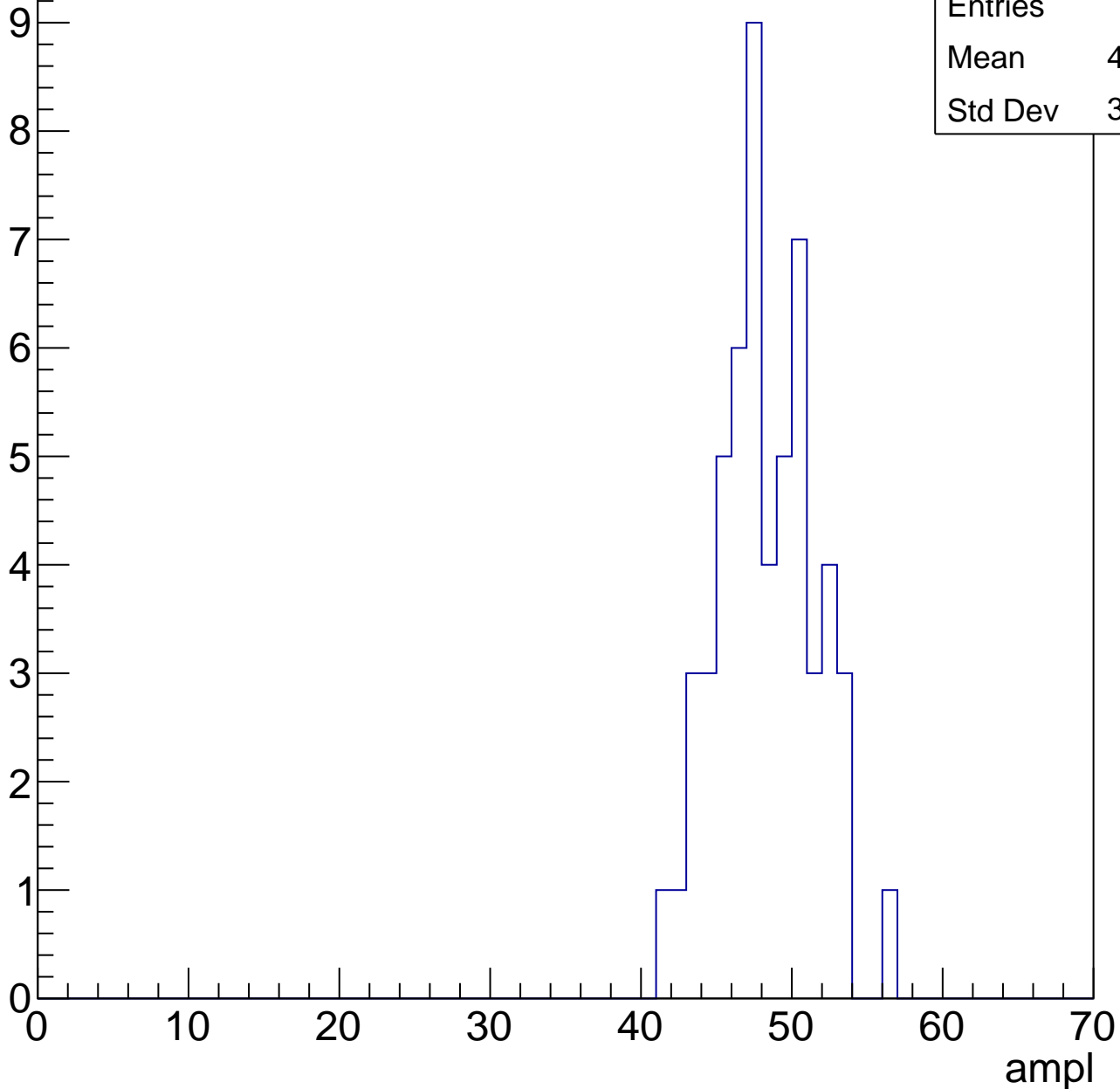


B1L003S, U16-ch118, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	47.84
Std Dev	3.155



B1L003S, U16-ch118, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	54.43
Std Dev	2.825

Entry

10

8

6

4

2

0

0

10

20

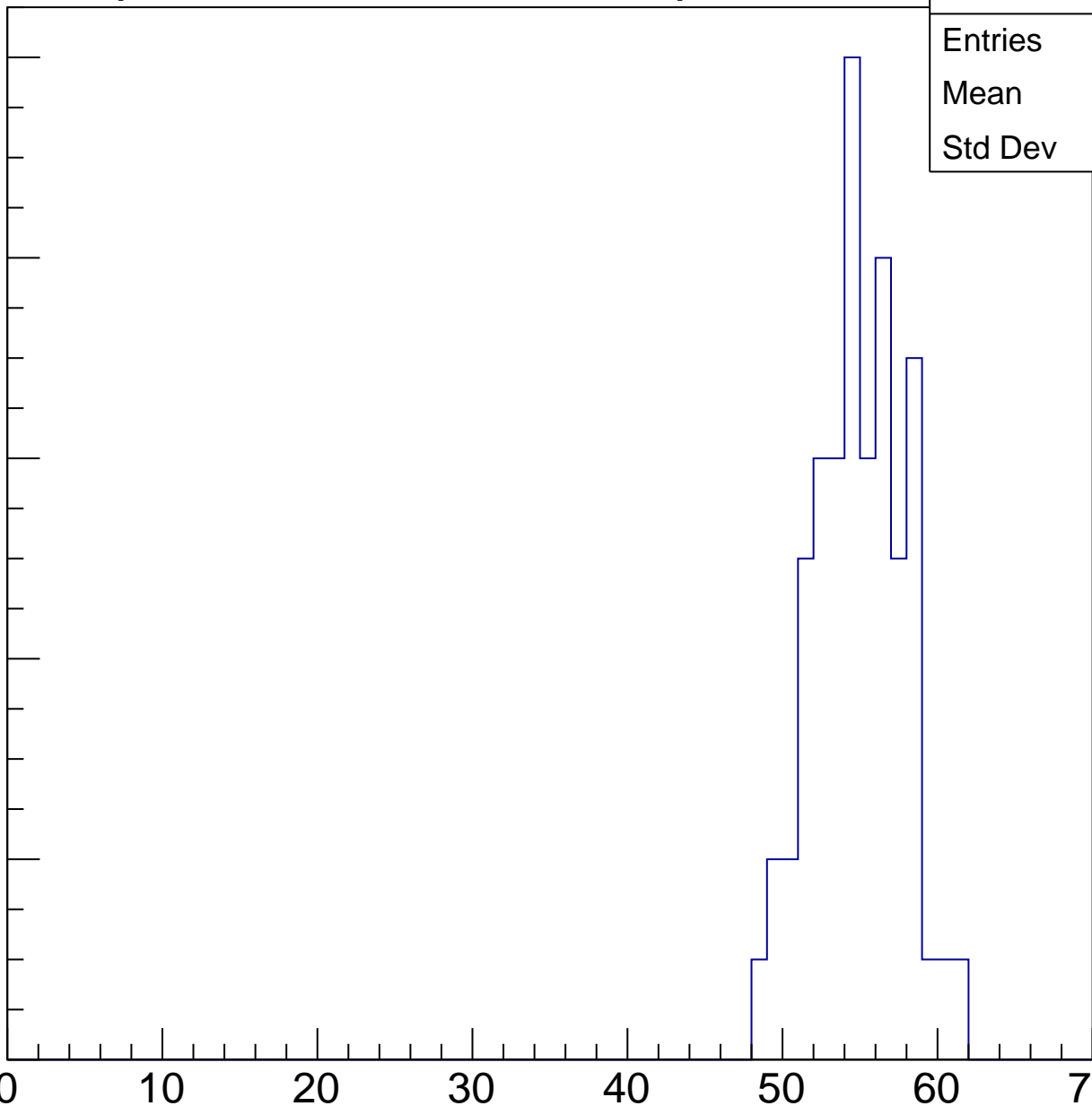
30

40

50

60

ampl



B1L003S, U16-ch118, adc5

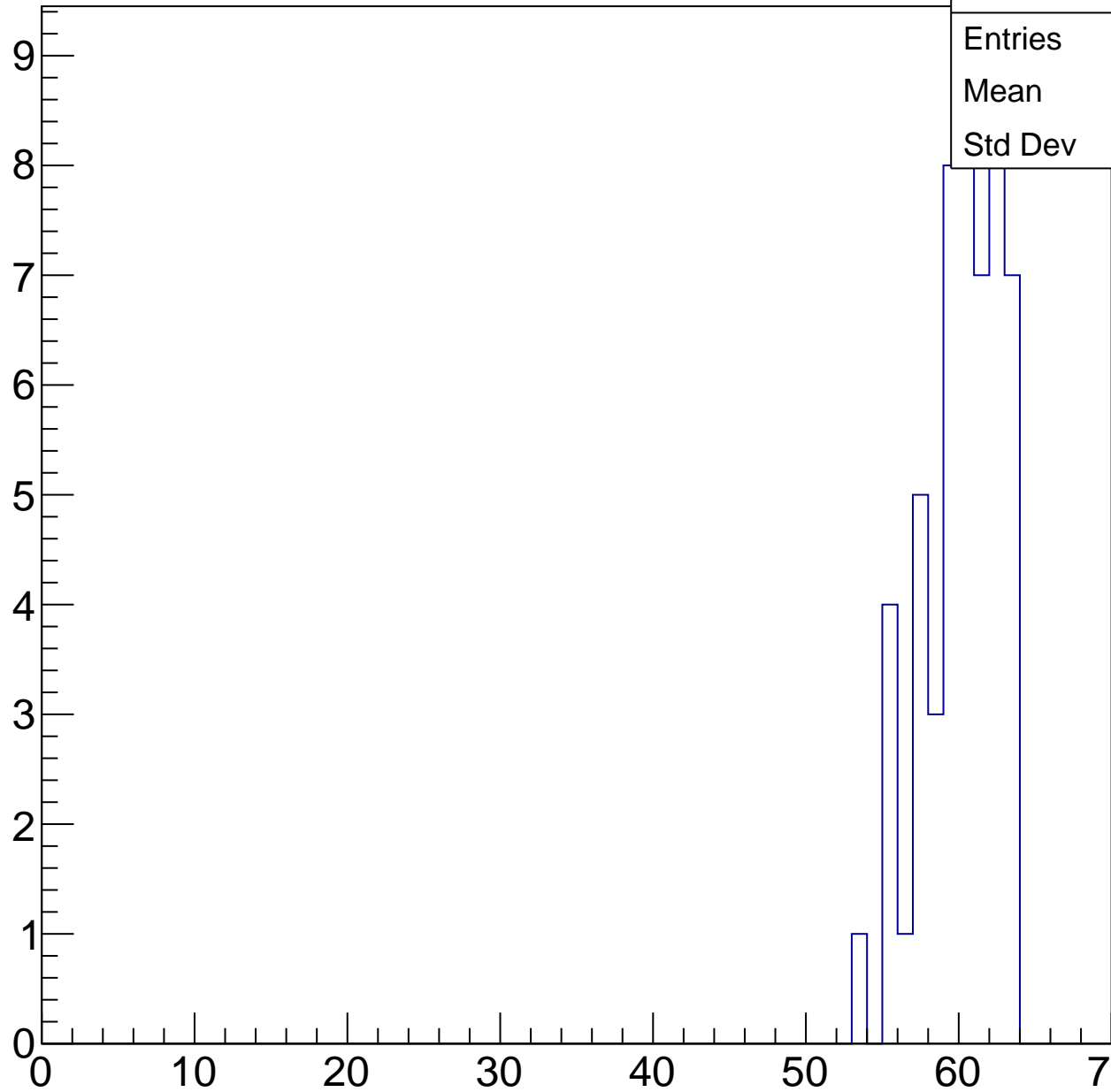
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.74
Std Dev	2.504

ampl



B1L003S, U16-ch118, adc6

calib_packv5_042523_0143.root, FC#13, port D2

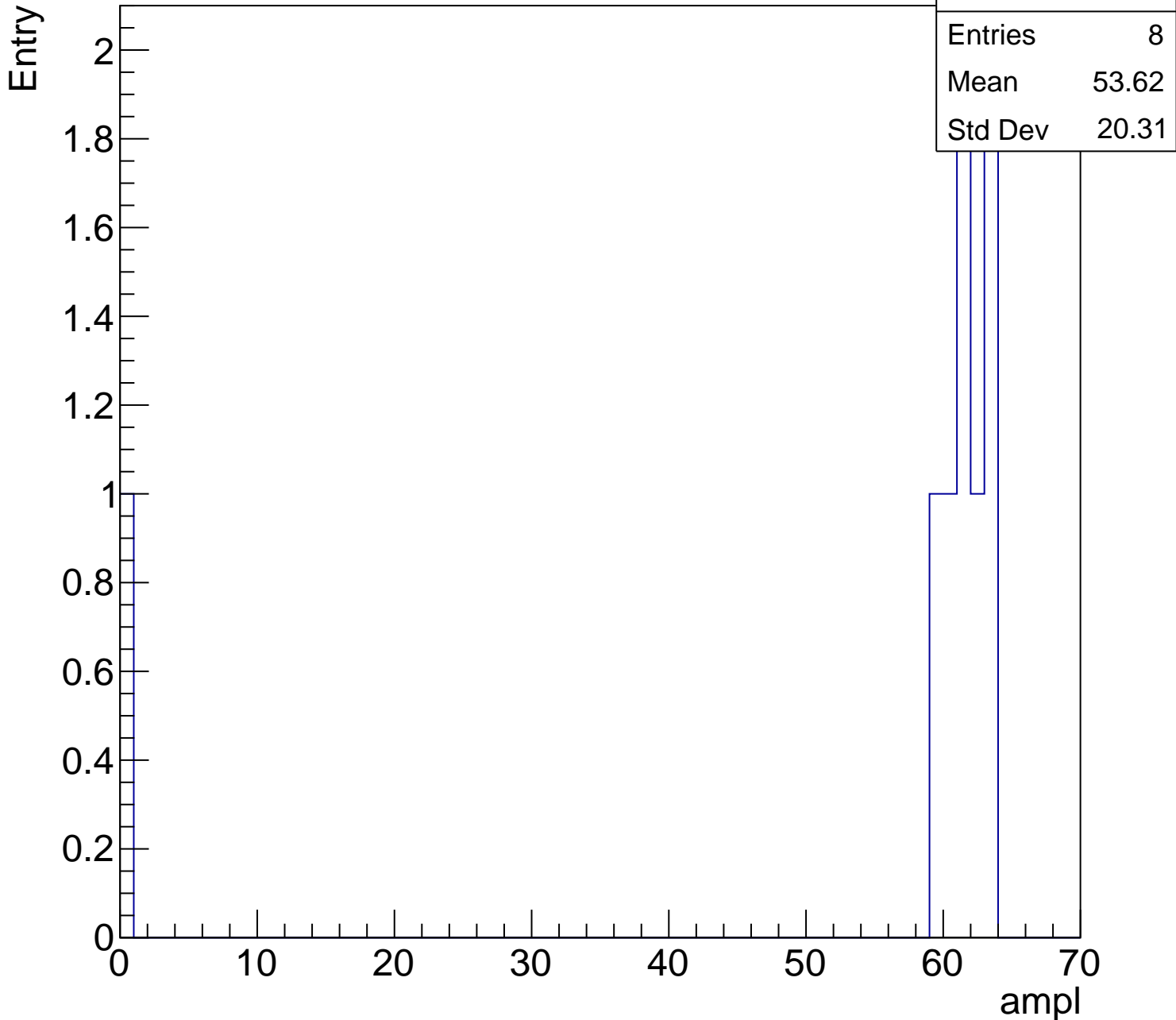
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.62
Std Dev	20.31

0 10 20 30 40 50 60 70

ampl



B1L003S, U16-ch118, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U16-ch119, adc0

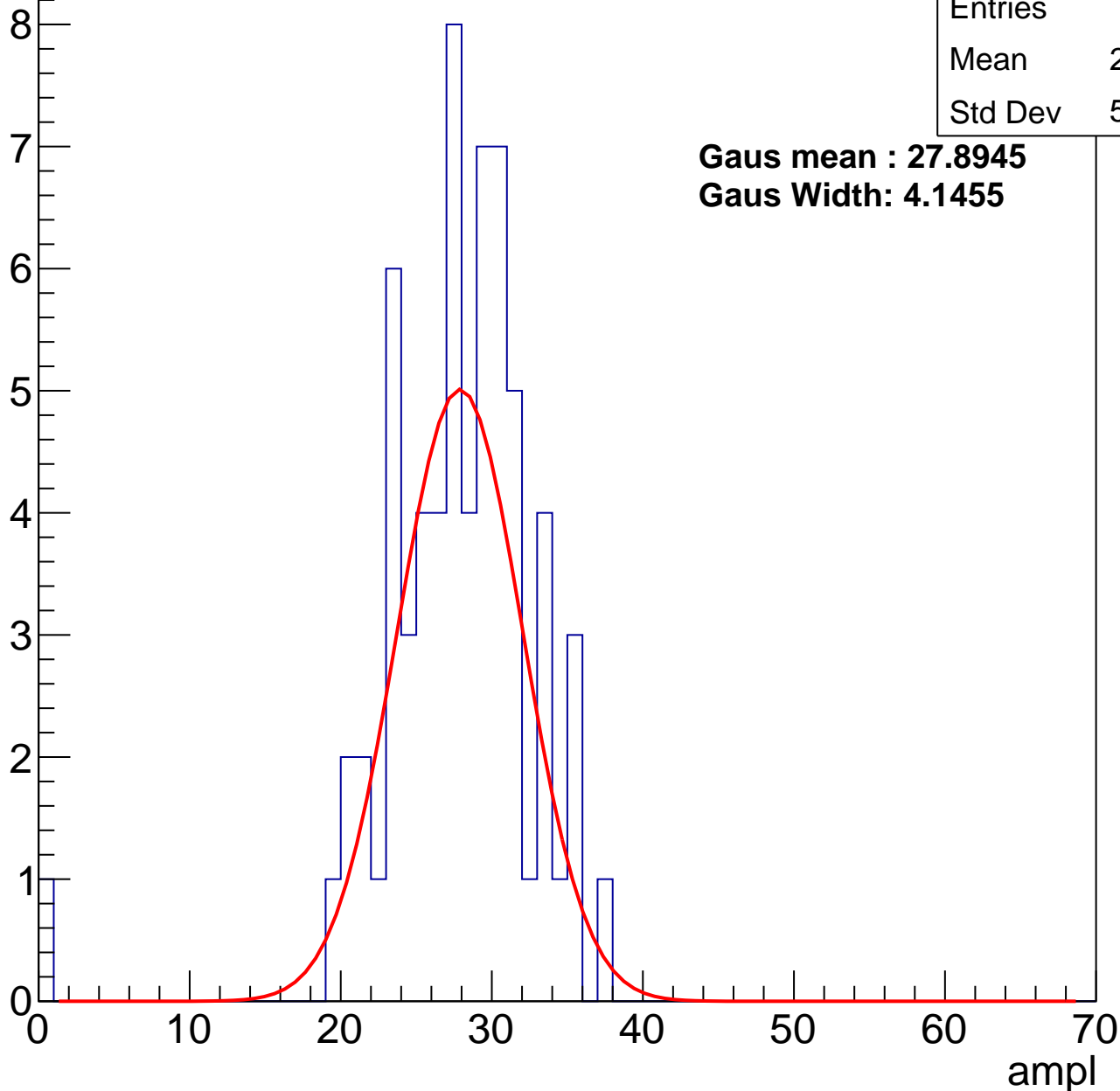
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	27.28
Std Dev	5.278

Gaus mean : 27.8945

Gaus Width: 4.1455



B1L003S, U16-ch119, adc1

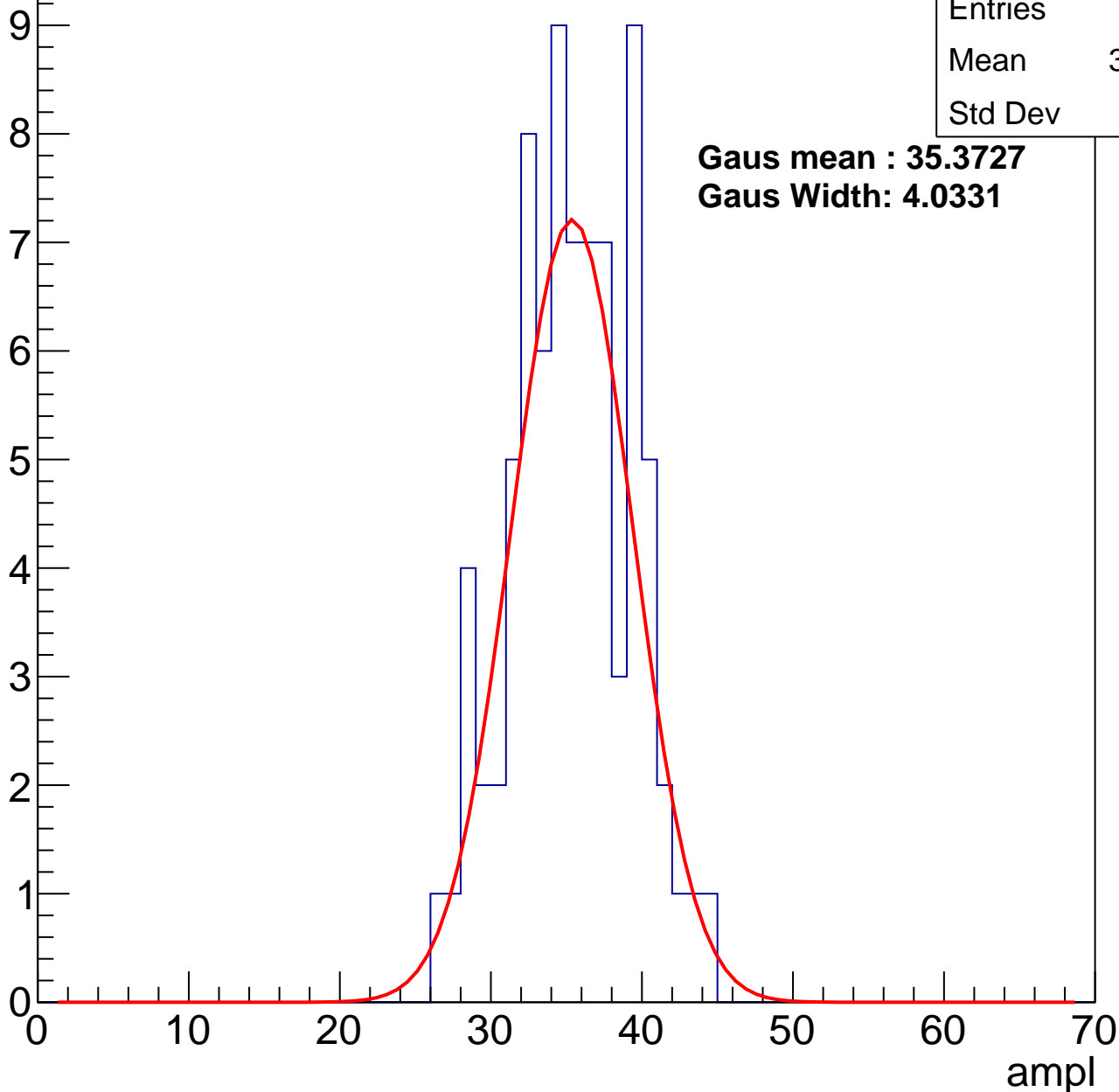
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	34.94
Std Dev	3.92

Gaus mean : 35.3727

Gaus Width: 4.0331



B1L003S, U16-ch119, adc2

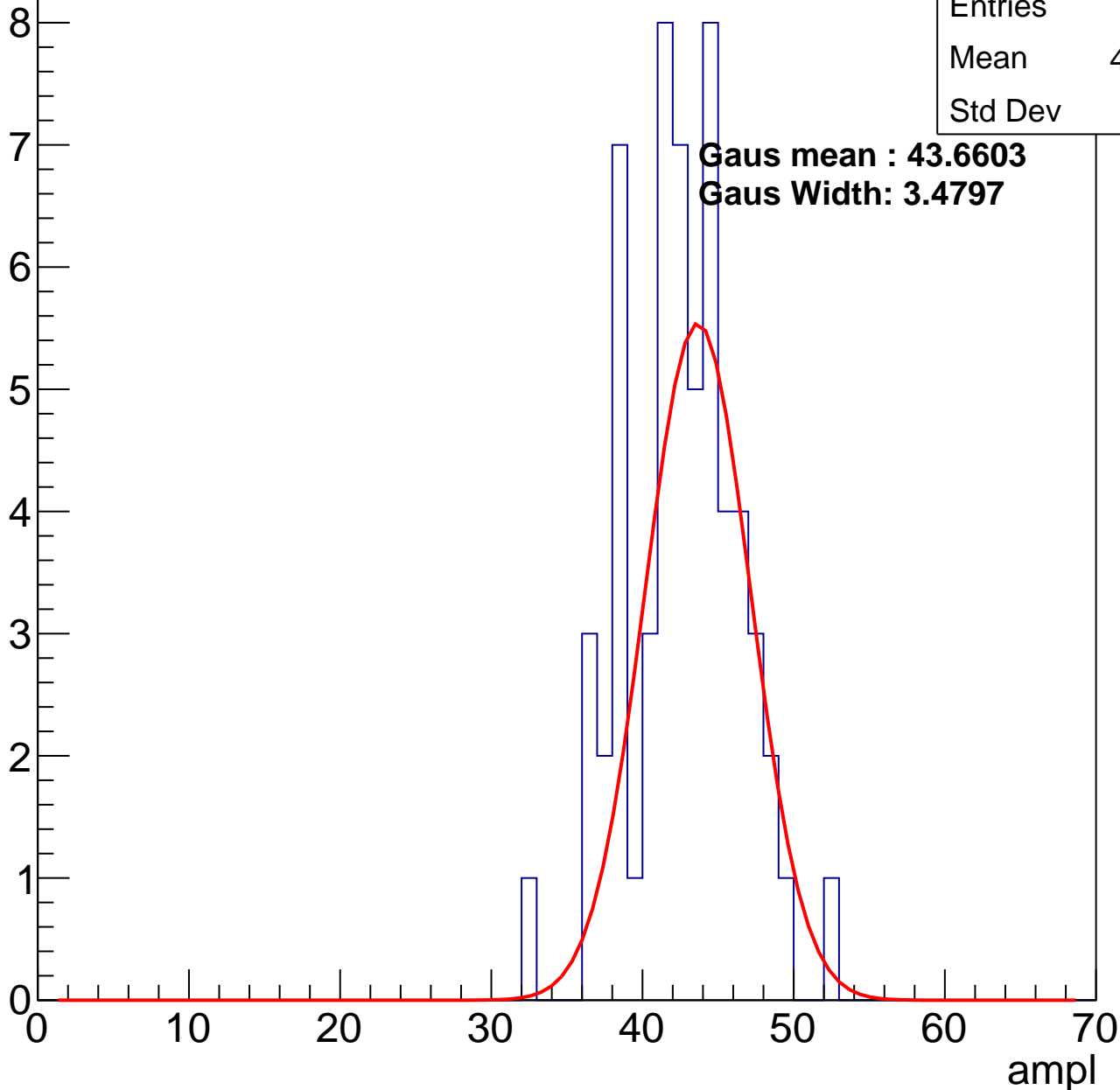
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.17
Std Dev	3.72

Gaus mean : 43.6603

Gaus Width: 3.4797

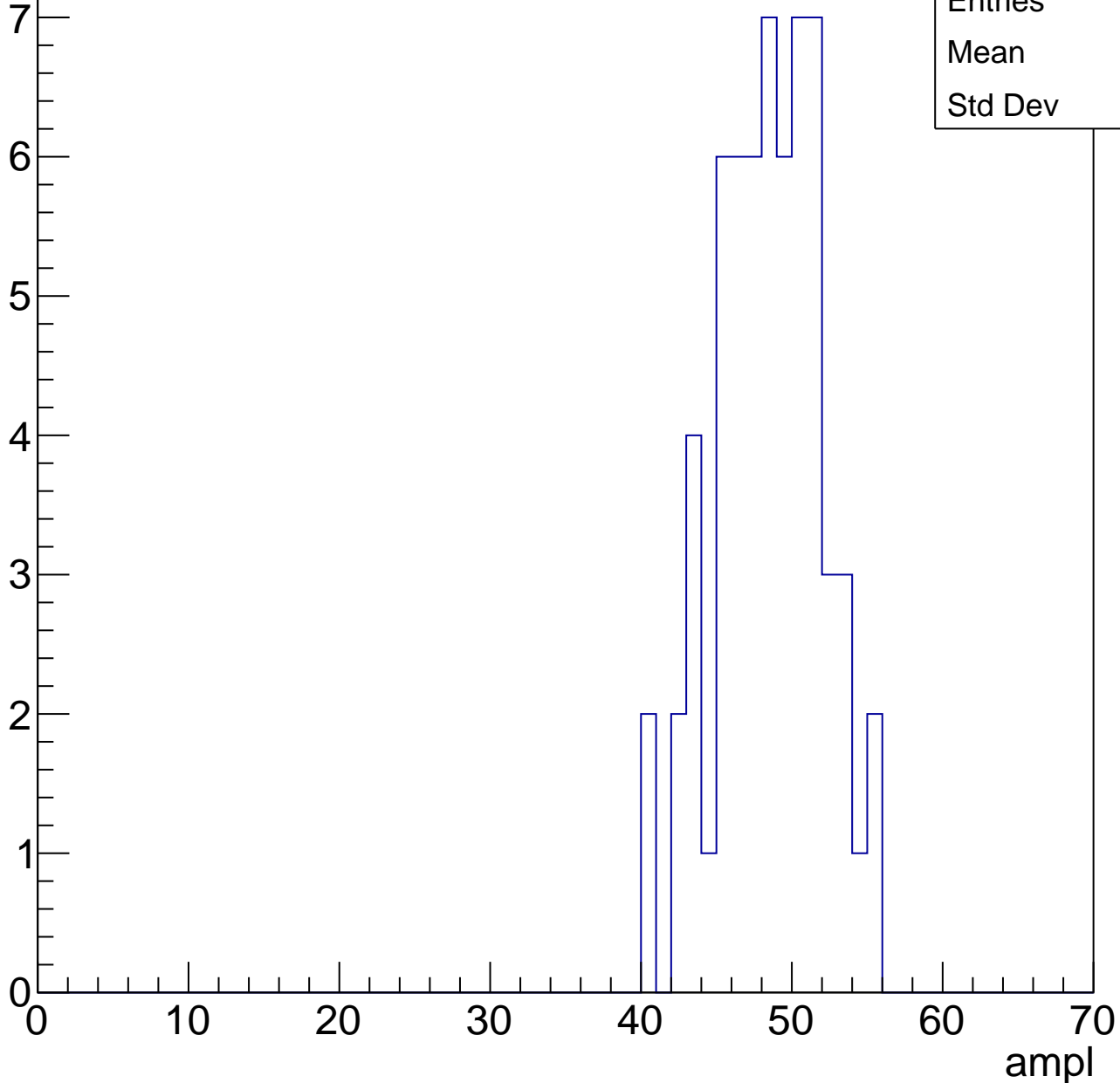


B1L003S, U16-ch119, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

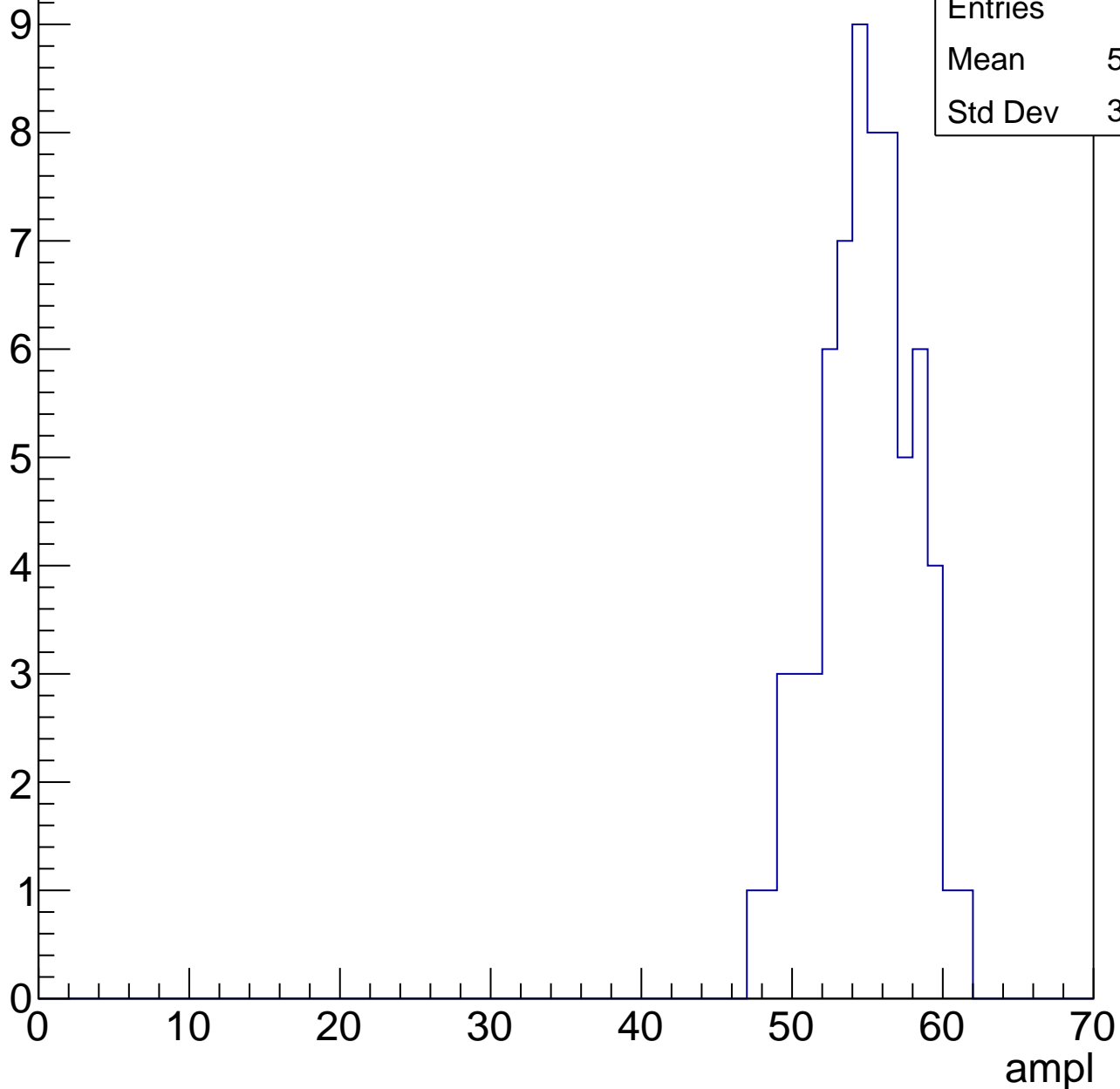
Entries	63
Mean	48
Std Dev	3.46



B1L003S, U16-ch119, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



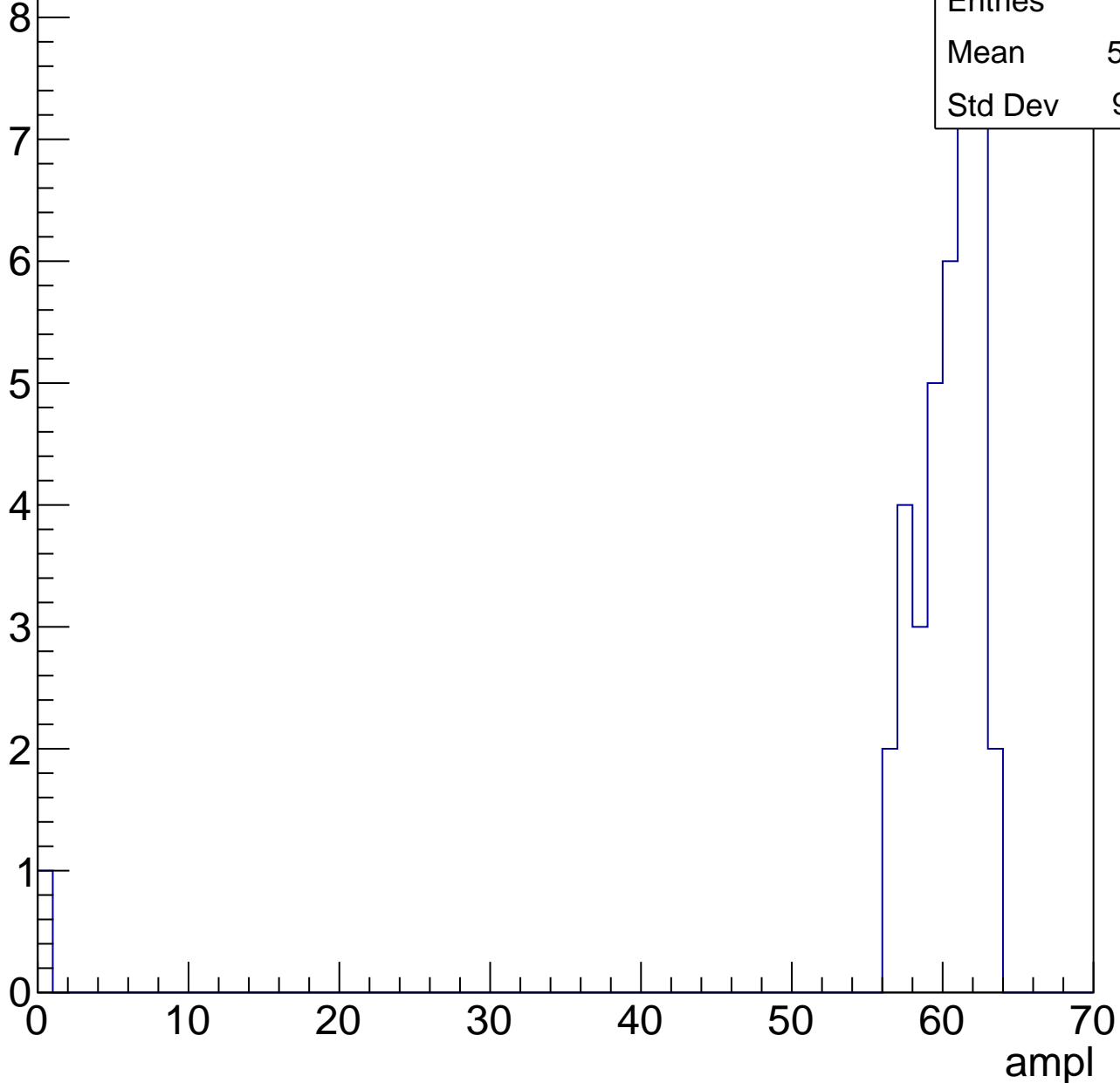
Entries	66
Mean	54.42
Std Dev	3.065

B1L003S, U16-ch119, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.44
Std Dev	9.671

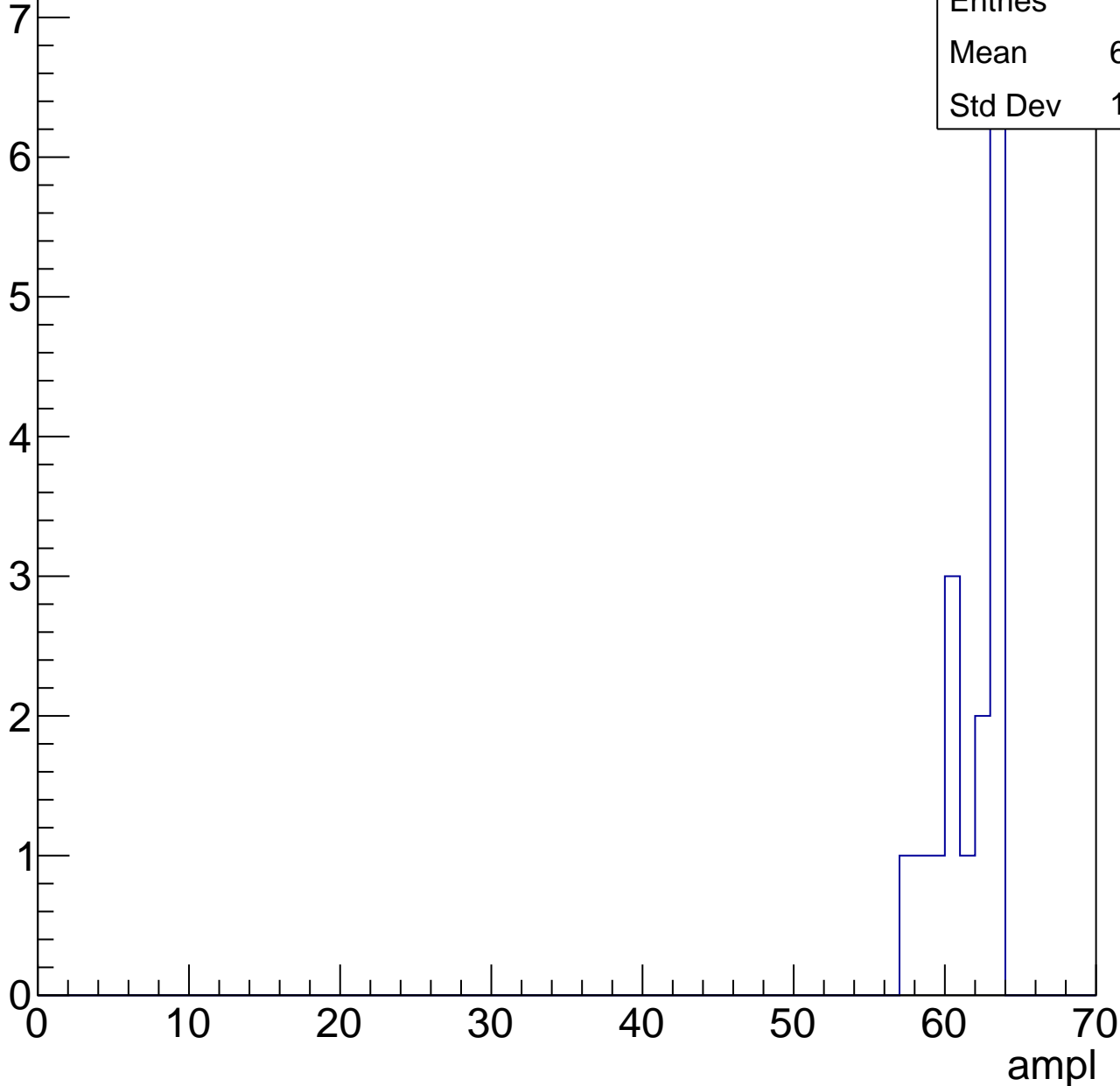


B1L003S, U16-ch119, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

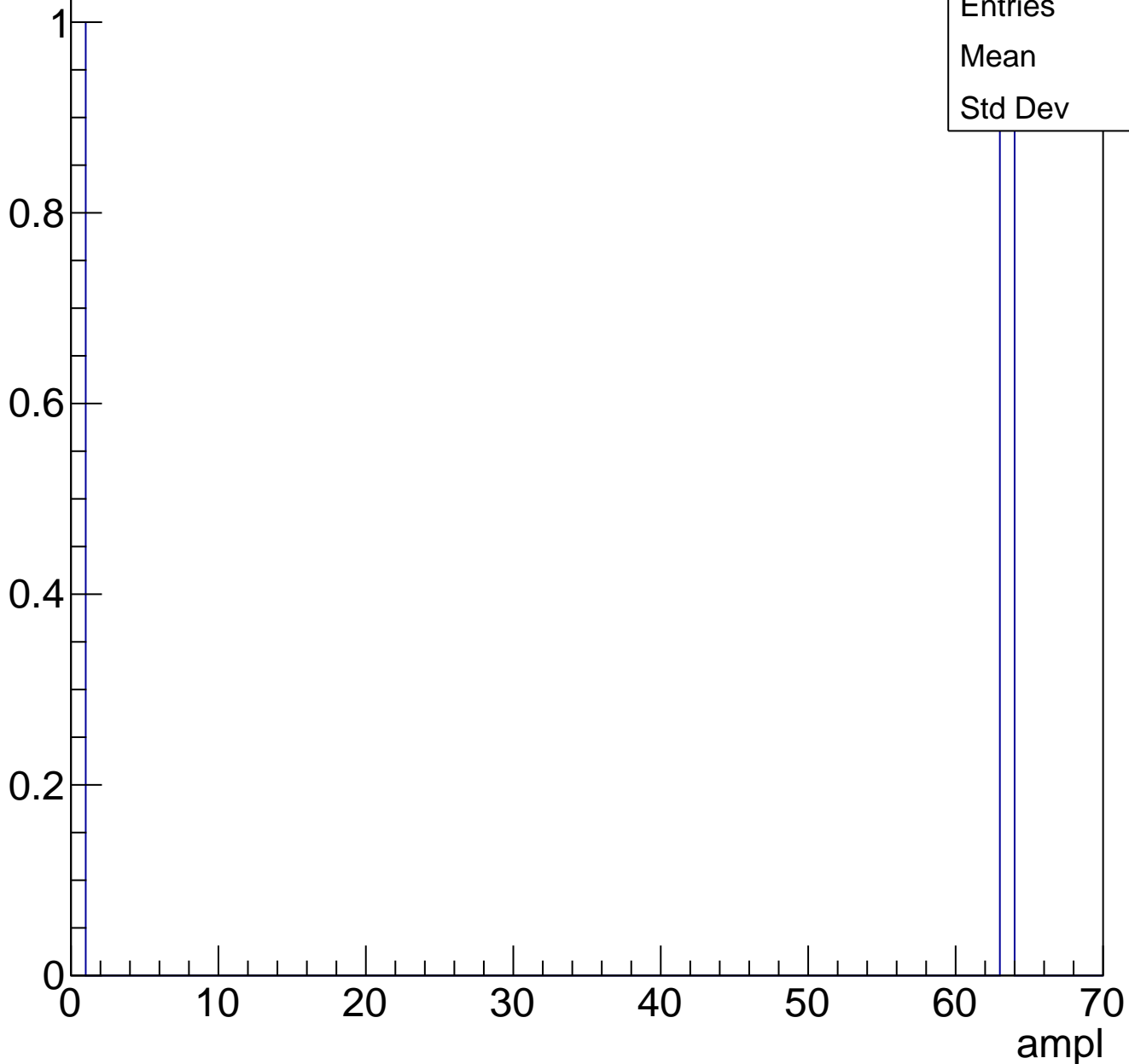
Entries	16
Mean	61.25
Std Dev	1.953



B1L003S, U16-ch119, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch120, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	27.05
Std Dev	4.897

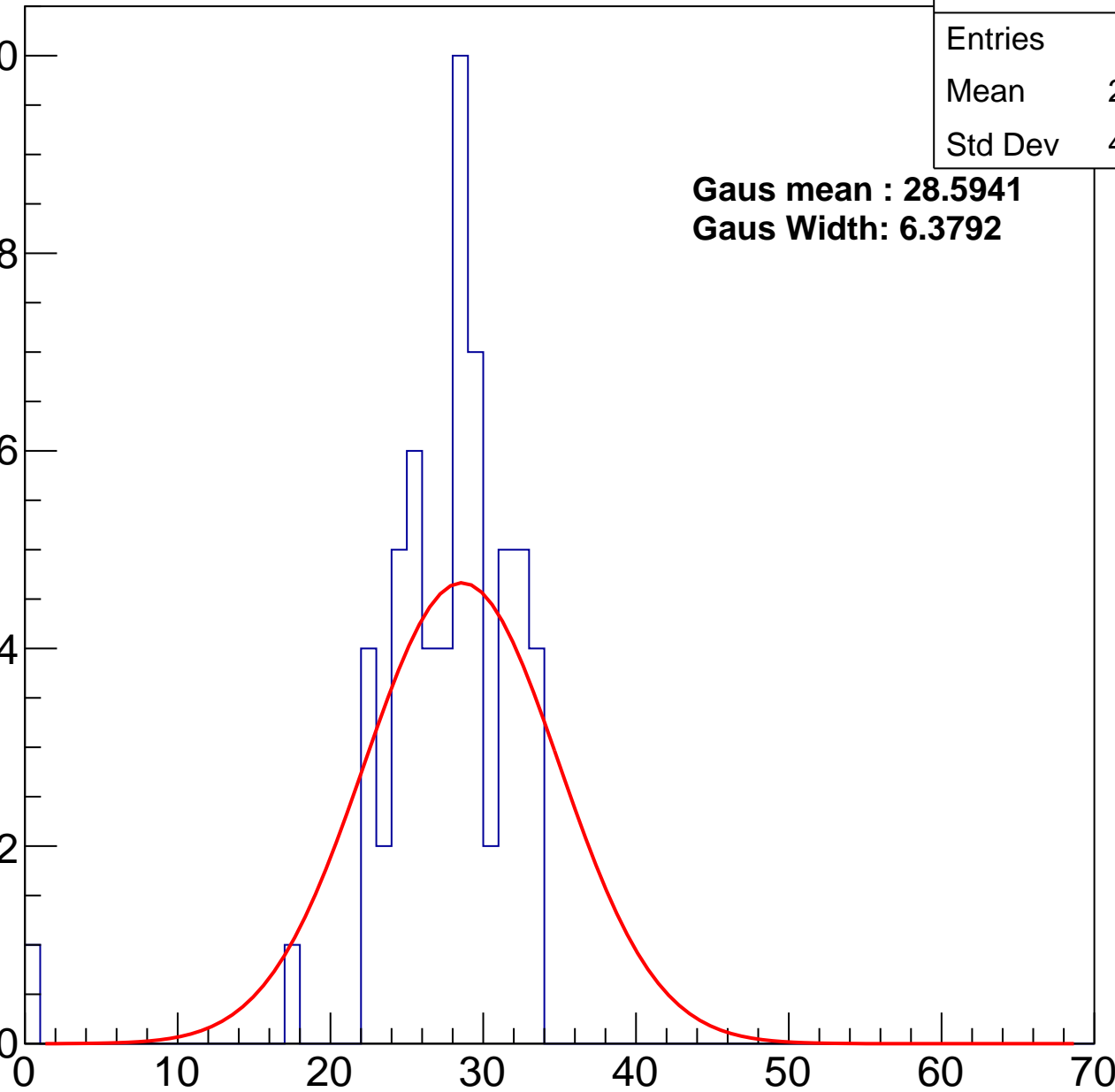
Gaus mean : 28.5941

Gaus Width: 6.3792

Entry

10
8
6
4
2
0

ampl



B1L003S, U16-ch120, adc1

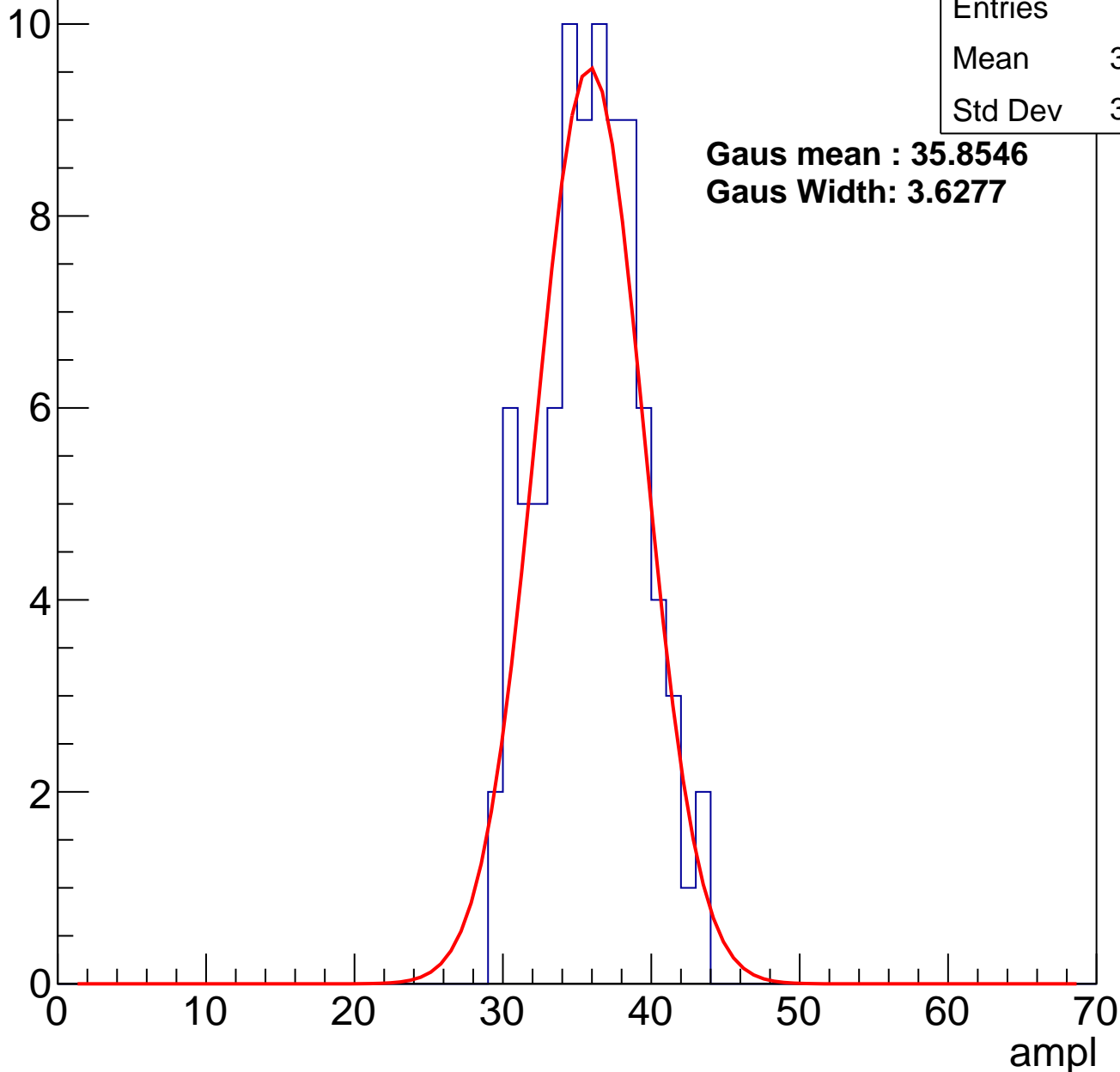
calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	35.47
Std Dev	3.339

Gaus mean : 35.8546

Gaus Width: 3.6277

Entry



B1L003S, U16-ch120, adc2

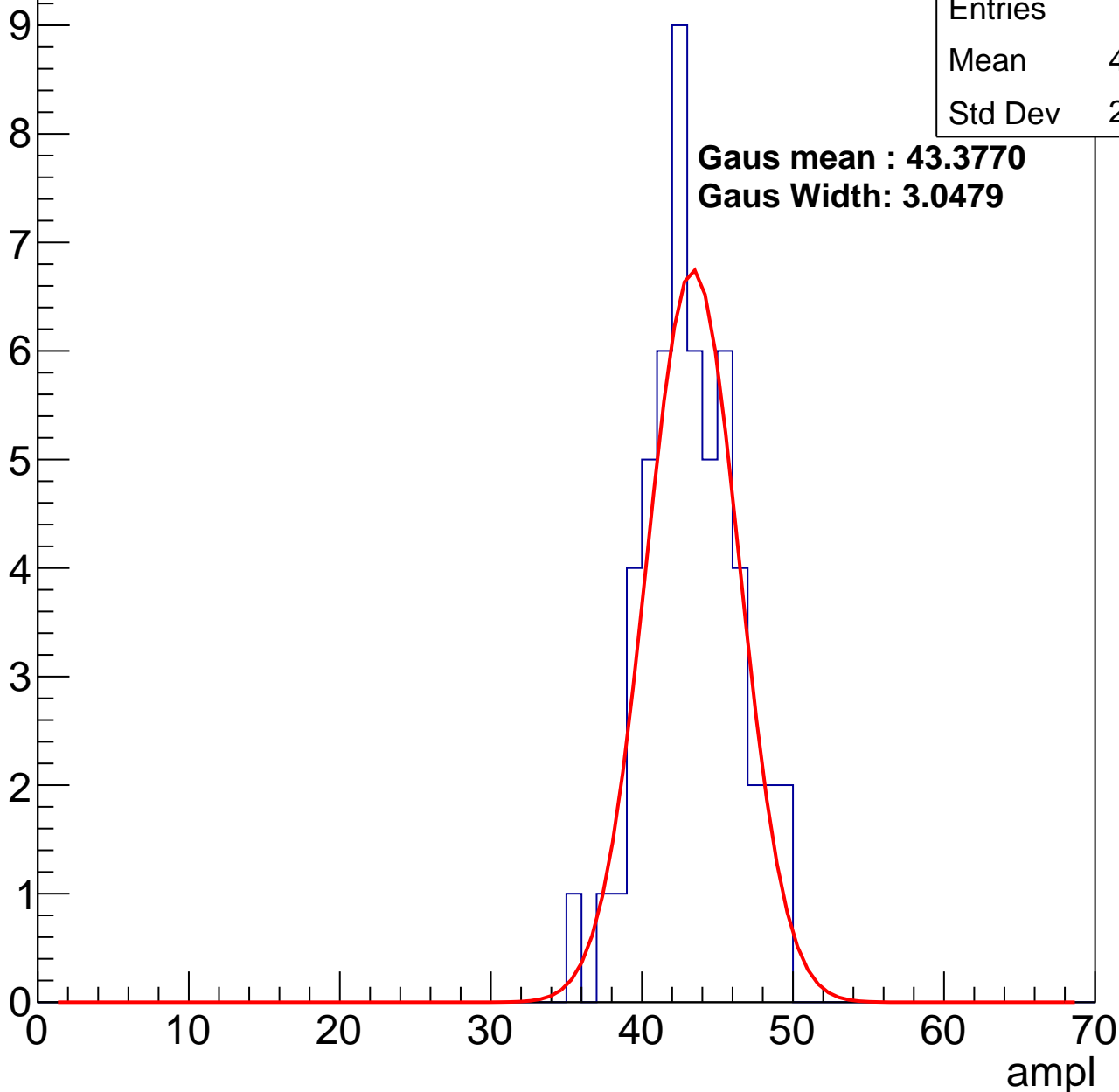
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.78
Std Dev	2.992

Gaus mean : 43.3770

Gaus Width: 3.0479

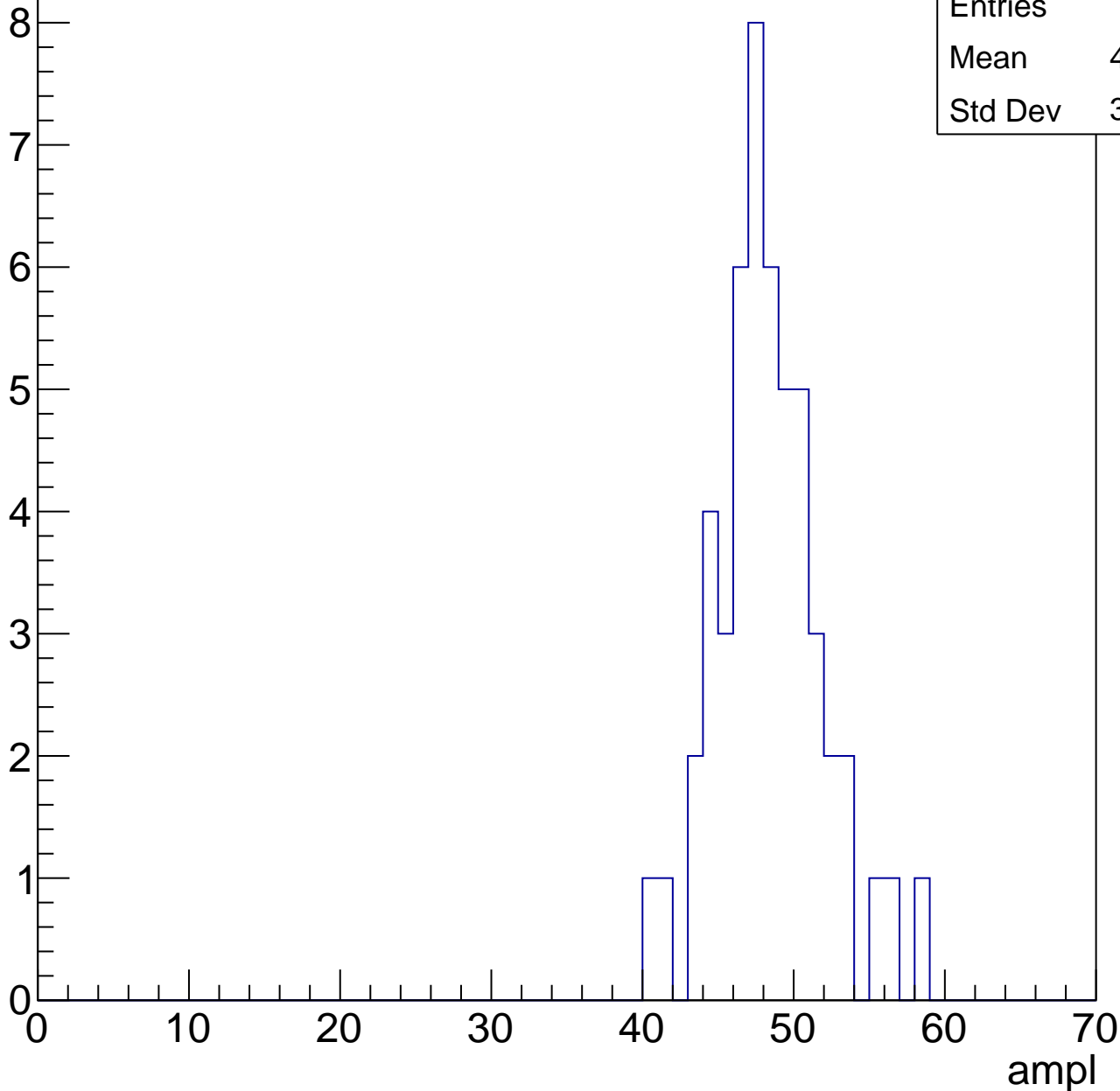


B1L003S, U16-ch120, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	47.94
Std Dev	3.523

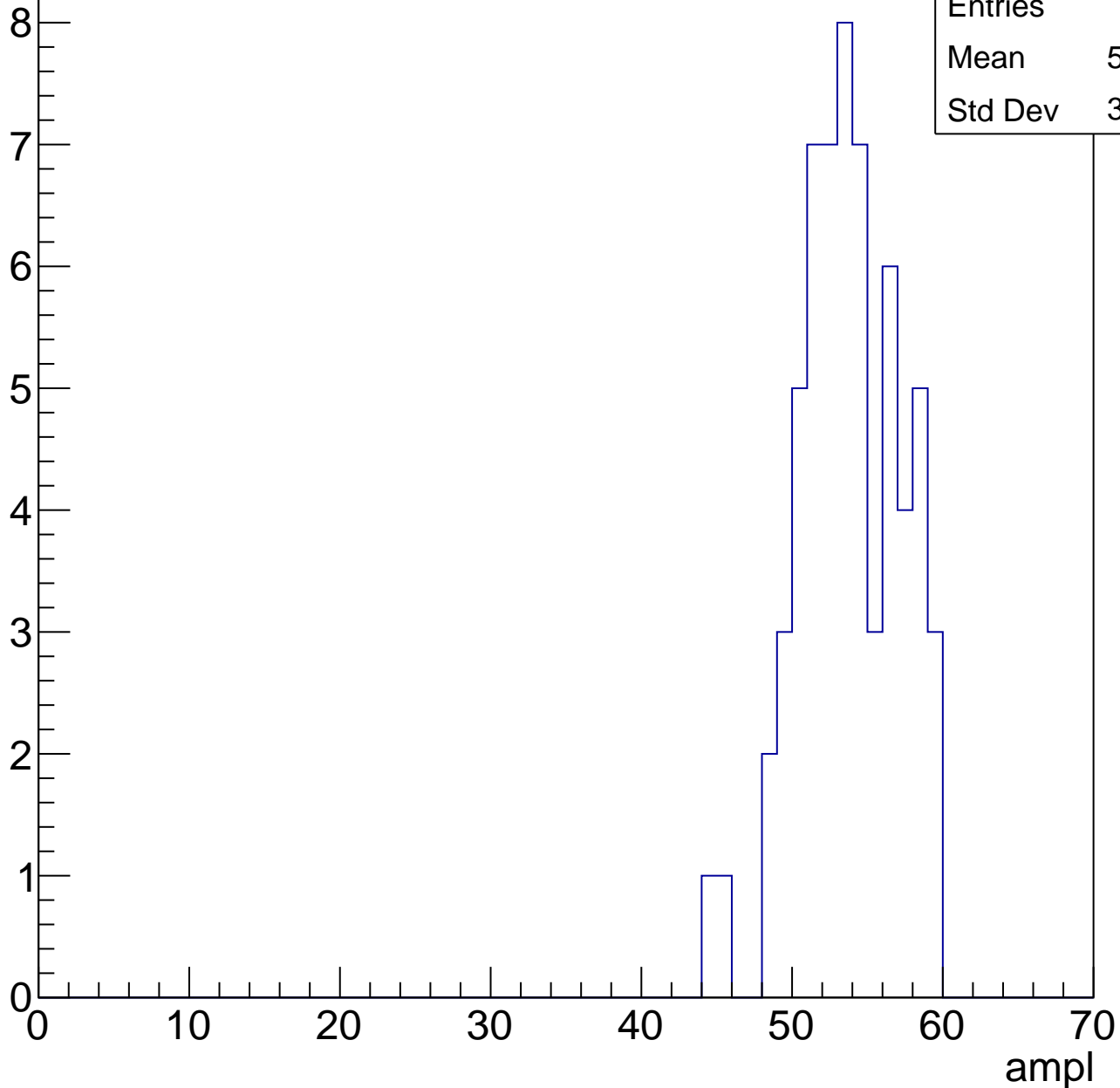


B1L003S, U16-ch120, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	53.24
Std Dev	3.334

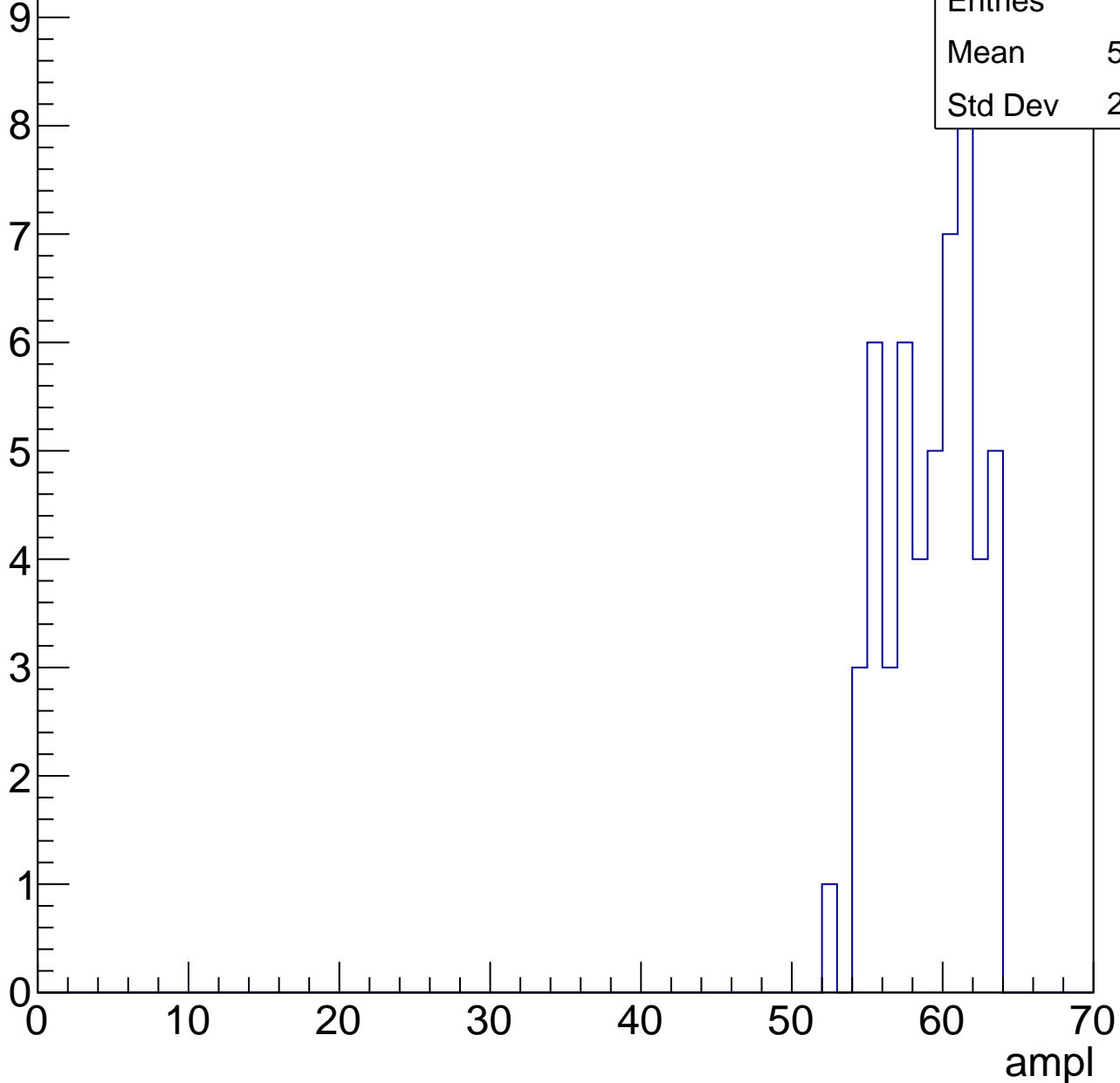


B1L003S, U16-ch120, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.74
Std Dev	2.849

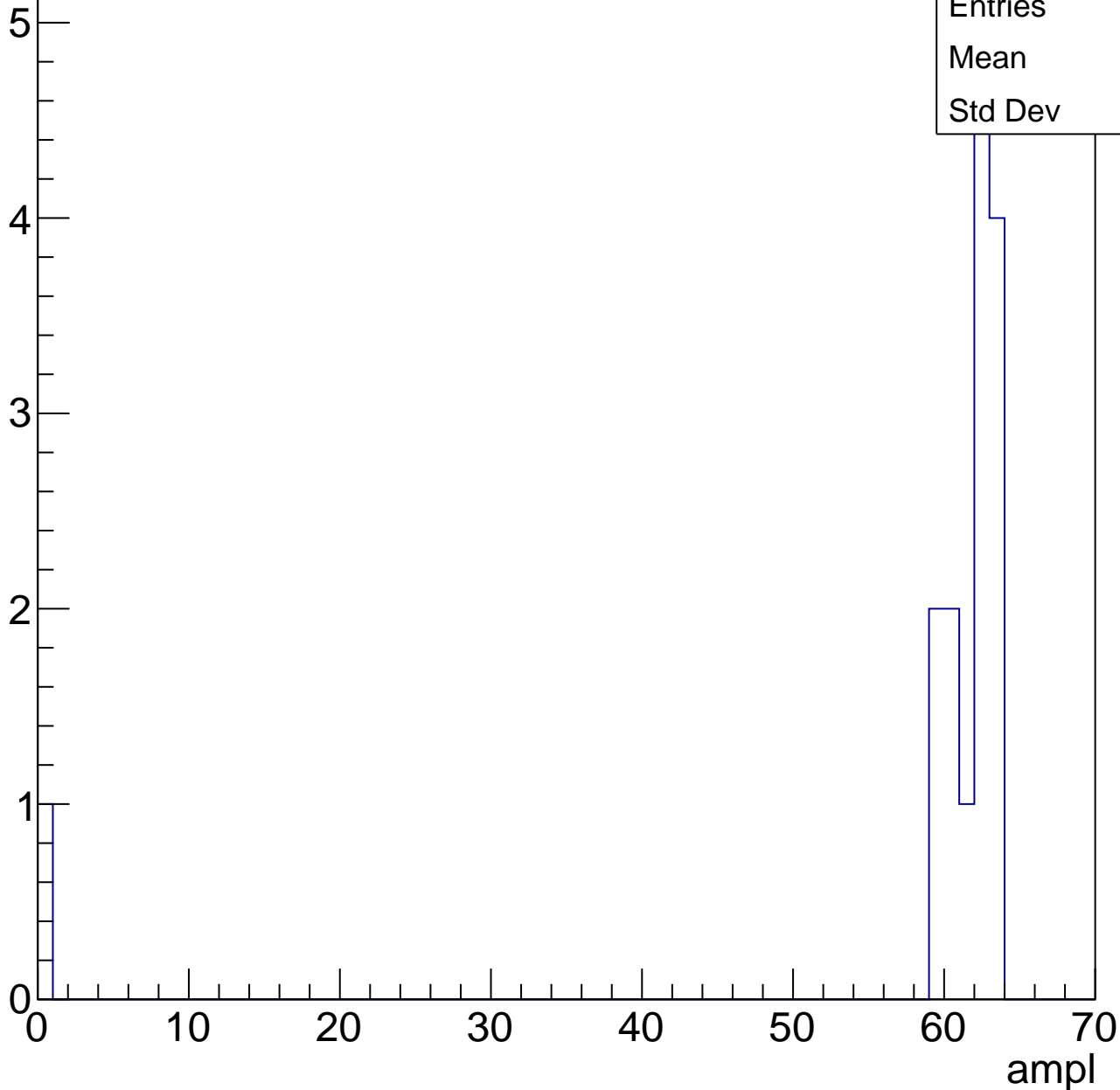


B1L003S, U16-ch120, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

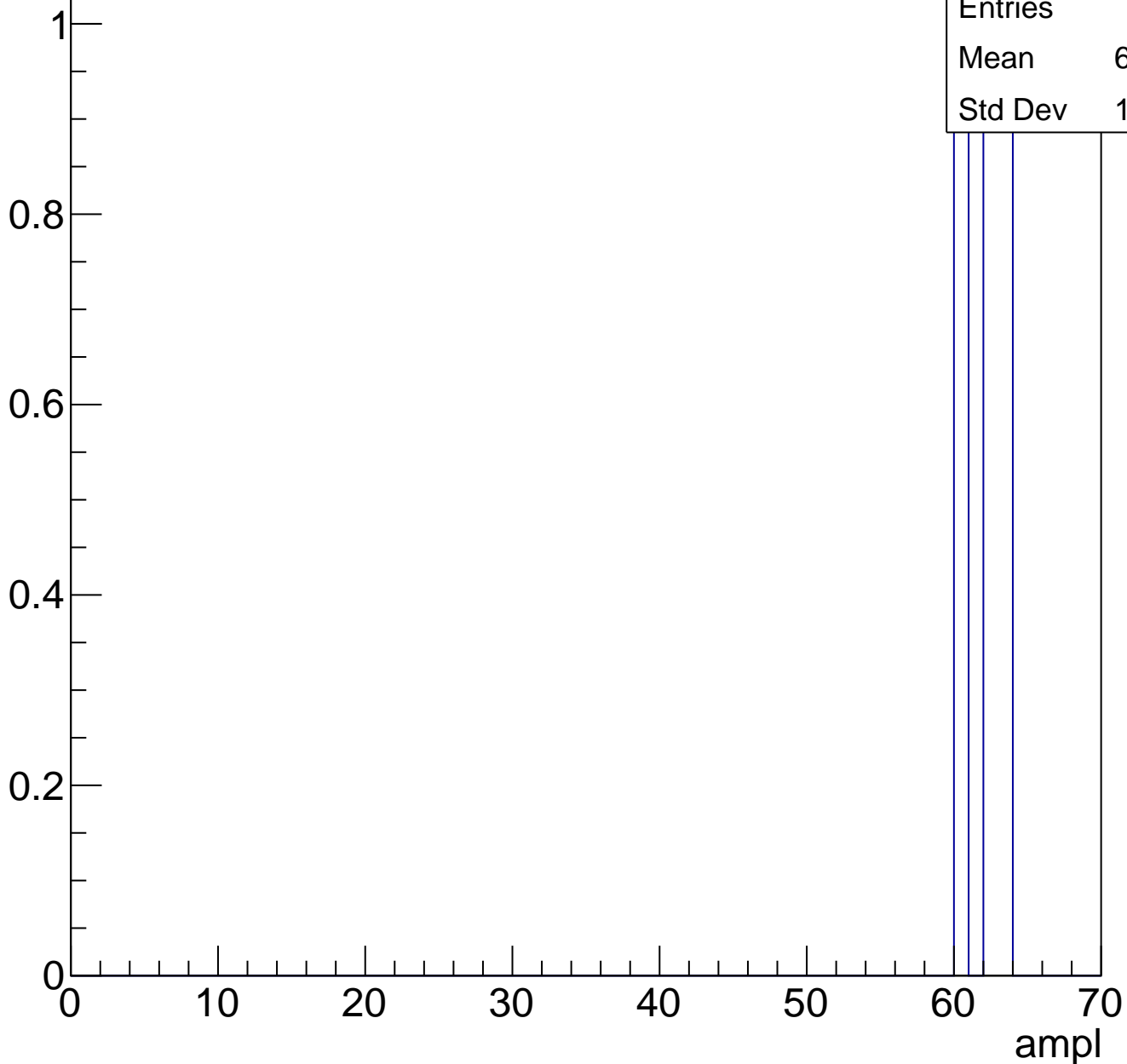
Entries	15
Mean	57.4
Std Dev	15.4



B1L003S, U16-ch120, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch121, adc0

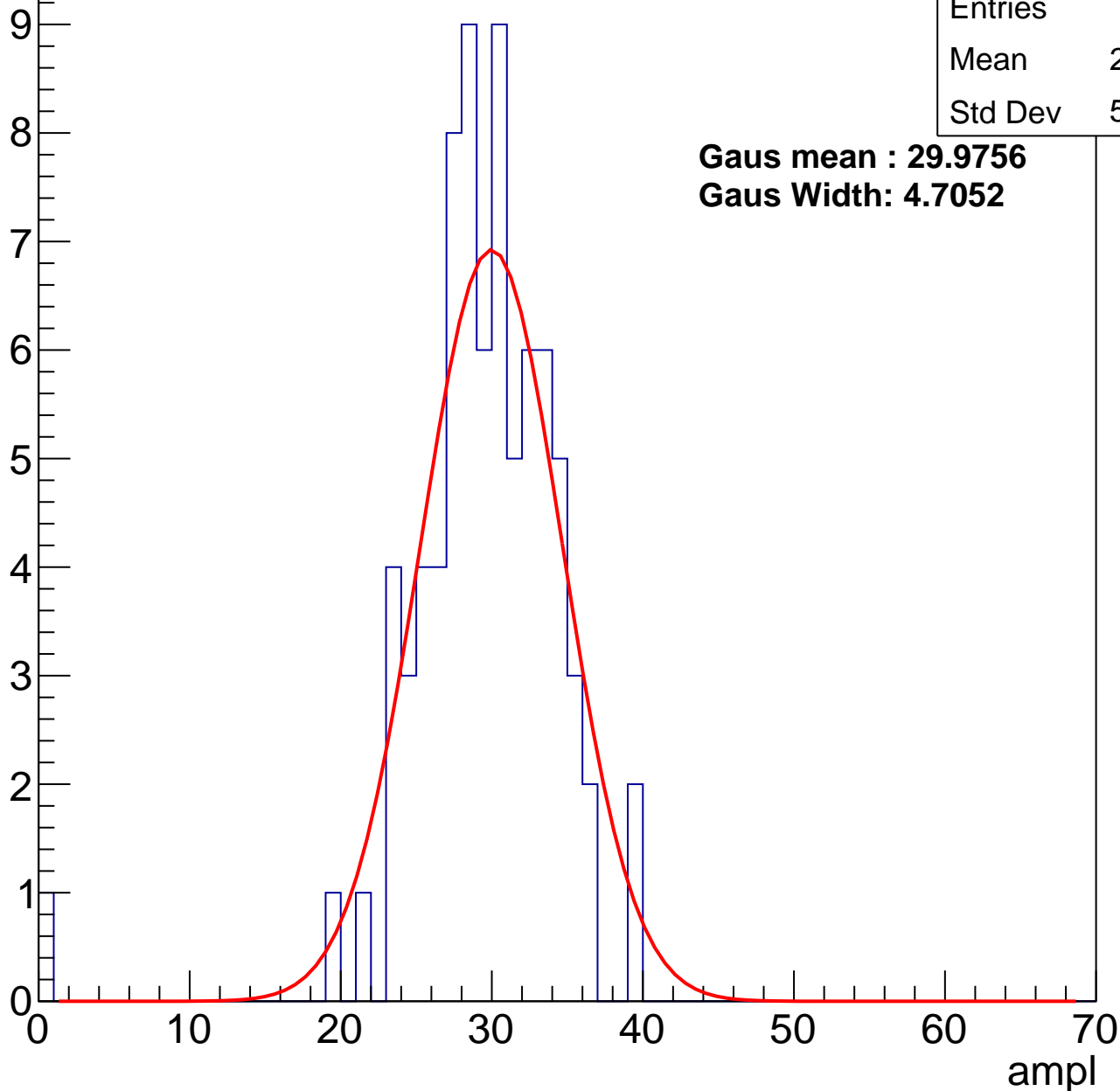
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	28.99
Std Dev	5.125

Gaus mean : 29.9756

Gaus Width: 4.7052



B1L003S, U16-ch121, adc1

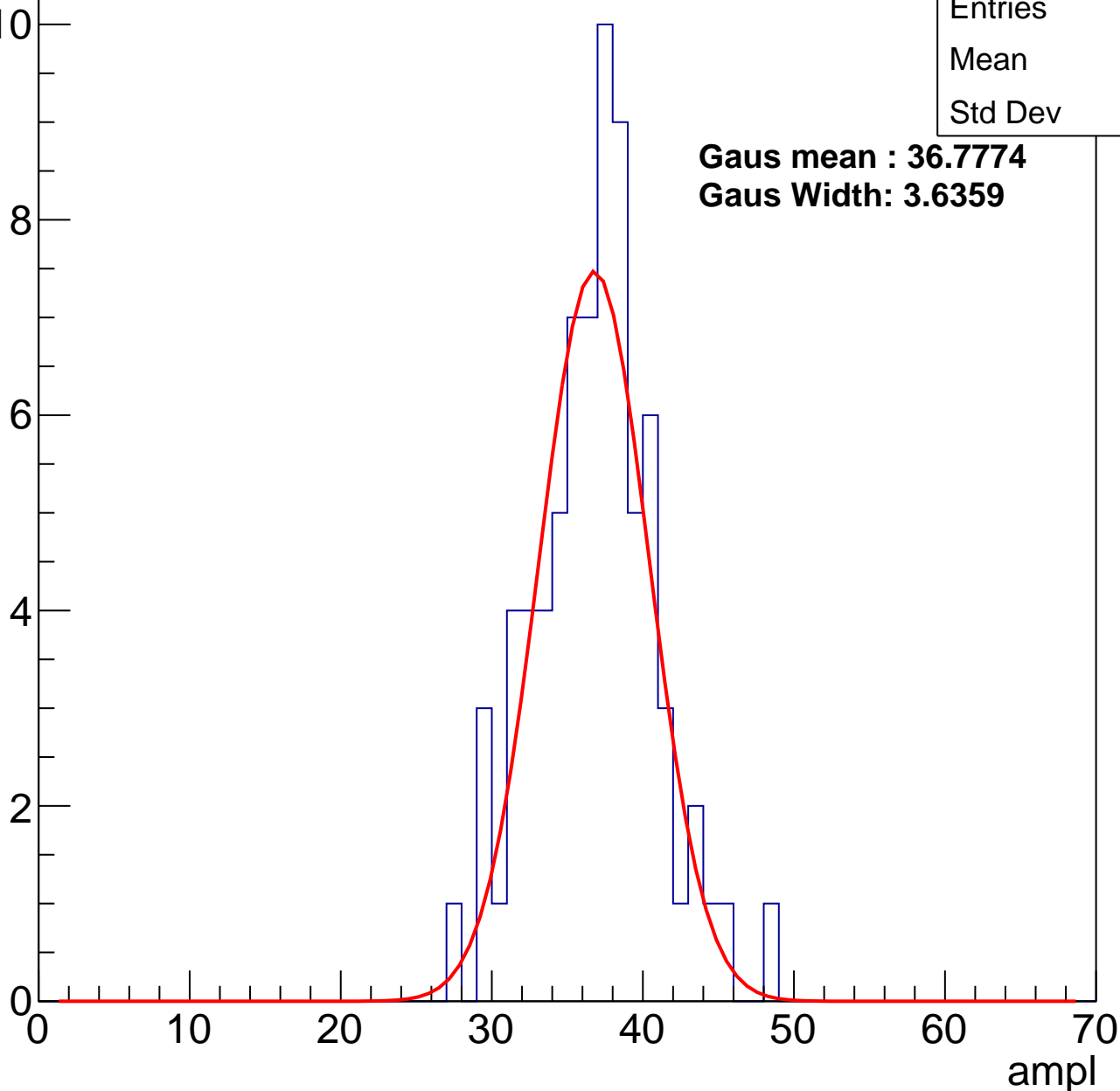
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.4
Std Dev	3.94

Gaus mean : 36.7774

Gaus Width: 3.6359



B1L003S, U16-ch121, adc2

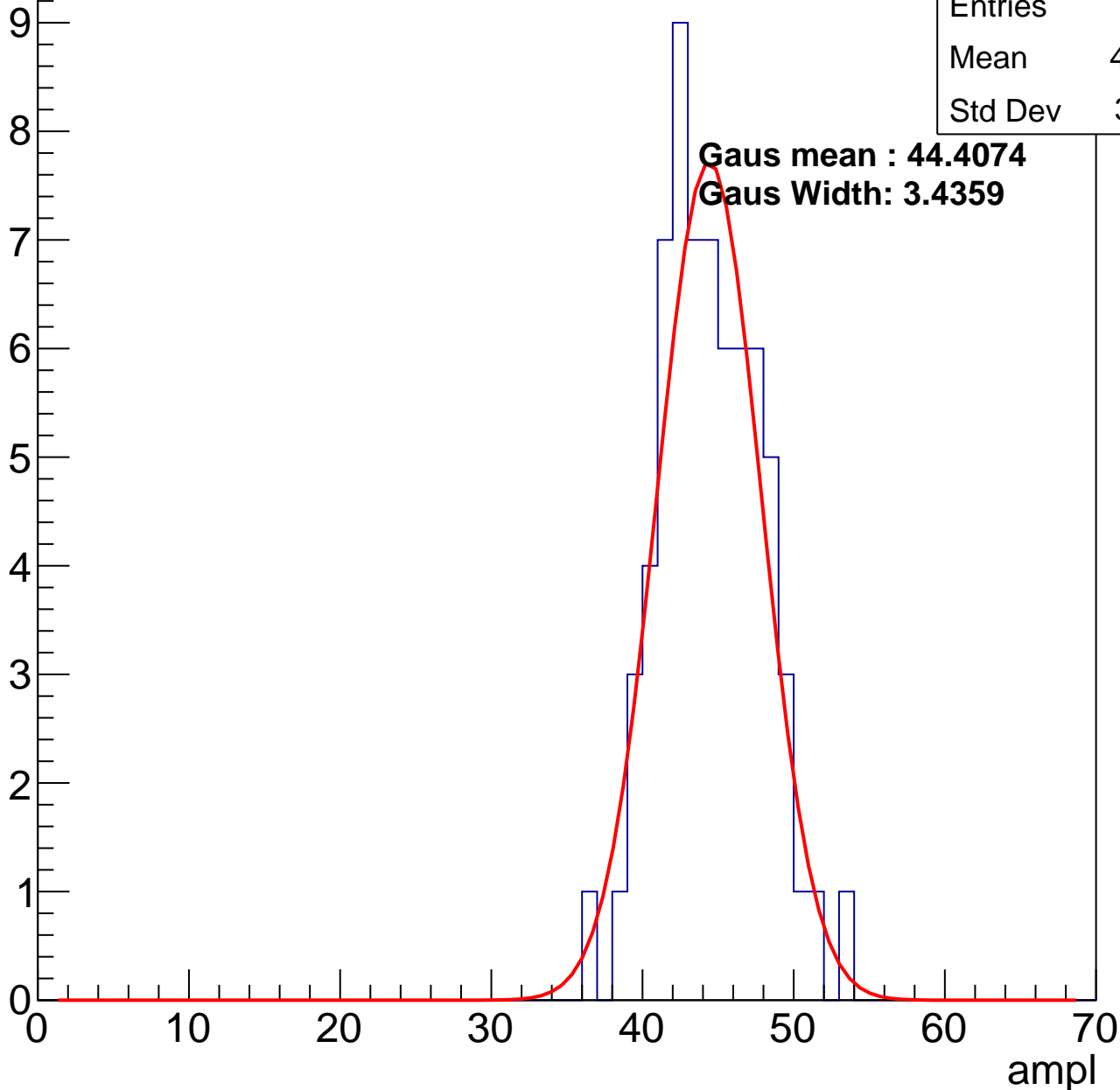
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	44.03
Std Dev	3.321

Gaus mean : 44.4074

Gaus Width: 3.4359

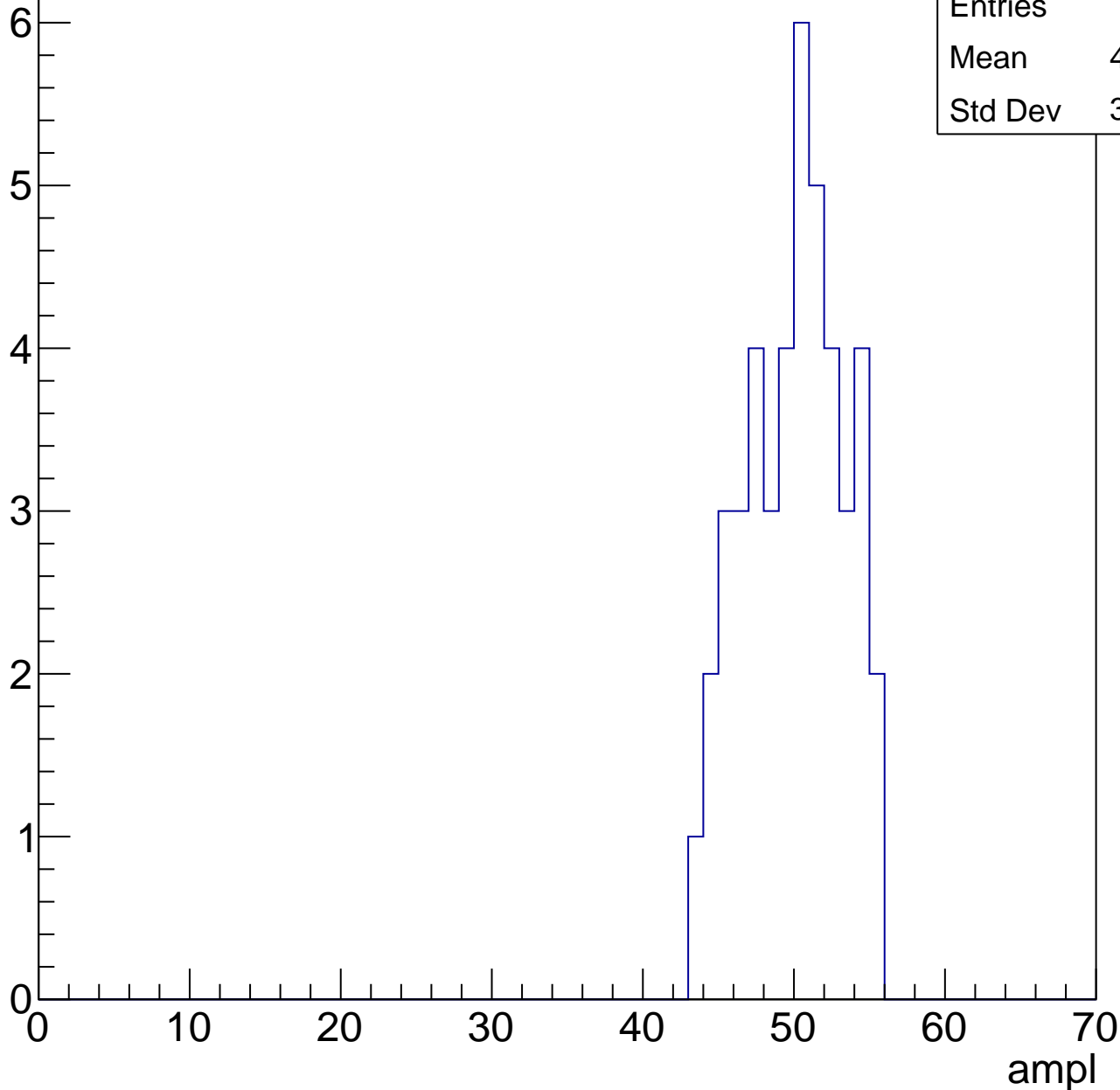


B1L003S, U16-ch121, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	49.55
Std Dev	3.194

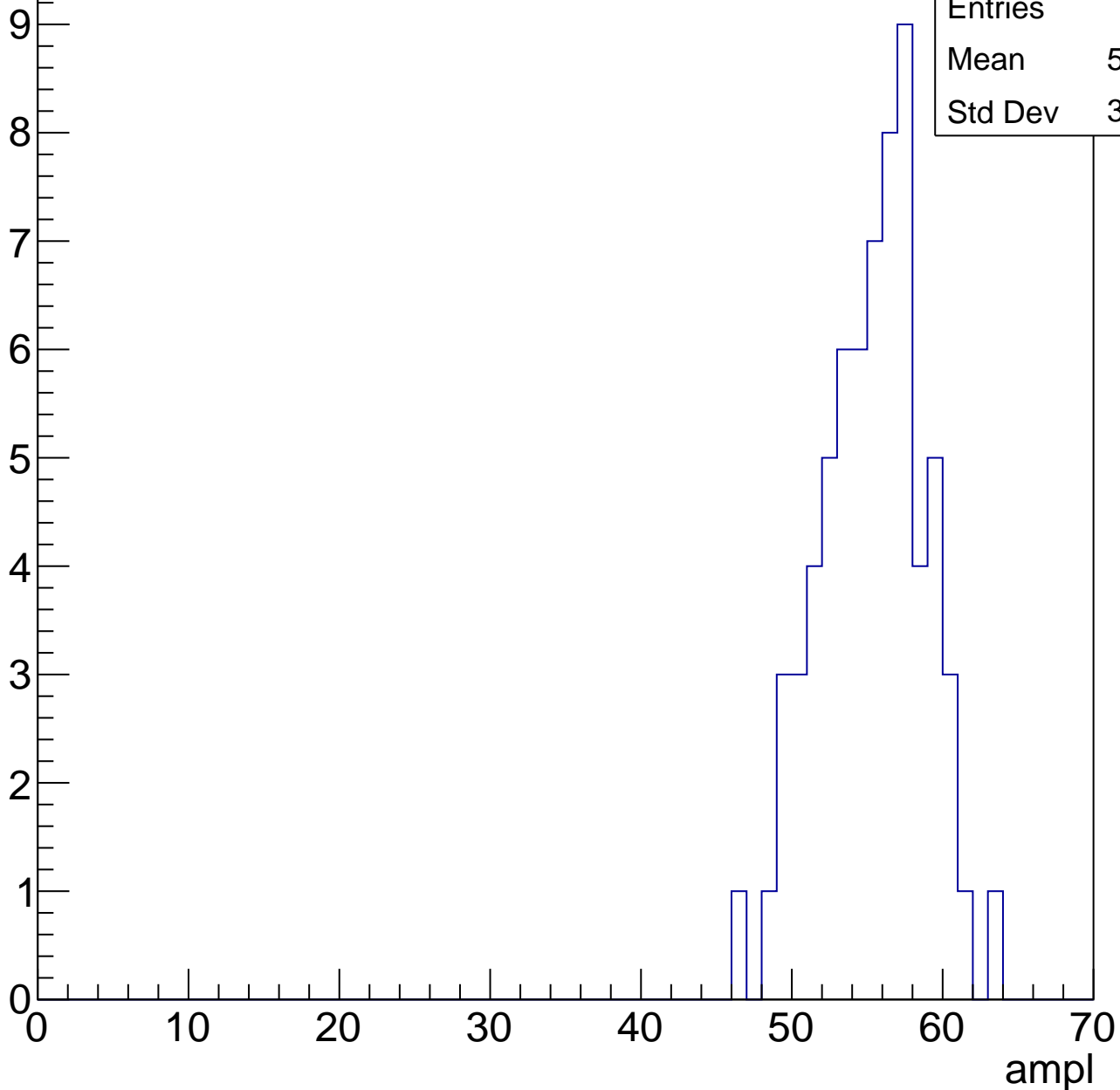


B1L003S, U16-ch121, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	54.84
Std Dev	3.423

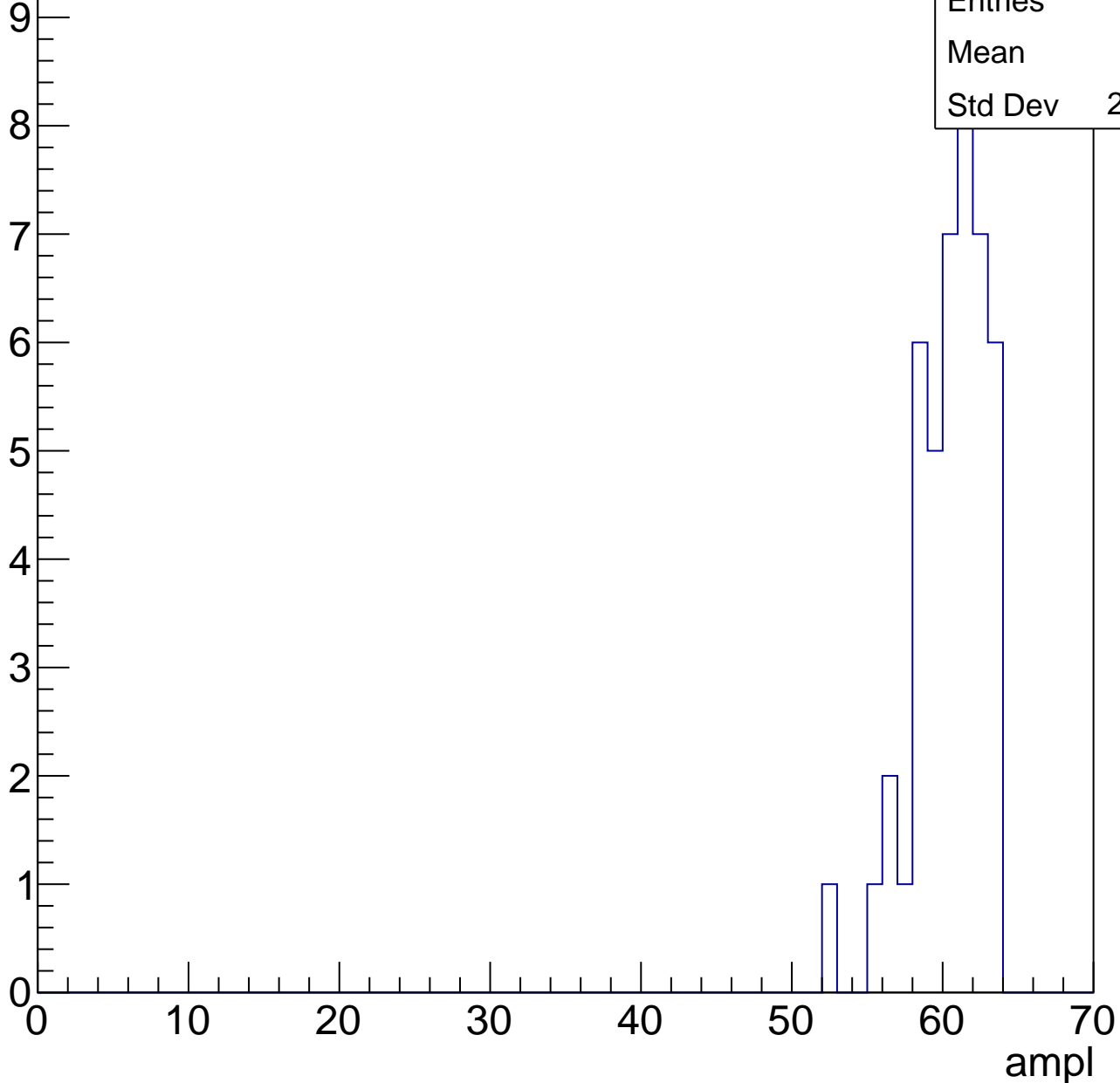


B1L003S, U16-ch121, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

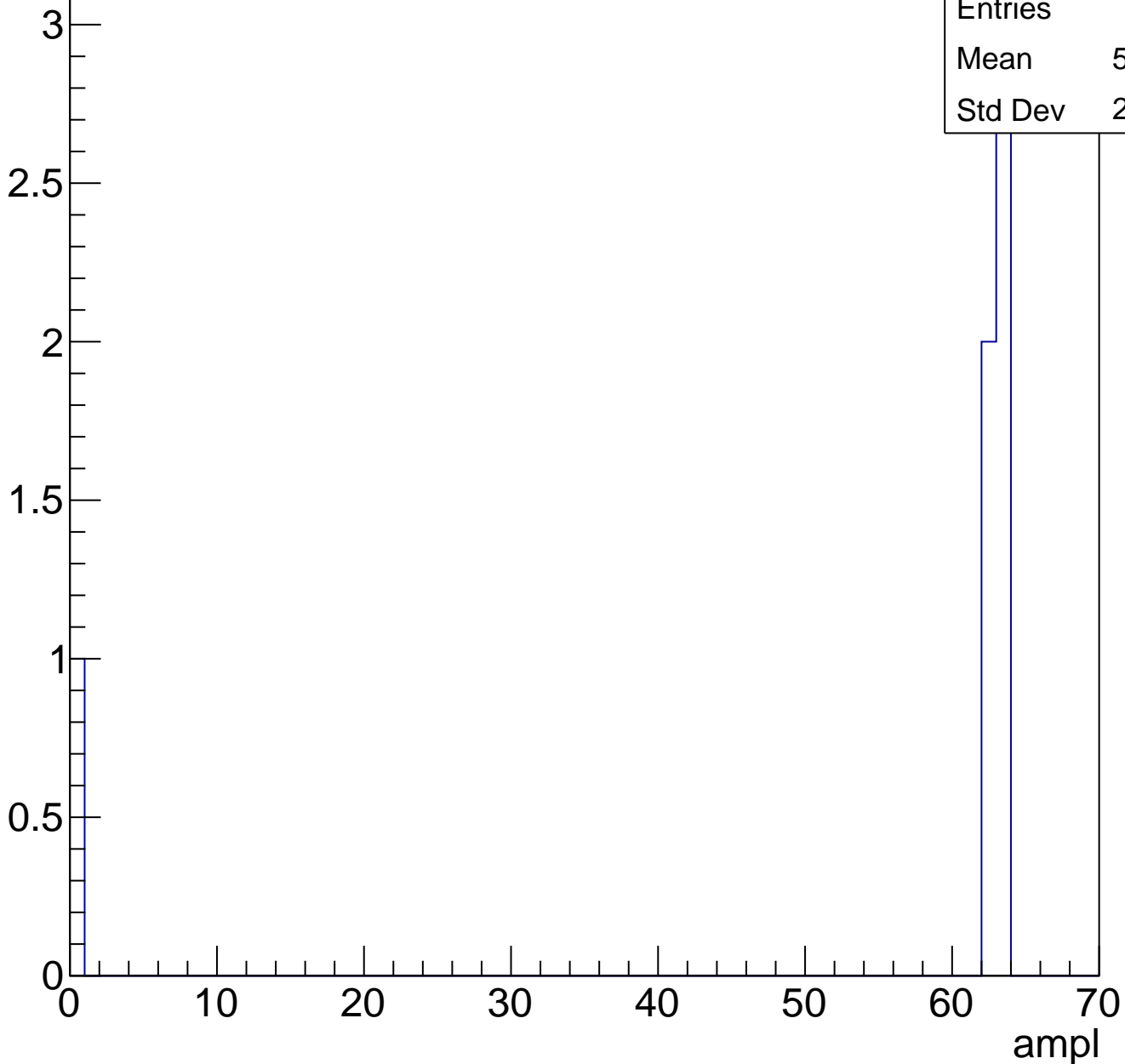
Entries	45
Mean	60
Std Dev	2.357



B1L003S, U16-ch121, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	6
Mean	52.17
Std Dev	23.33

B1L003S, U16-ch121, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch122, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	28.13
Std Dev	6.891

Gaus mean : 29.9433

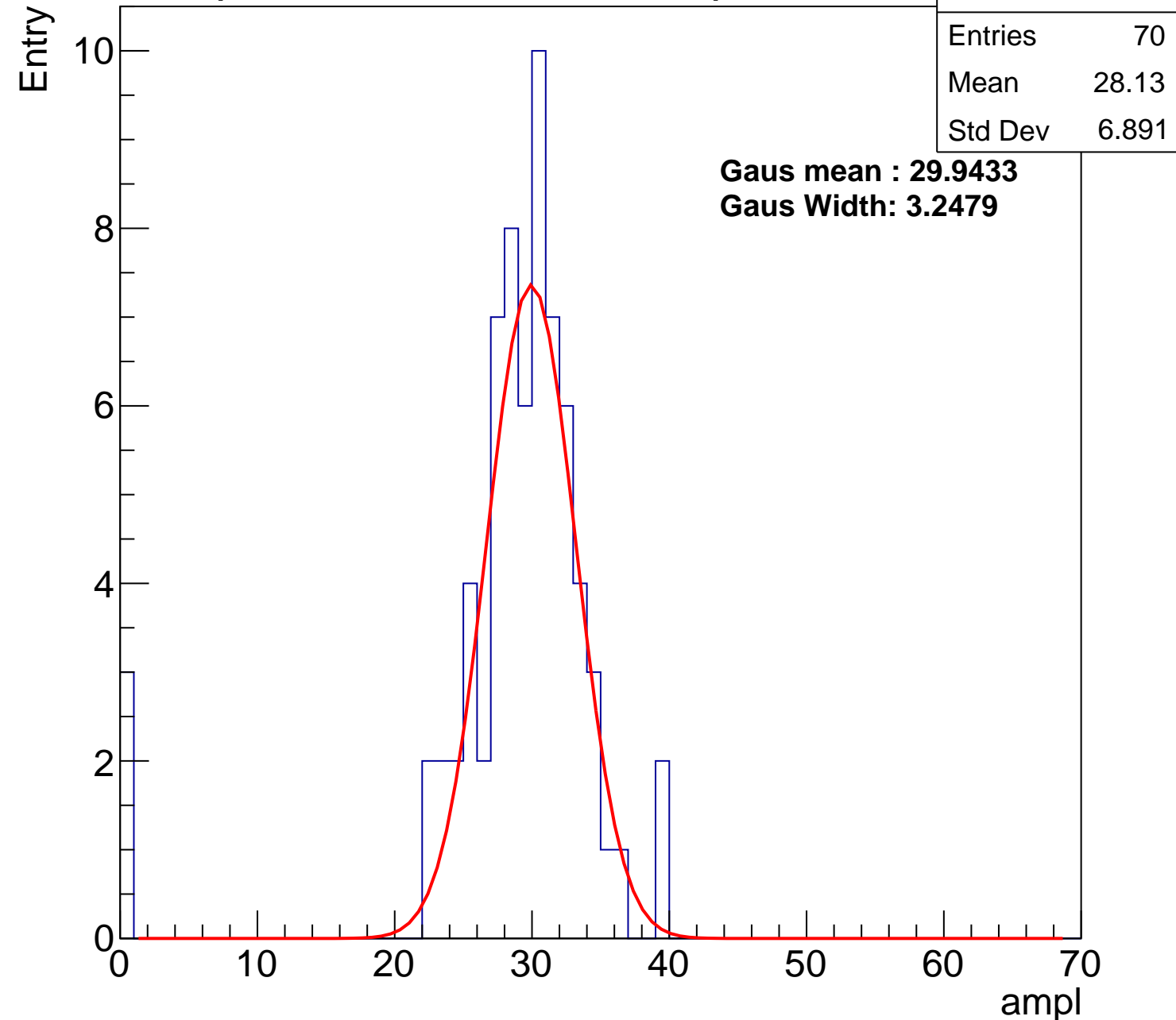
Gaus Width: 3.2479

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch122, adc1

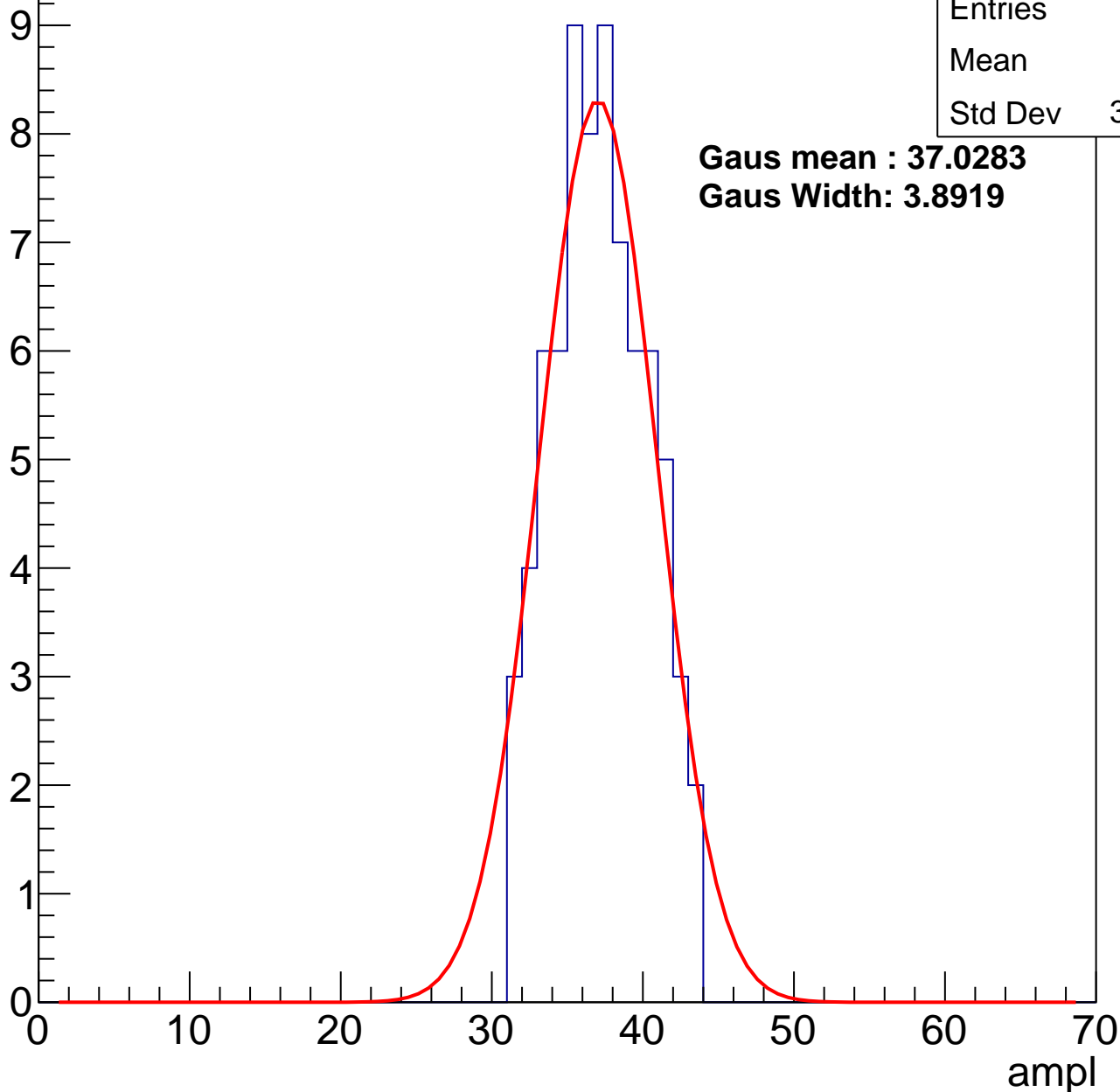
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	36.7
Std Dev	3.092

Gaus mean : 37.0283

Gaus Width: 3.8919



B1L003S, U16-ch122, adc2

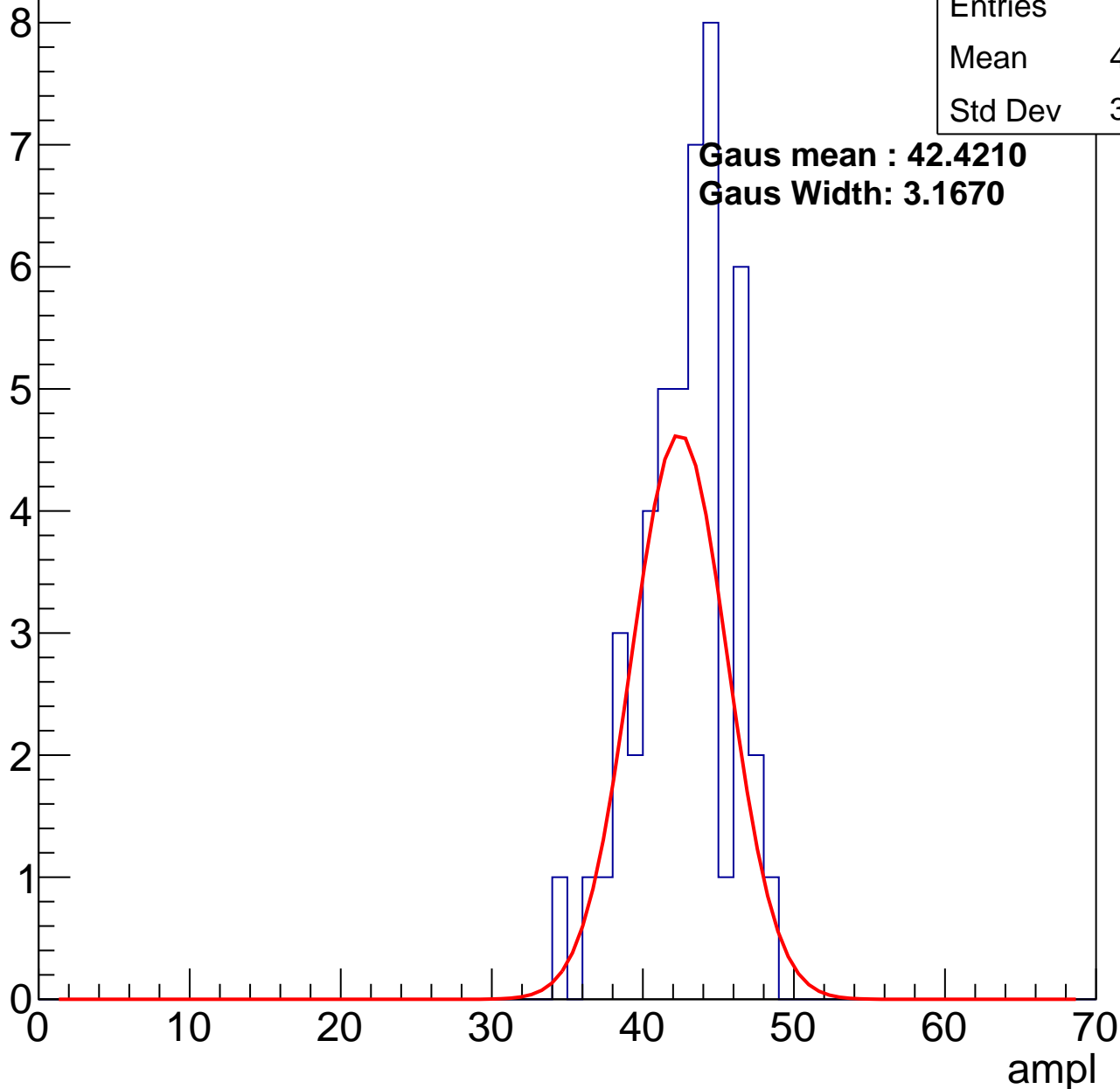
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	42.34
Std Dev	3.048

Gaus mean : 42.4210

Gaus Width: 3.1670

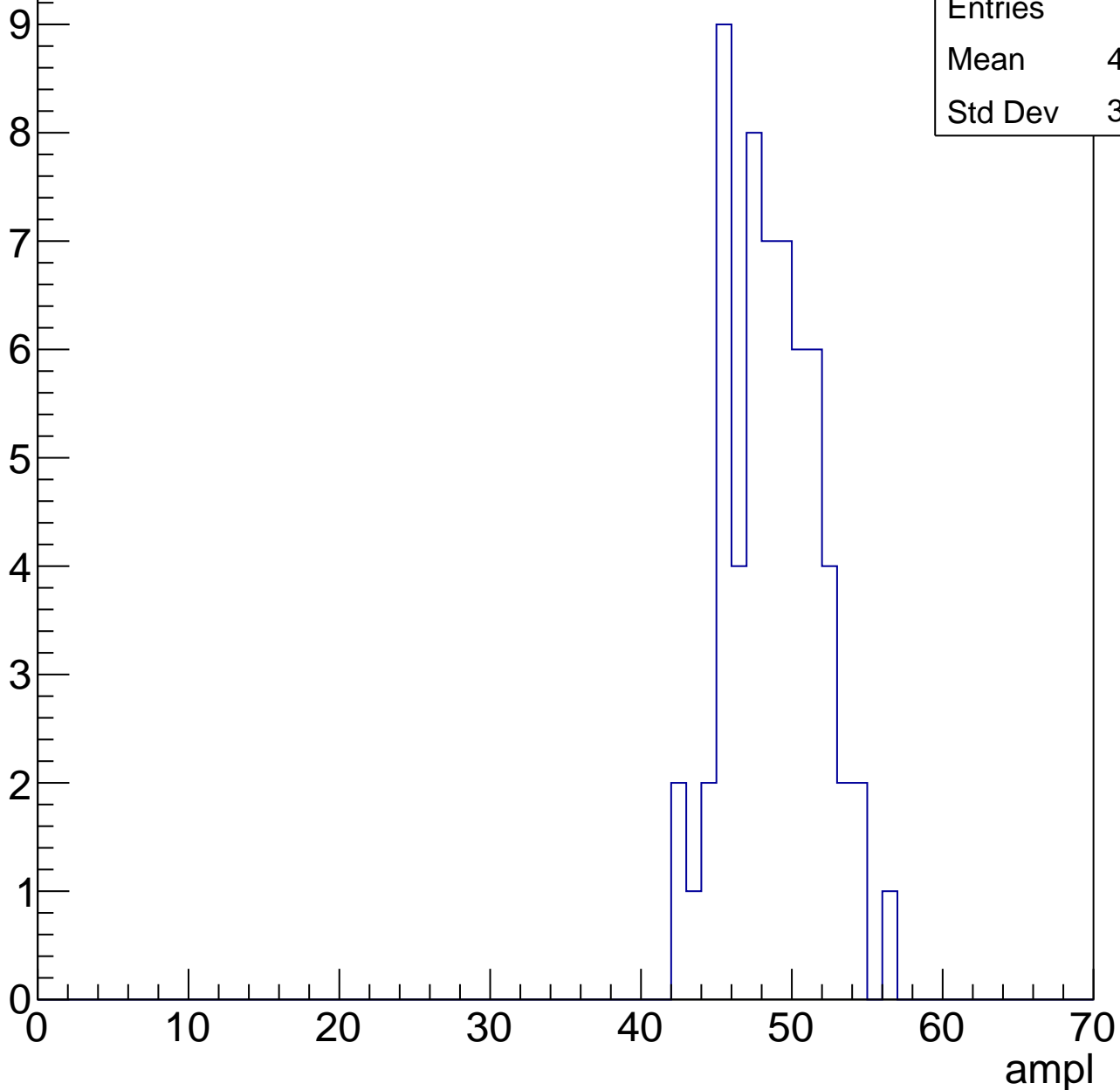


B1L003S, U16-ch122, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.25
Std Dev	3.044

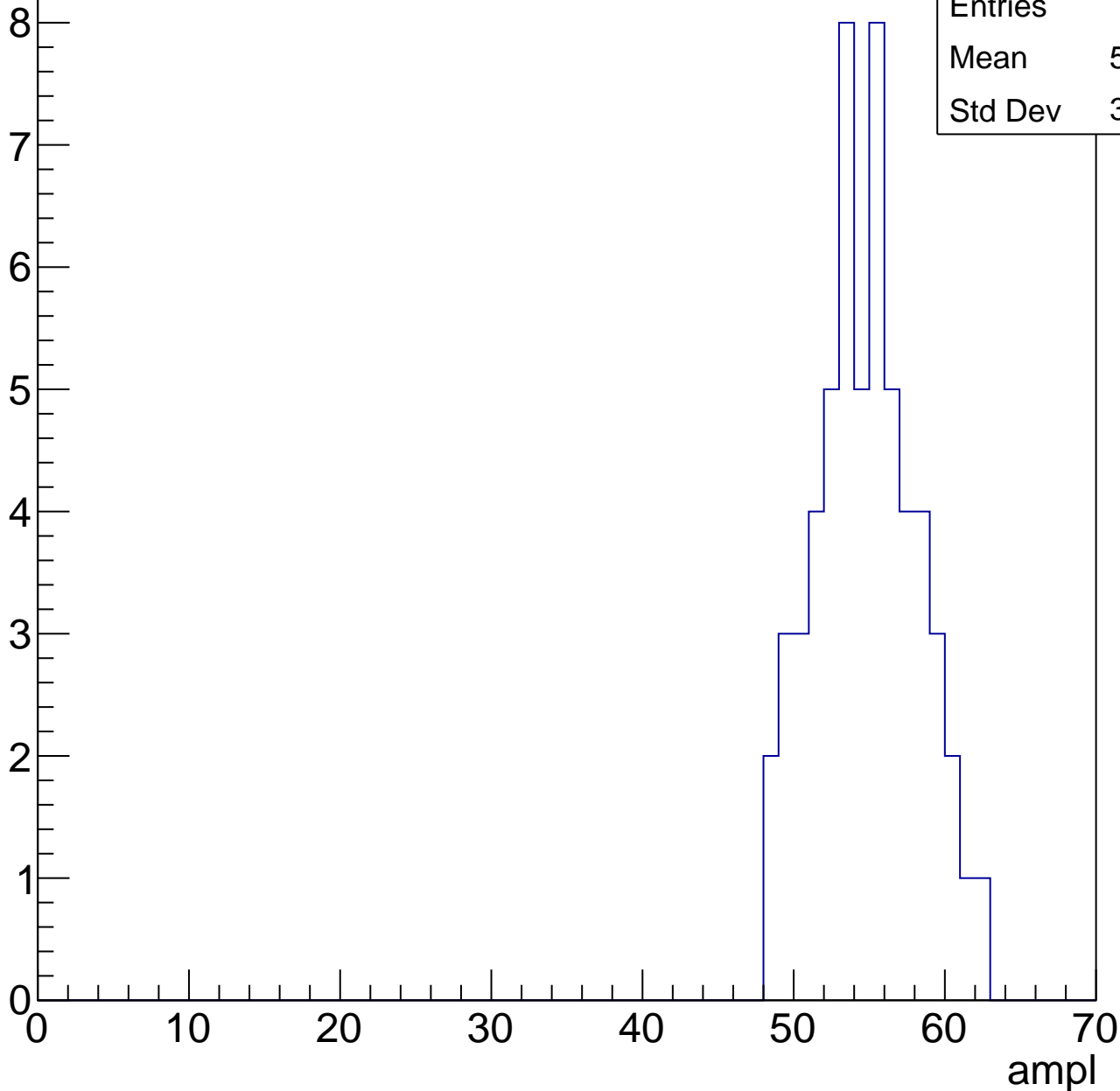


B1L003S, U16-ch122, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.33
Std Dev	3.324

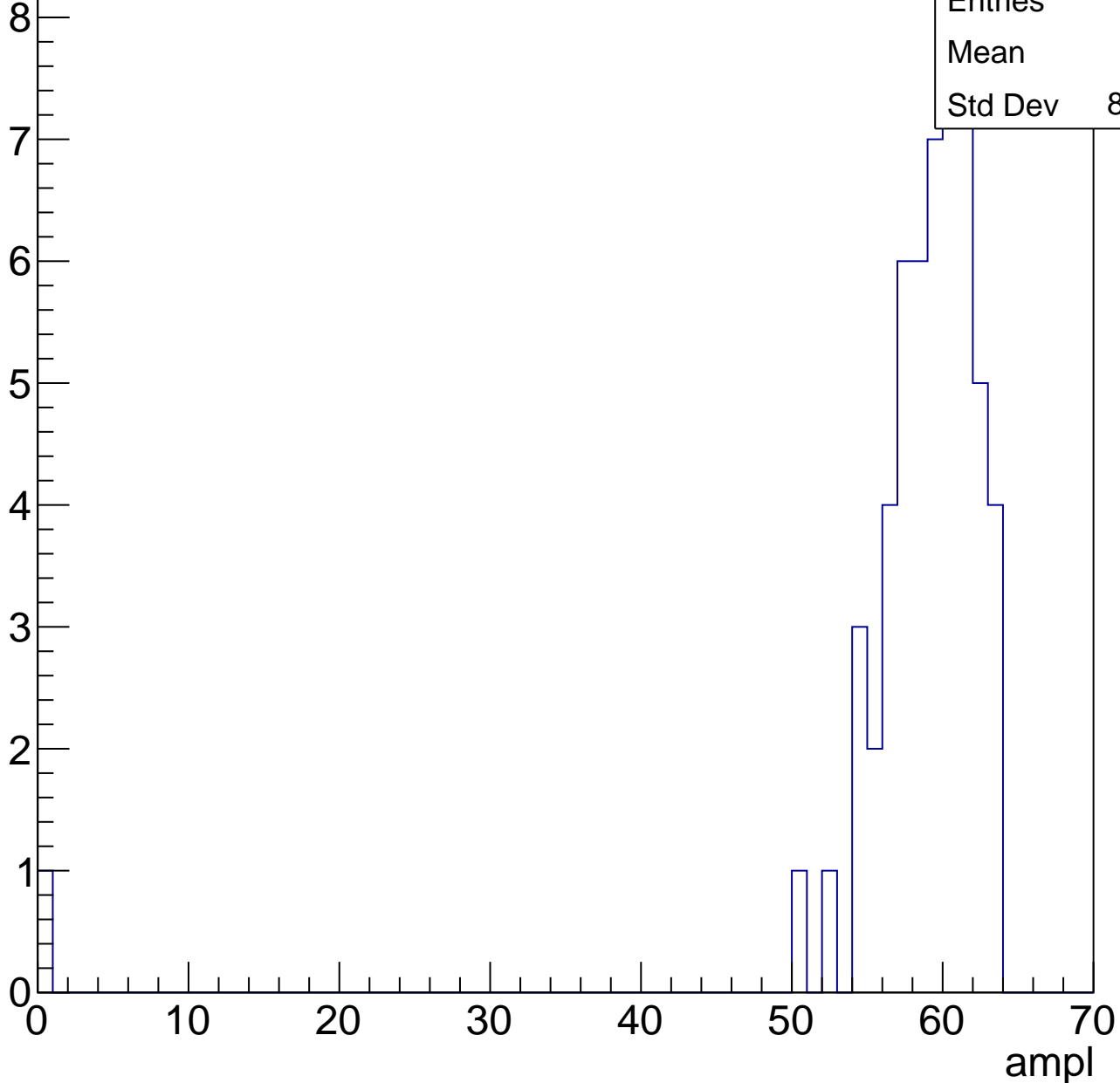


B1L003S, U16-ch122, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	57.7
Std Dev	8.278

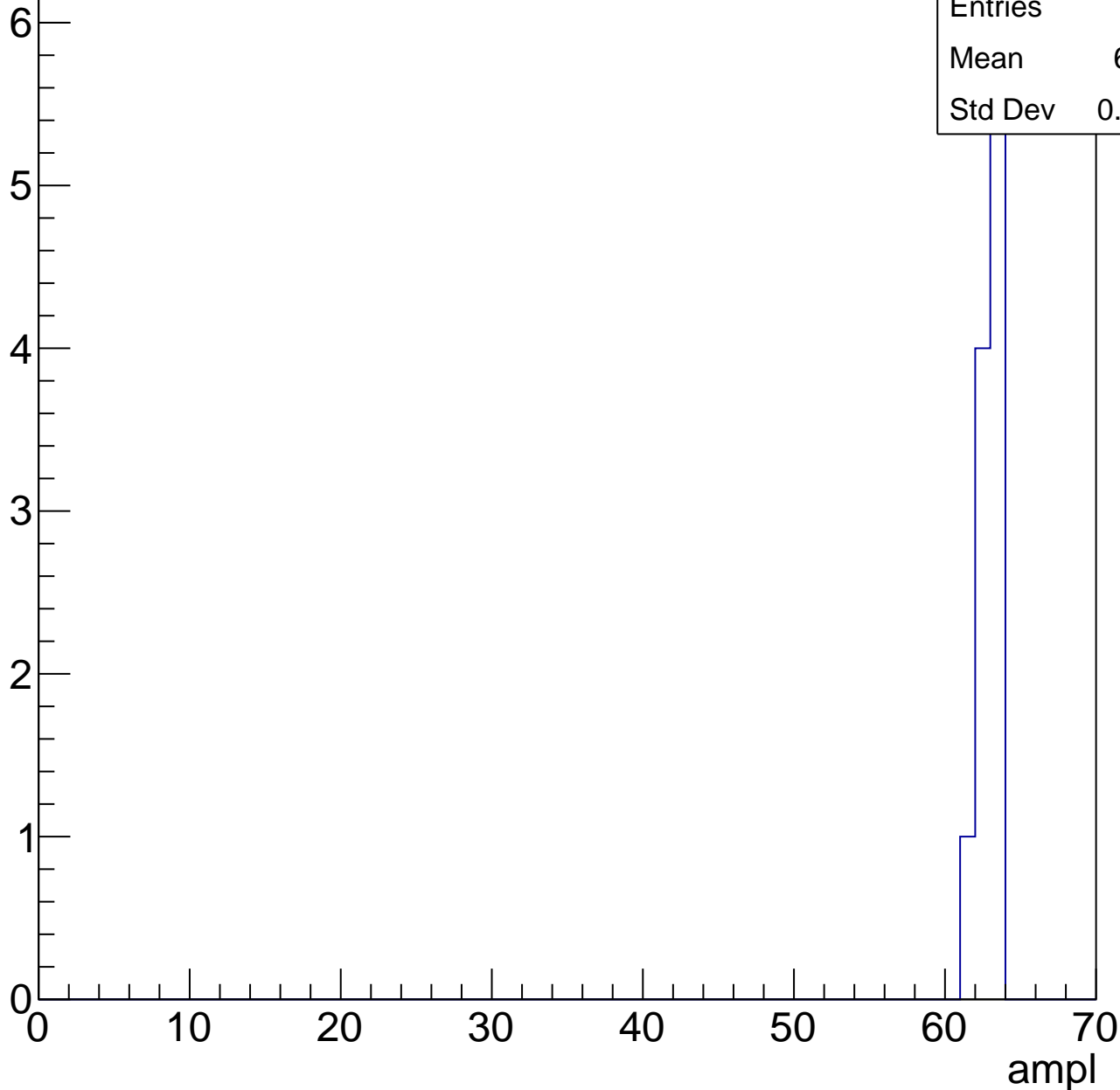


B1L003S, U16-ch122, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.45
Std Dev	0.6556



B1L003S, U16-ch122, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch123, adc0

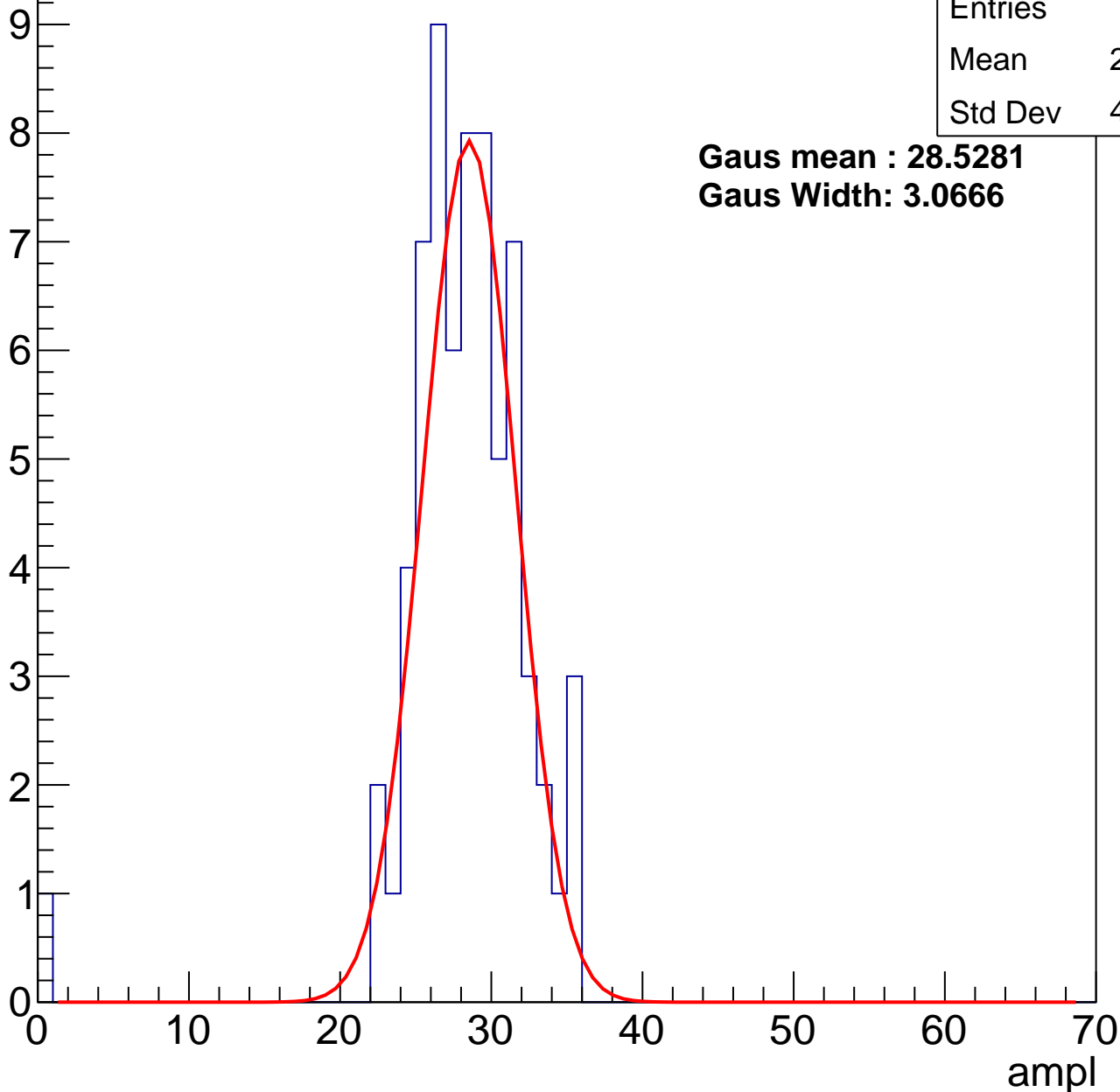
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	27.73
Std Dev	4.599

Gaus mean : 28.5281

Gaus Width: 3.0666



B1L003S, U16-ch123, adc1

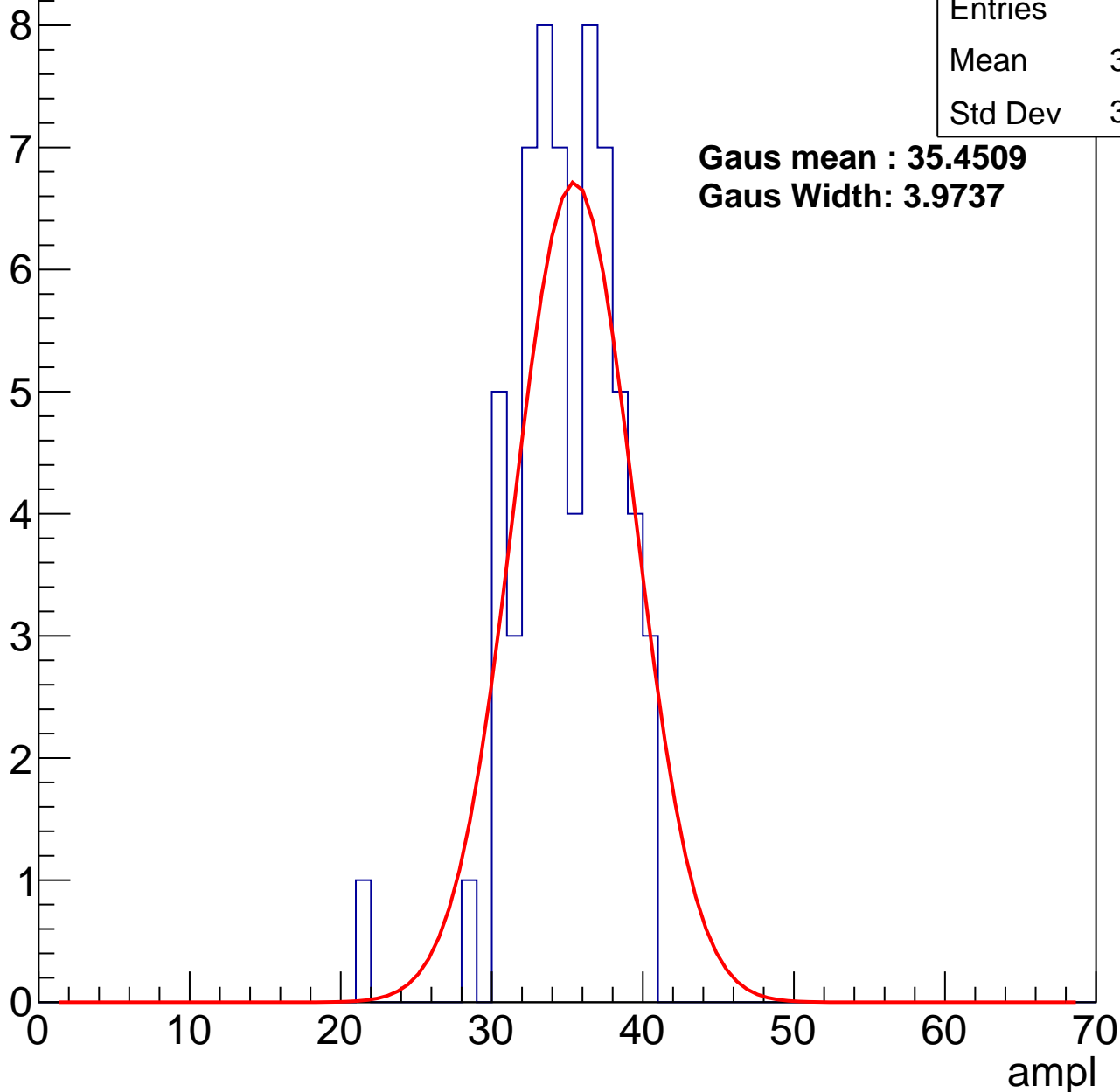
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	34.46
Std Dev	3.384

Gaus mean : 35.4509

Gaus Width: 3.9737



B1L003S, U16-ch123, adc2

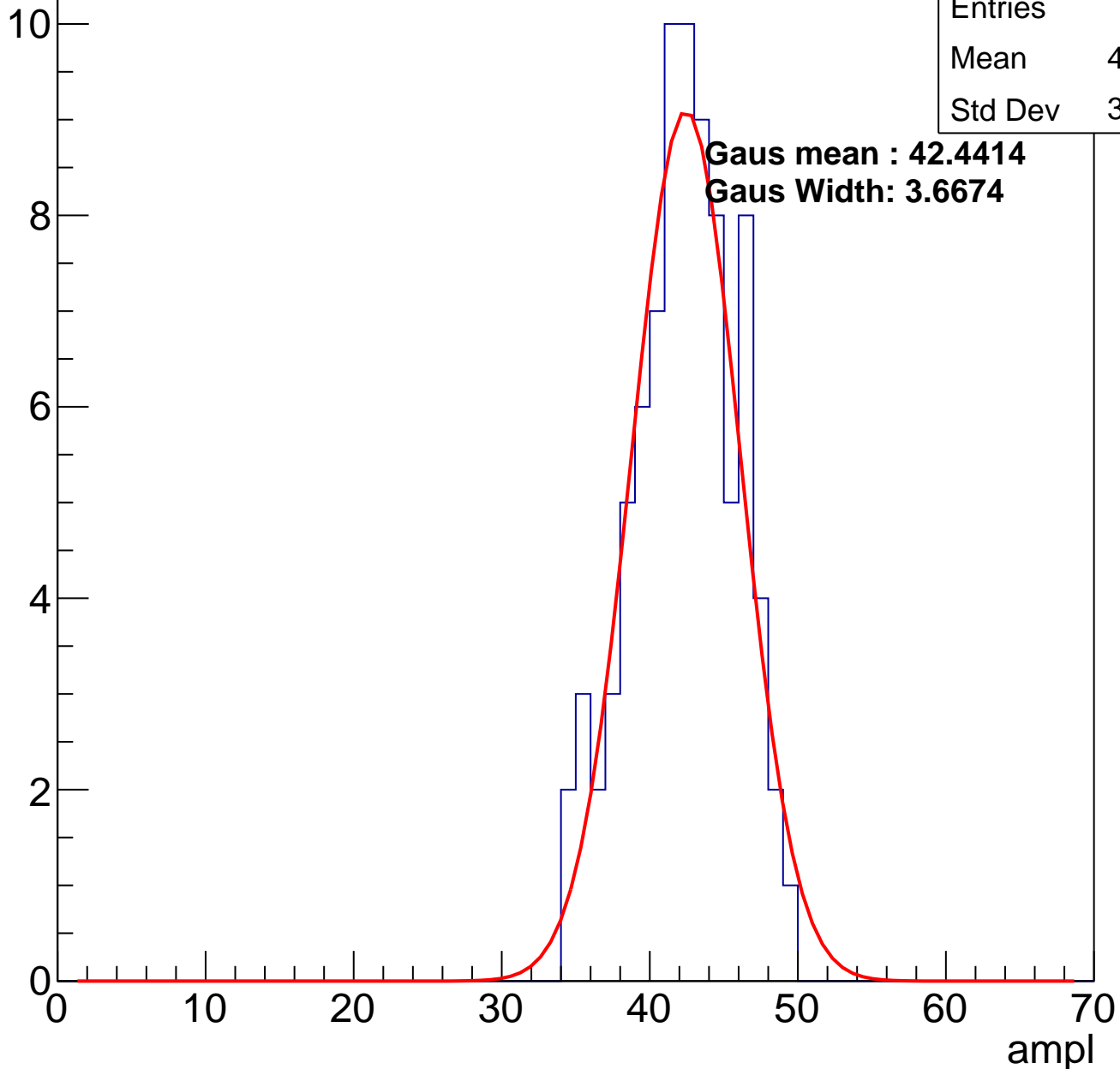
calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	41.82
Std Dev	3.475

Gaus mean : 42.4414

Gaus Width: 3.6674

Entry

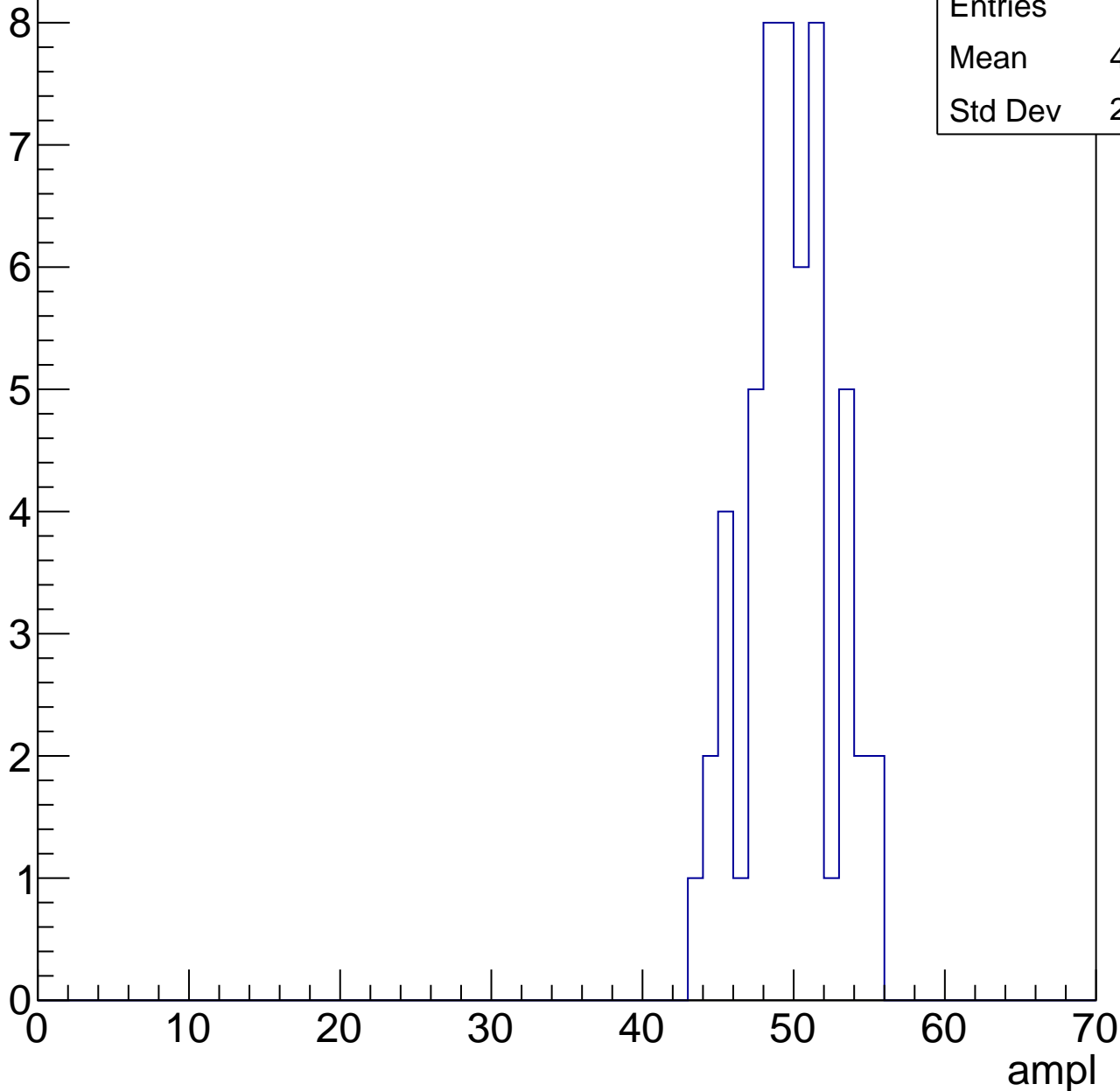


B1L003S, U16-ch123, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	49.26
Std Dev	2.856

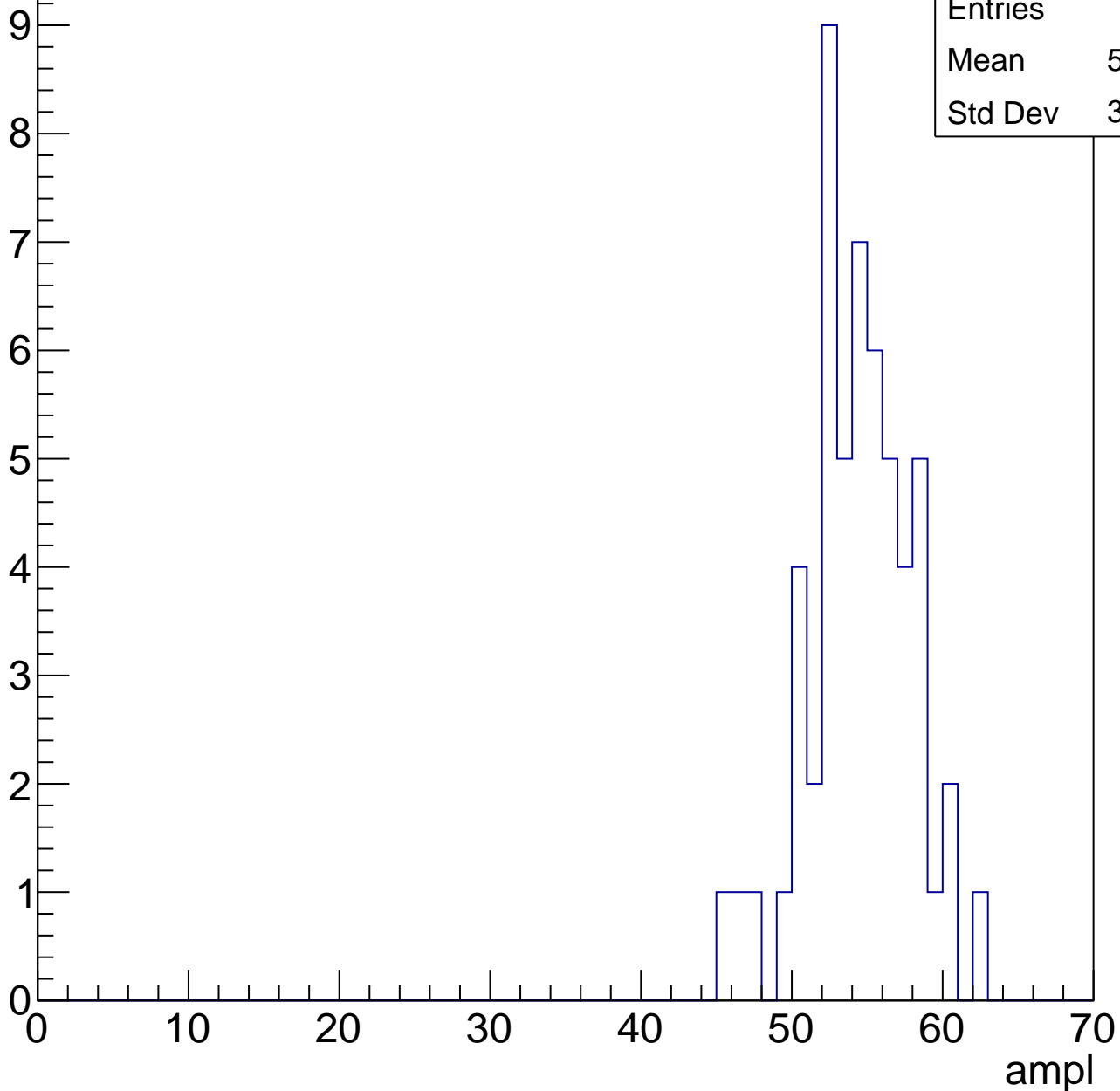


B1L003S, U16-ch123, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	53.98
Std Dev	3.424

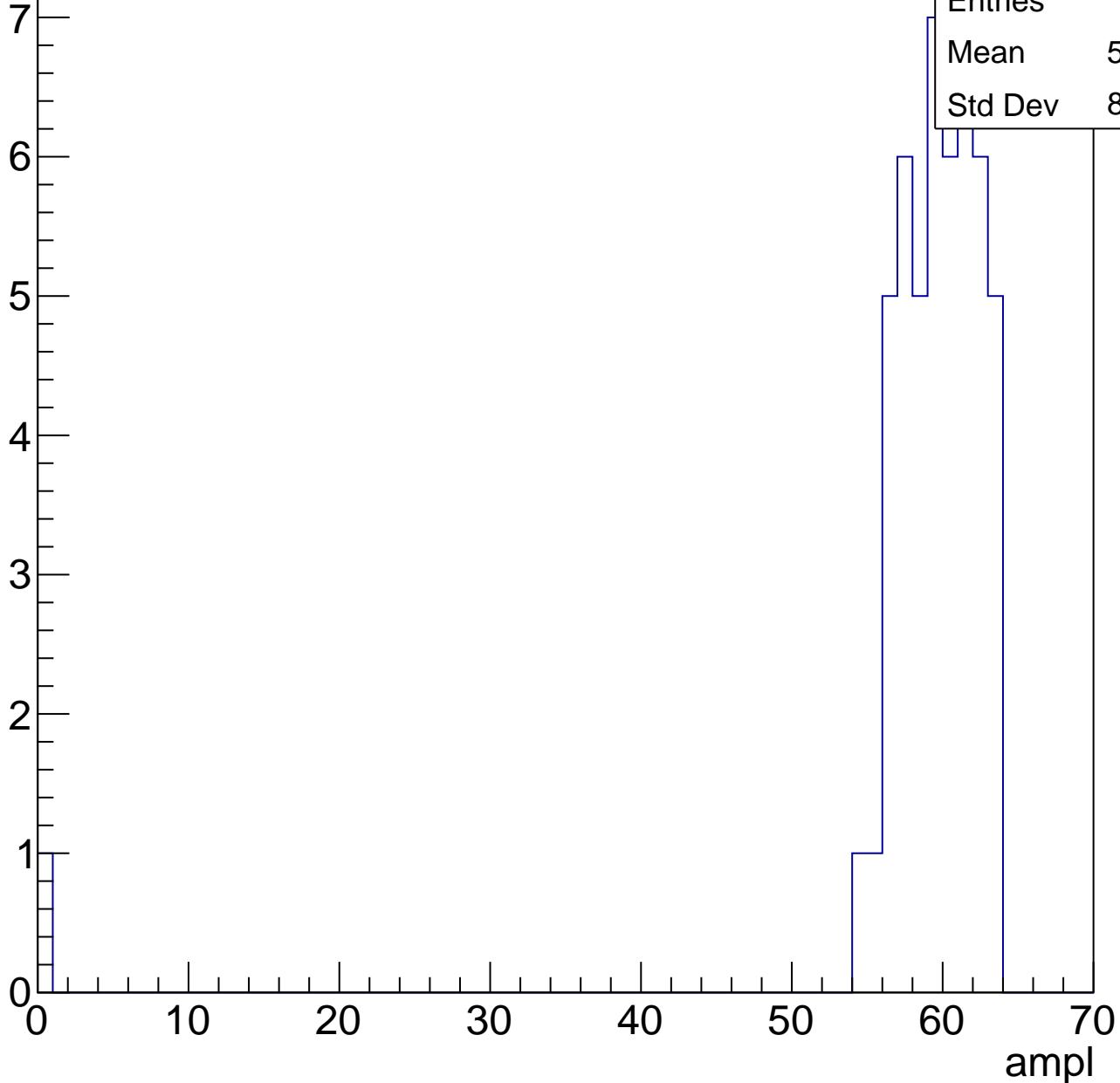


B1L003S, U16-ch123, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	58.16
Std Dev	8.636

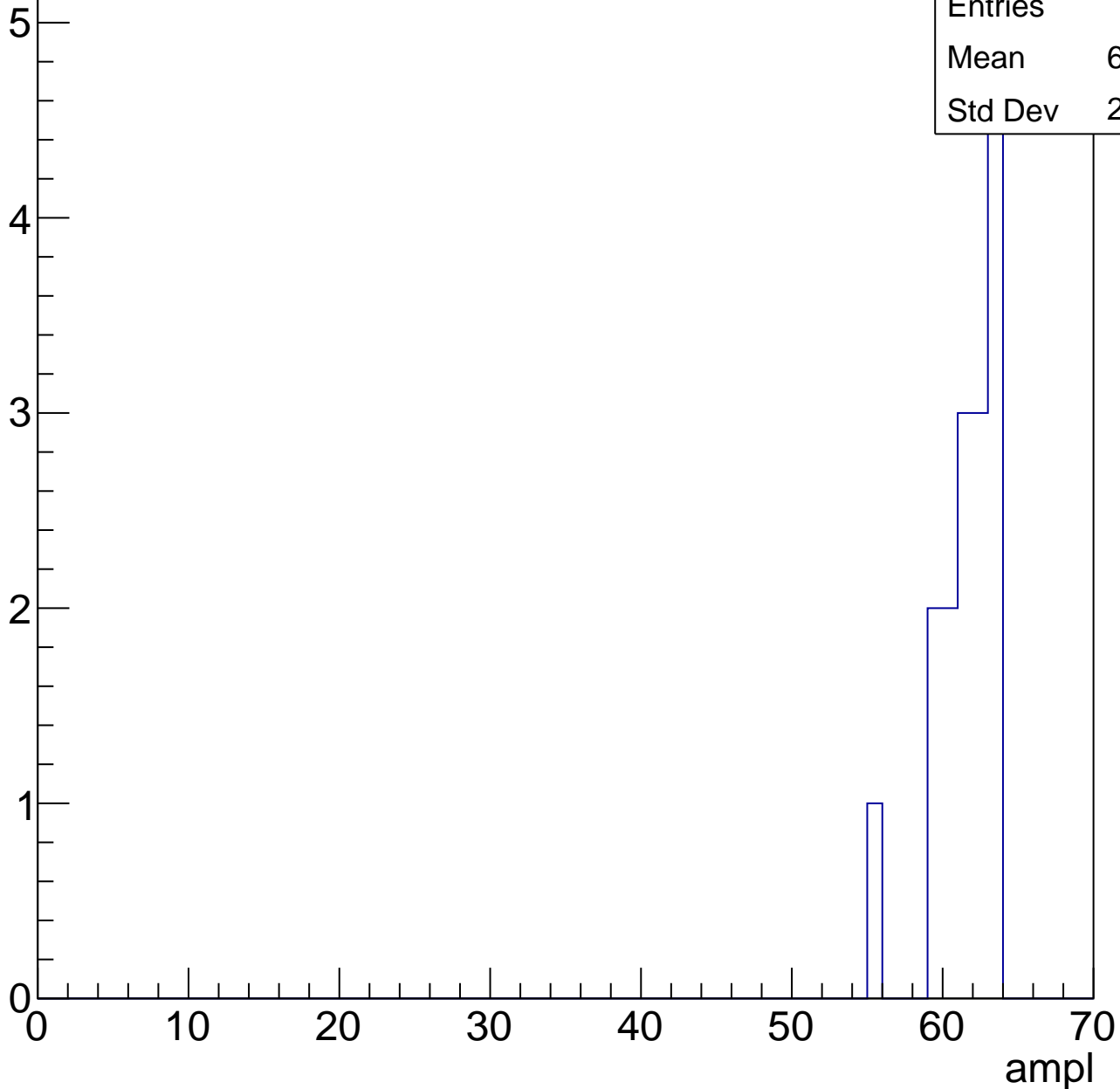


B1L003S, U16-ch123, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	61.06
Std Dev	2.076



B1L003S, U16-ch123, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U16-ch124, adc0

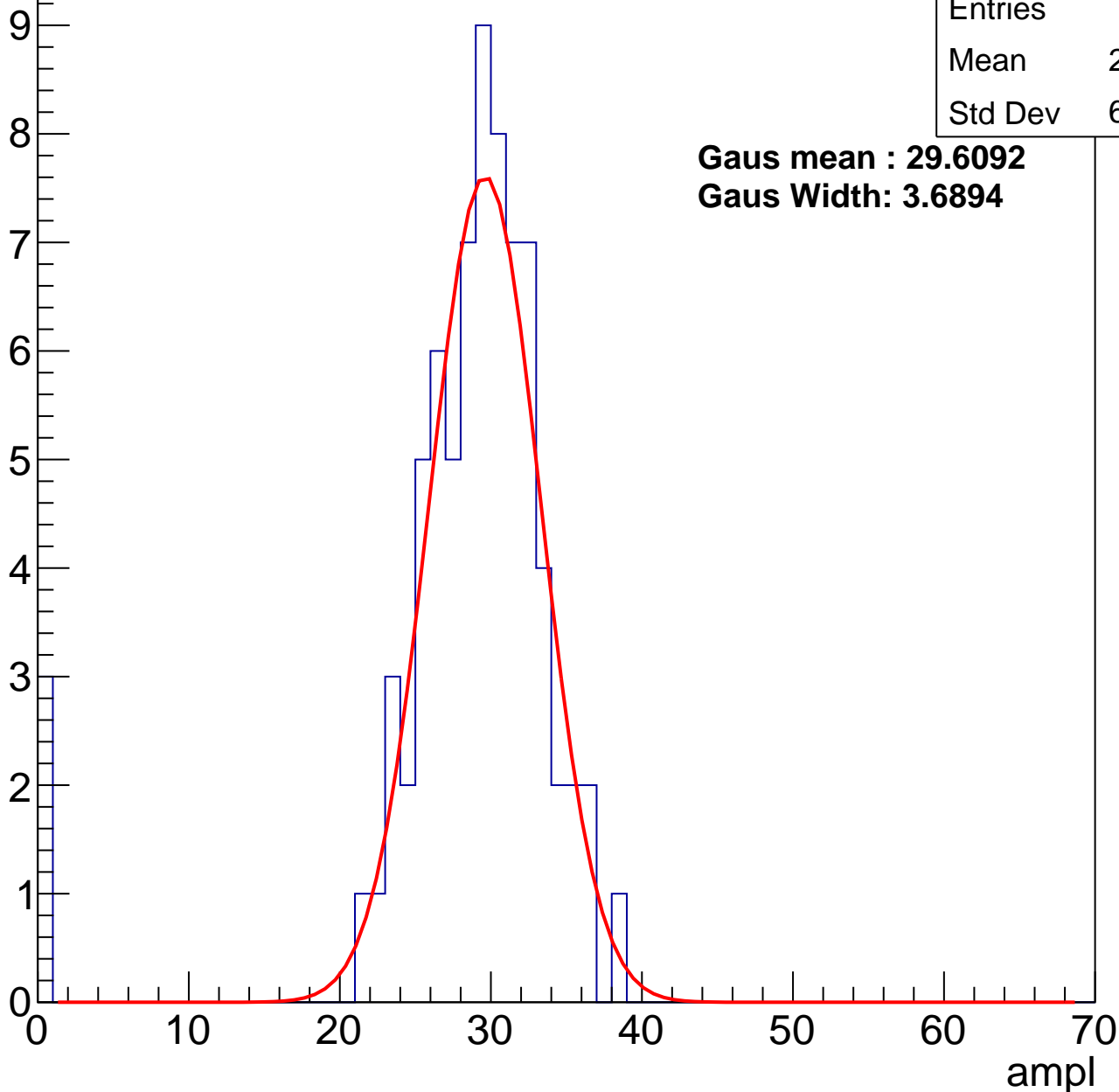
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	27.92
Std Dev	6.665

Gaus mean : 29.6092

Gaus Width: 3.6894



B1L003S, U16-ch124, adc1

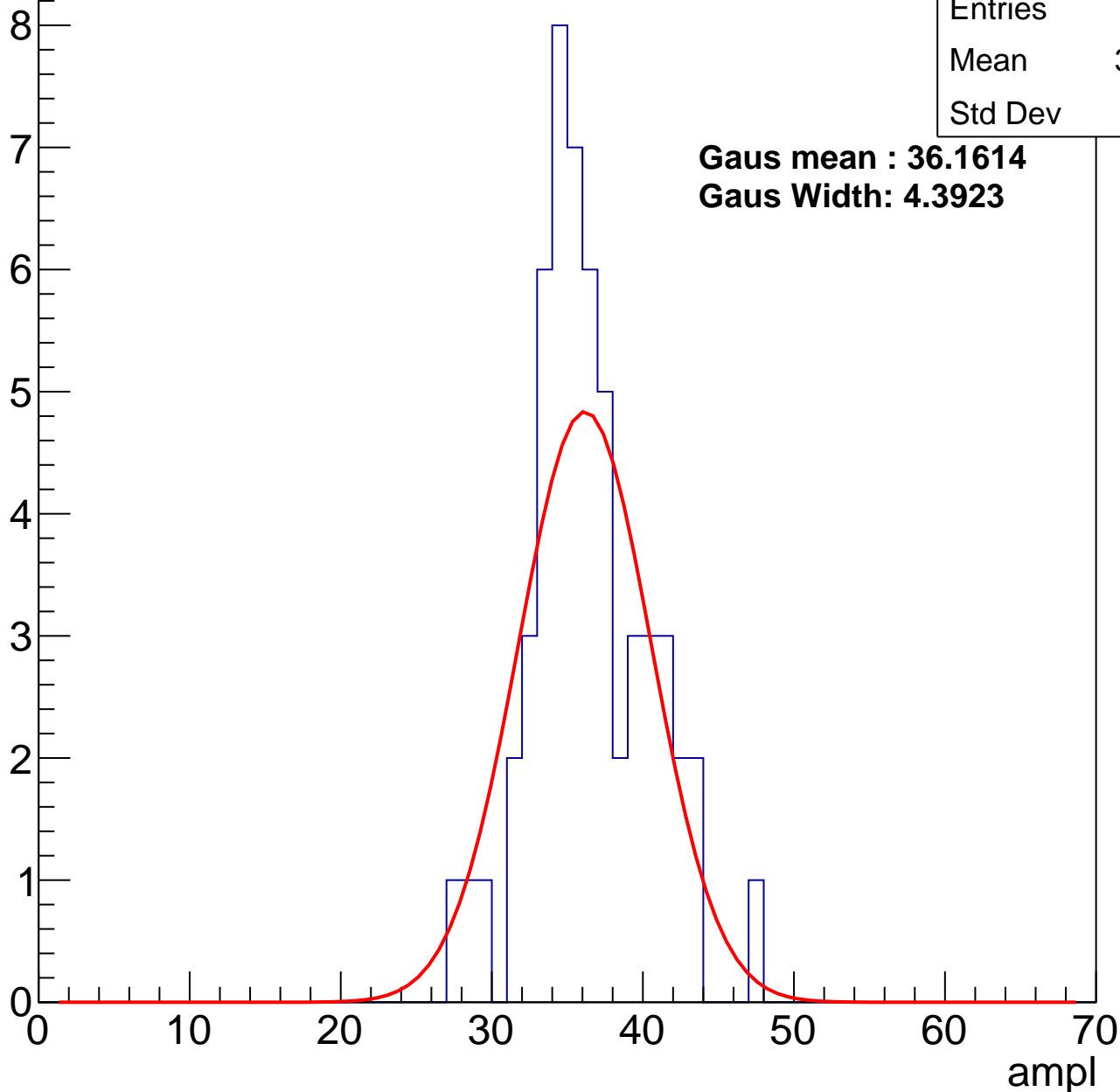
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35.91
Std Dev	3.87

Gaus mean : 36.1614

Gaus Width: 4.3923



B1L003S, U16-ch124, adc2

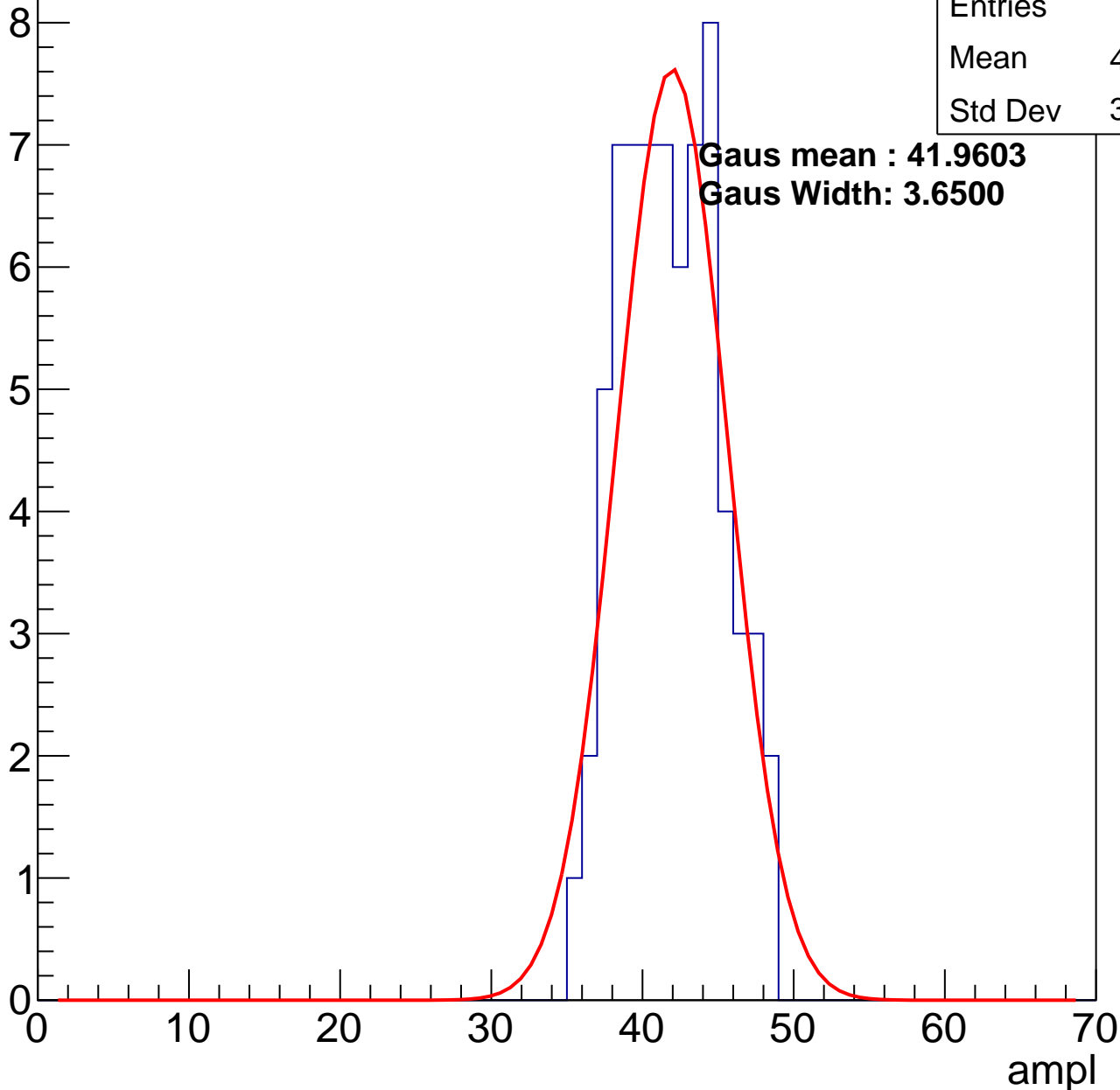
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.42
Std Dev	3.192

Gaus mean : 41.9603

Gaus Width: 3.6500

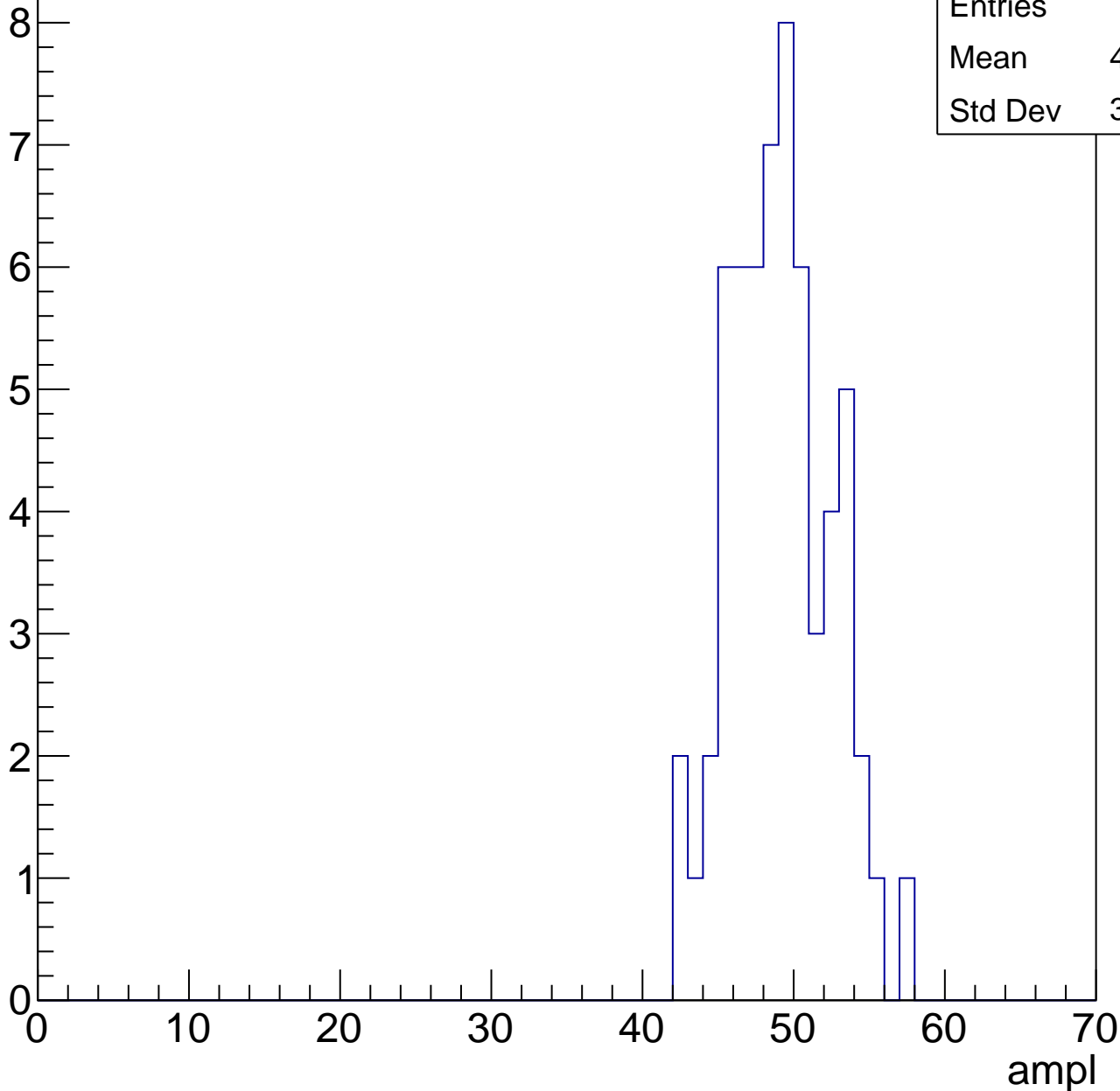


B1L003S, U16-ch124, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.62
Std Dev	3.266

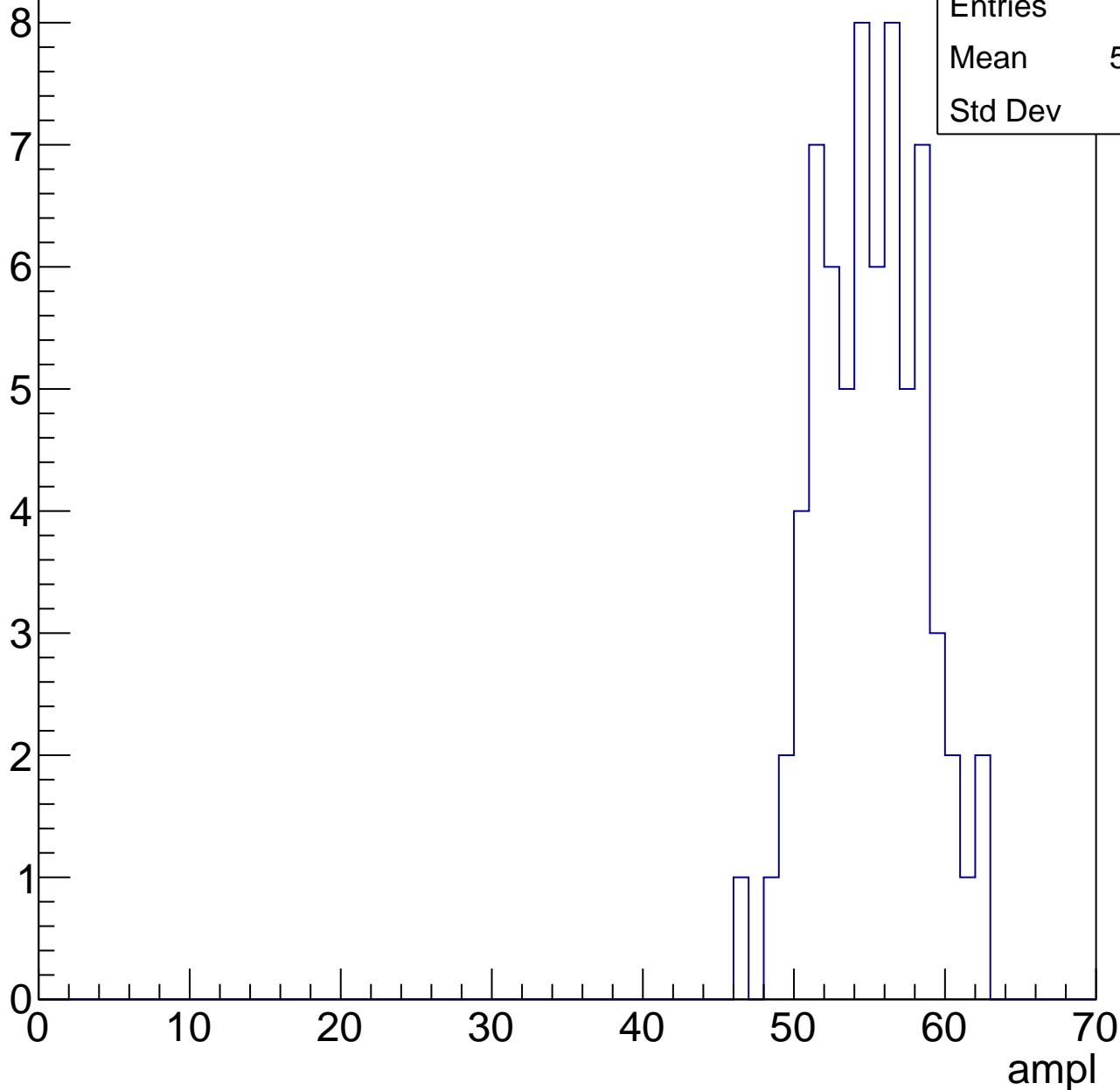


B1L003S, U16-ch124, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	54.54
Std Dev	3.44

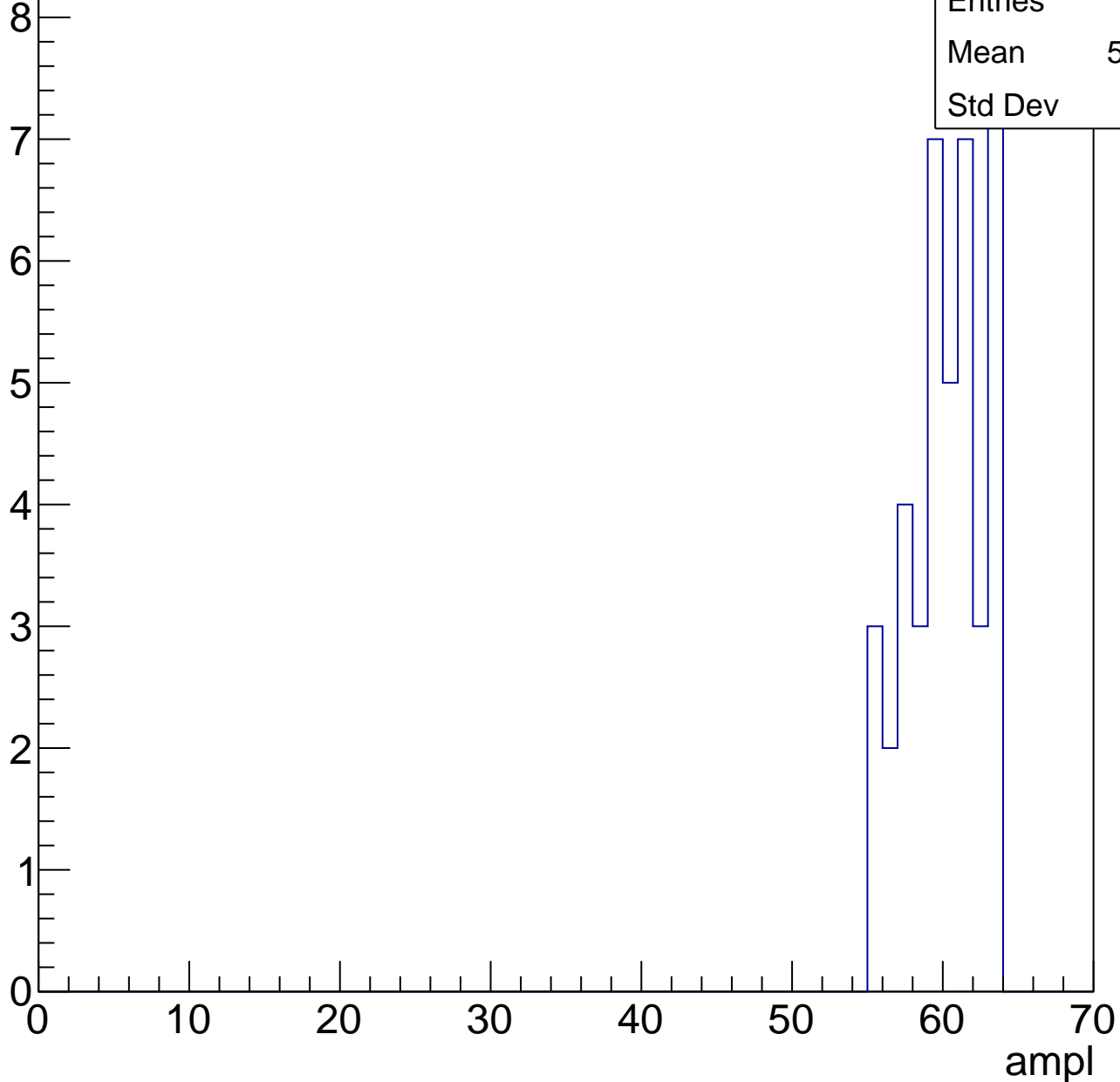


B1L003S, U16-ch124, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.74
Std Dev	2.44

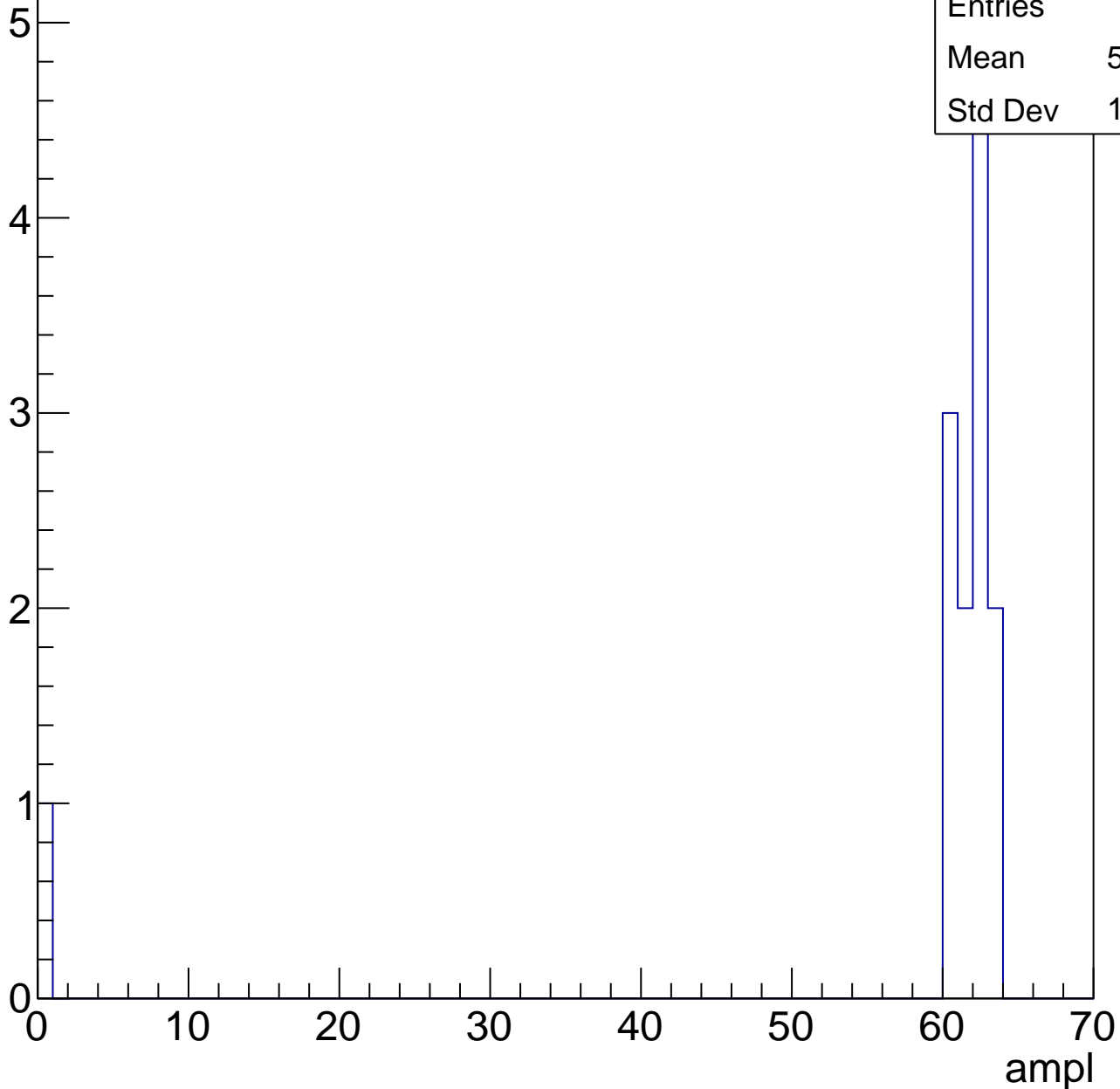


B1L003S, U16-ch124, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	56.77
Std Dev	16.42



B1L003S, U16-ch124, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch125, adc0

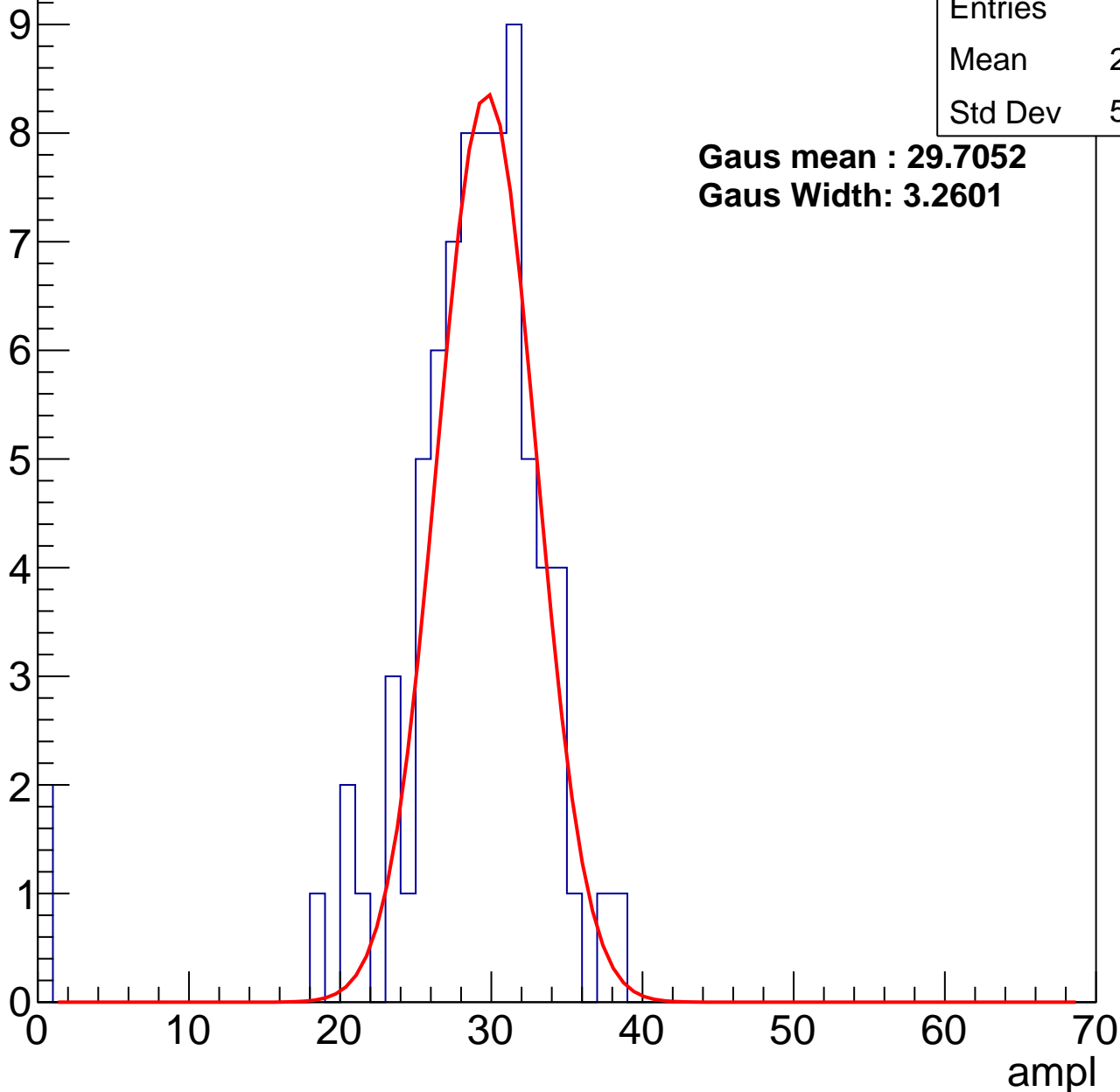
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	27.99
Std Dev	5.907

Gaus mean : 29.7052

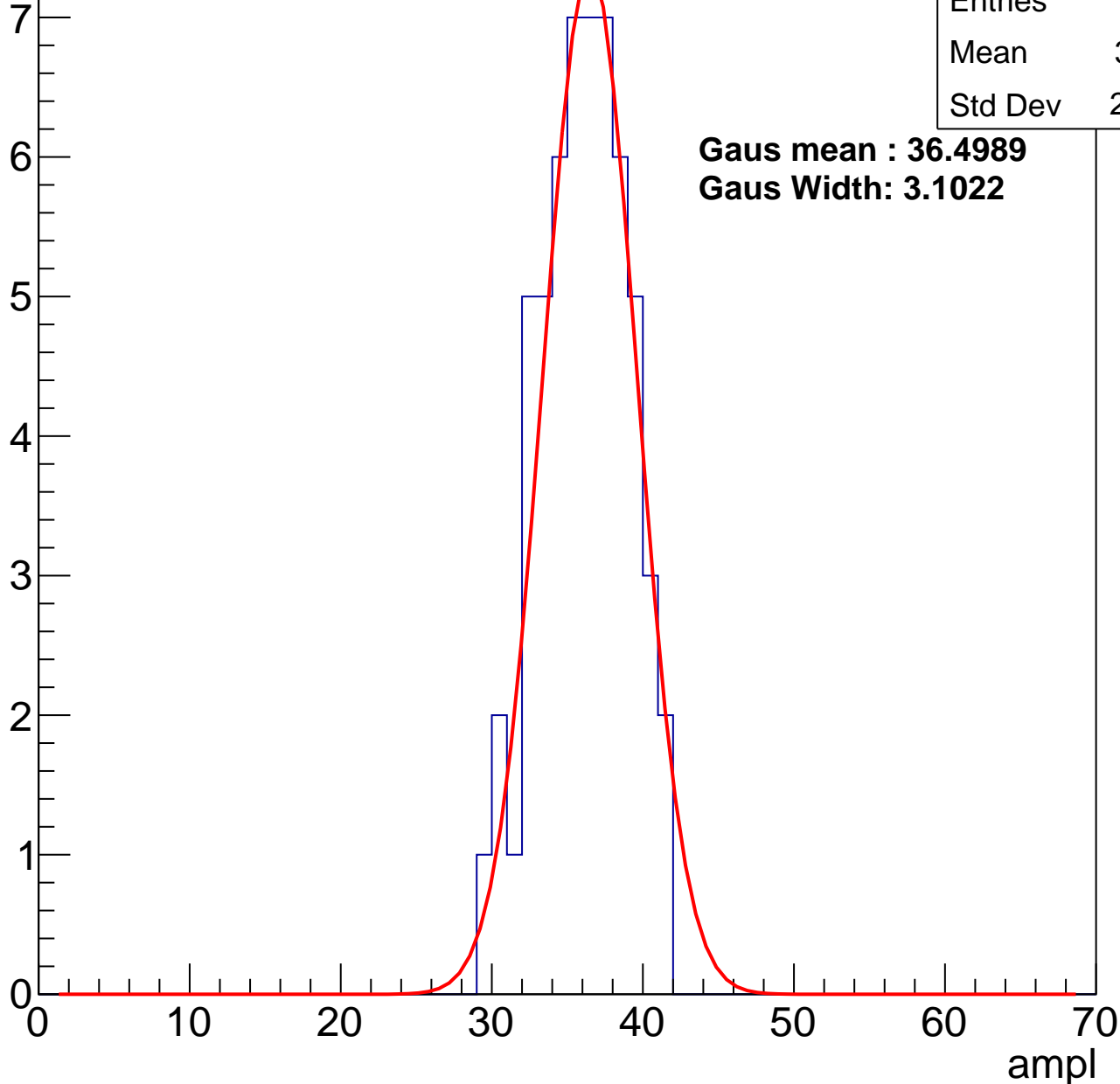
Gaus Width: 3.2601



B1L003S, U16-ch125, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch125, adc2

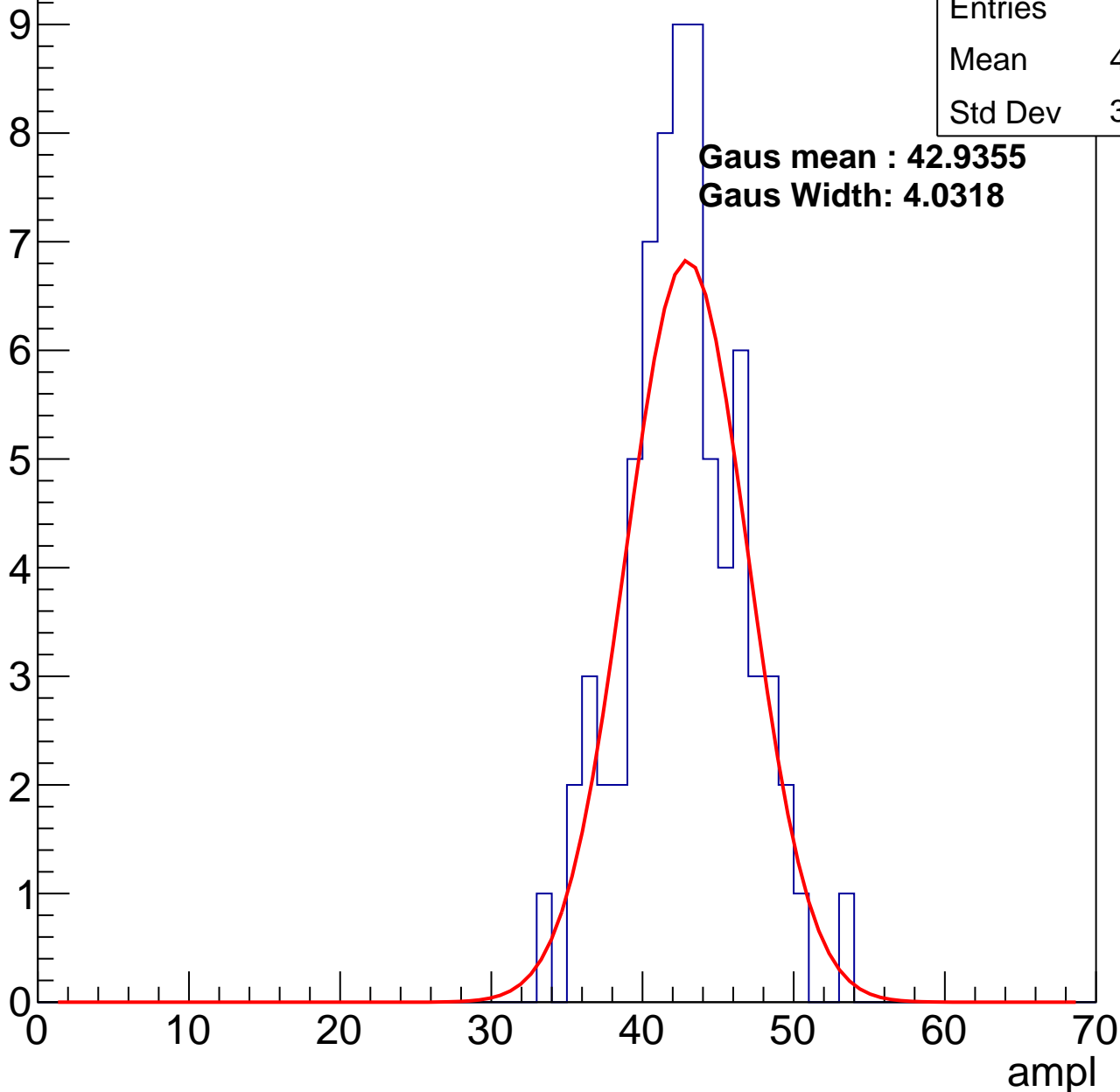
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.34
Std Dev	3.826

Gaus mean : 42.9355

Gaus Width: 4.0318

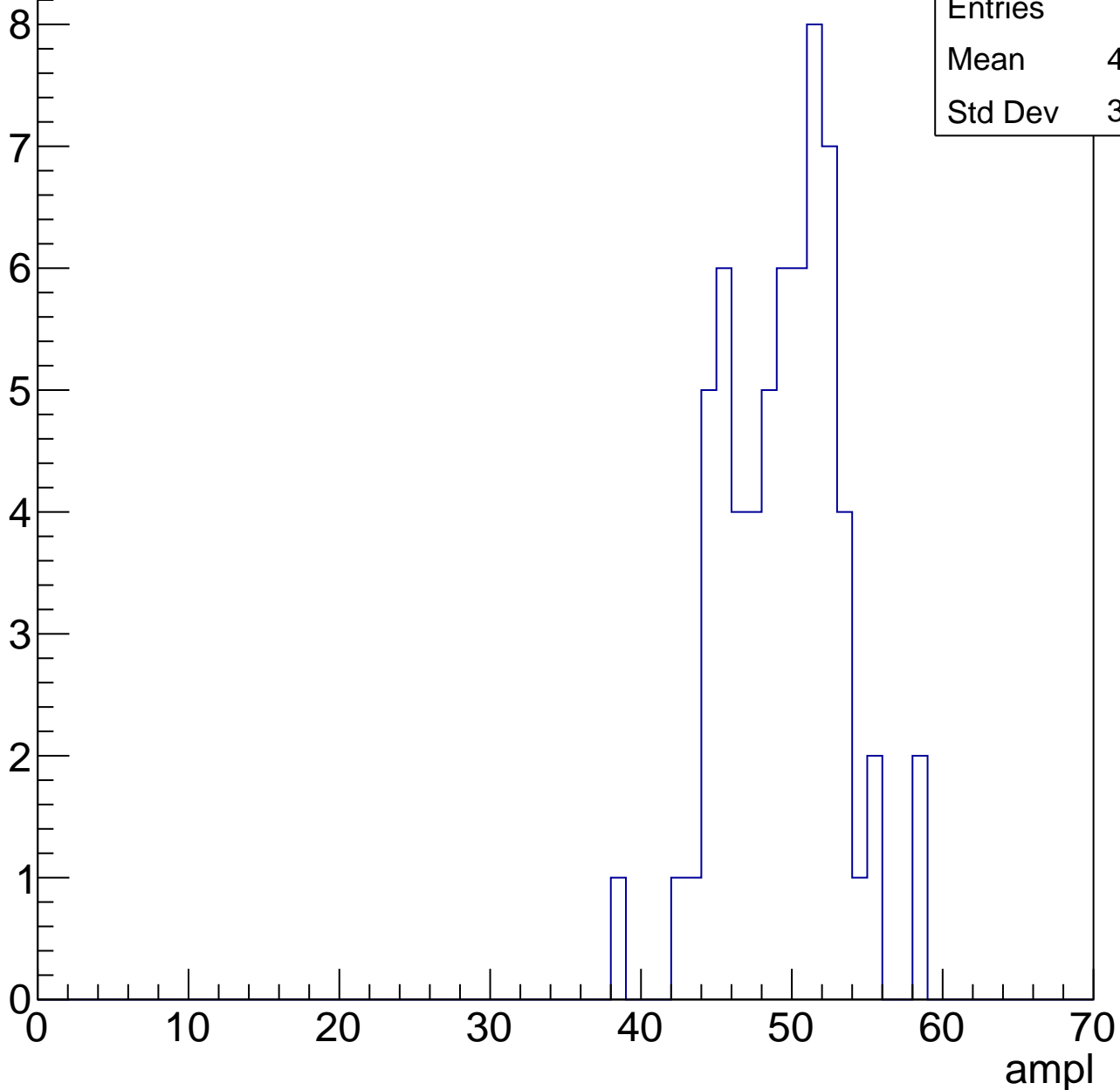


B1L003S, U16-ch125, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	48.94
Std Dev	3.796

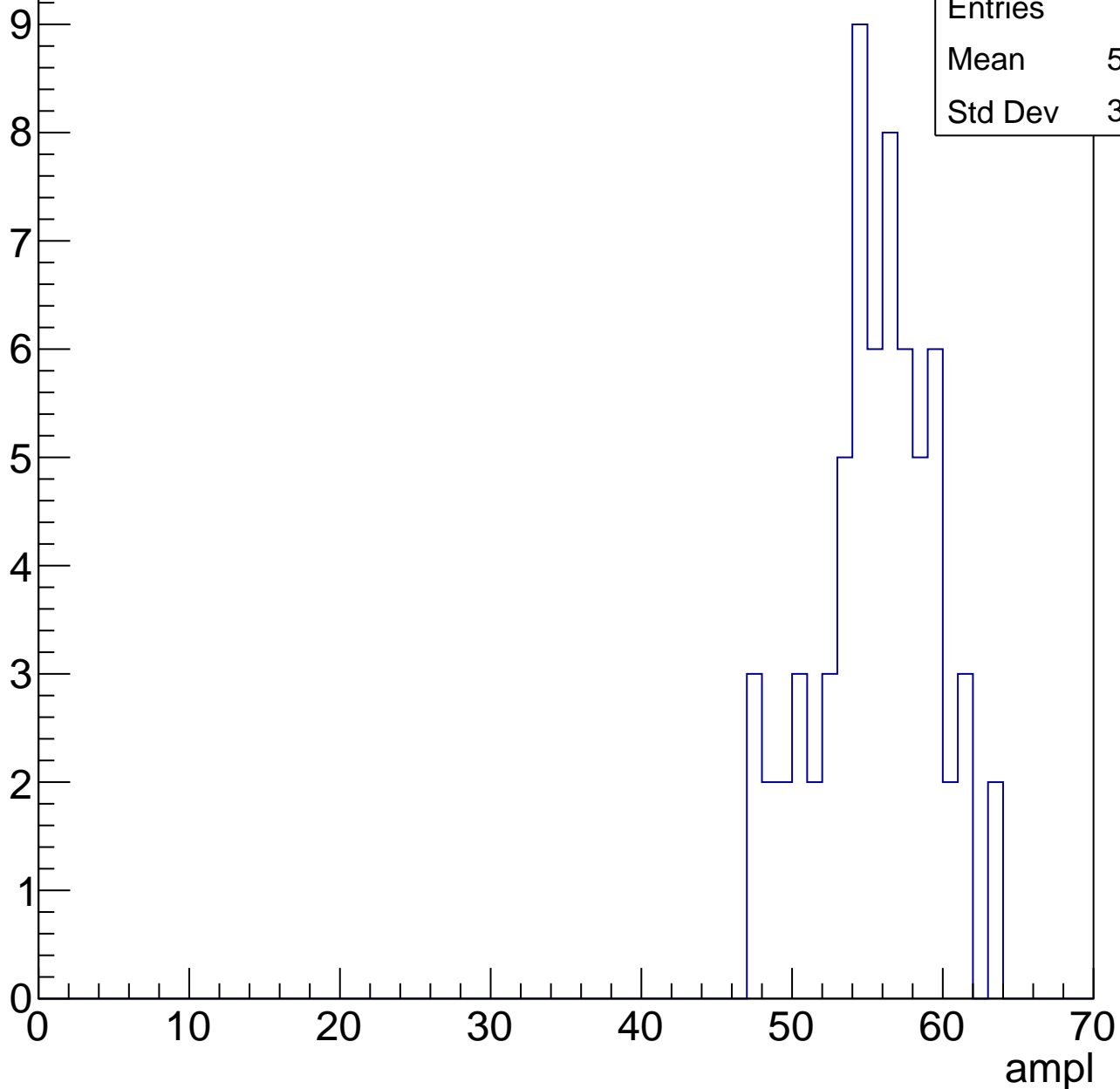


B1L003S, U16-ch125, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.03
Std Dev	3.832

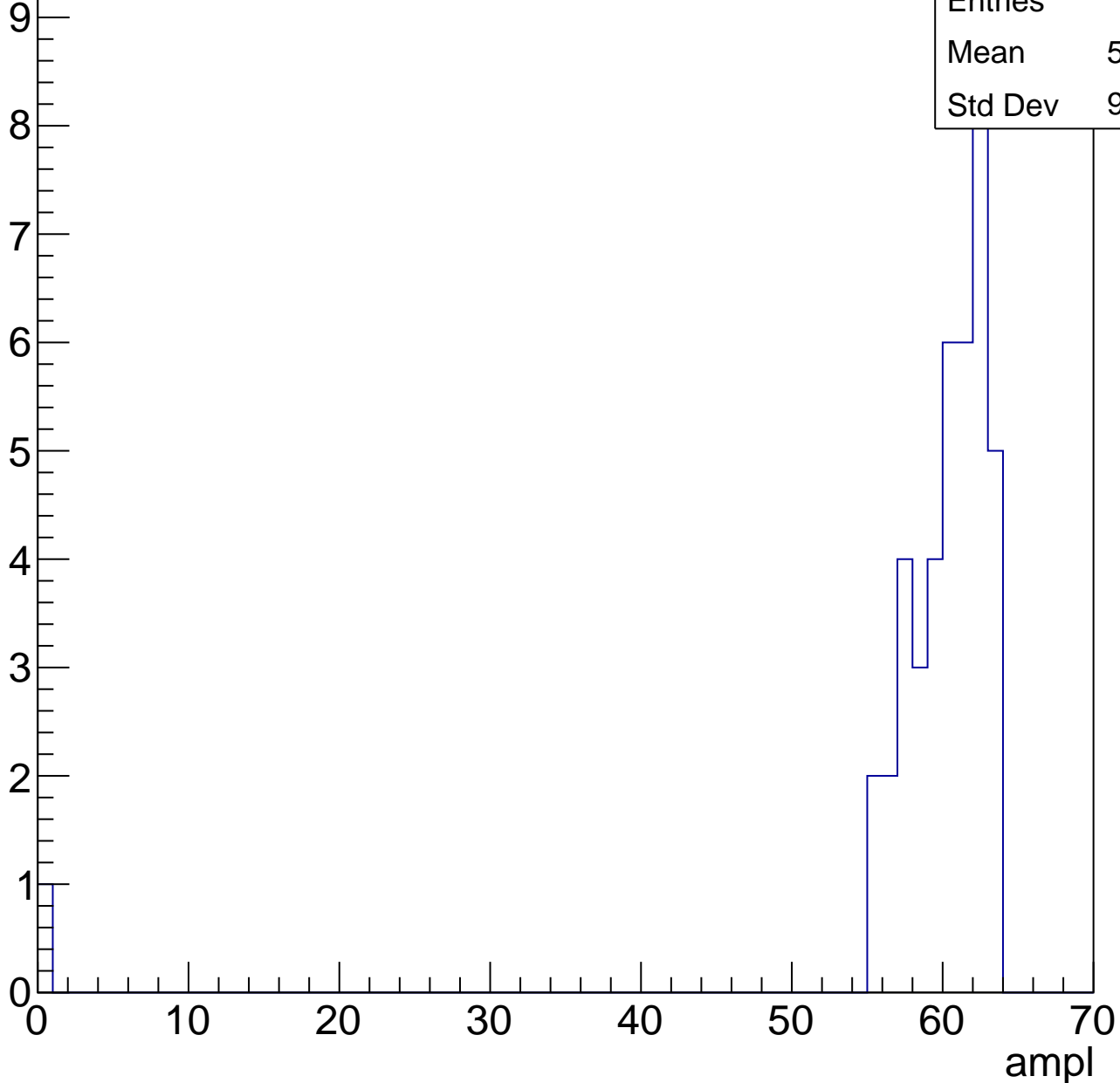


B1L003S, U16-ch125, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

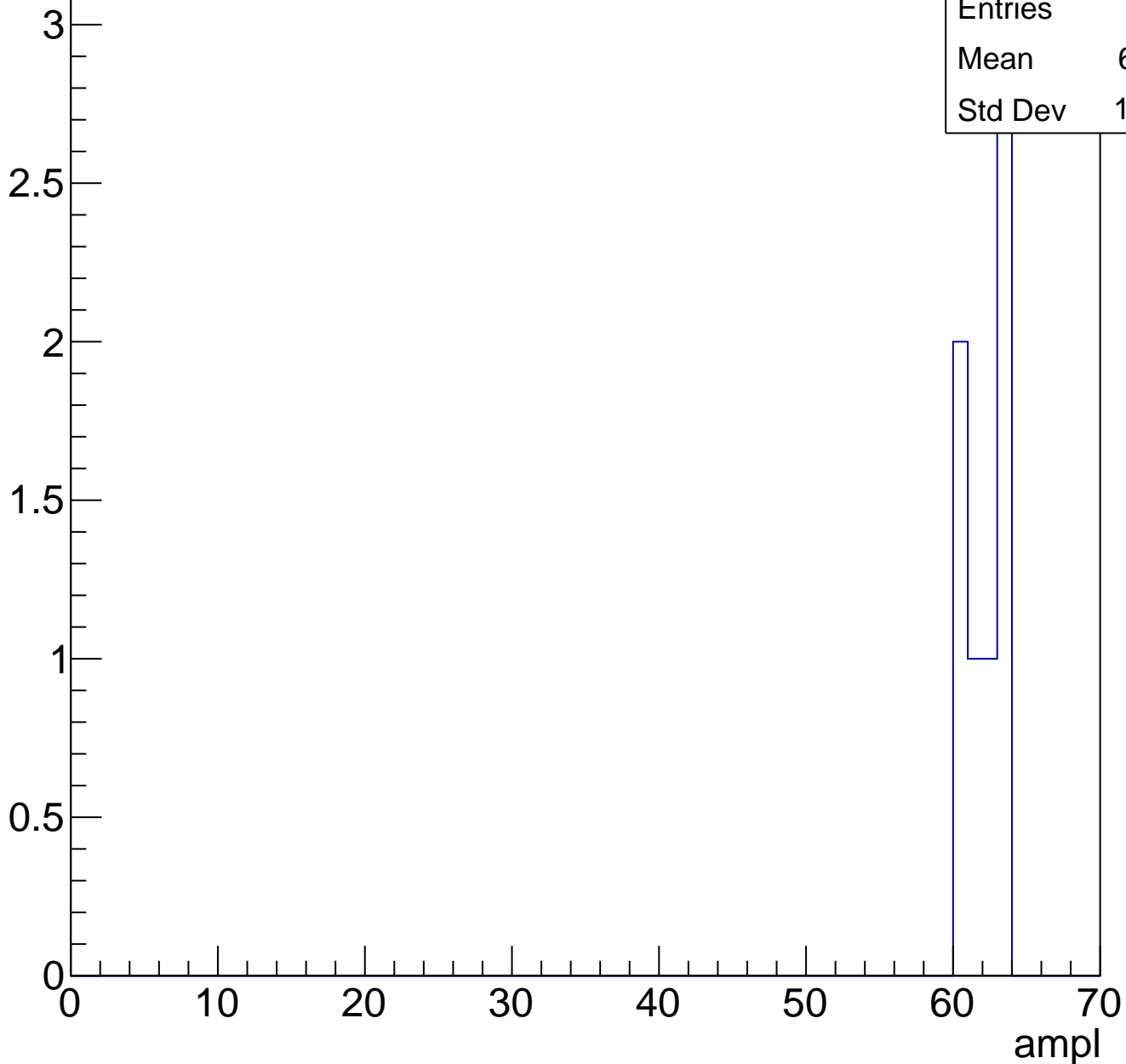
Entries	42
Mean	58.55
Std Dev	9.427



B1L003S, U16-ch125, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch125, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U16-ch126, adc0

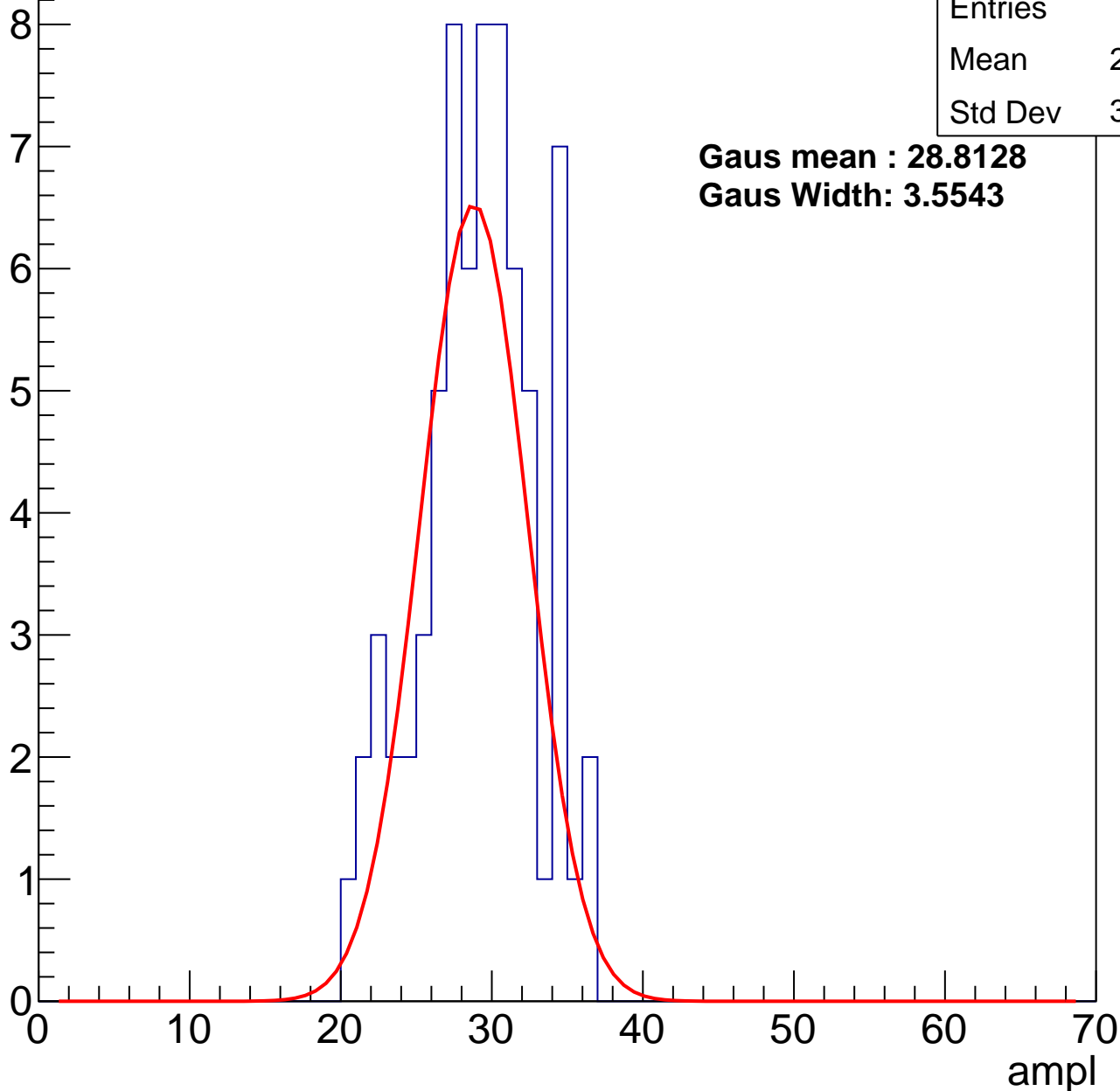
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.67
Std Dev	3.786

Gaus mean : 28.8128

Gaus Width: 3.5543



B1L003S, U16-ch126, adc1

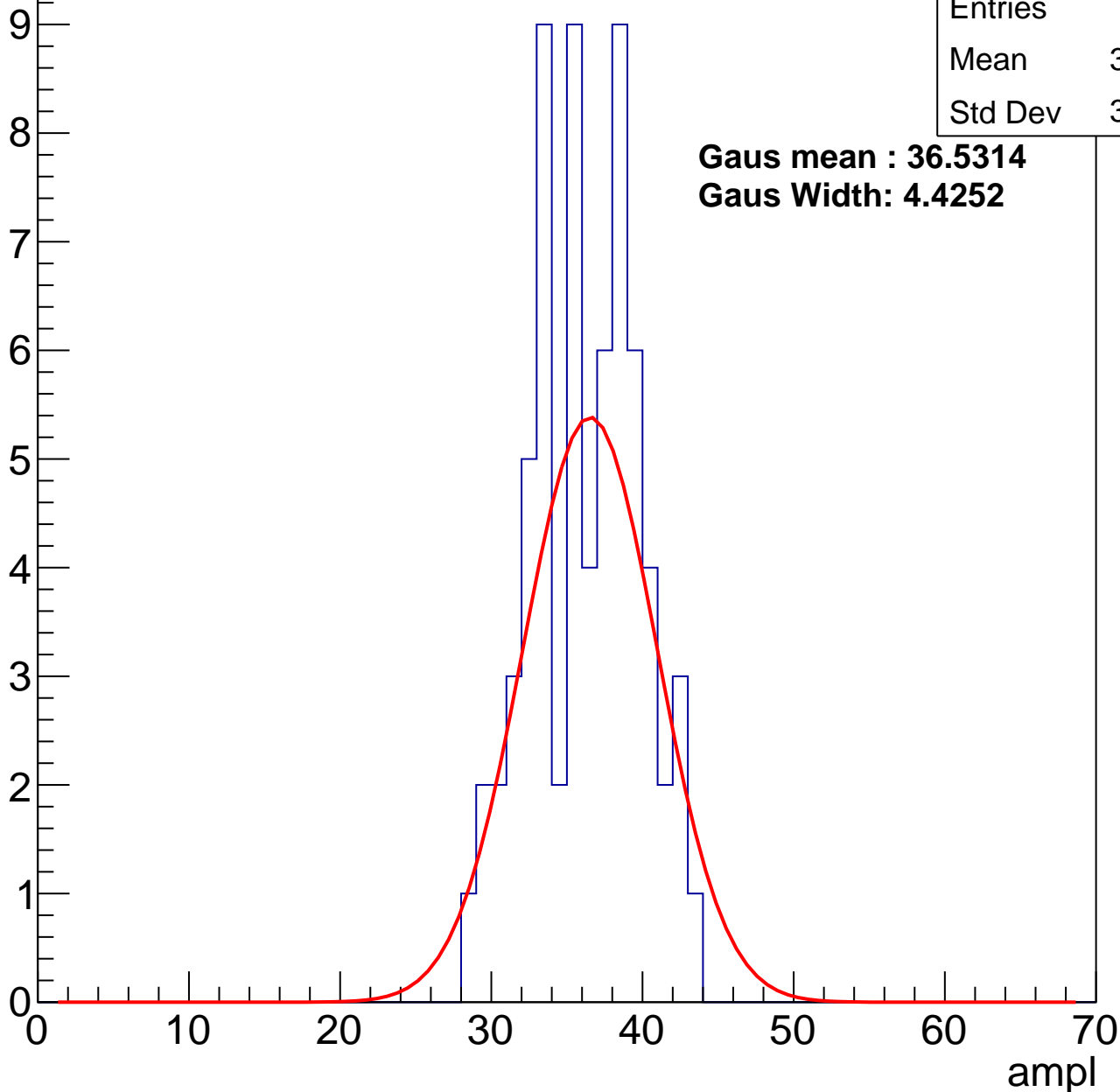
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.76
Std Dev	3.536

Gaus mean : 36.5314

Gaus Width: 4.4252



B1L003S, U16-ch126, adc2

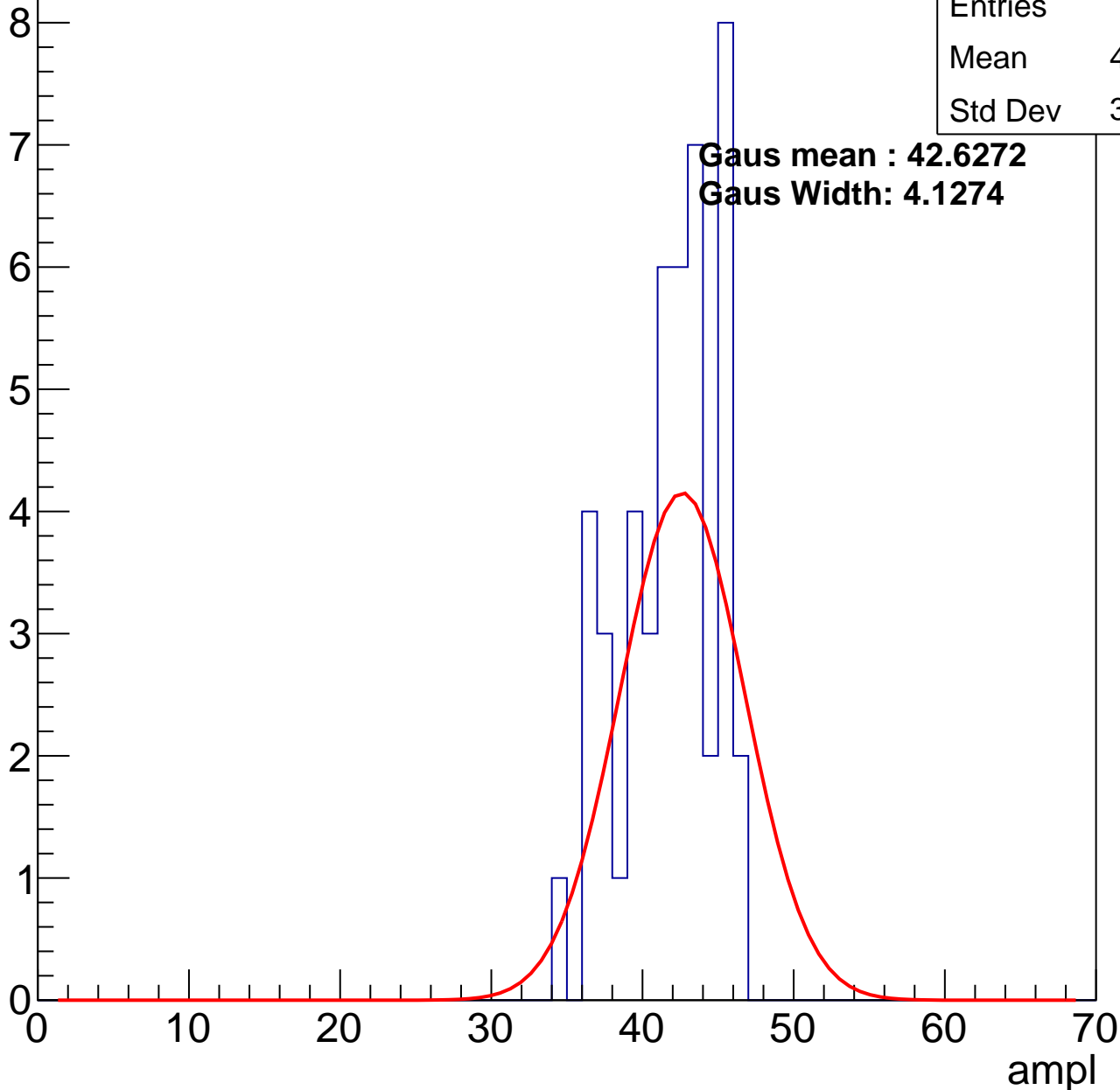
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	41.32
Std Dev	3.095

Gaus mean : 42.6272

Gaus Width: 4.1274

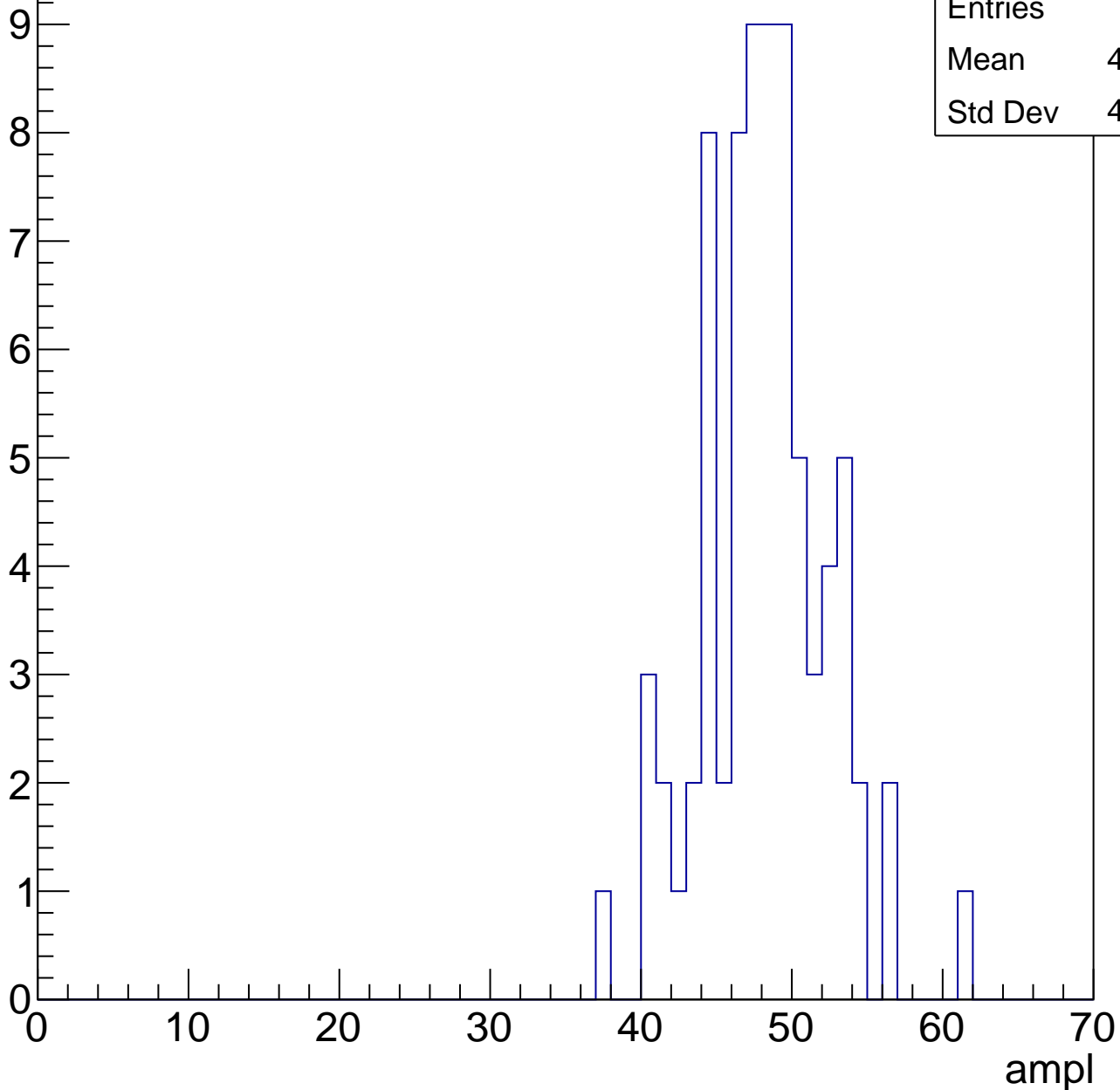


B1L003S, U16-ch126, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	47.76
Std Dev	4.126

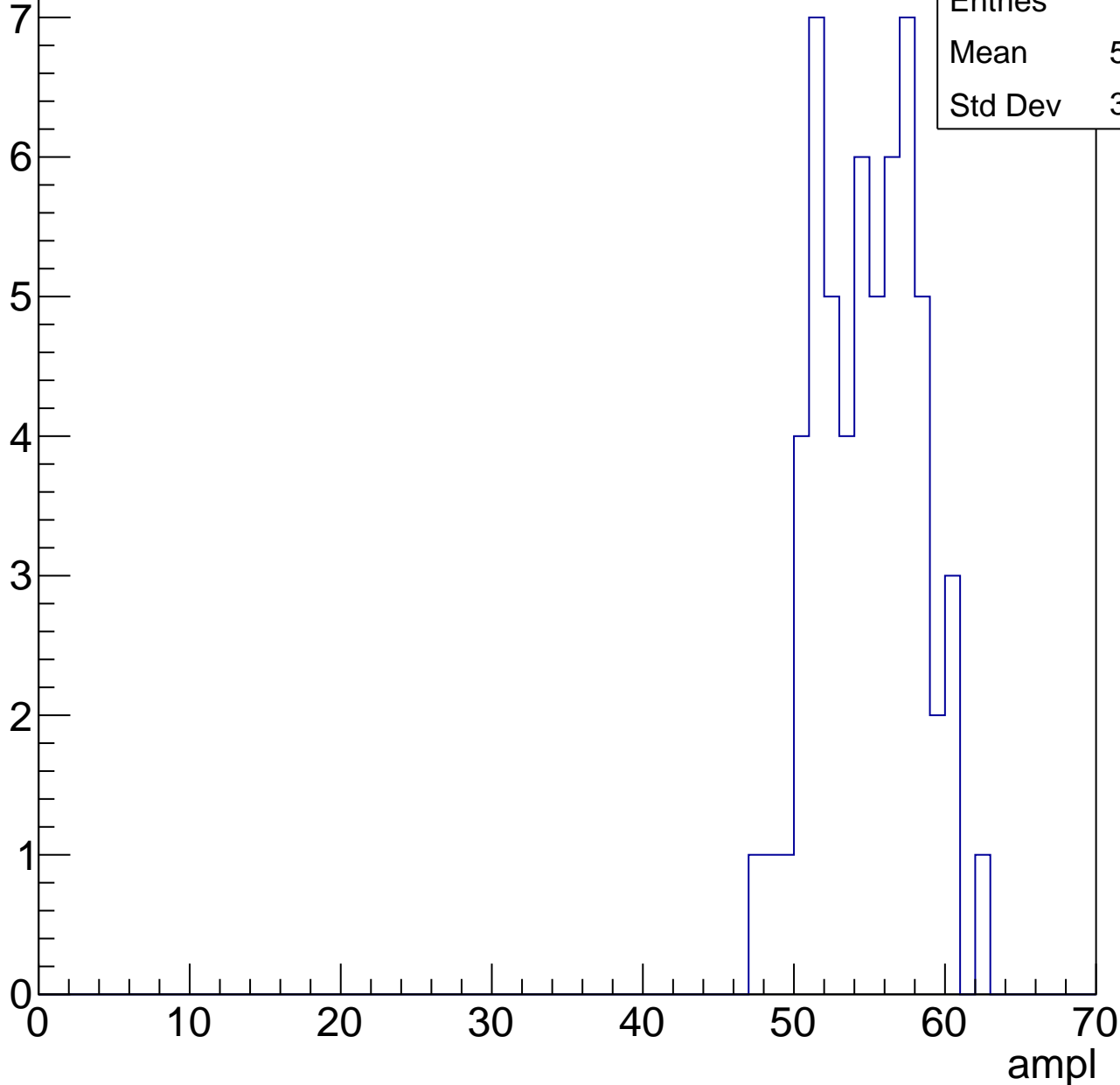


B1L003S, U16-ch126, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.43
Std Dev	3.333

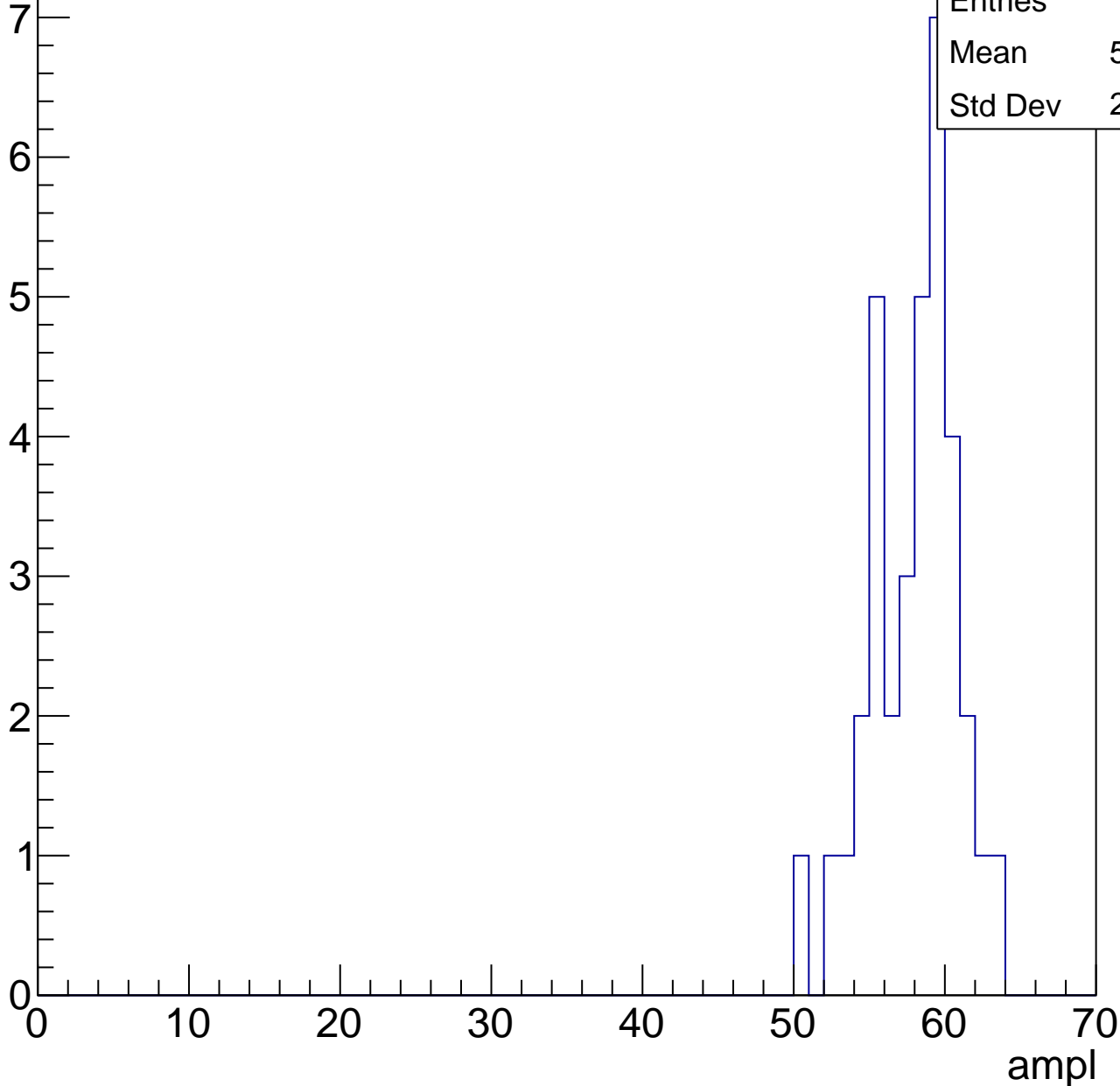


B1L003S, U16-ch126, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	57.46
Std Dev	2.852

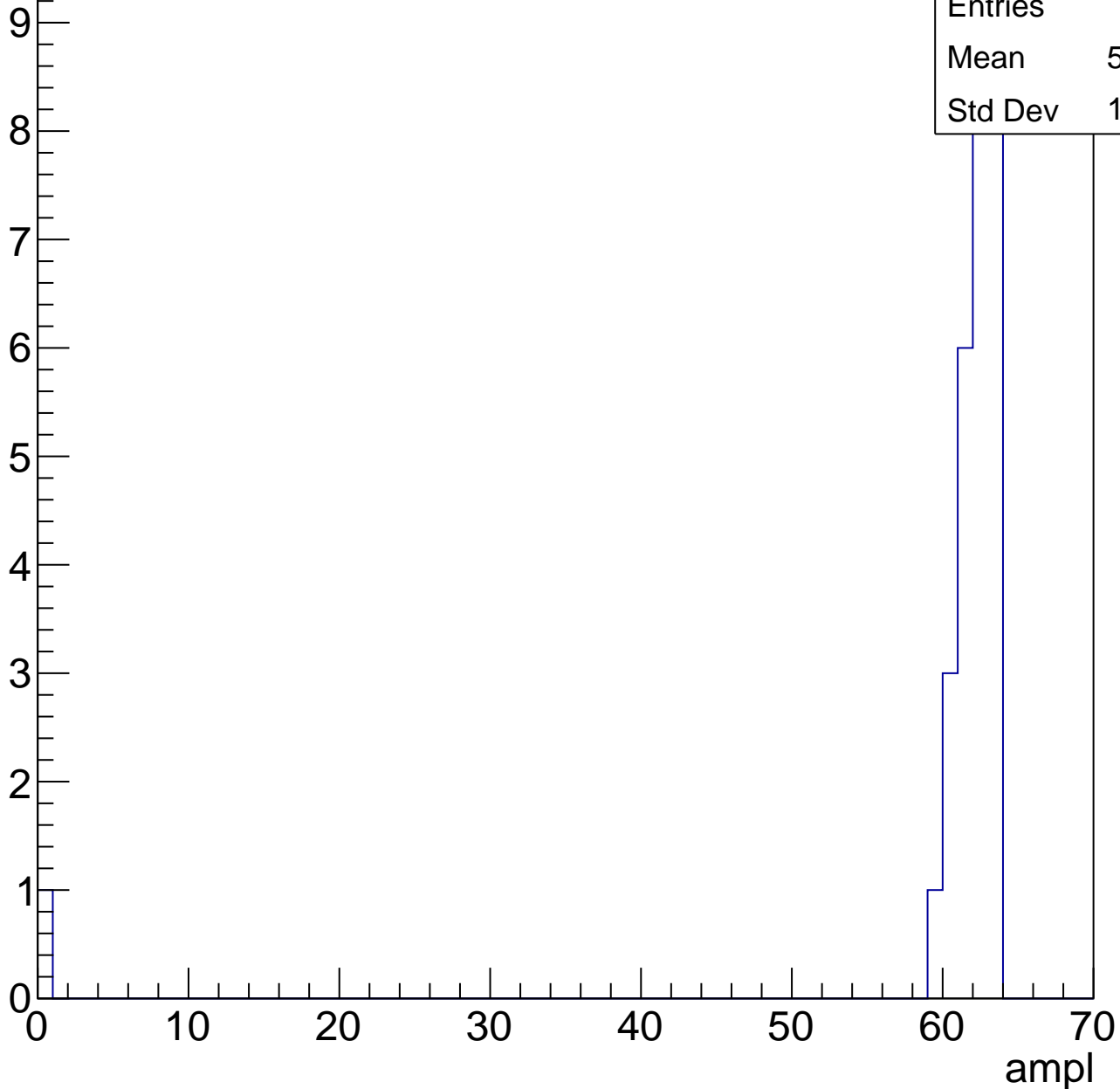


B1L003S, U16-ch126, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	28
Mean	59.57
Std Dev	11.52



B1L003S, U16-ch126, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	30.5
Std Dev	30.5

B1L003S, U16-ch127, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	26.76
Std Dev	7.613

Gaus mean : 29.0015

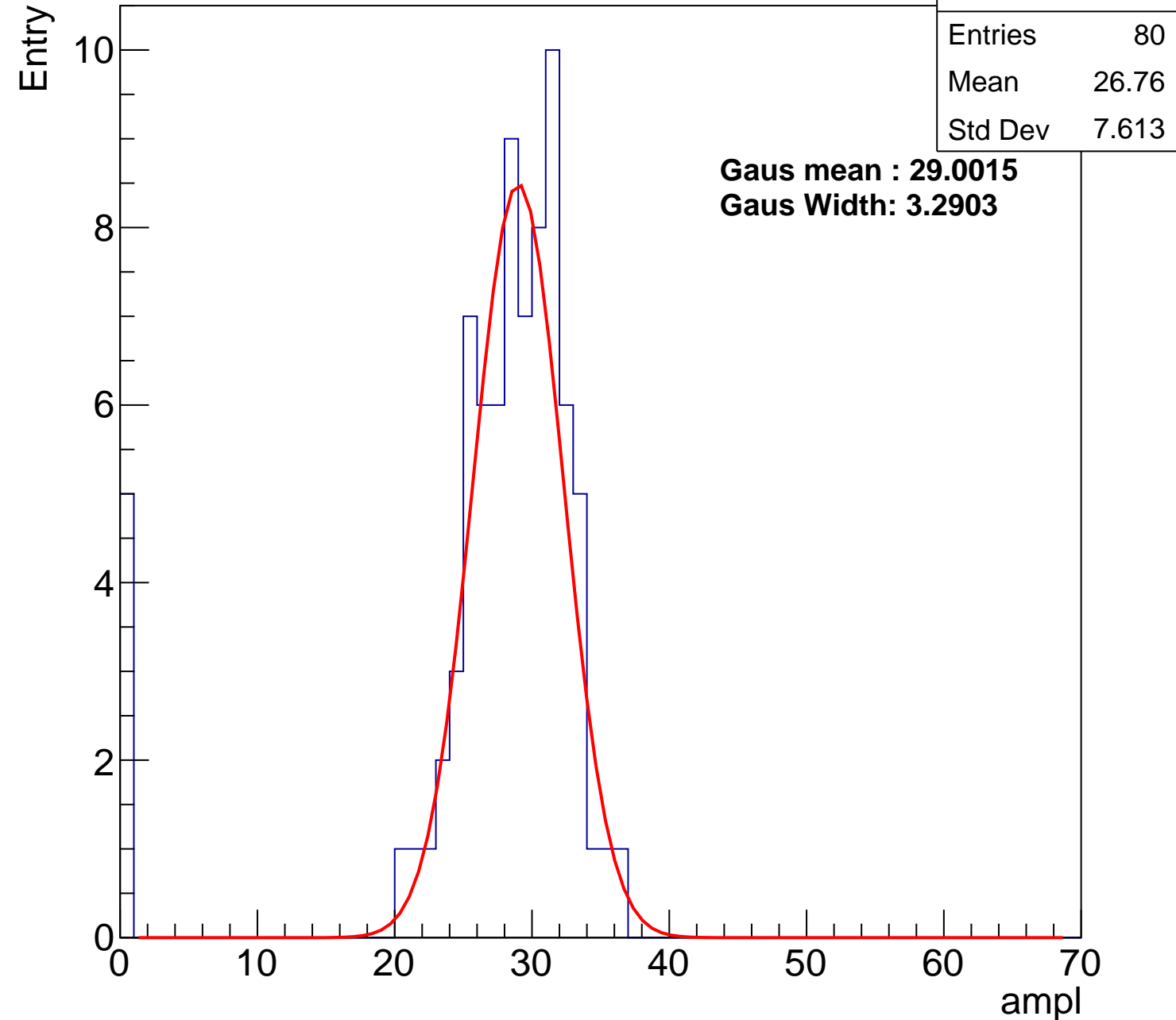
Gaus Width: 3.2903

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U16-ch127, adc1

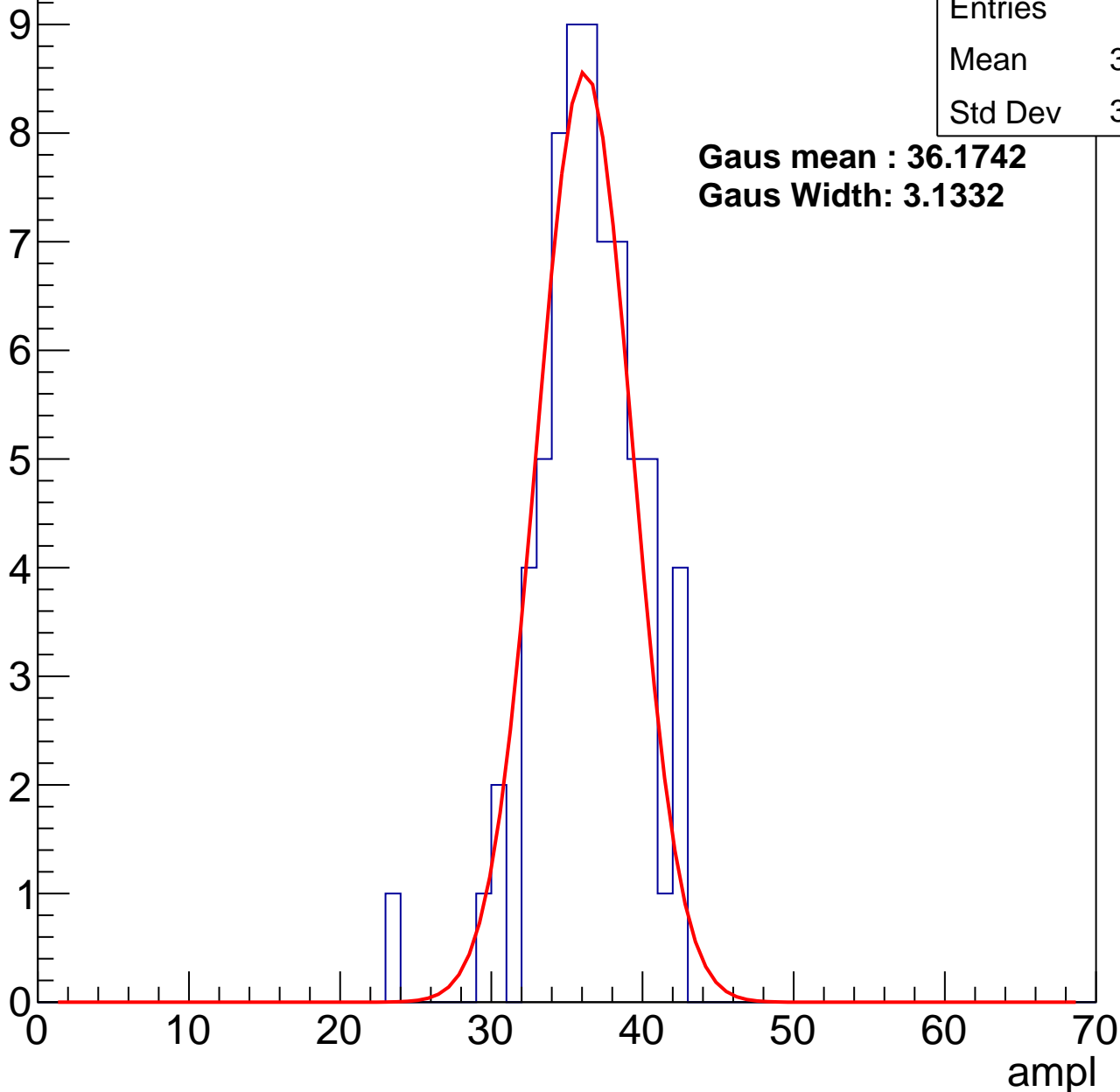
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.96
Std Dev	3.363

Gaus mean : 36.1742

Gaus Width: 3.1332



B1L003S, U16-ch127, adc2

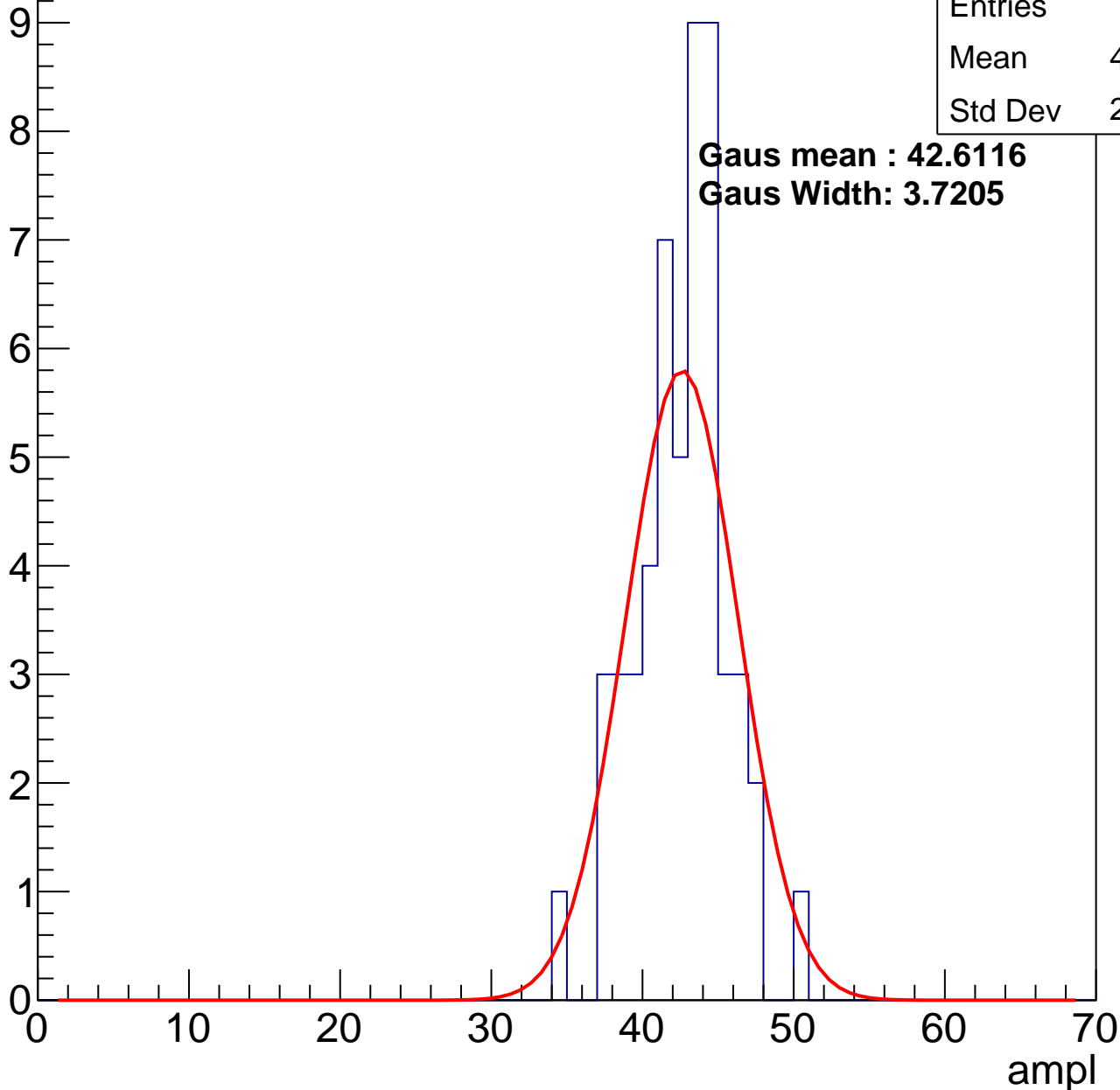
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.13
Std Dev	2.978

Gaus mean : 42.6116

Gaus Width: 3.7205

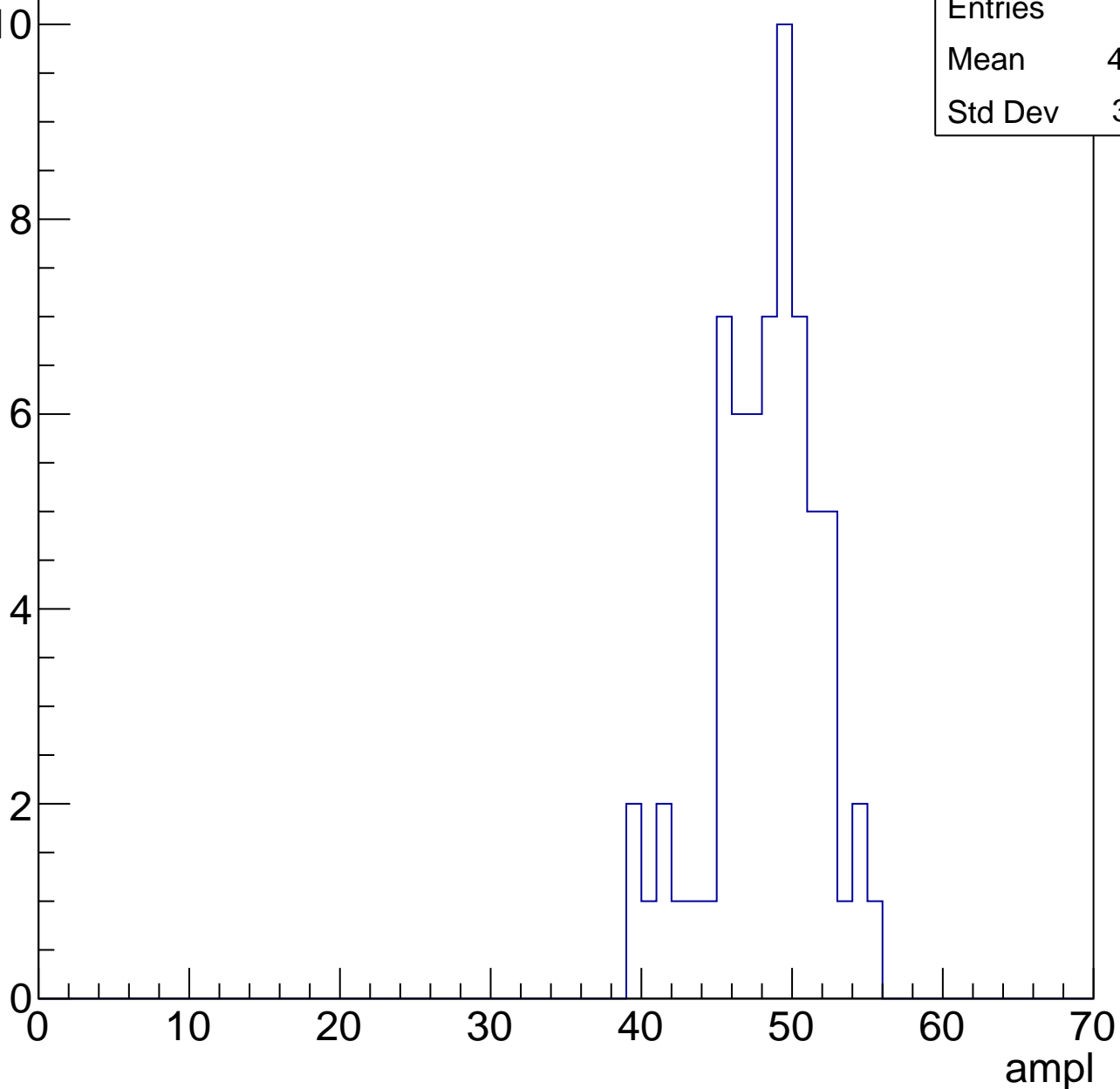


B1L003S, U16-ch127, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	47.83
Std Dev	3.511

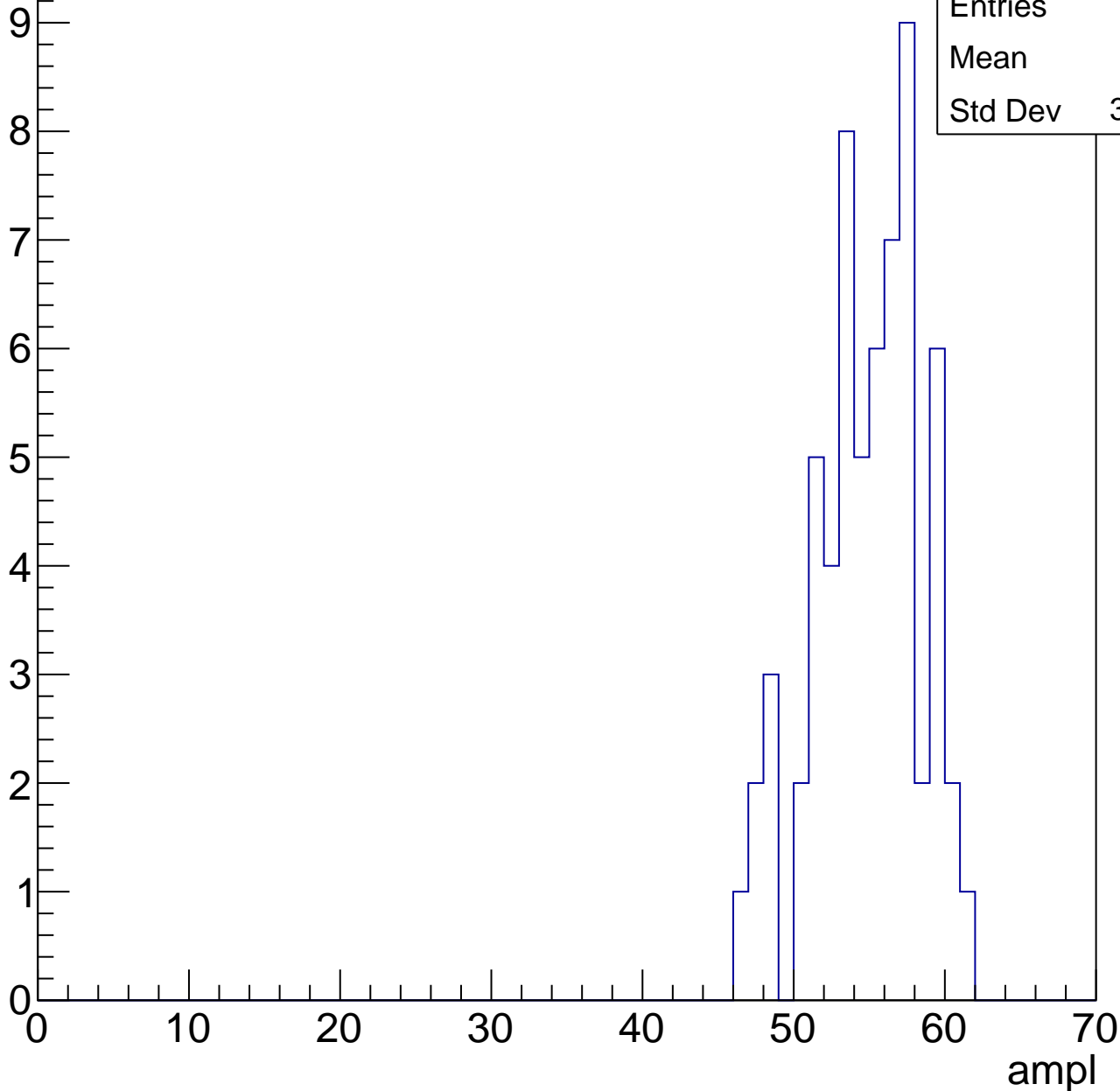


B1L003S, U16-ch127, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

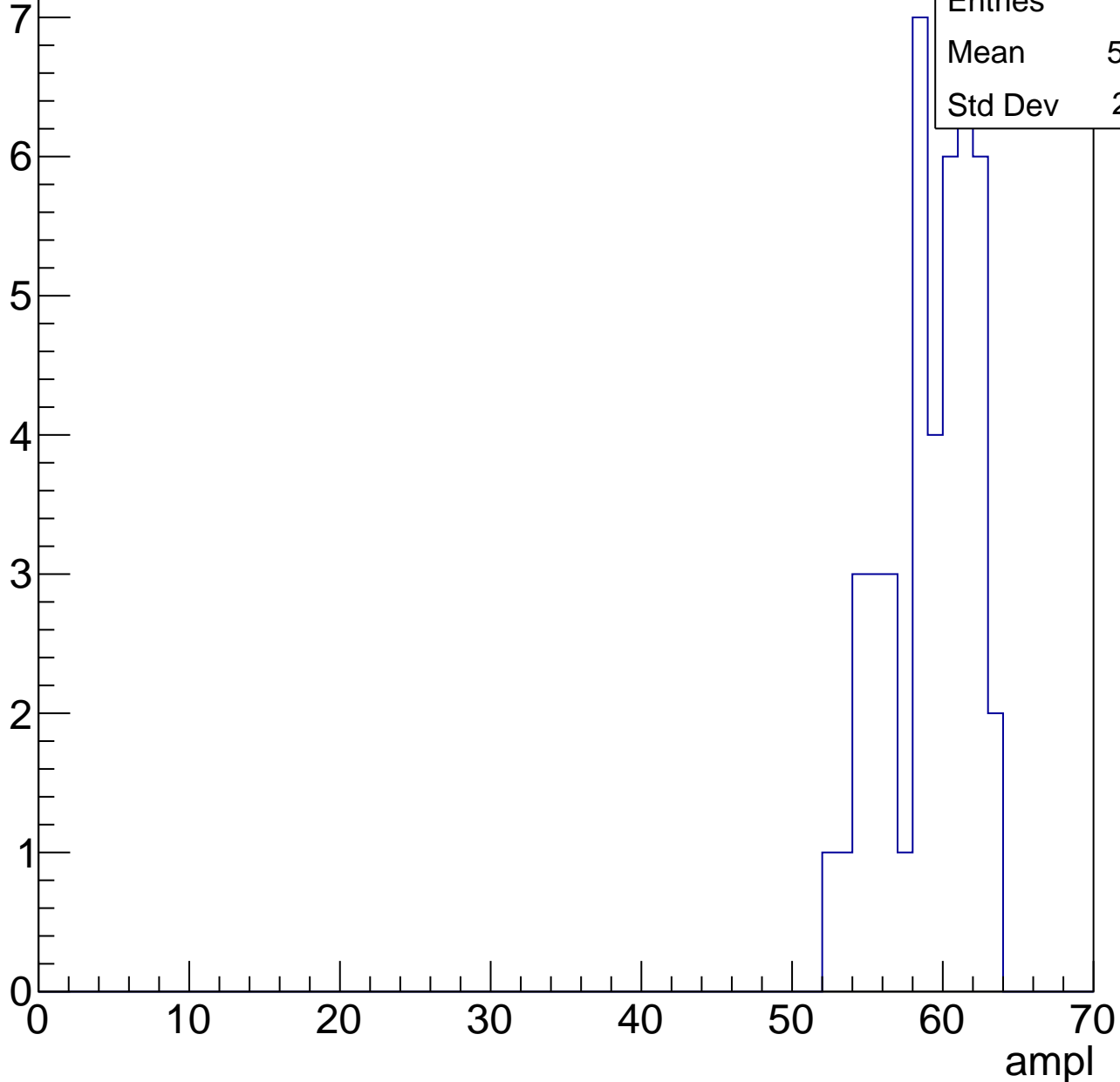
Entries	63
Mean	54.4
Std Dev	3.517



B1L003S, U16-ch127, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

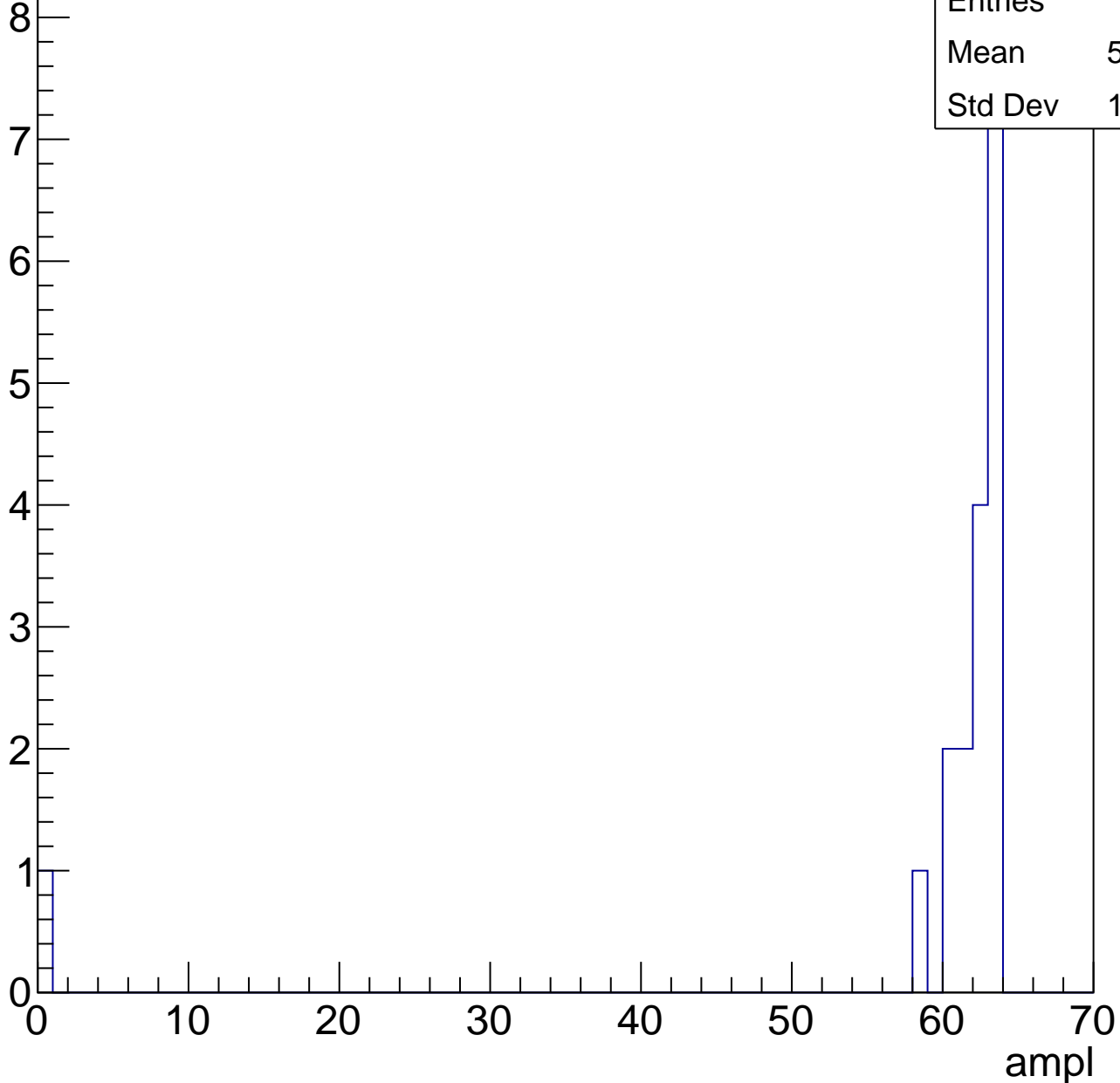


B1L003S, U16-ch127, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.44
Std Dev	14.24



B1L003S, U16-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U16-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

