

B1L100S, U13-ch0, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	29.37
Std Dev	4.715

Gaus mean : 30.0973

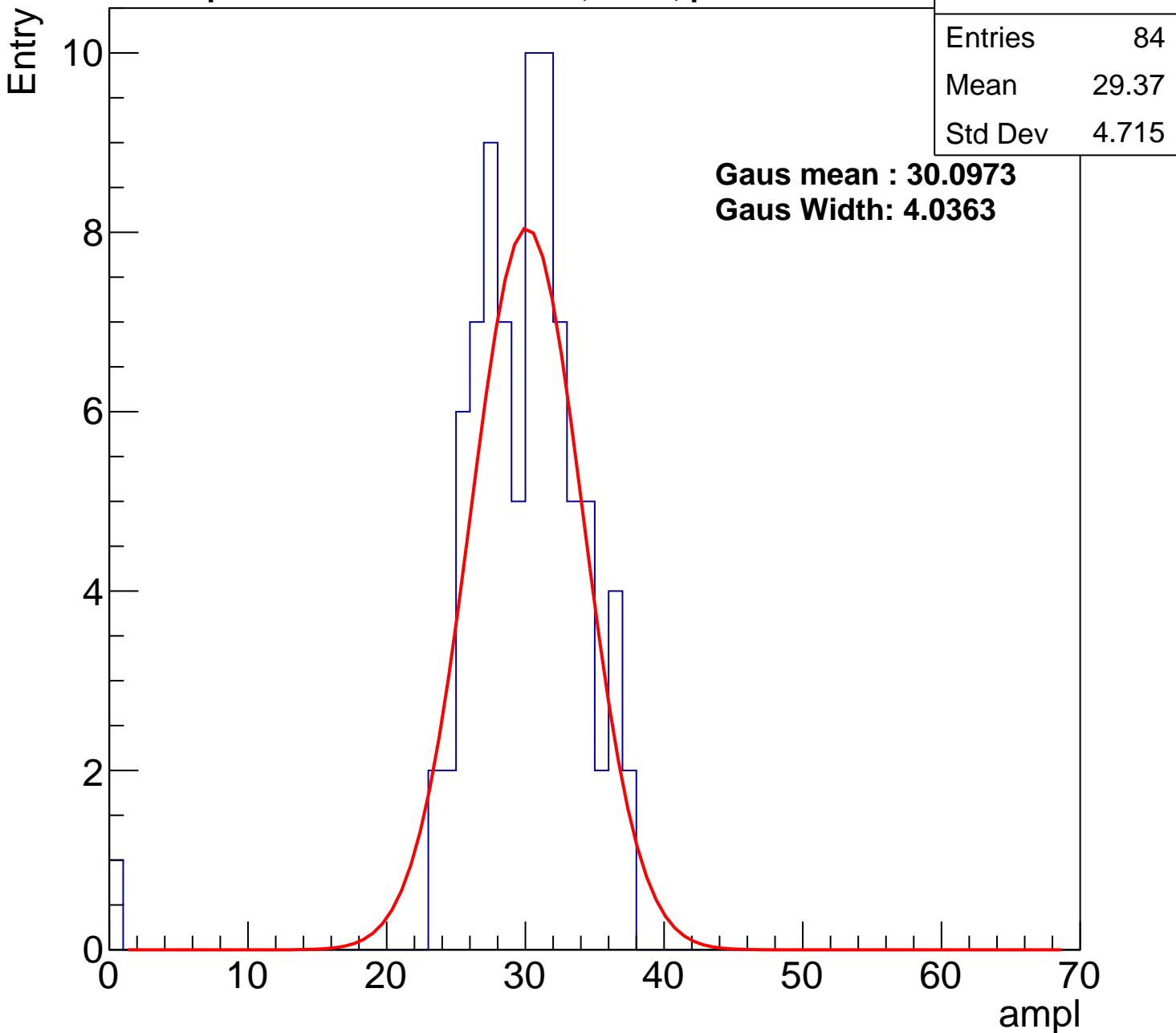
Gaus Width: 4.0363

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch0, adc1

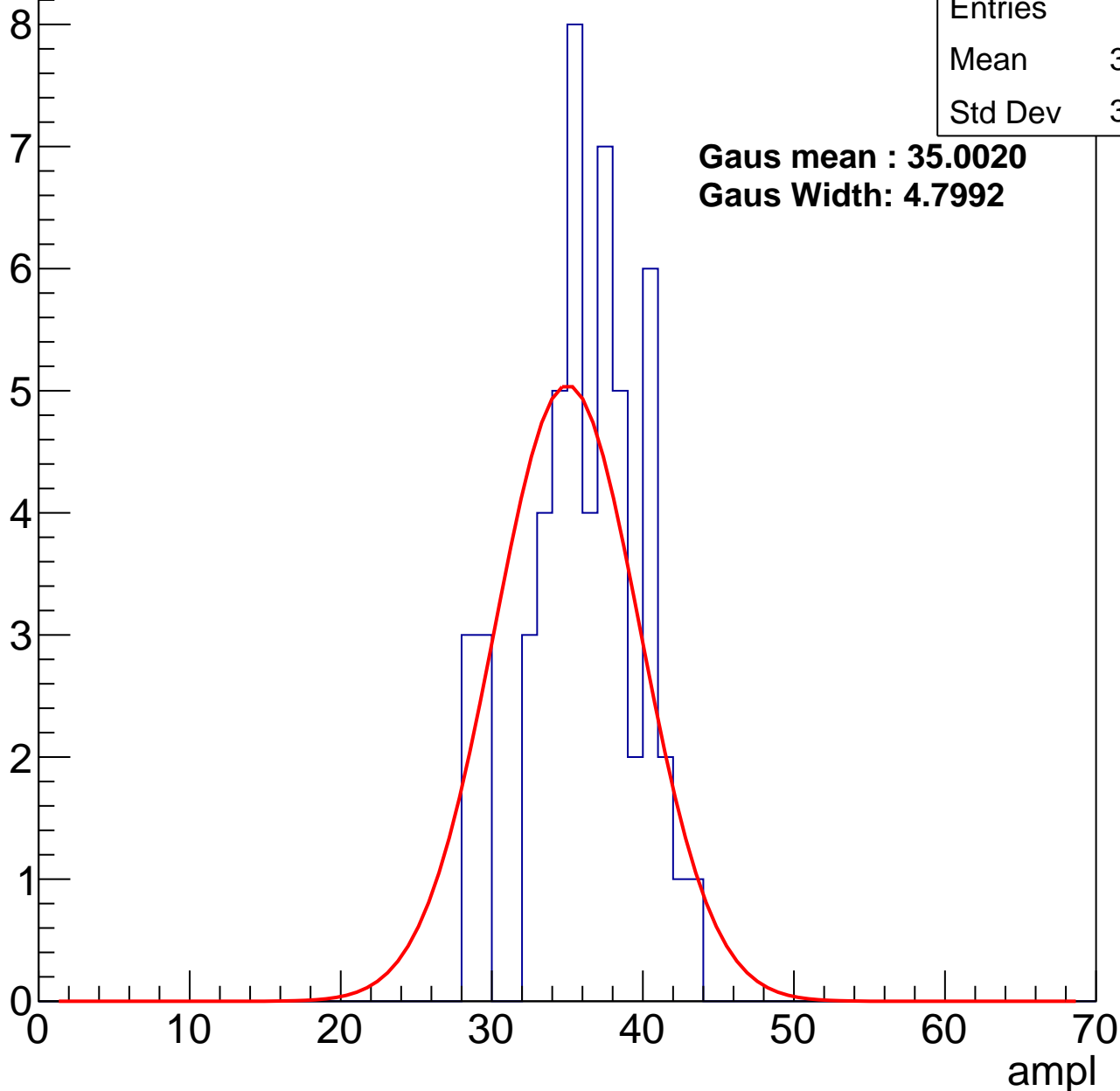
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	35.69
Std Dev	3.656

Gaus mean : 35.0020

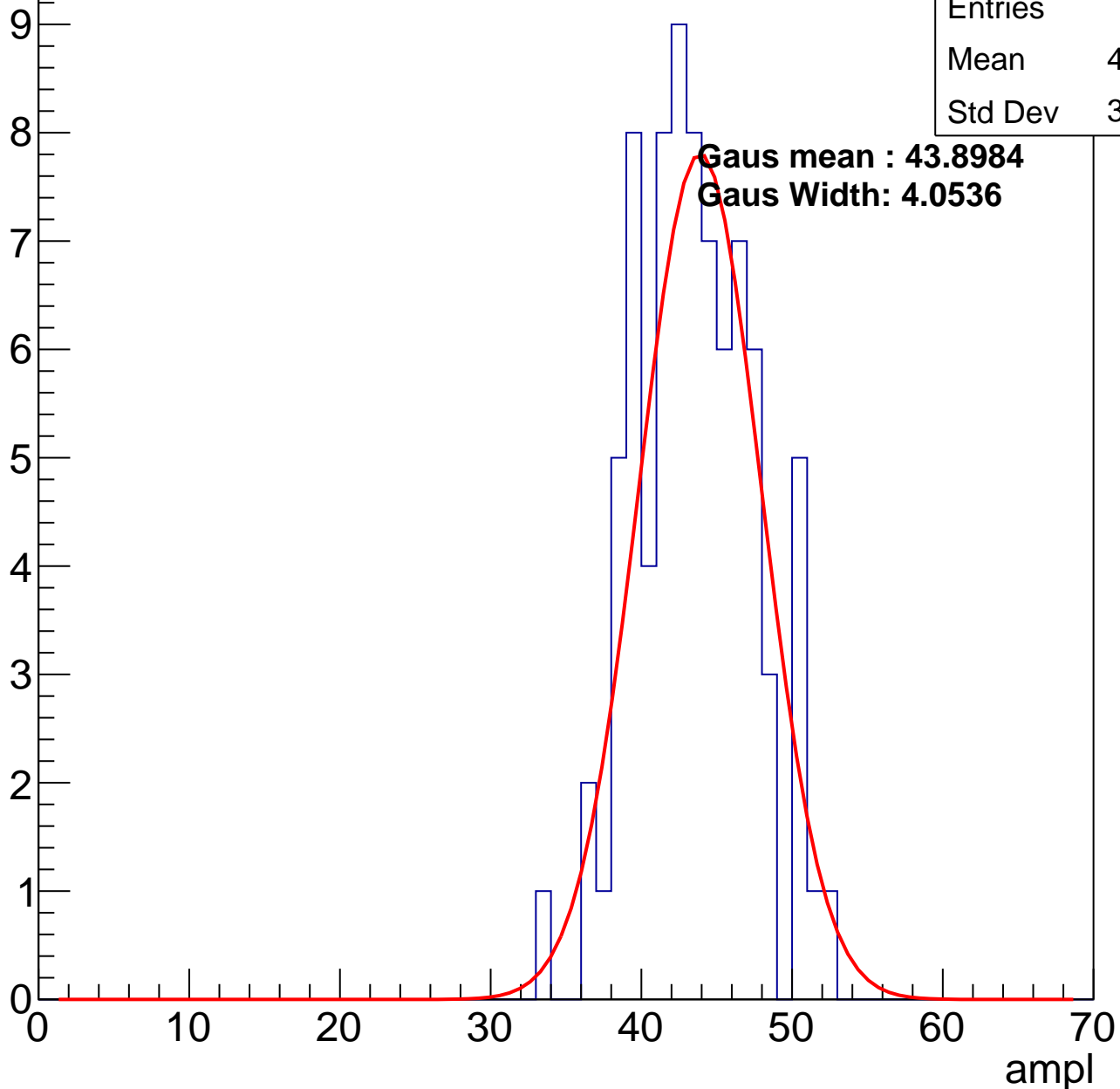
Gaus Width: 4.7992



B1L100S, U13-ch0, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	82
Mean	43.09
Std Dev	3.855

Gaus mean : 43.8984

Gaus Width: 4.0536

B1L100S, U13-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

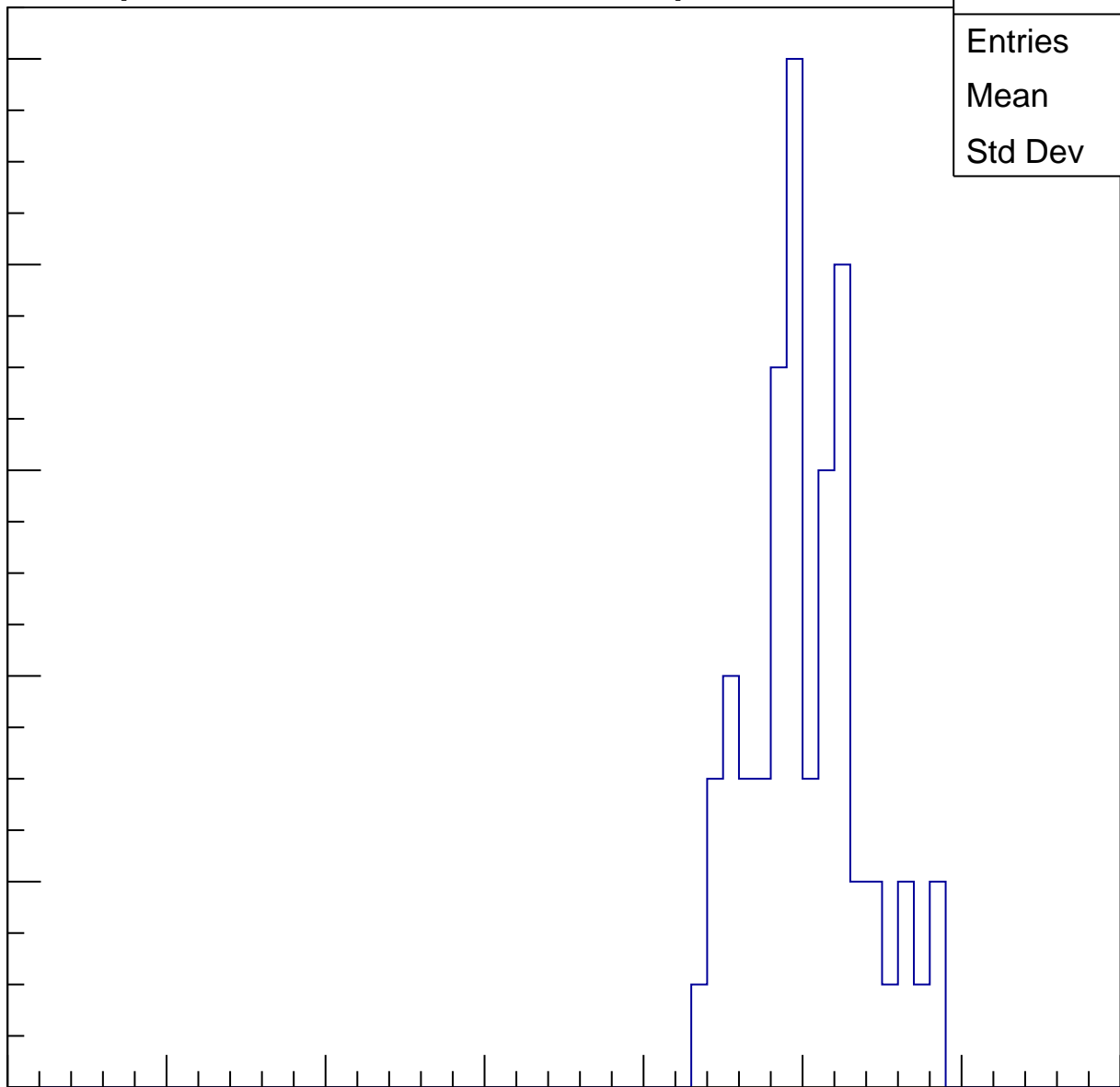
Entries	58
Mean	49.76
Std Dev	3.544

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

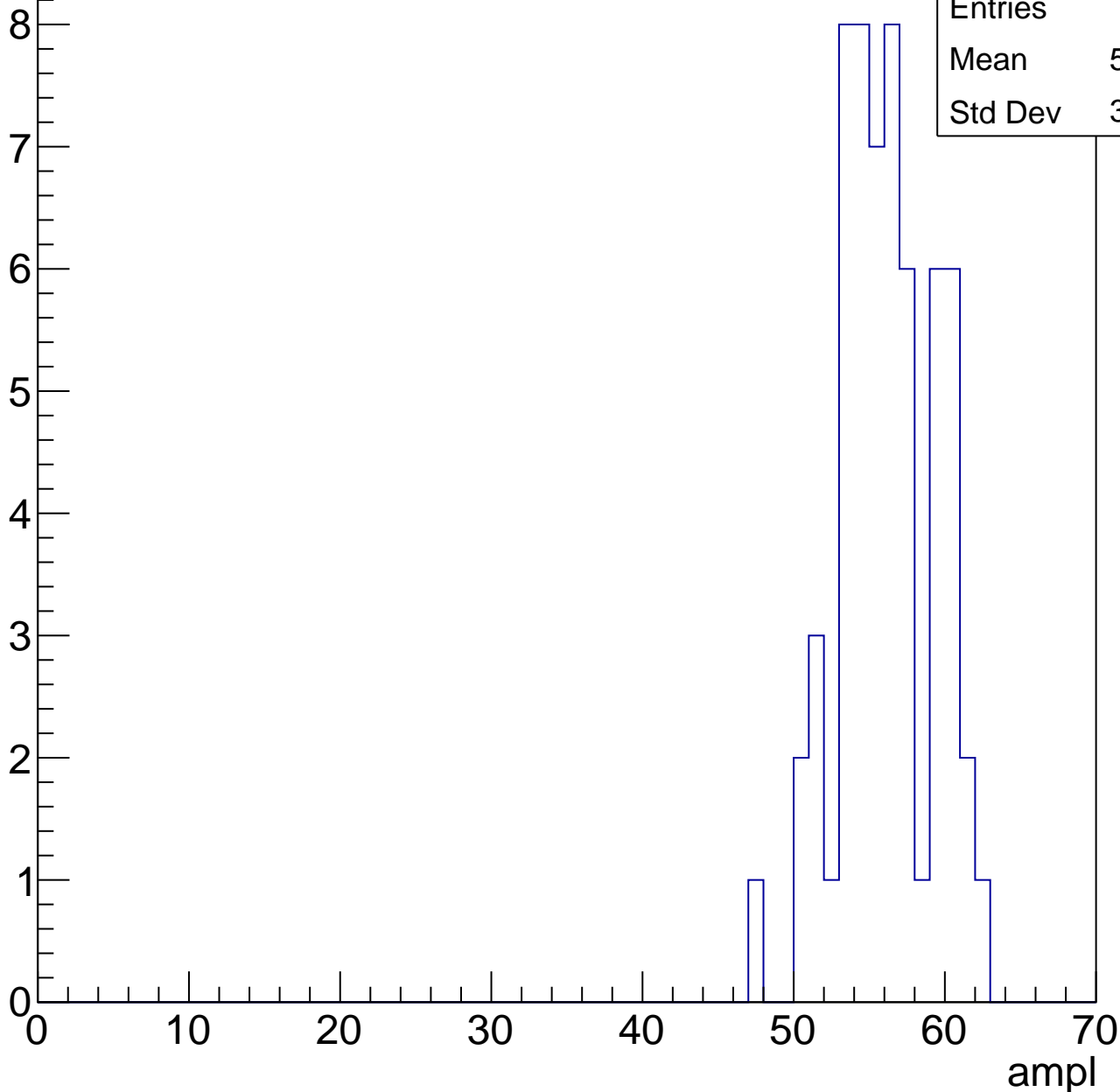


B1L100S, U13-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

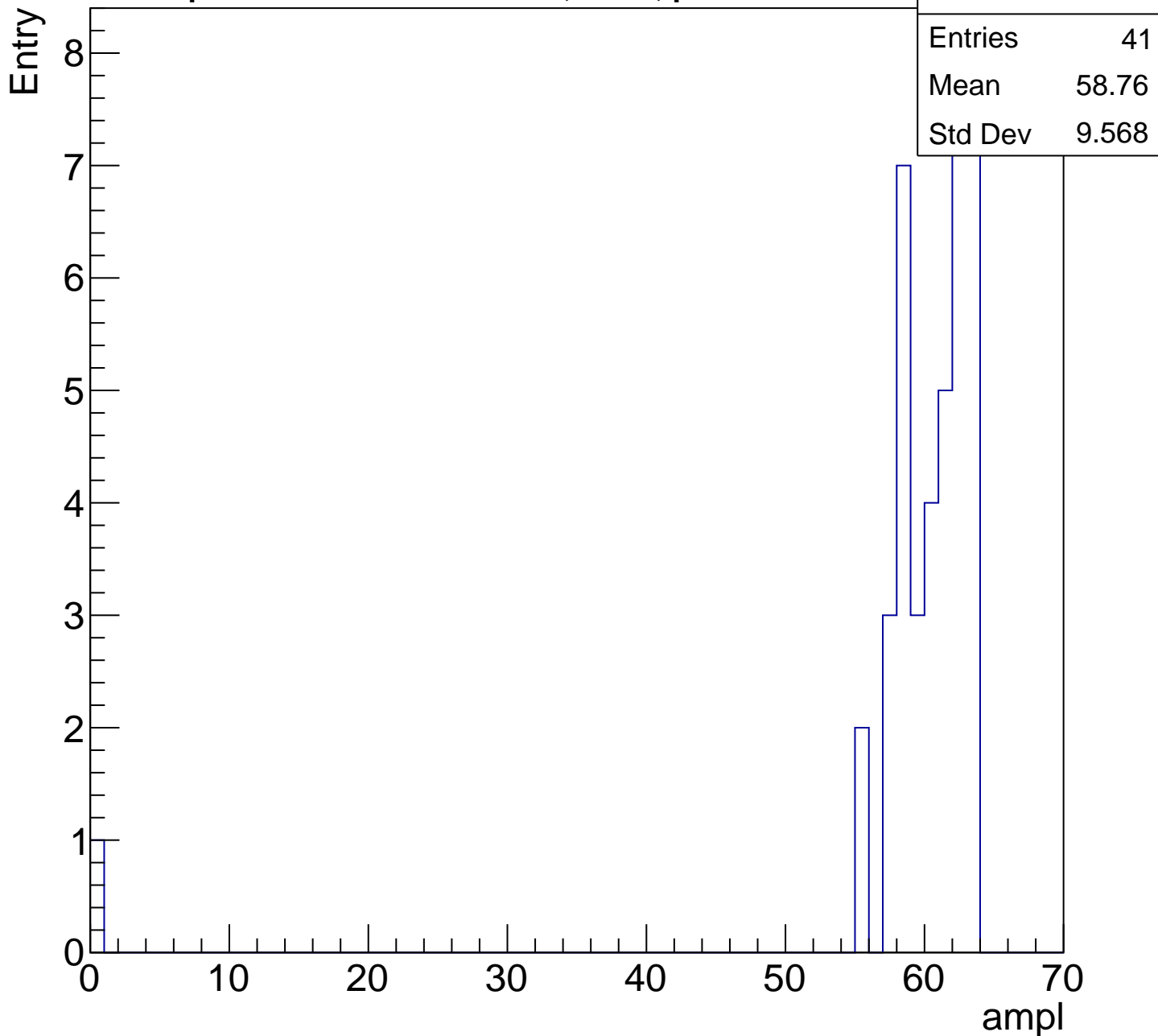
Entry

Entries	60
Mean	55.65
Std Dev	3.145



B1L100S, U13-ch0, adc5

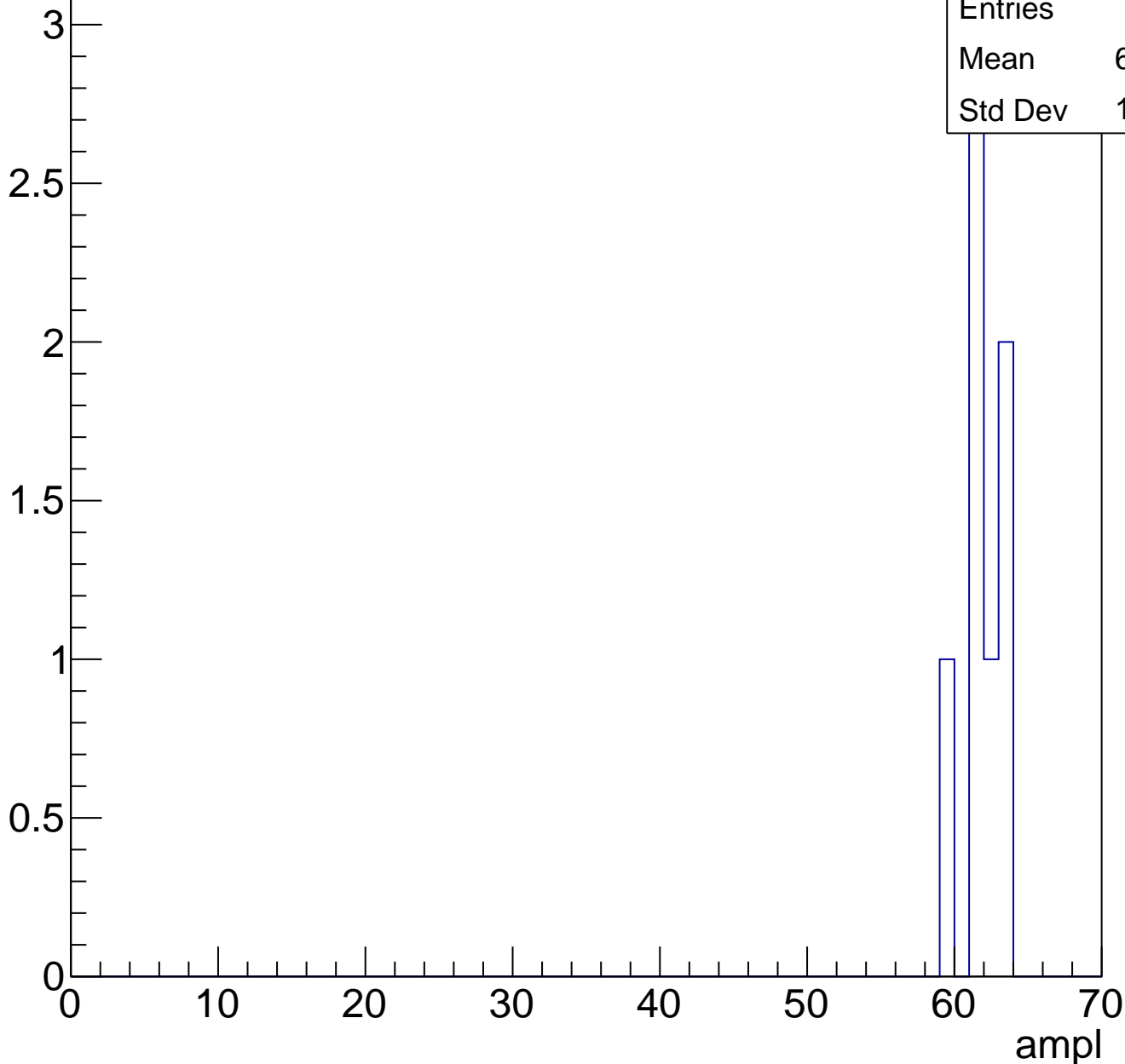
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch1, adc0

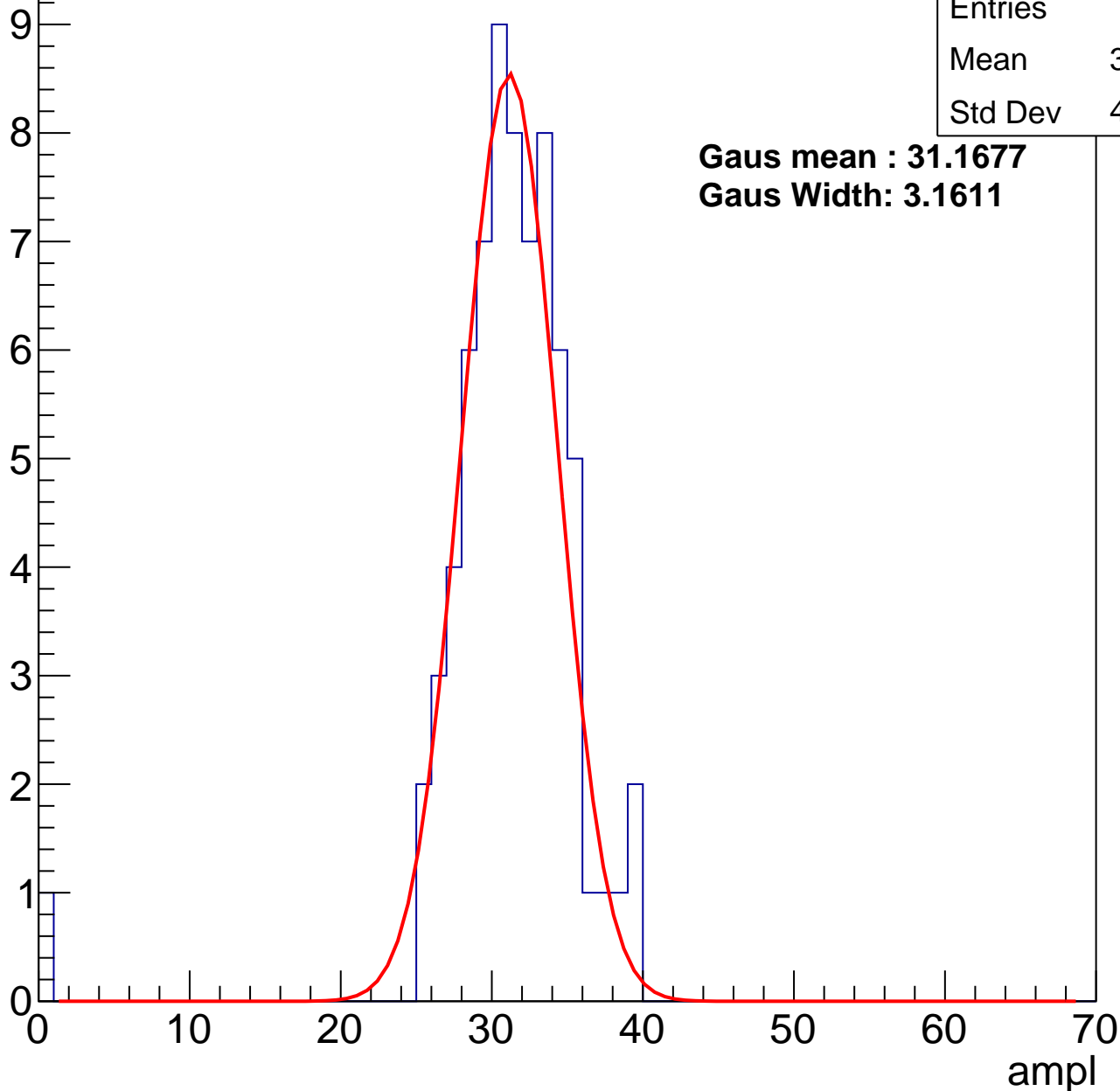
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.72
Std Dev	4.847

Gaus mean : 31.1677

Gaus Width: 3.1611



B1L100S, U13-ch1, adc1

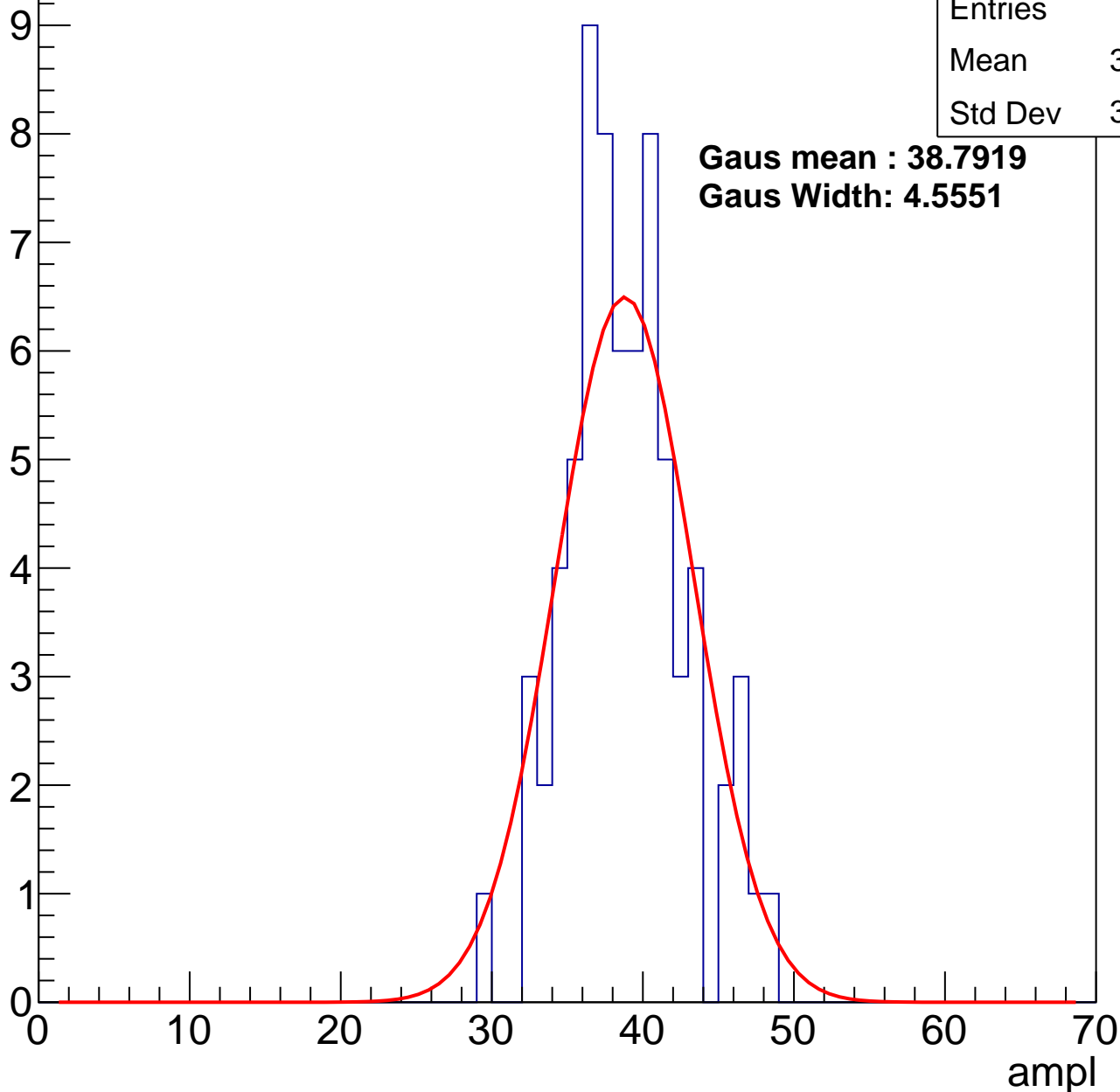
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.45
Std Dev	3.899

Gaus mean : 38.7919

Gaus Width: 4.5551



B1L100S, U13-ch1, adc2

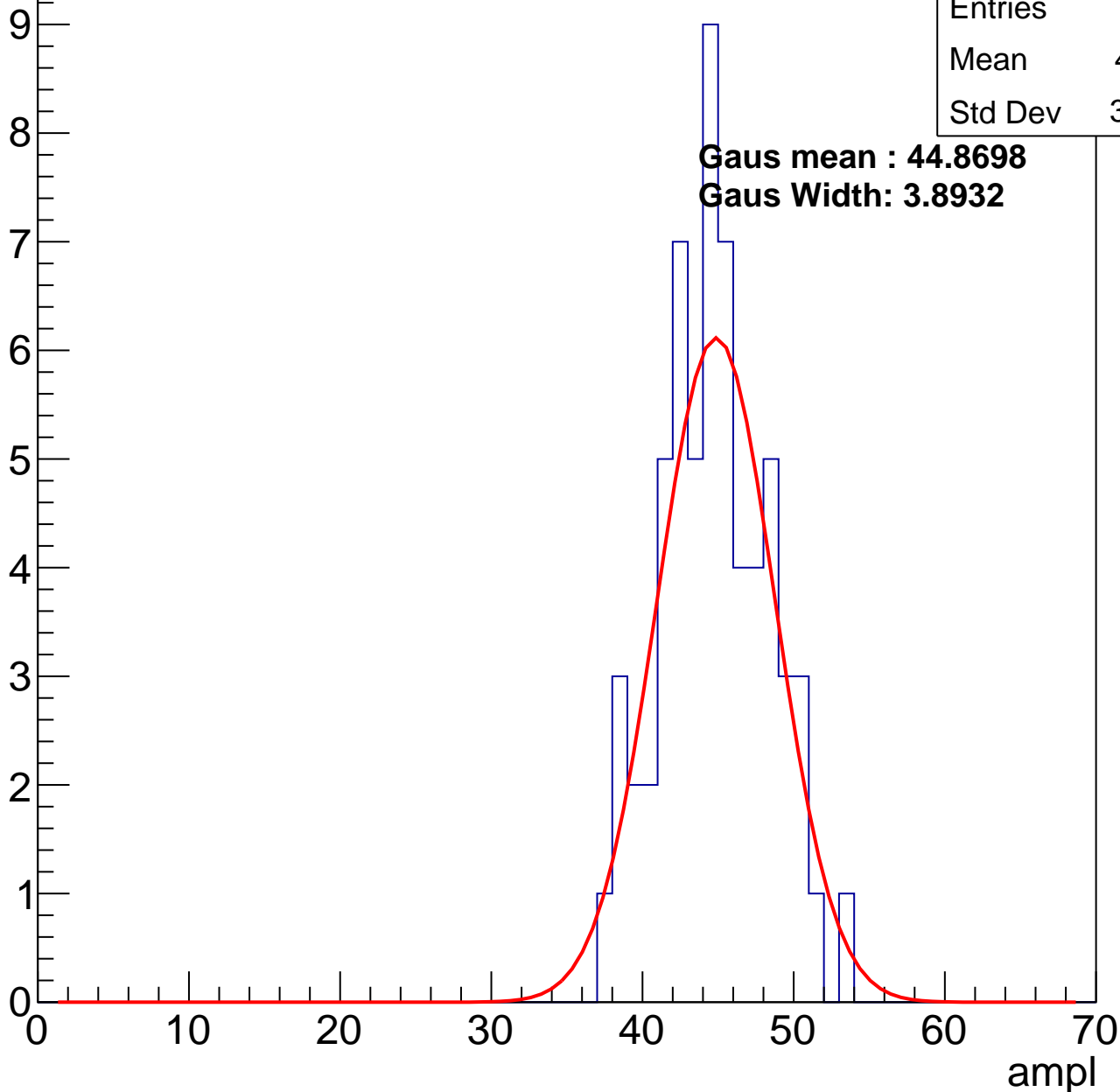
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.31
Std Dev	3.508

Gaus mean : 44.8698

Gaus Width: 3.8932

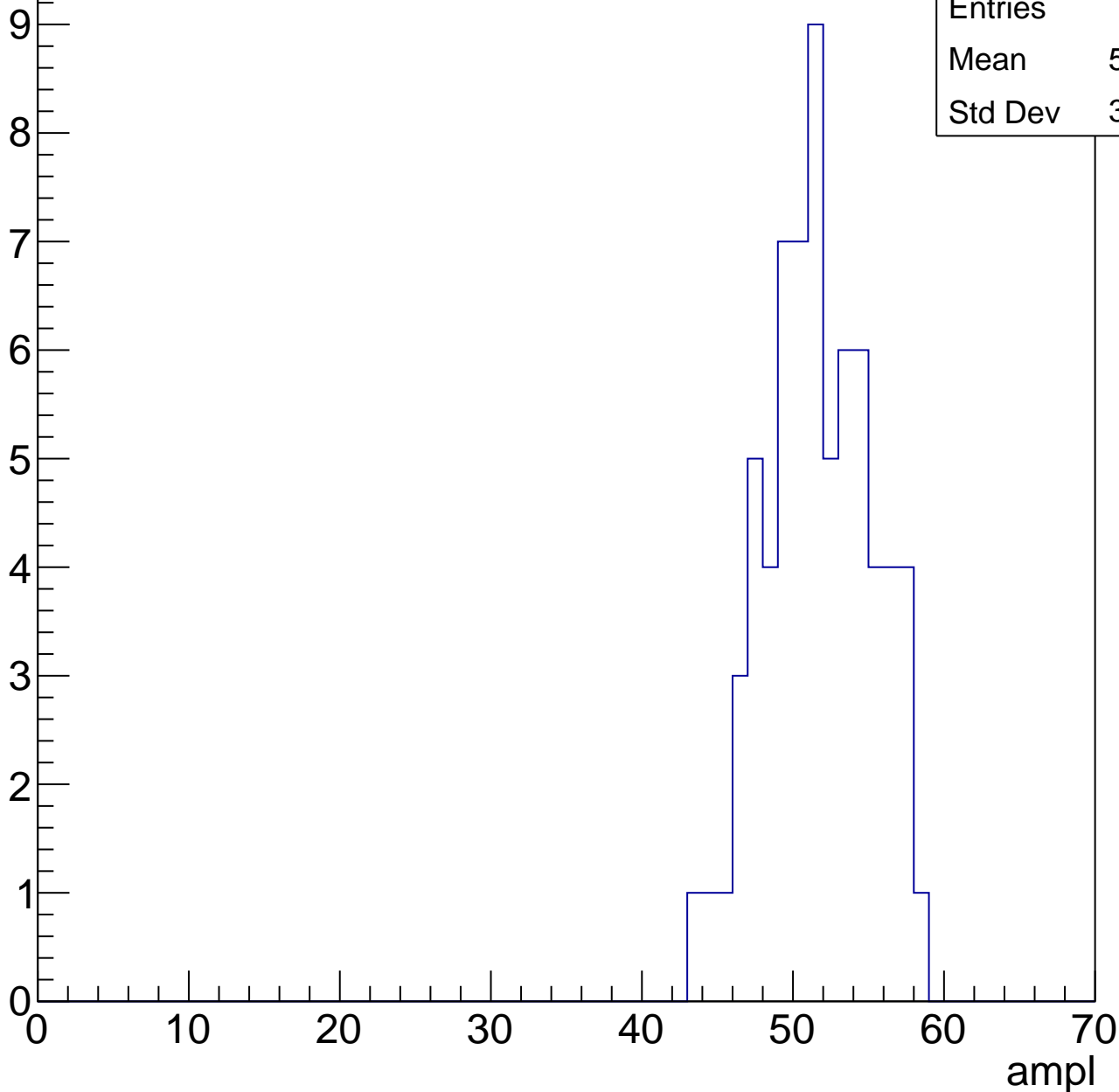


B1L100S, U13-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	51.19
Std Dev	3.457



B1L100S, U13-ch1, adc4

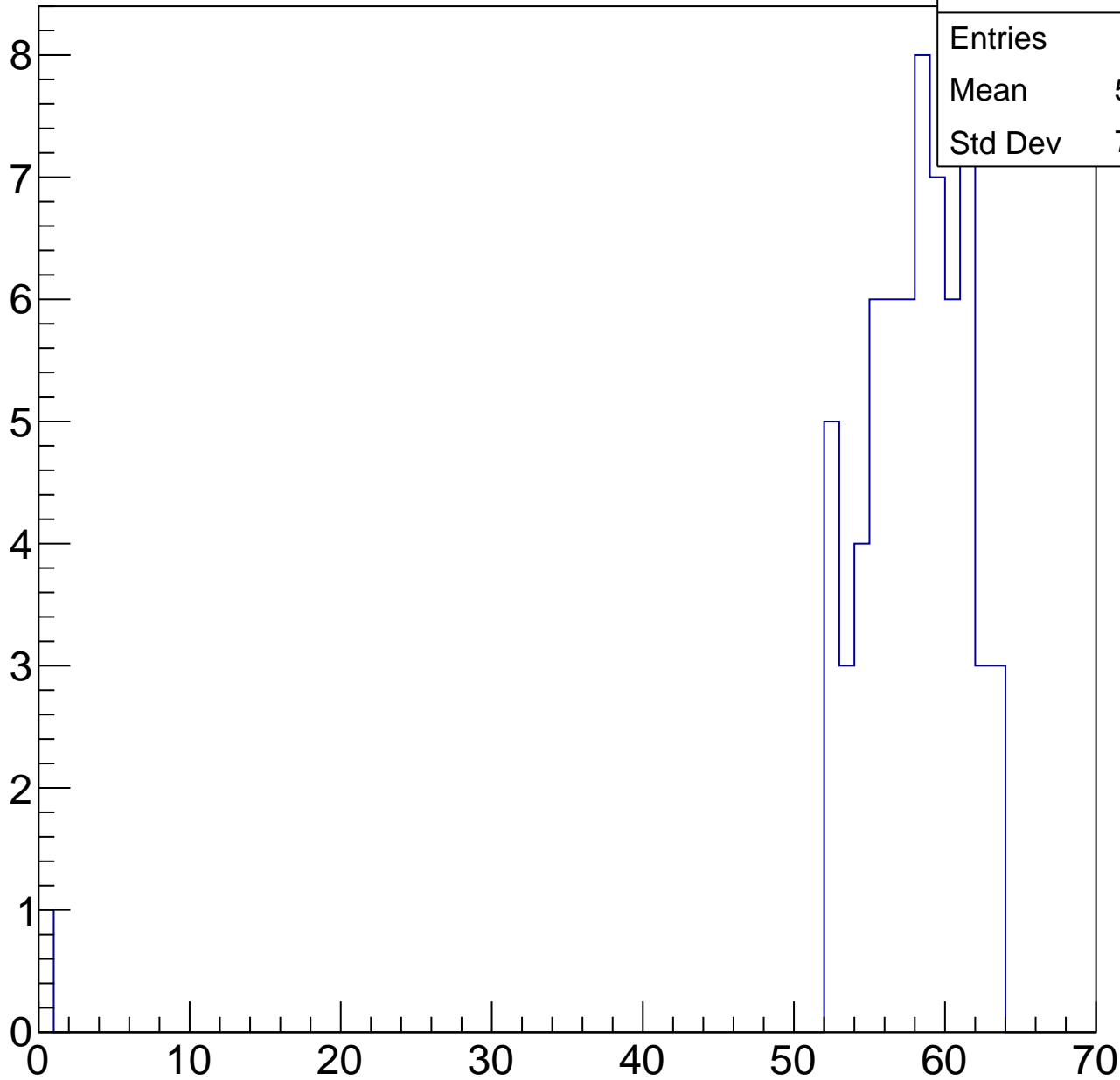
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	66
Mean	56.71
Std Dev	7.671

ampl

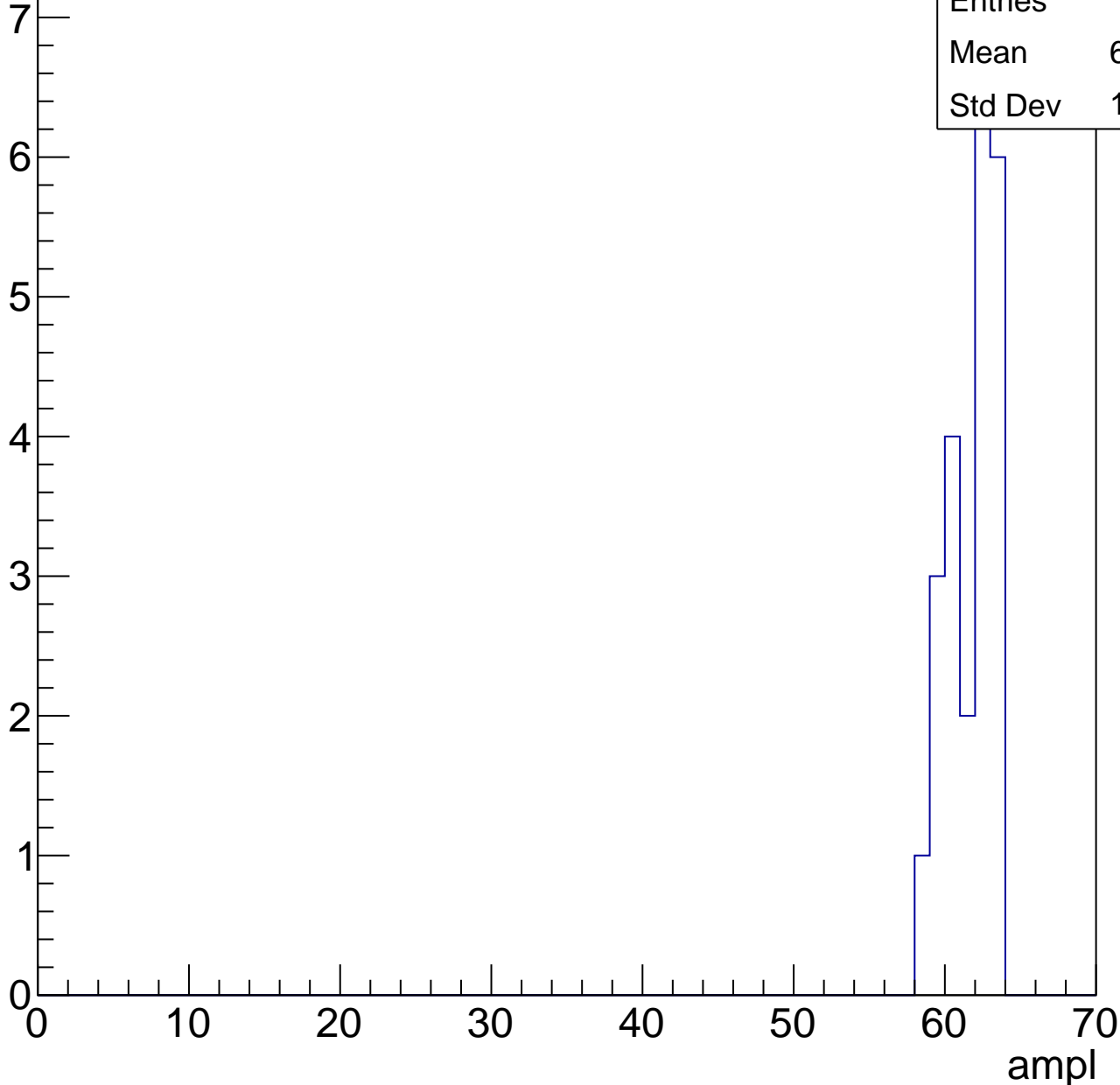


B1L100S, U13-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	61.26
Std Dev	1.538



B1L100S, U13-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch2, adc0

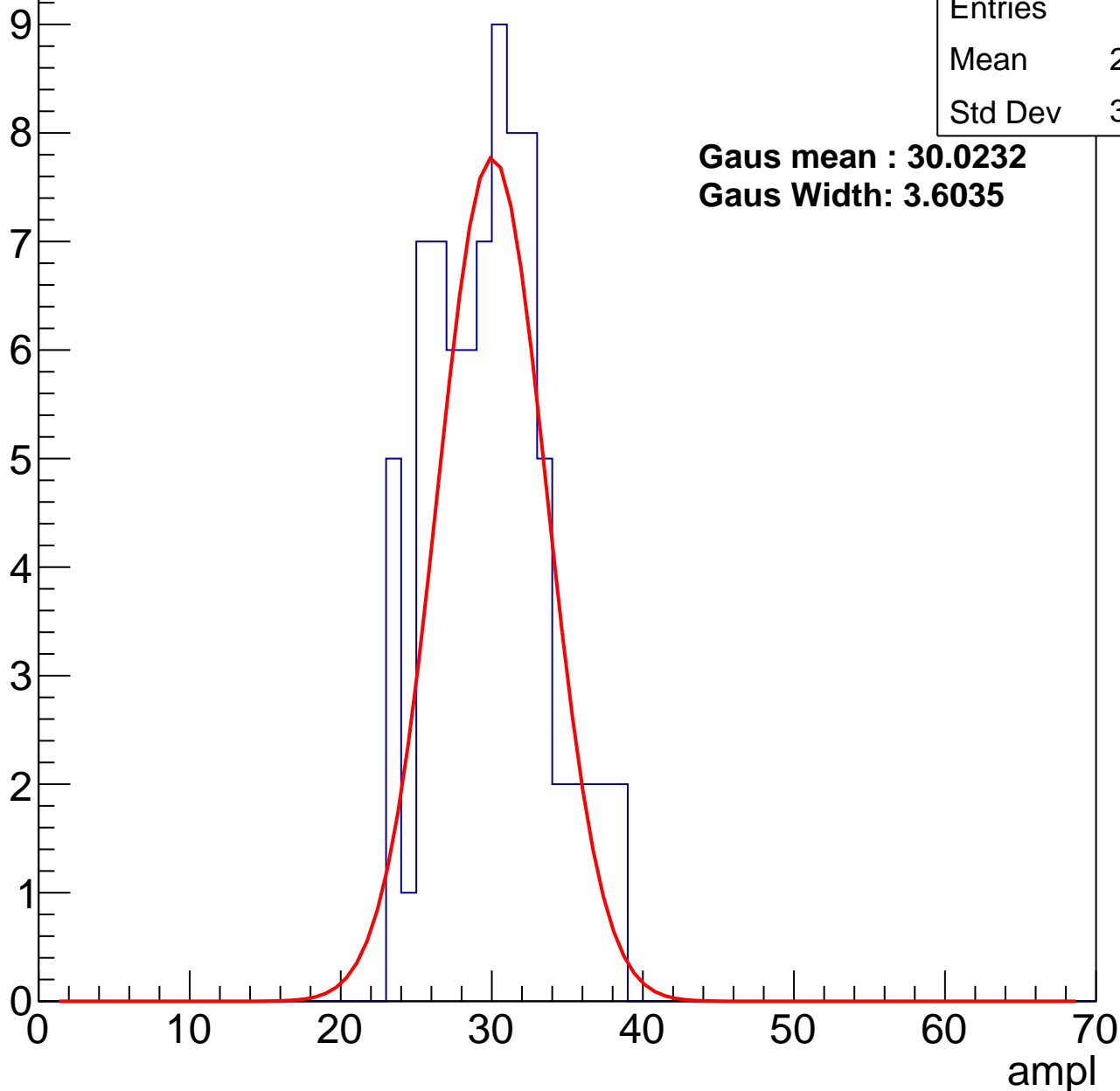
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	29.47
Std Dev	3.724

Gaus mean : 30.0232

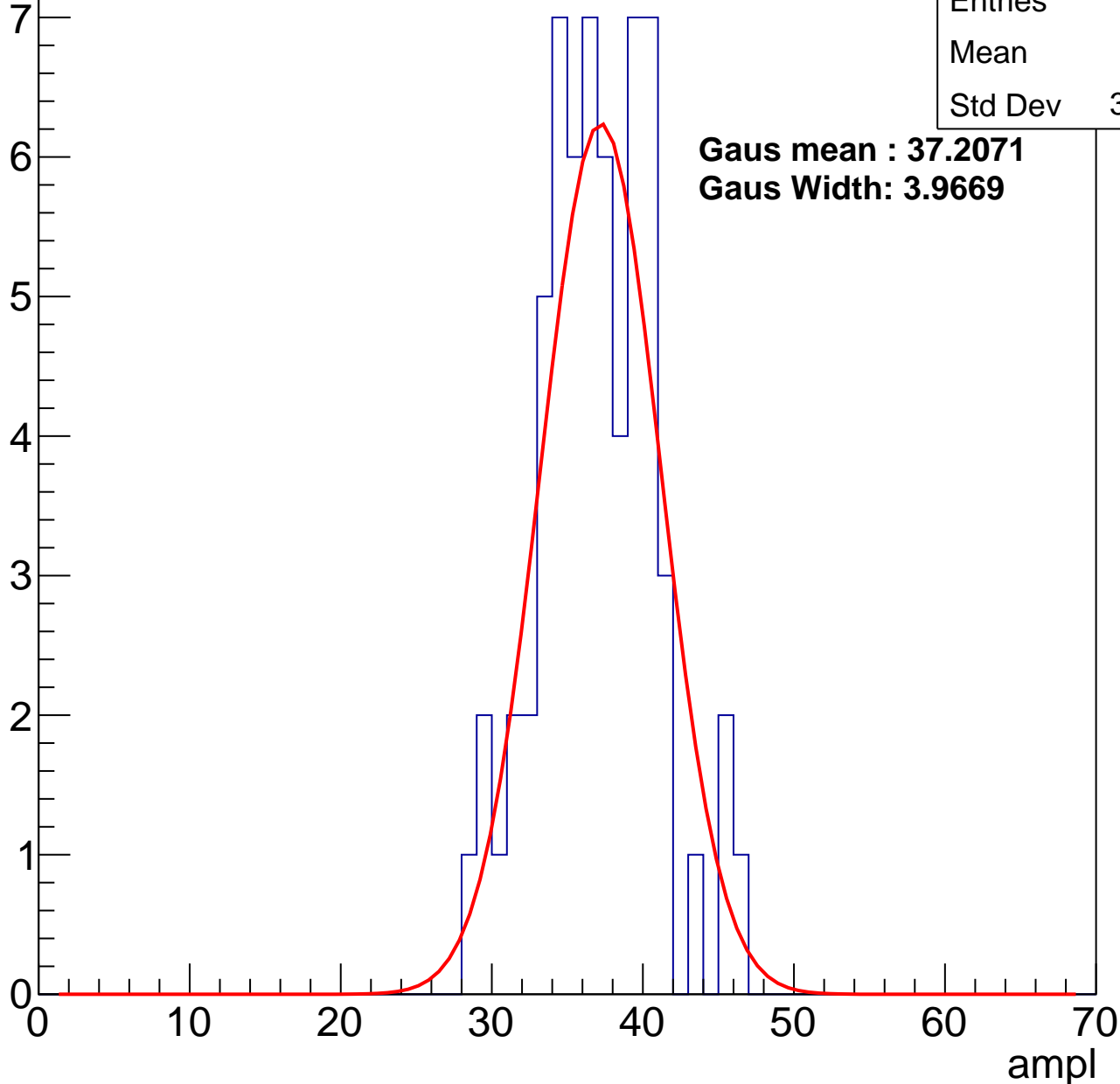
Gaus Width: 3.6035



B1L100S, U13-ch2, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch2, adc2

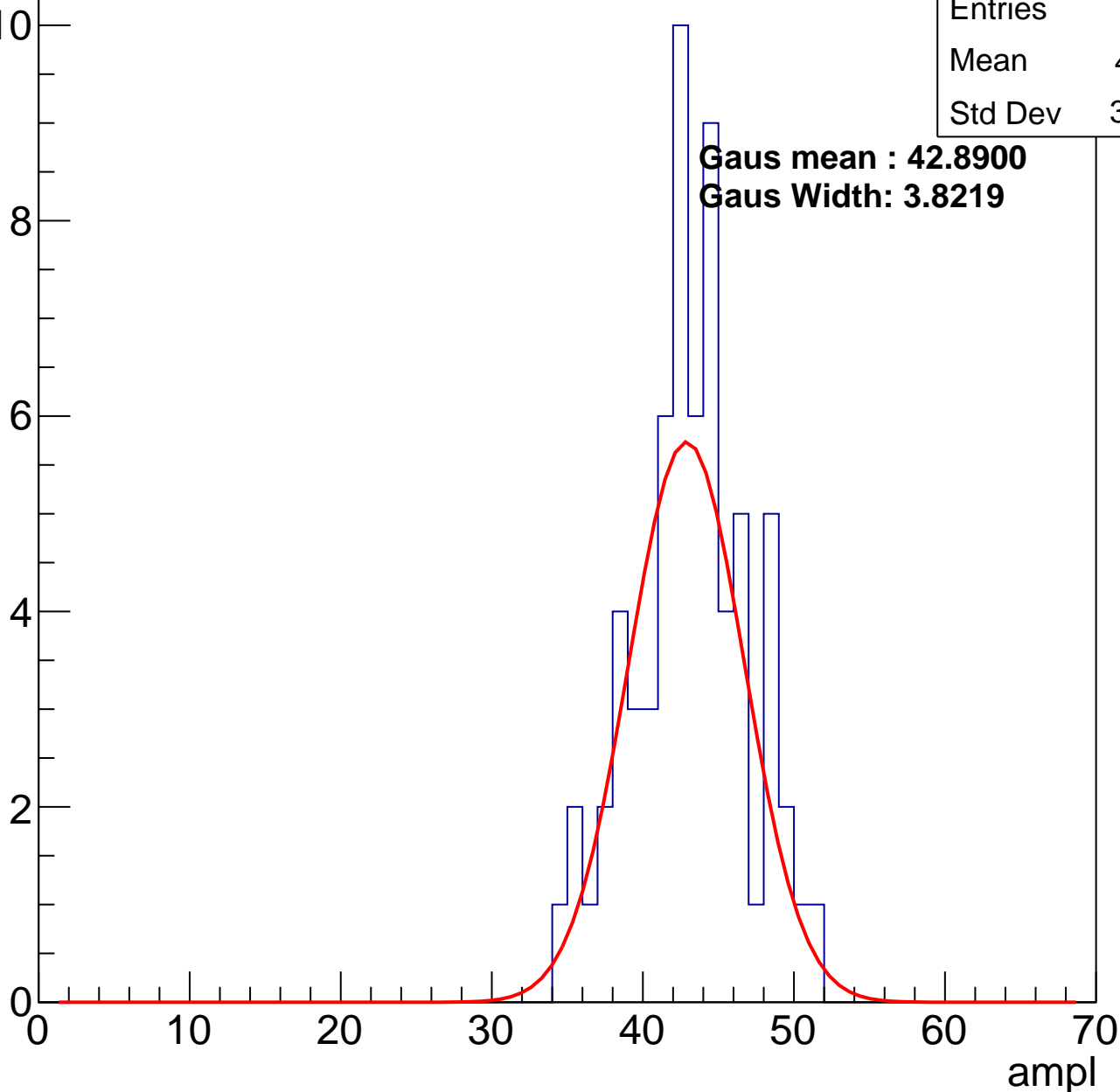
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.71
Std Dev	3.753

Gaus mean : 42.8900

Gaus Width: 3.8219

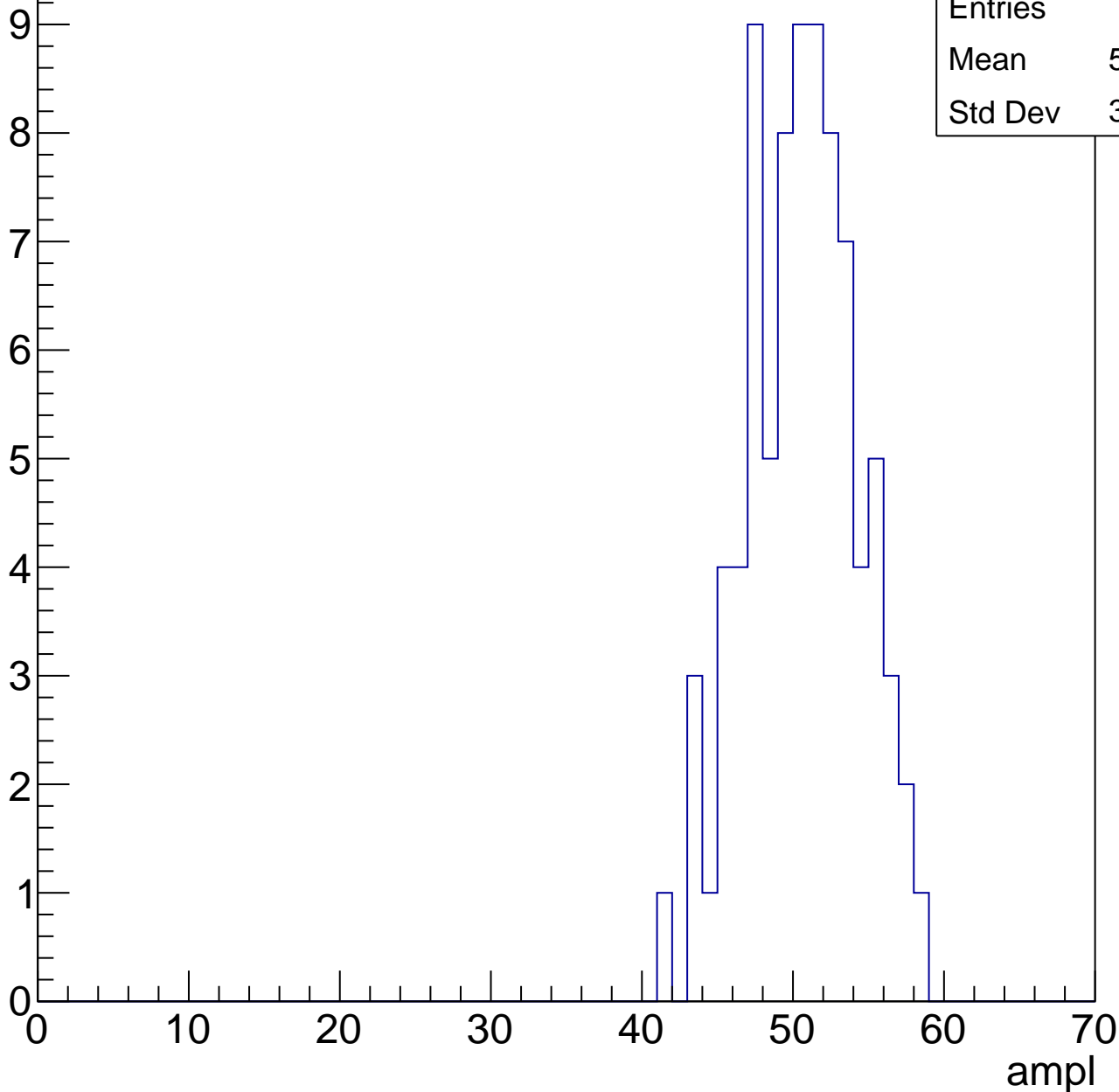


B1L100S, U13-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

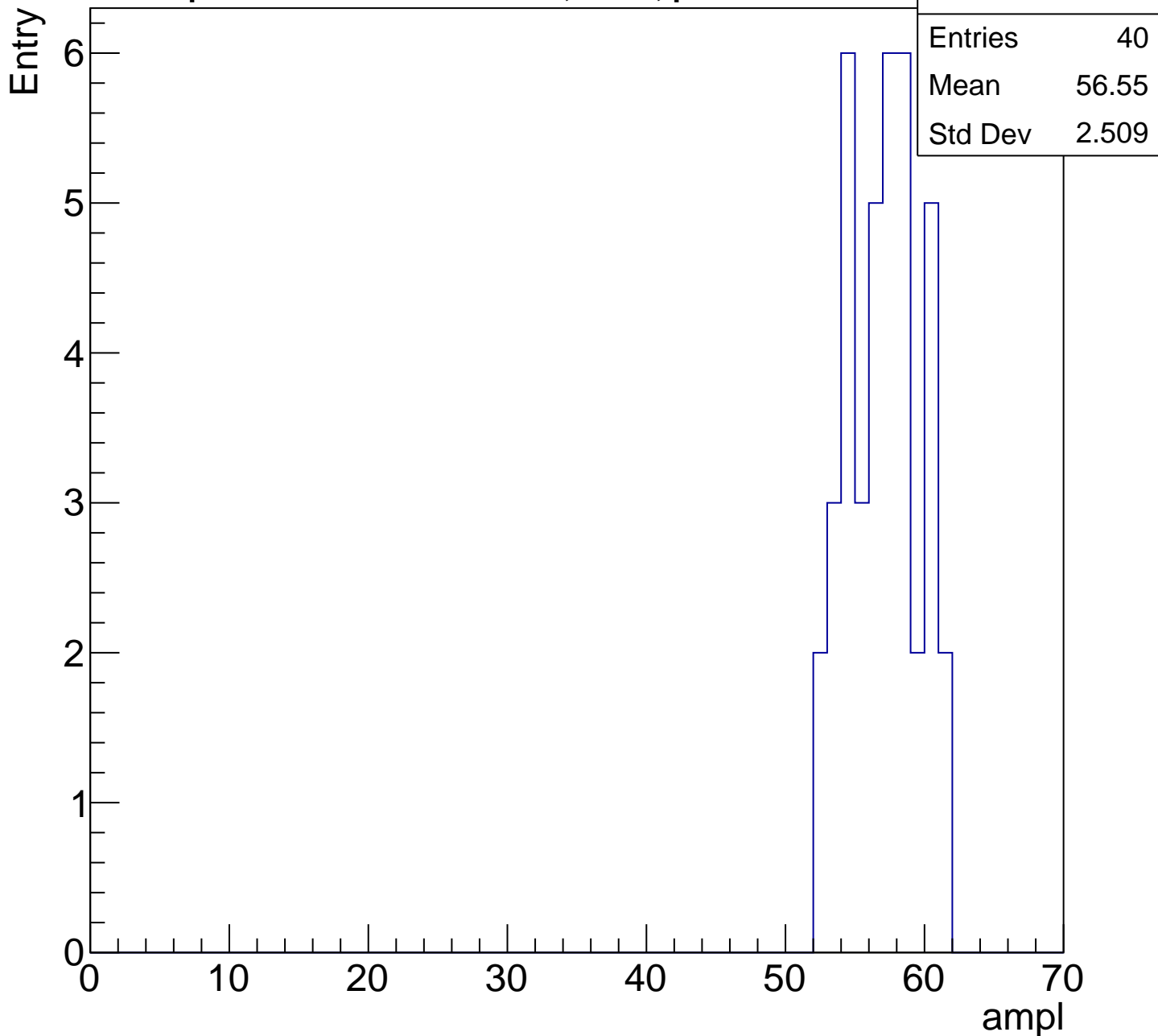
Entry

Entries	83
Mean	50.12
Std Dev	3.638



B1L100S, U13-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

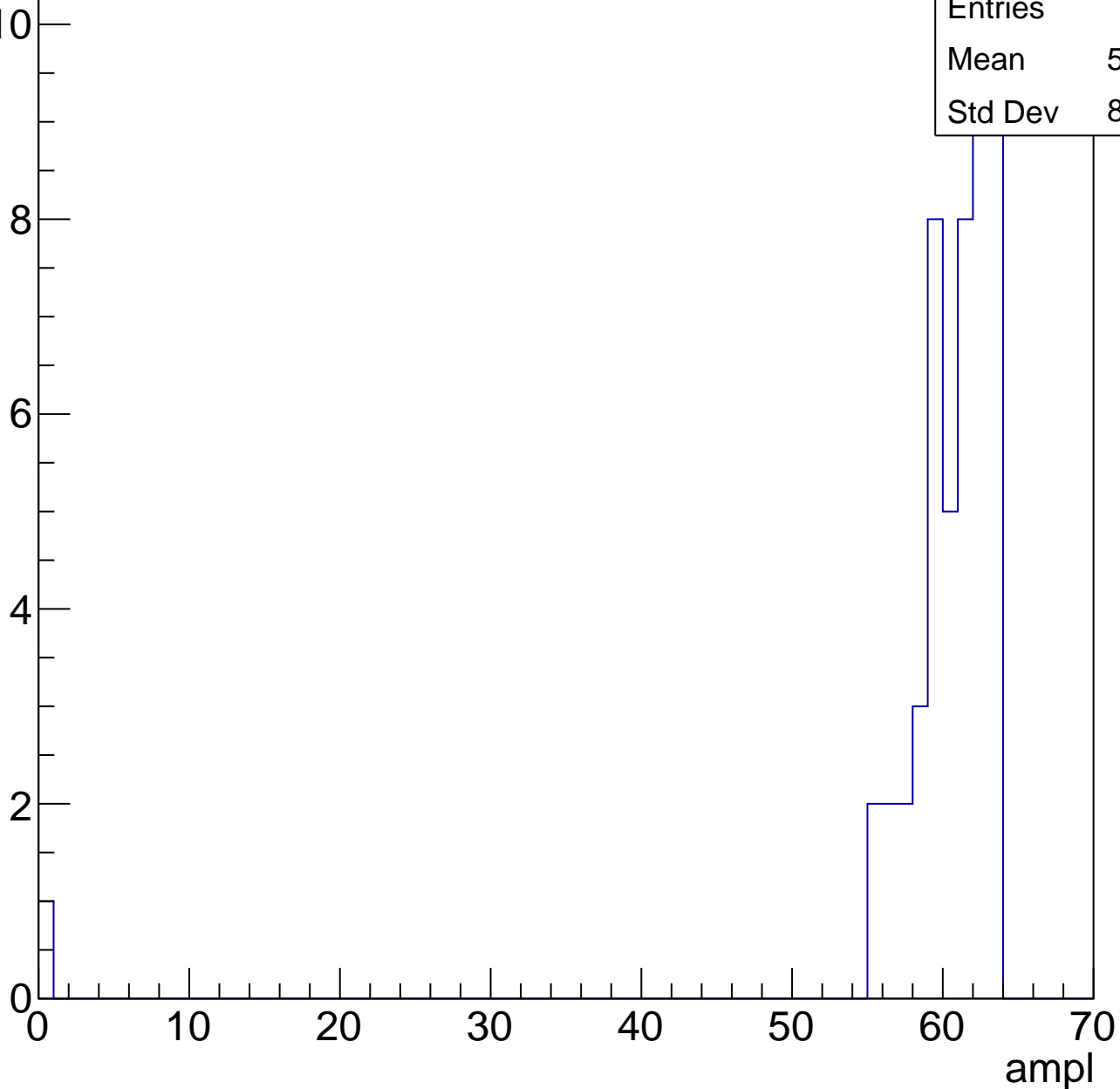


B1L100S, U13-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	59.22
Std Dev	8.662



B1L100S, U13-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L100S, U13-ch3, adc0

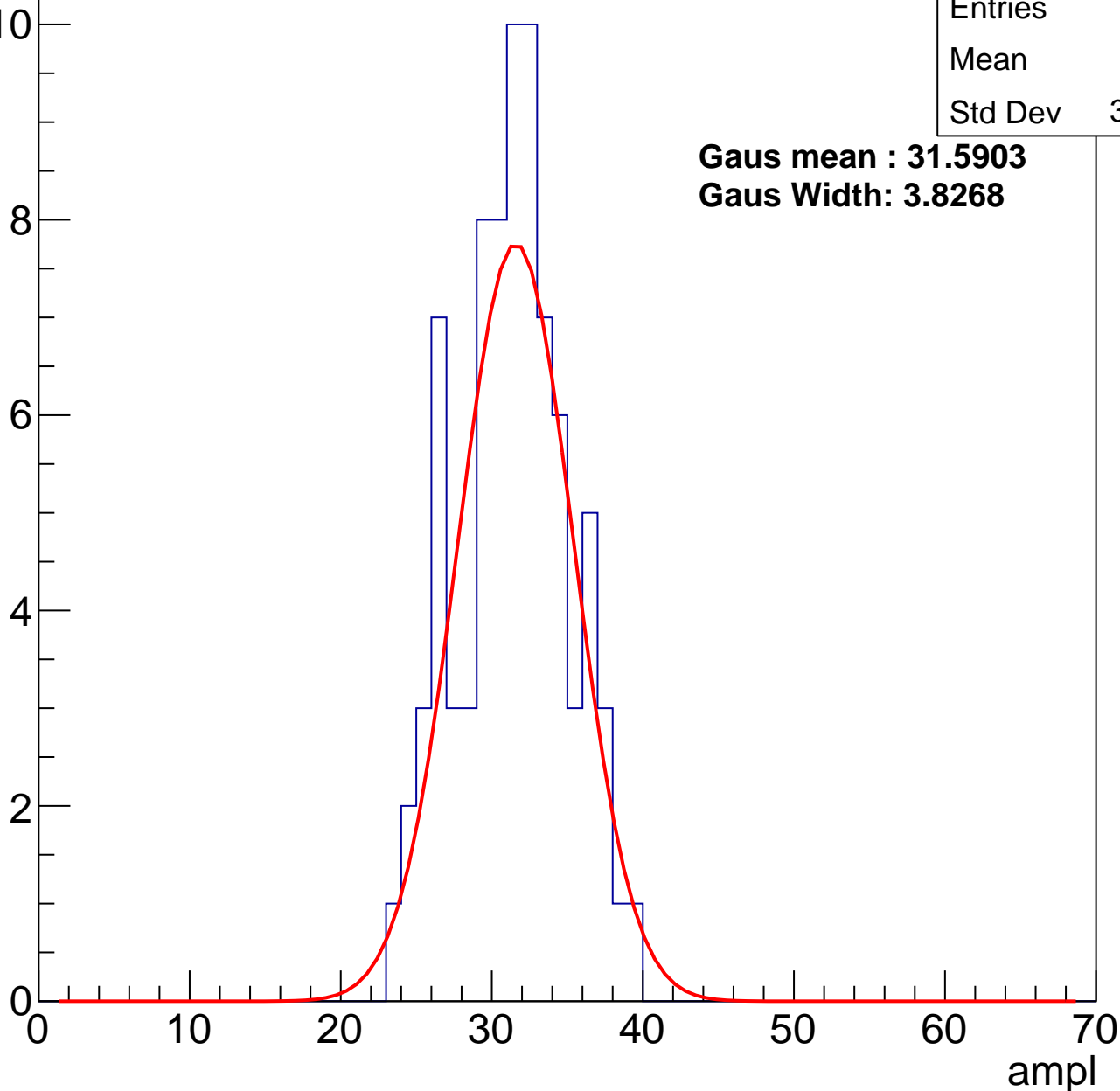
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	30.9
Std Dev	3.592

Gaus mean : 31.5903

Gaus Width: 3.8268



B1L100S, U13-ch3, adc1

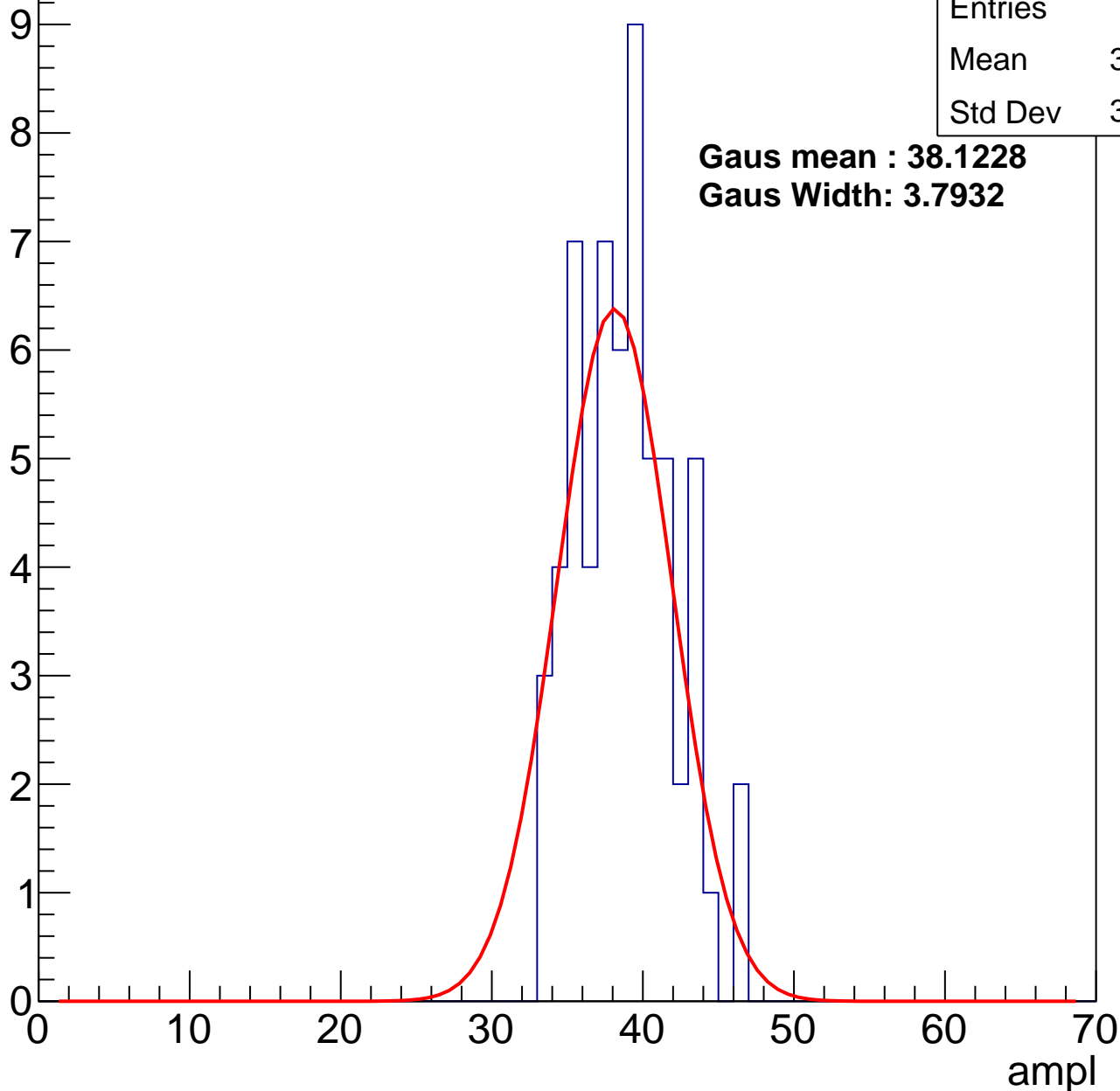
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	38.37
Std Dev	3.194

Gaus mean : 38.1228

Gaus Width: 3.7932



B1L100S, U13-ch3, adc2

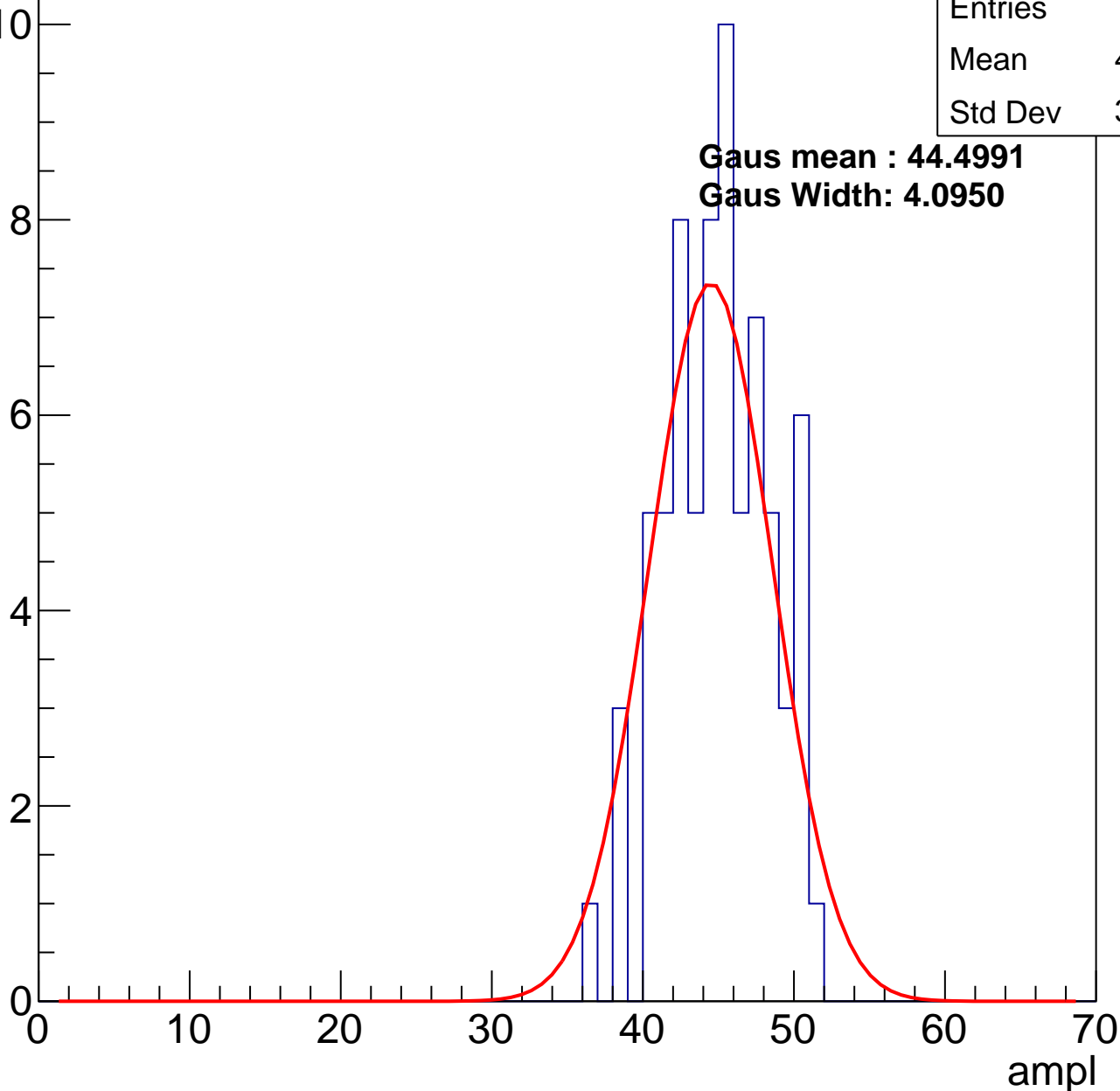
calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	44.51
Std Dev	3.391

Gaus mean : 44.4991

Gaus Width: 4.0950

Entry



B1L100S, U13-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

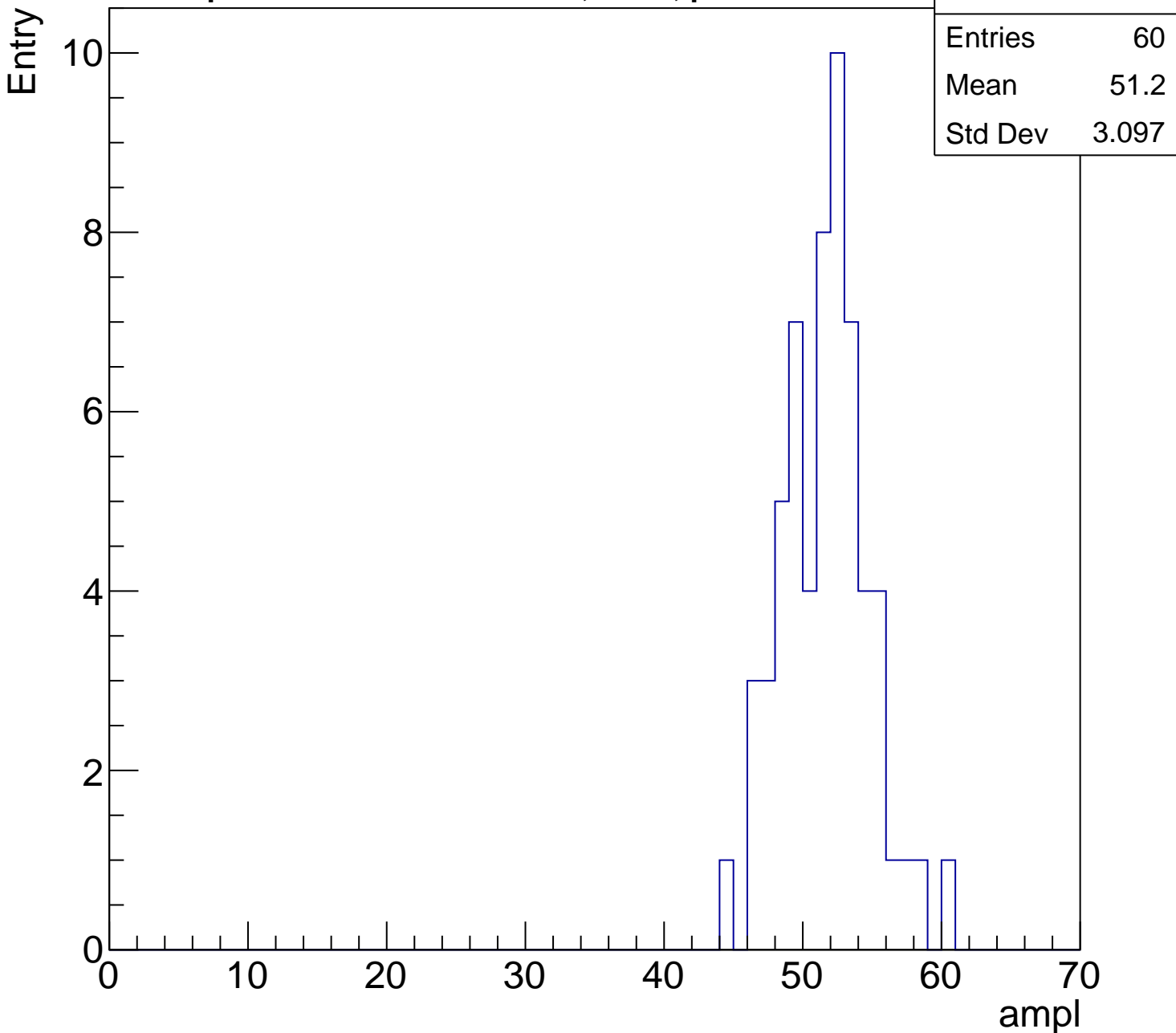
Entries	60
Mean	51.2
Std Dev	3.097

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

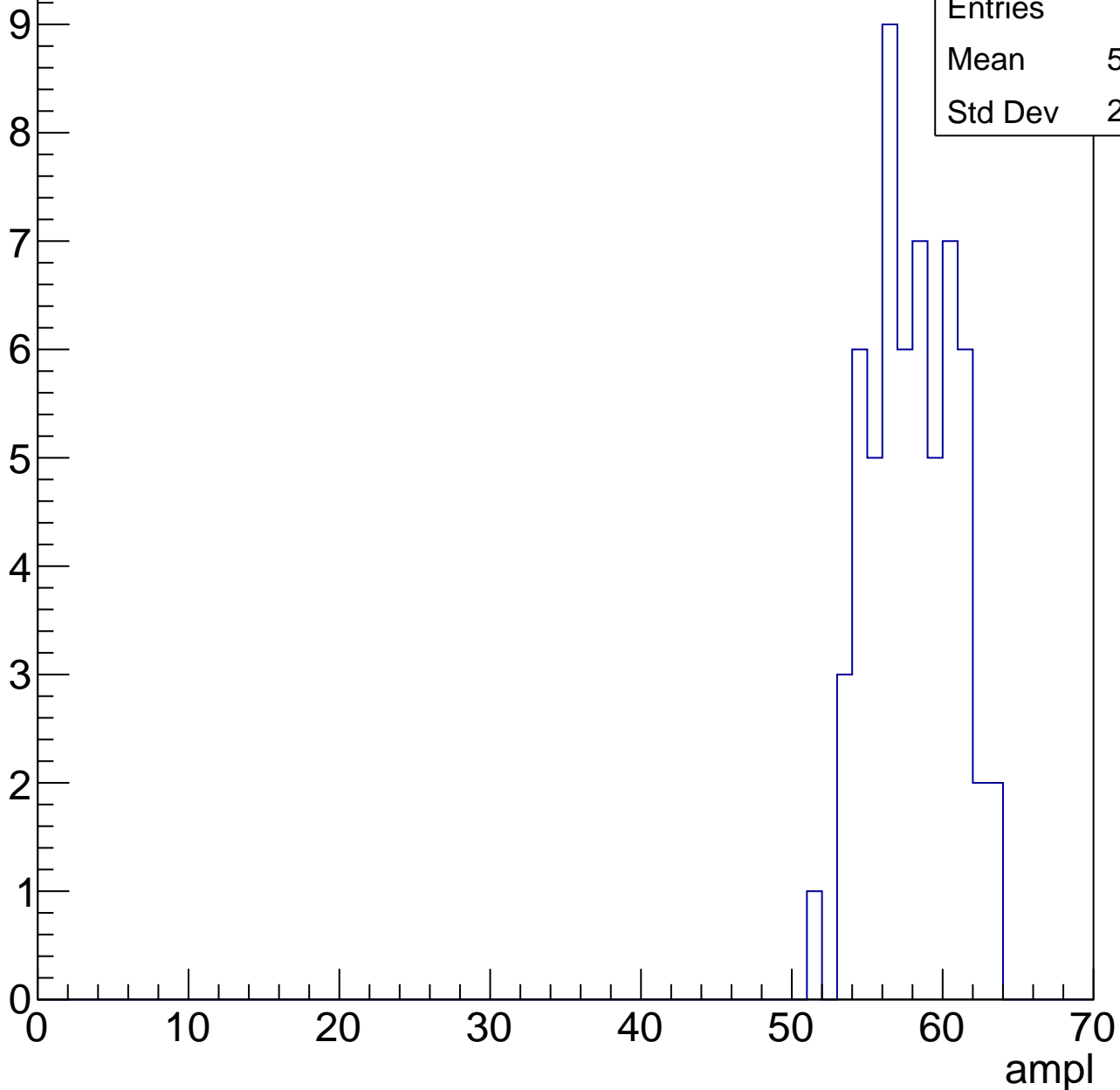


B1L100S, U13-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

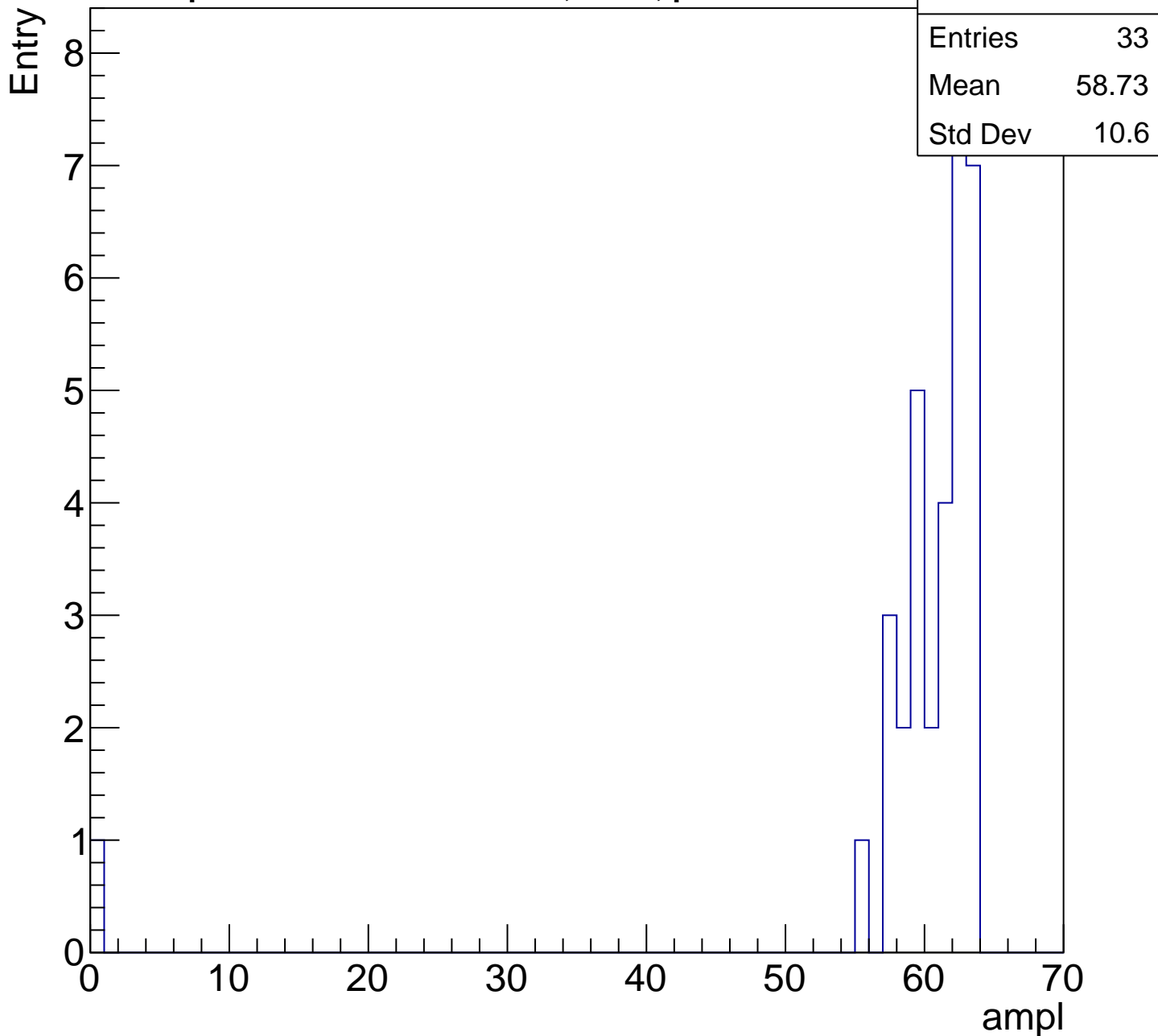
Entry

Entries	59
Mean	57.49
Std Dev	2.795



B1L100S, U13-ch3, adc5

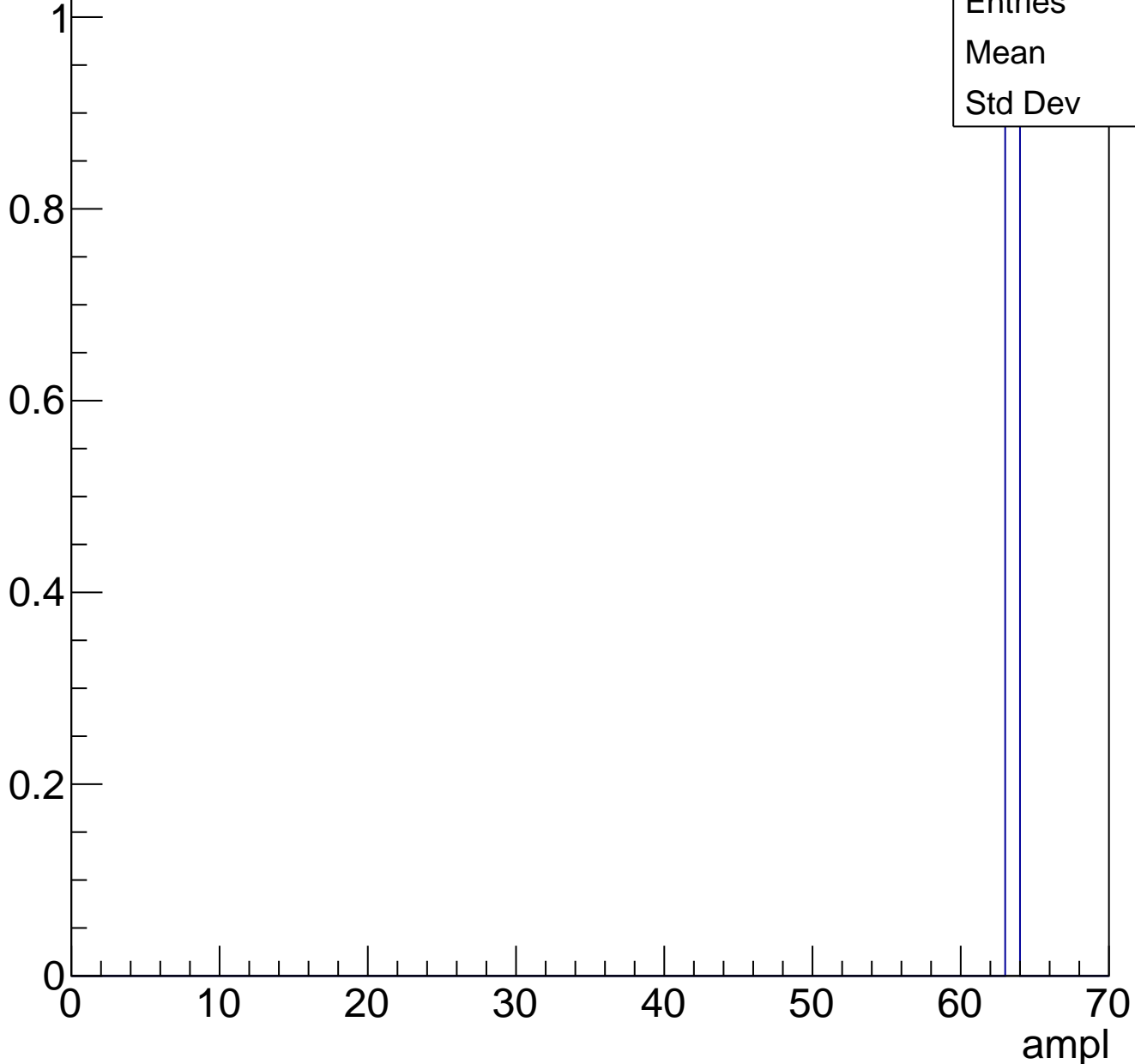
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch4, adc0

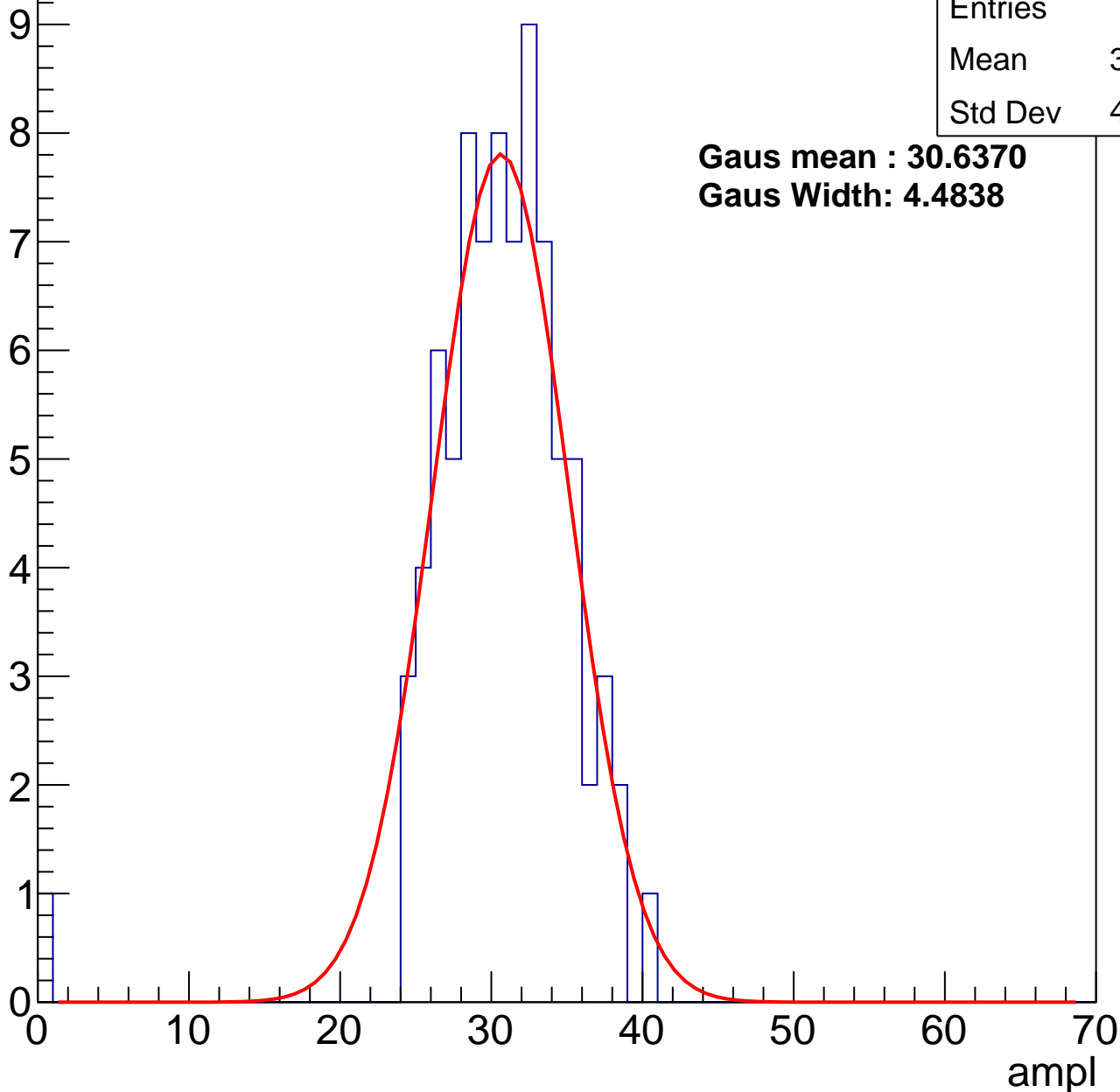
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	30.24
Std Dev	4.959

Gaus mean : 30.6370

Gaus Width: 4.4838



B1L100S, U13-ch4, adc1

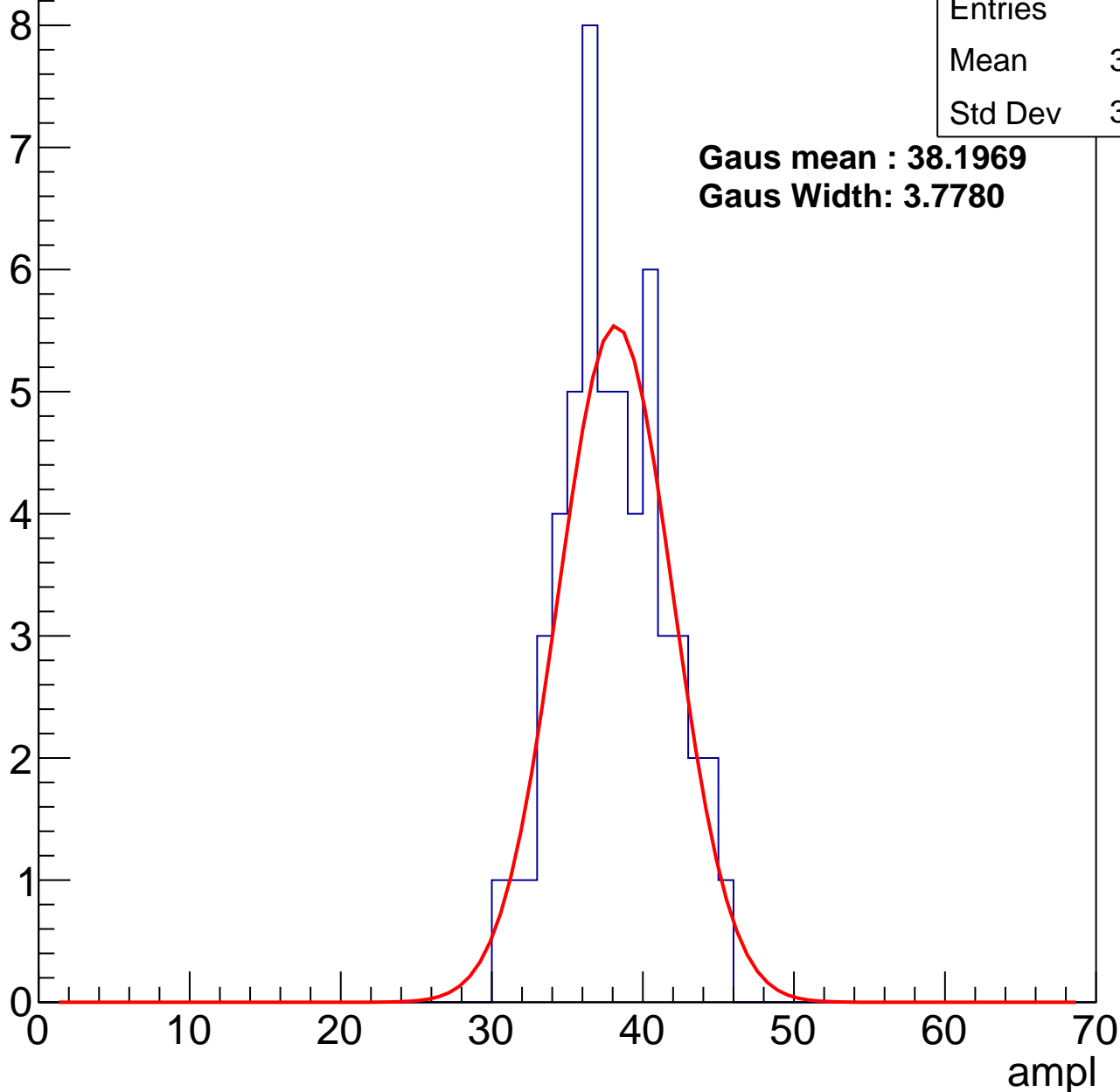
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	37.59
Std Dev	3.429

Gaus mean : 38.1969

Gaus Width: 3.7780



B1L100S, U13-ch4, adc2

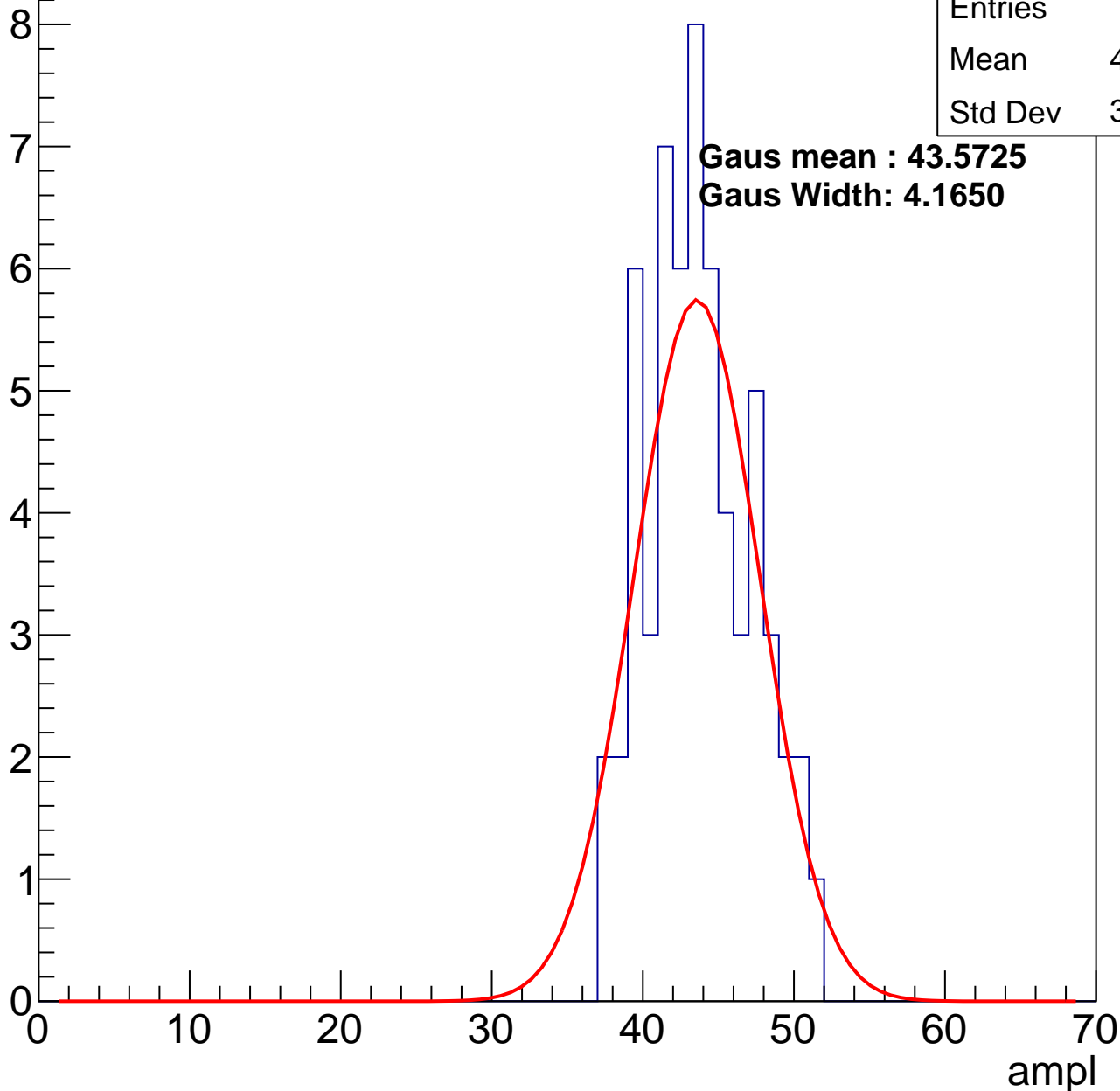
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.28
Std Dev	3.445

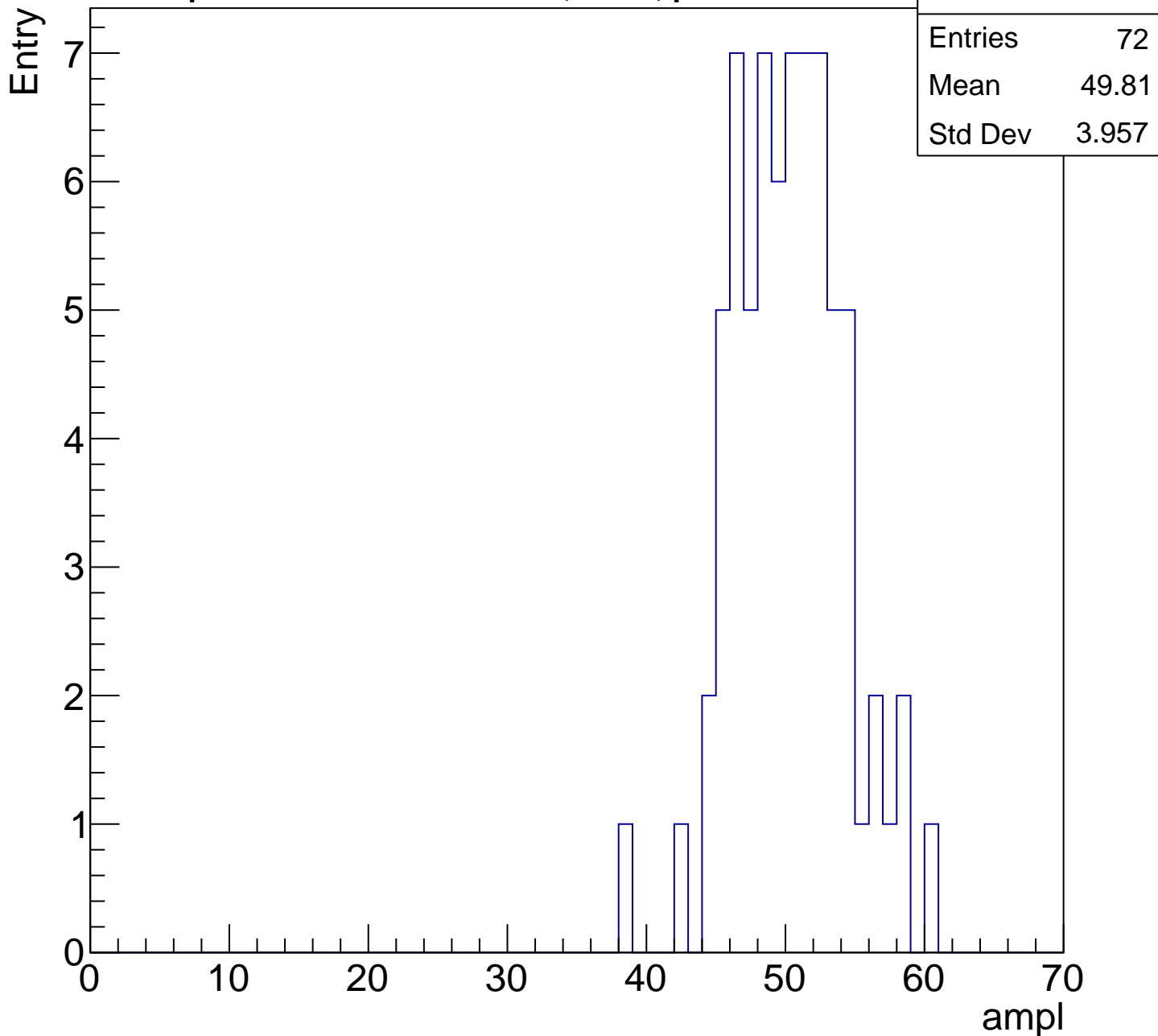
Gaus mean : 43.5725

Gaus Width: 4.1650



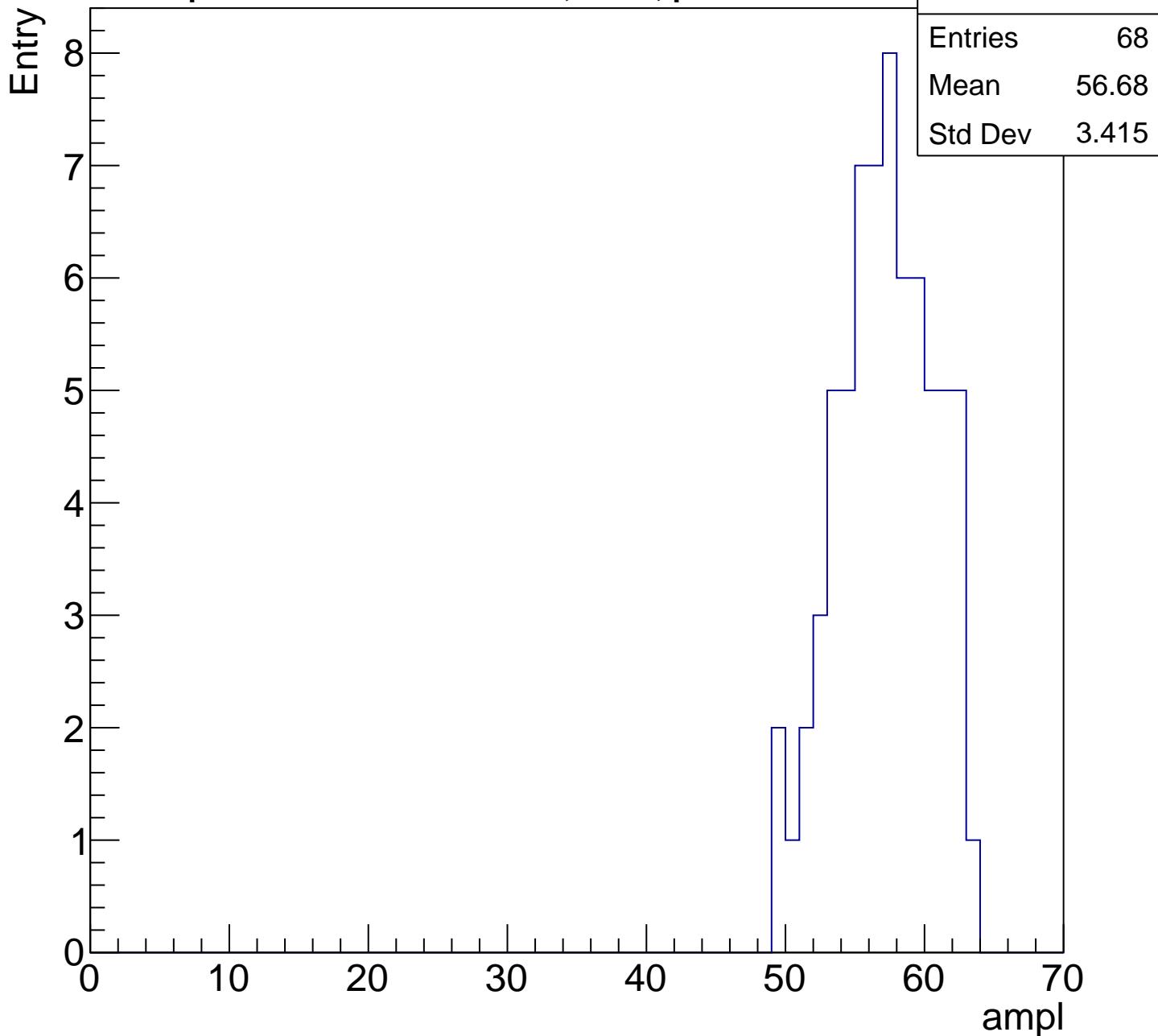
B1L100S, U13-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

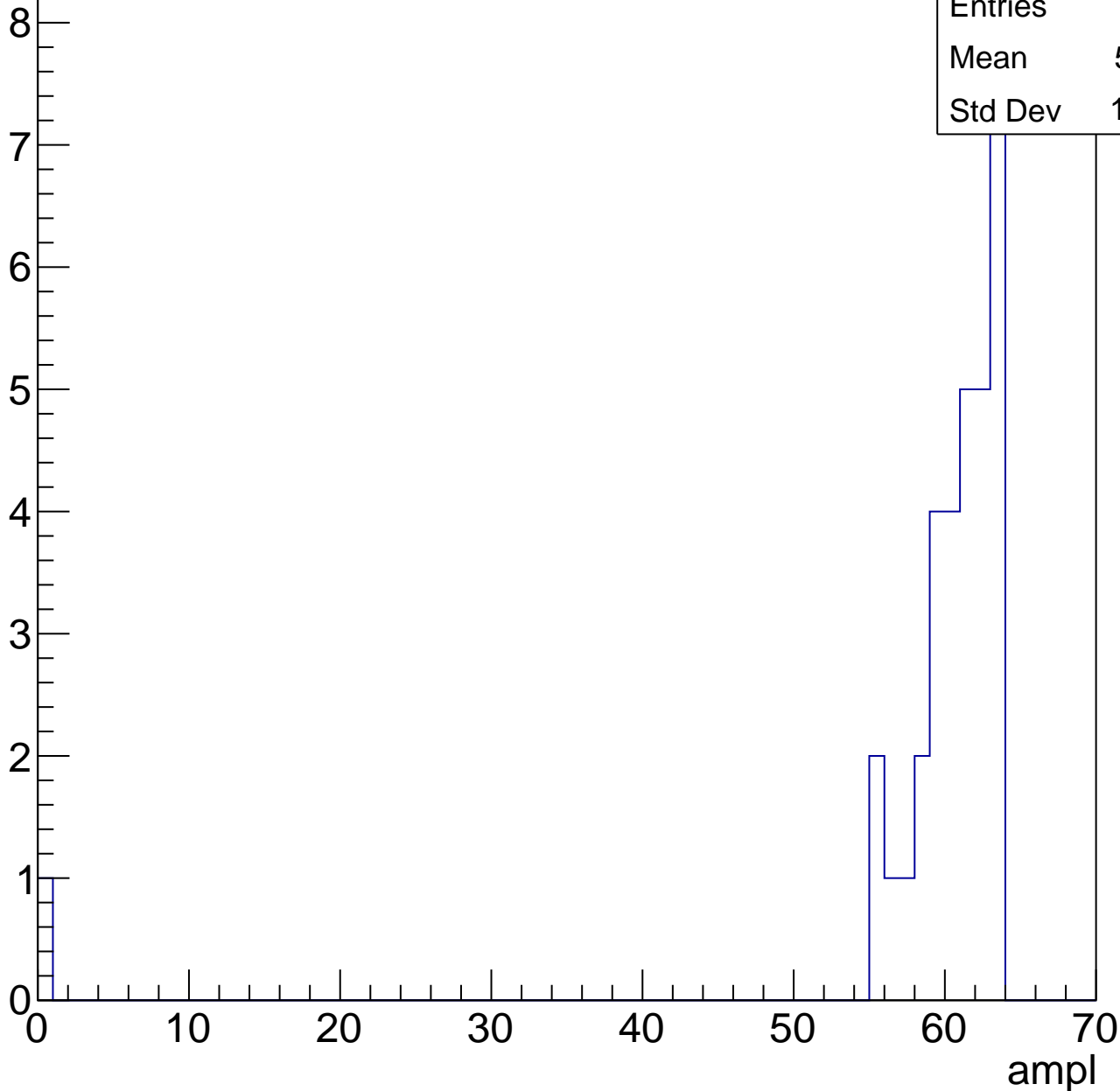


B1L100S, U13-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

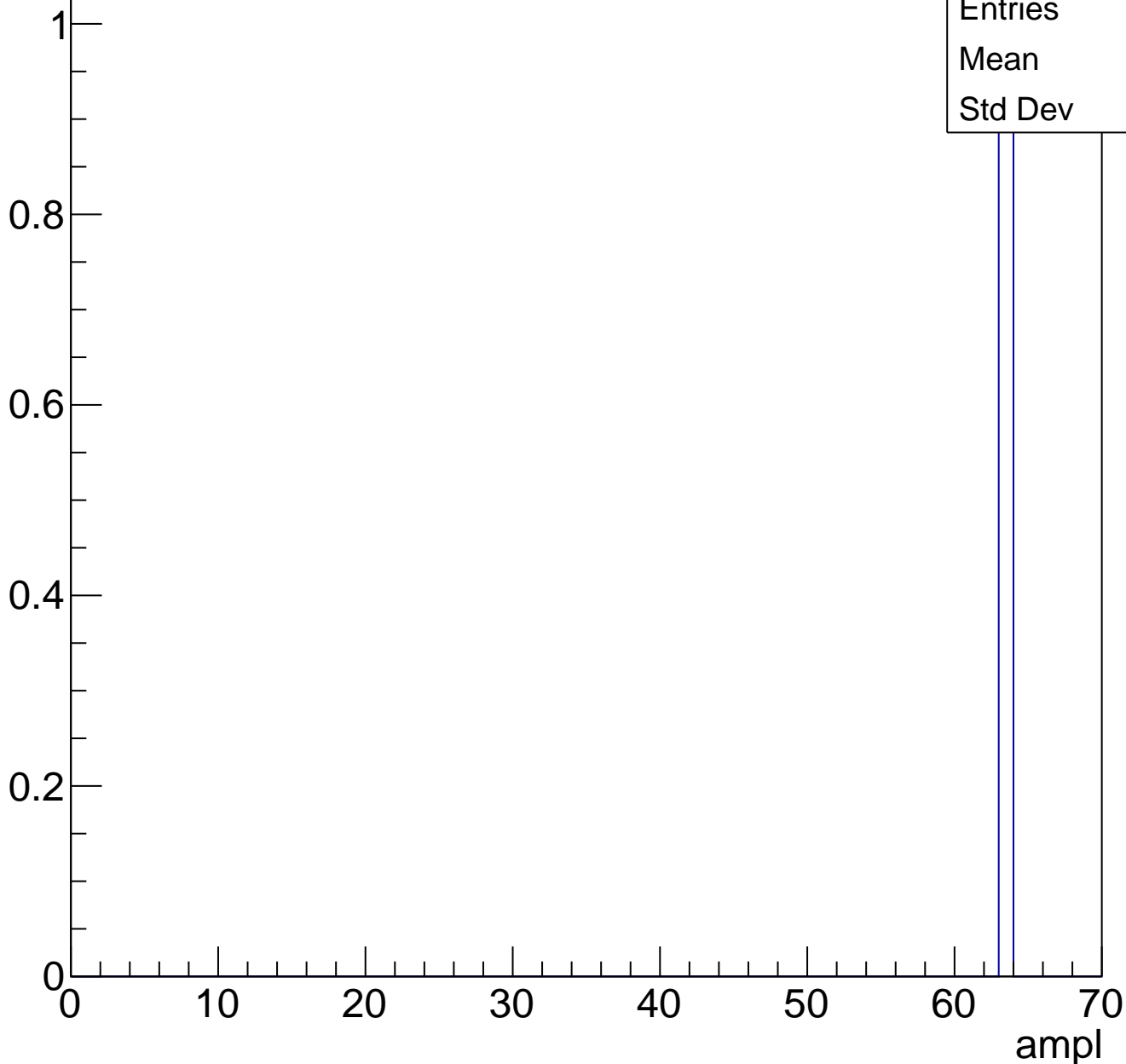
Entries	33
Mean	58.61
Std Dev	10.62



B1L100S, U13-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch5, adc0

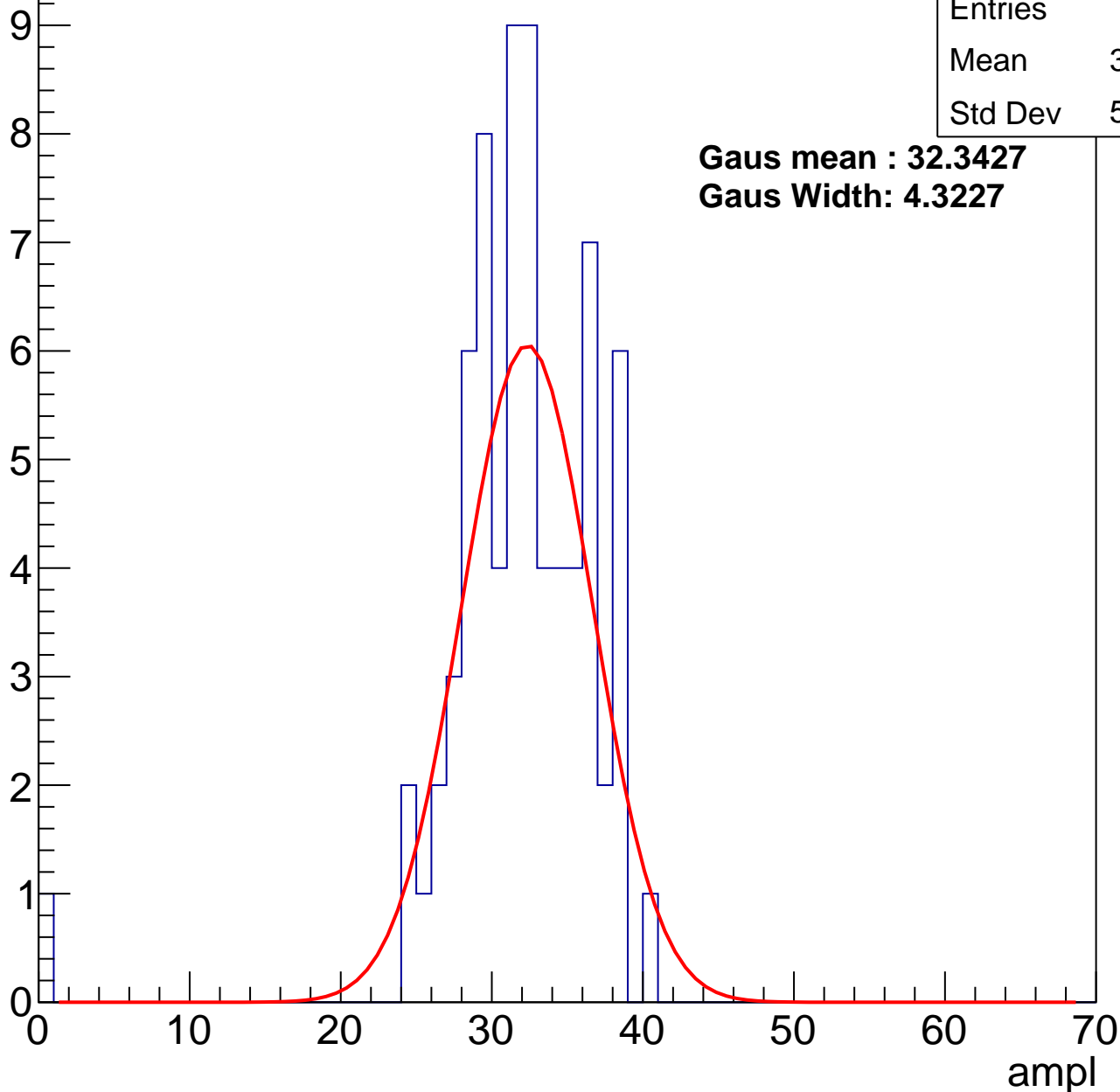
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	31.44
Std Dev	5.255

Gaus mean : 32.3427

Gaus Width: 4.3227



B1L100S, U13-ch5, adc1

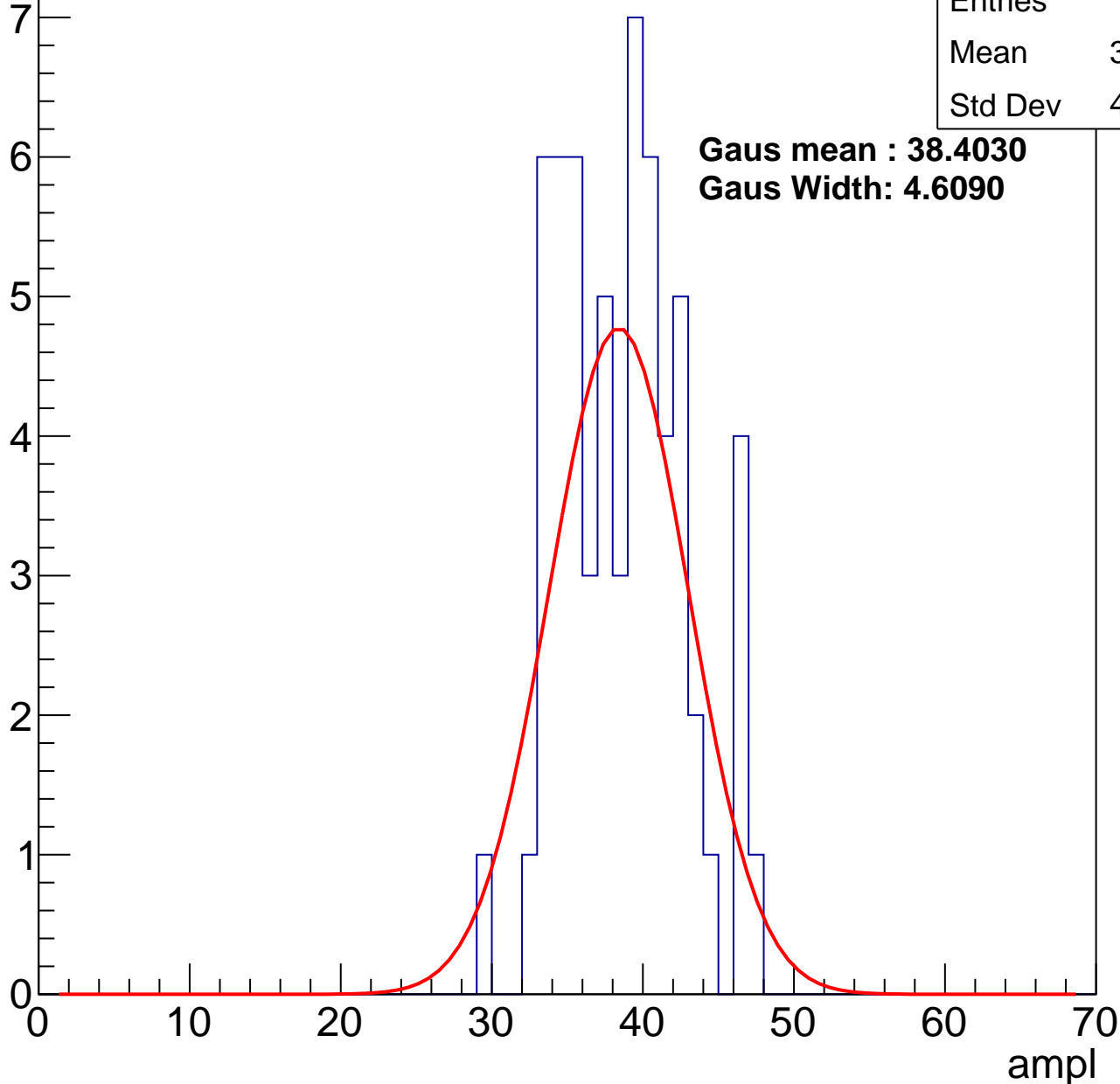
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	38.16
Std Dev	4.054

Gaus mean : 38.4030

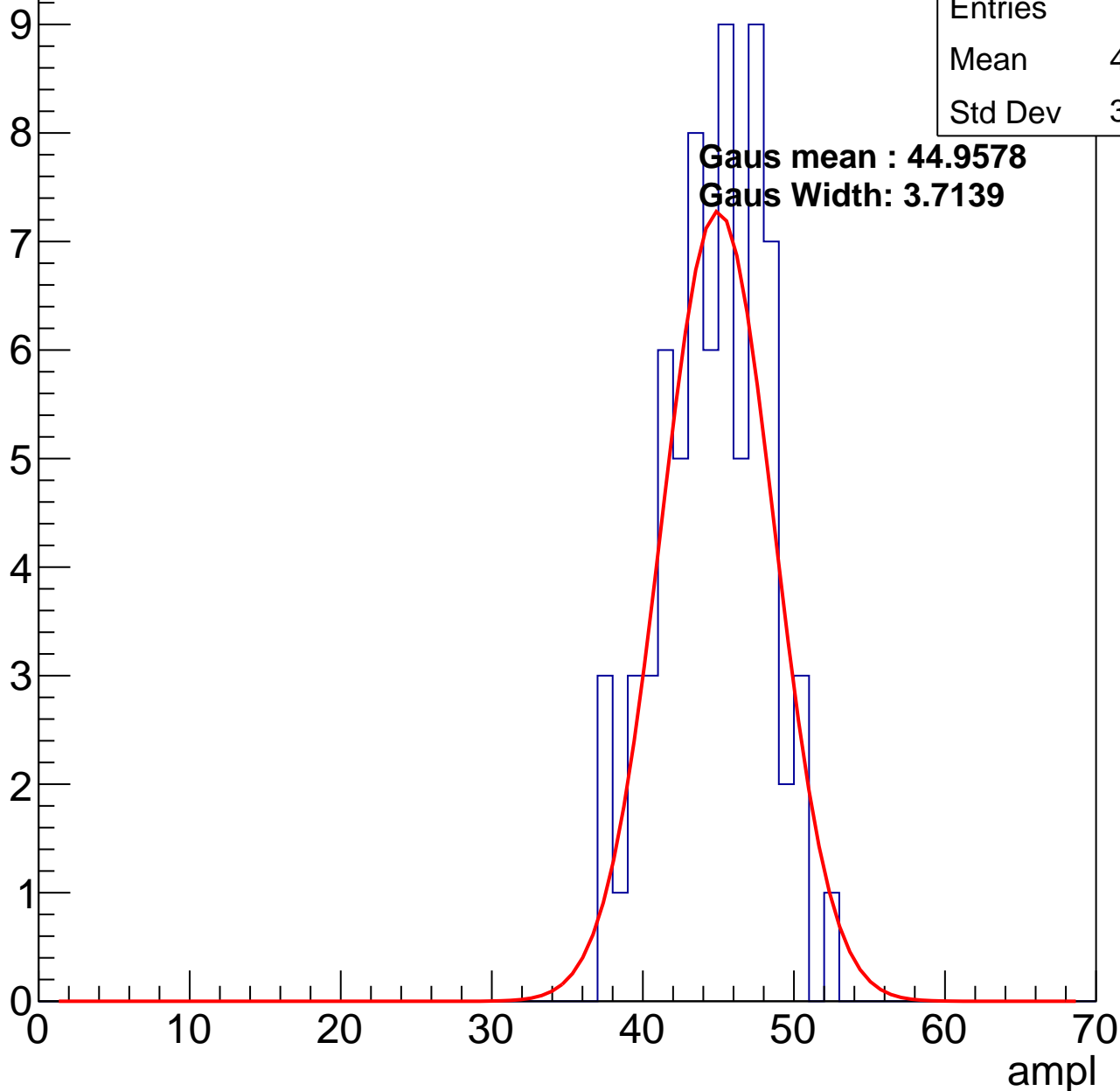
Gaus Width: 4.6090



B1L100S, U13-ch5, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

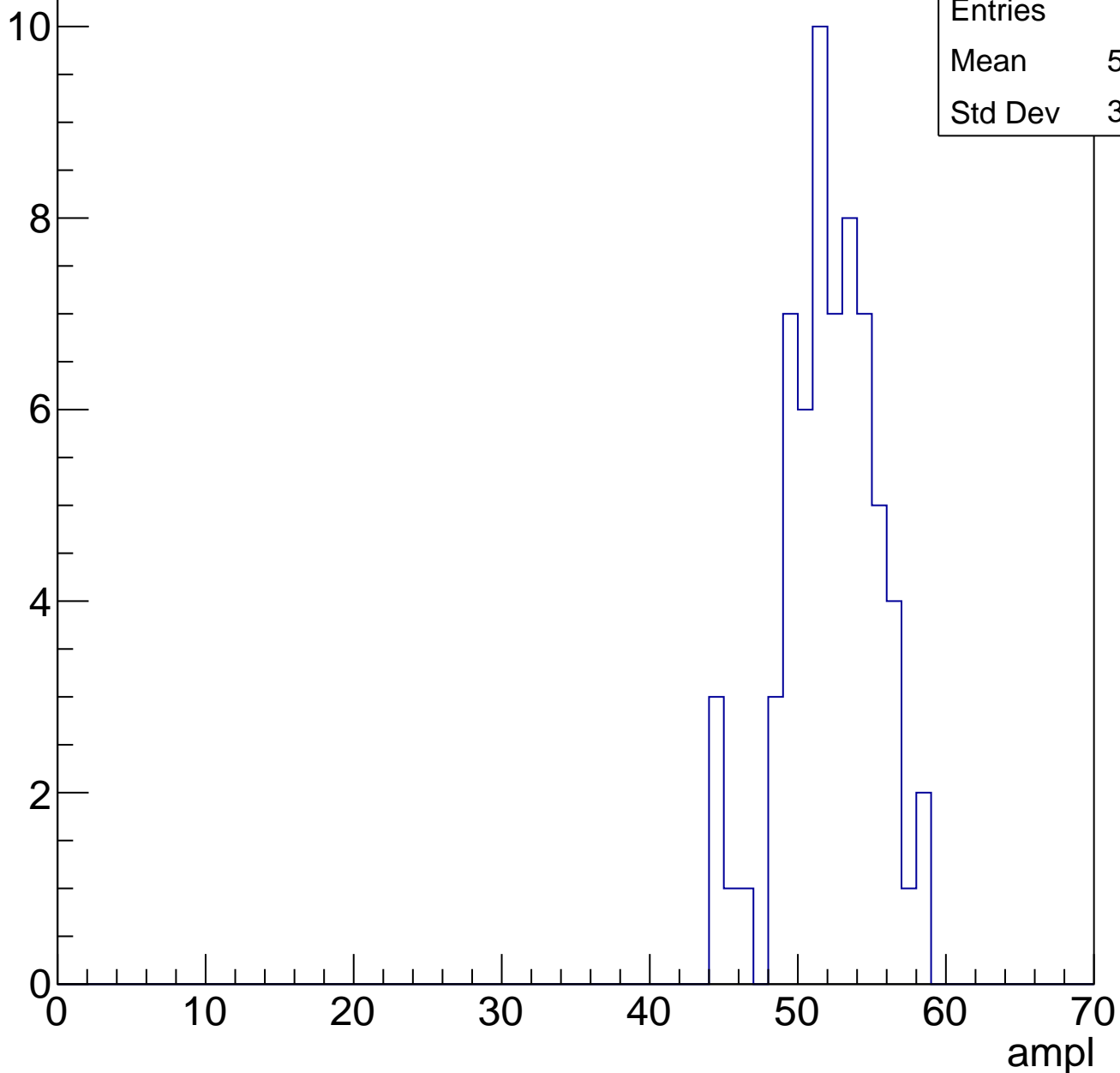


B1L100S, U13-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

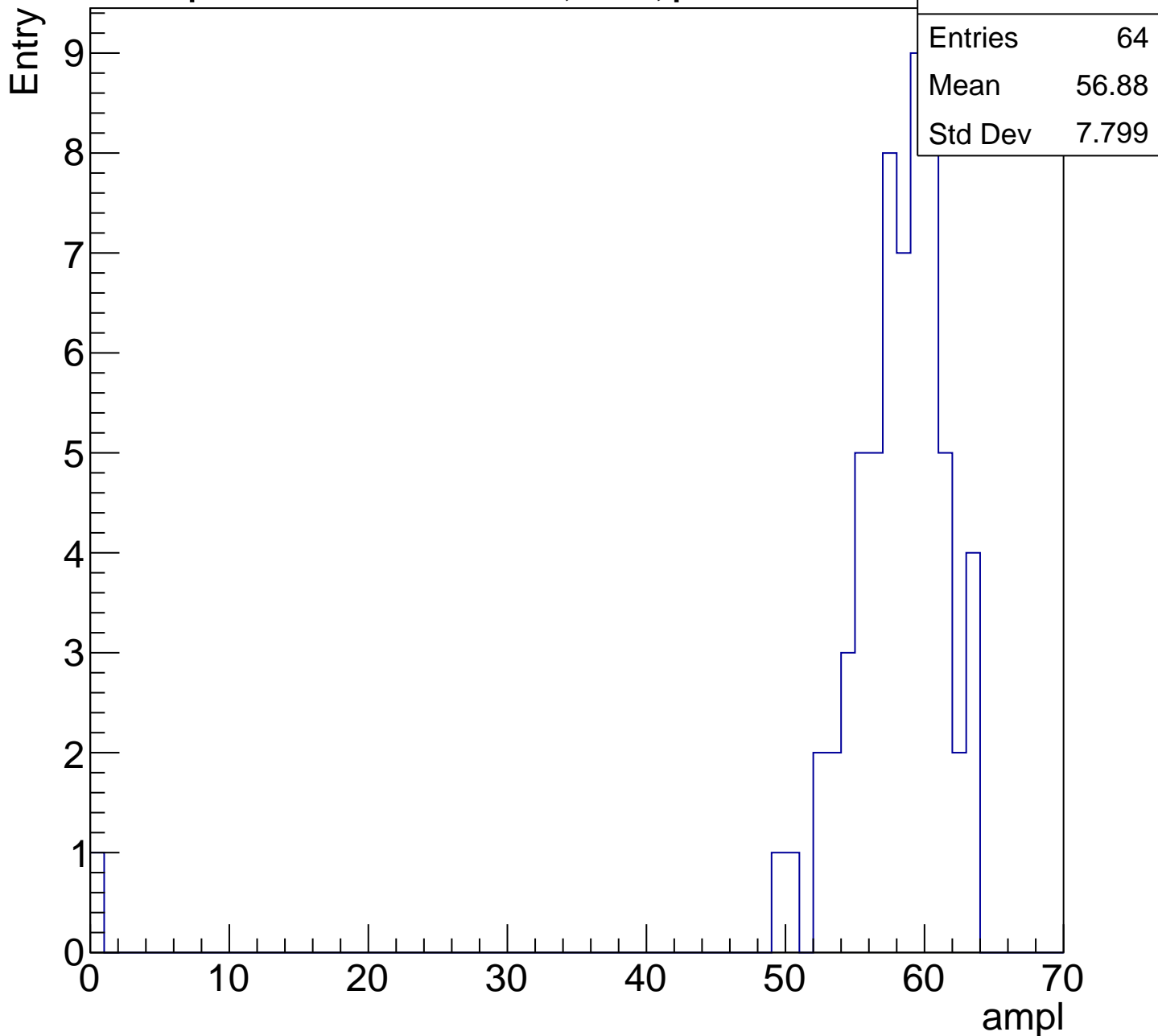
Entry

Entries	65
Mean	51.66
Std Dev	3.178



B1L100S, U13-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

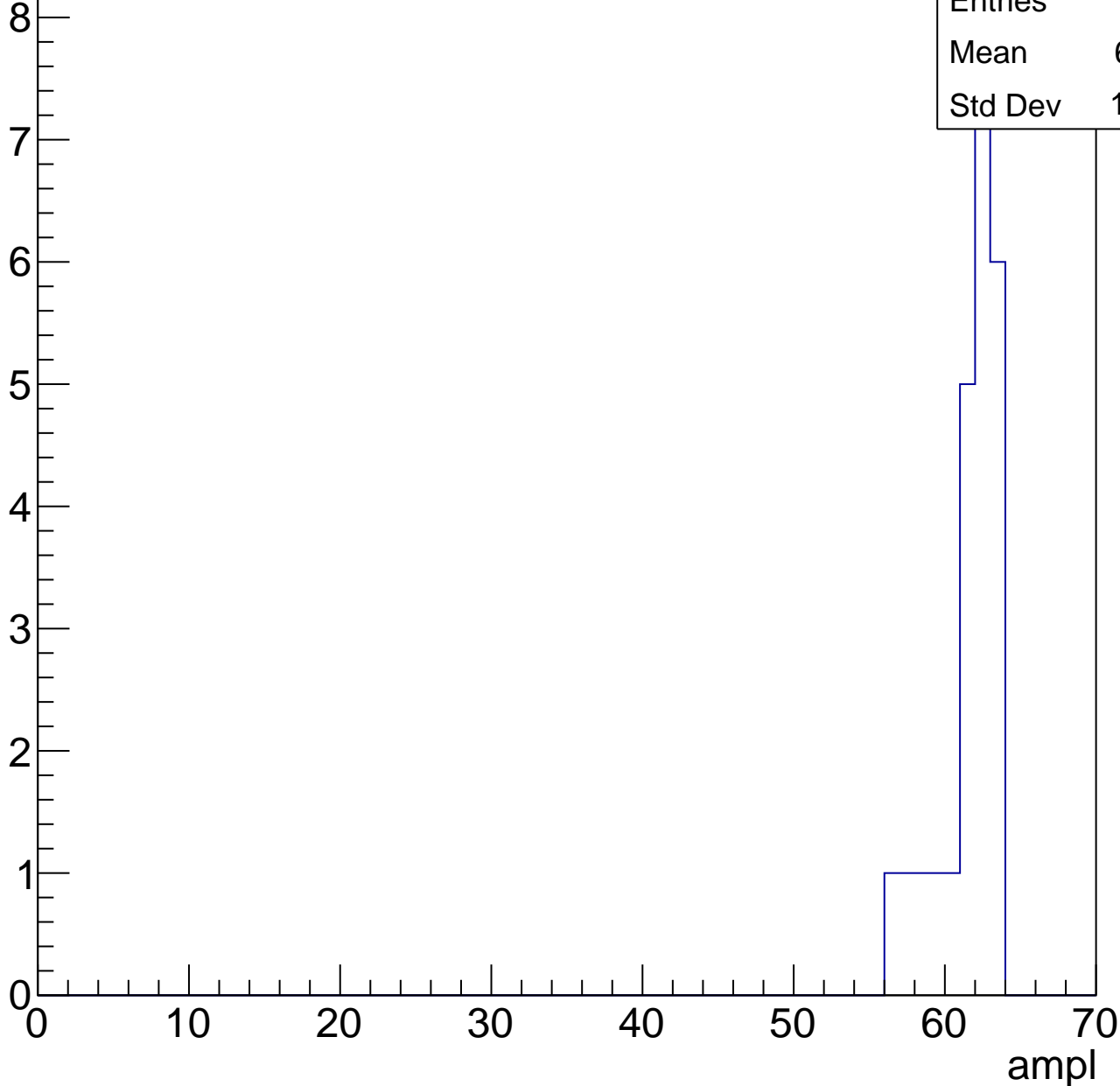


B1L100S, U13-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	61.21
Std Dev	1.893



B1L100S, U13-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

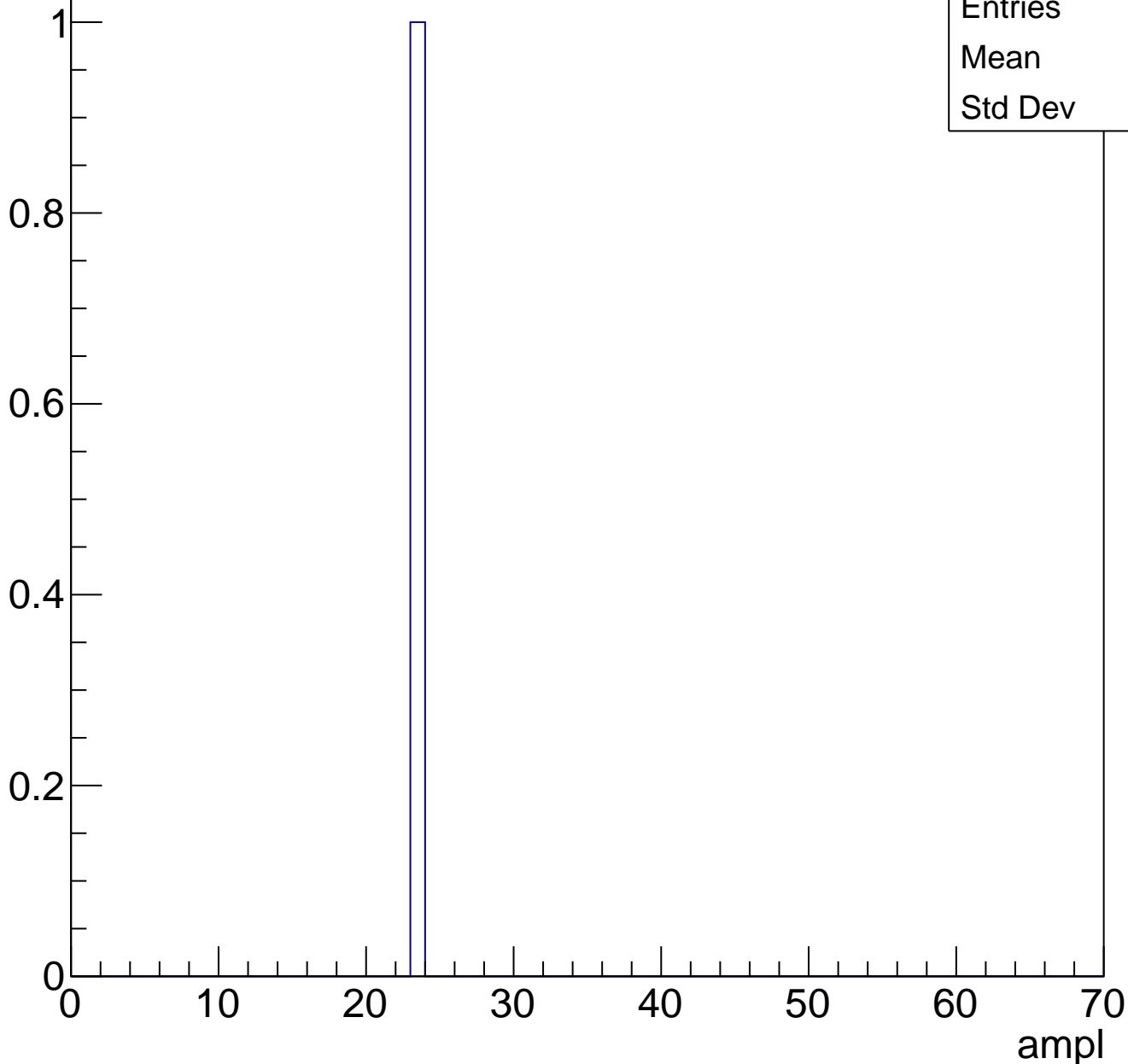


Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U13-ch6, adc0

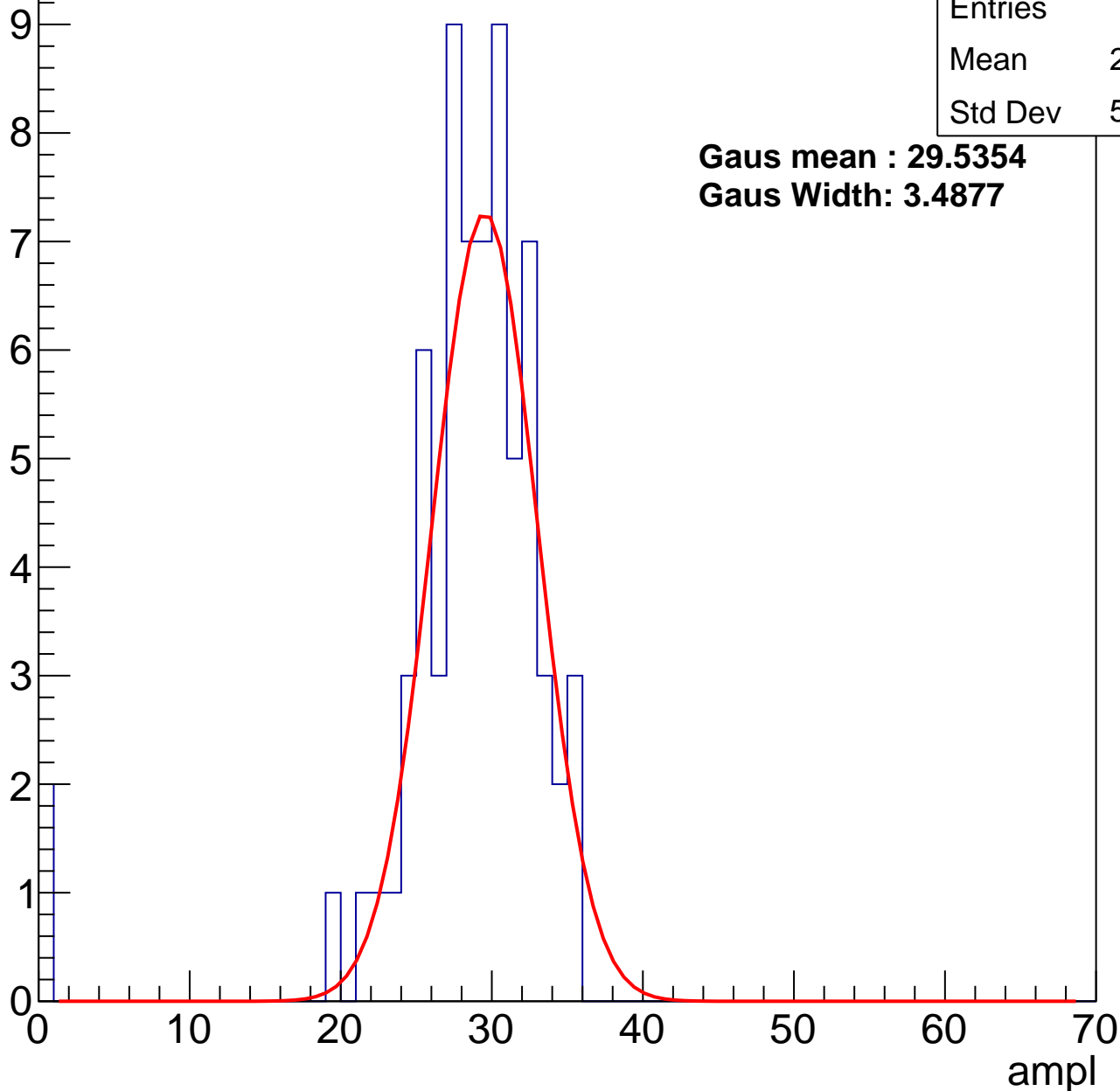
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	27.83
Std Dev	5.824

Gaus mean : 29.5354

Gaus Width: 3.4877



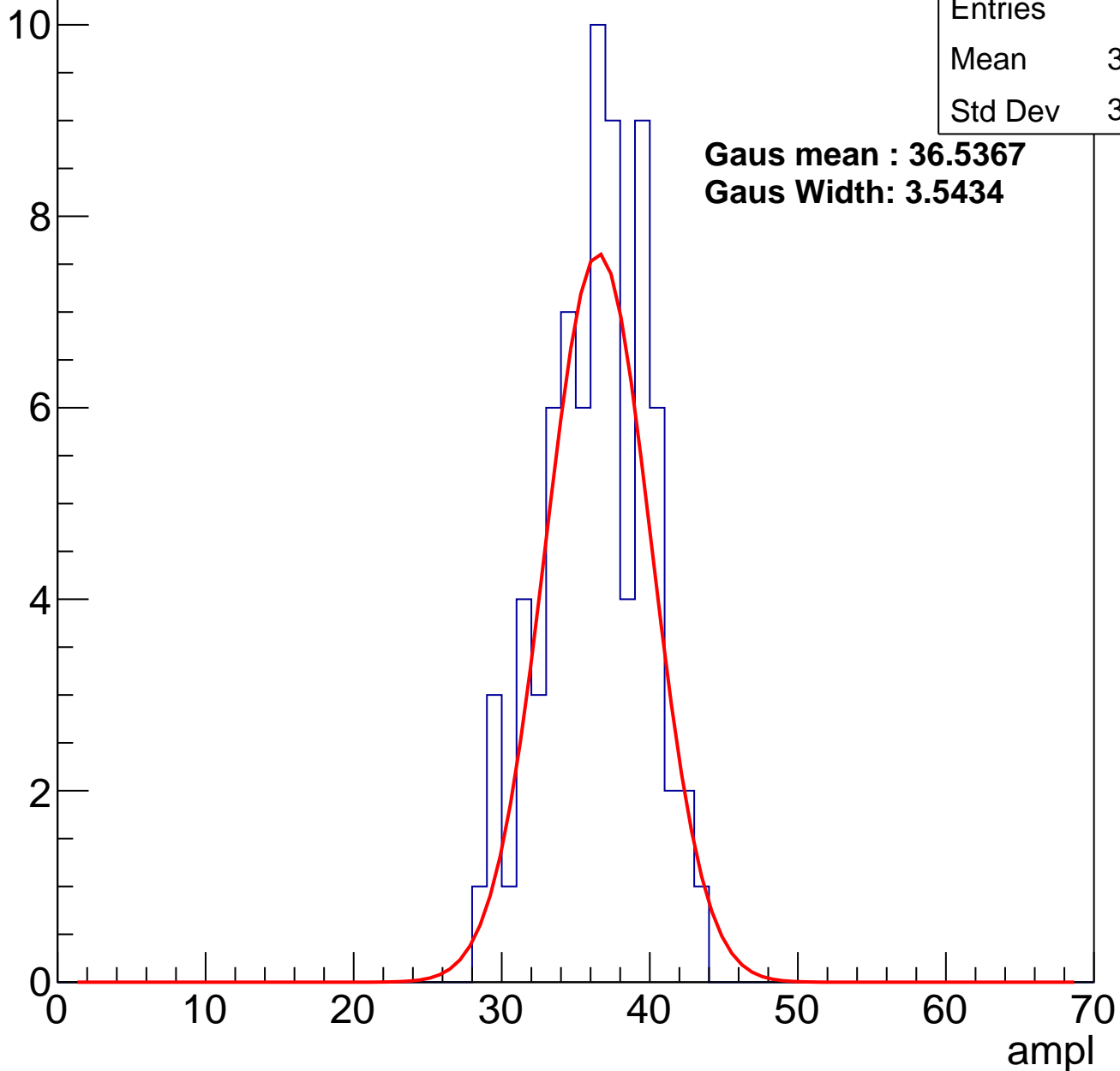
B1L100S, U13-ch6, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	35.89
Std Dev	3.399

Gaus mean : 36.5367
Gaus Width: 3.5434

Entry



B1L100S, U13-ch6, adc2

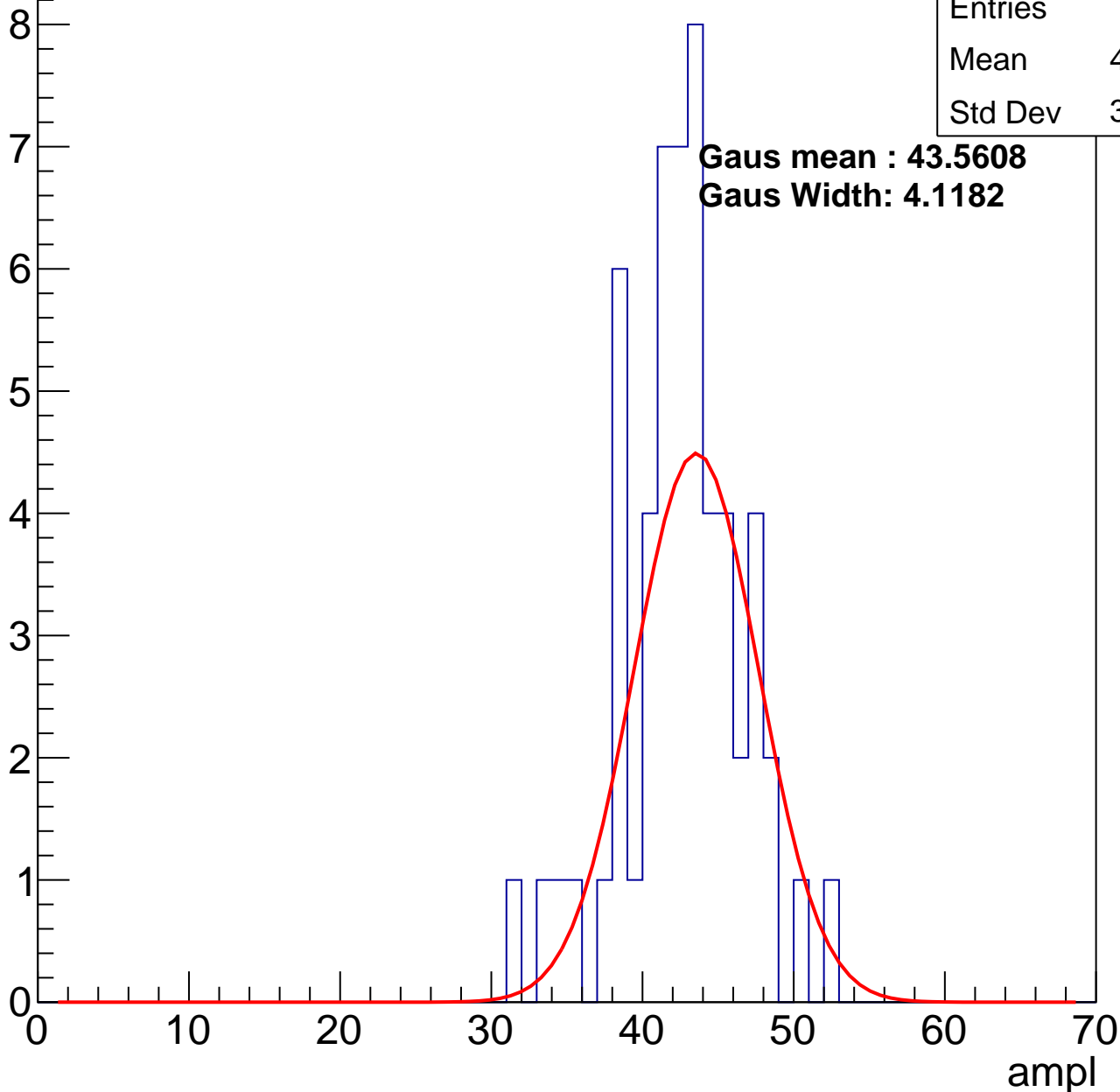
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.07
Std Dev	3.999

Gaus mean : 43.5608

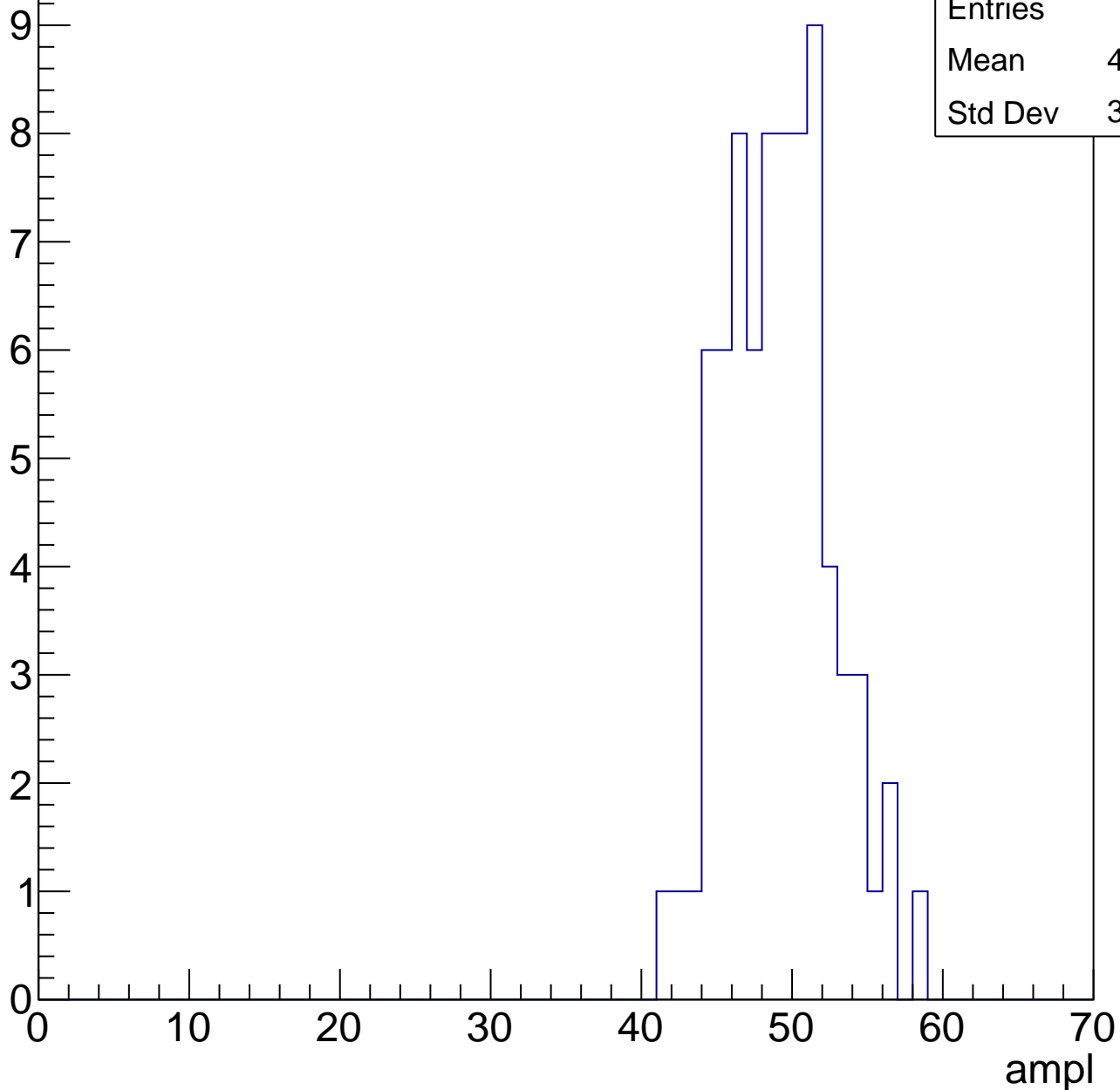
Gaus Width: 4.1182



B1L100S, U13-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

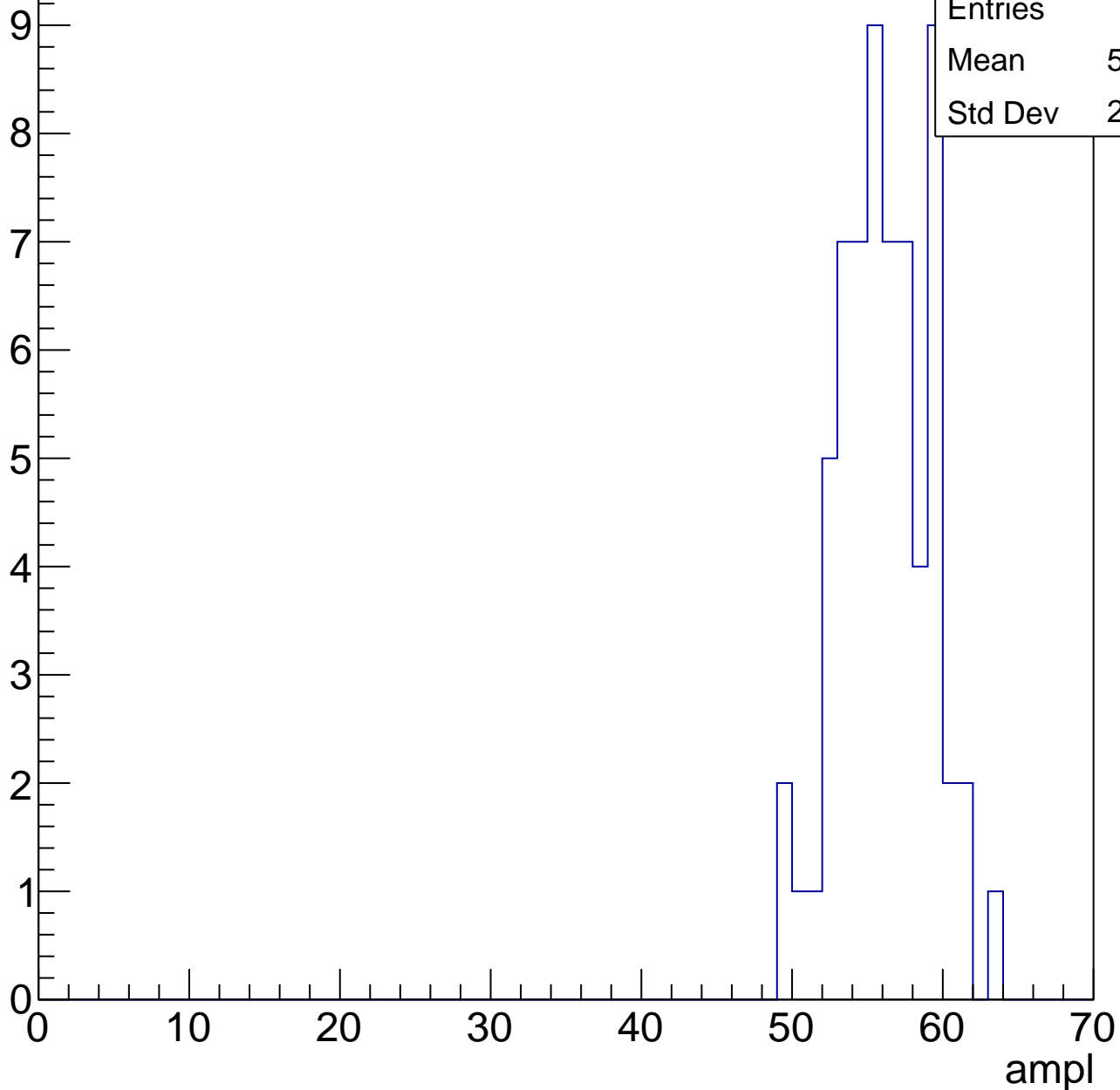
Entry



B1L100S, U13-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

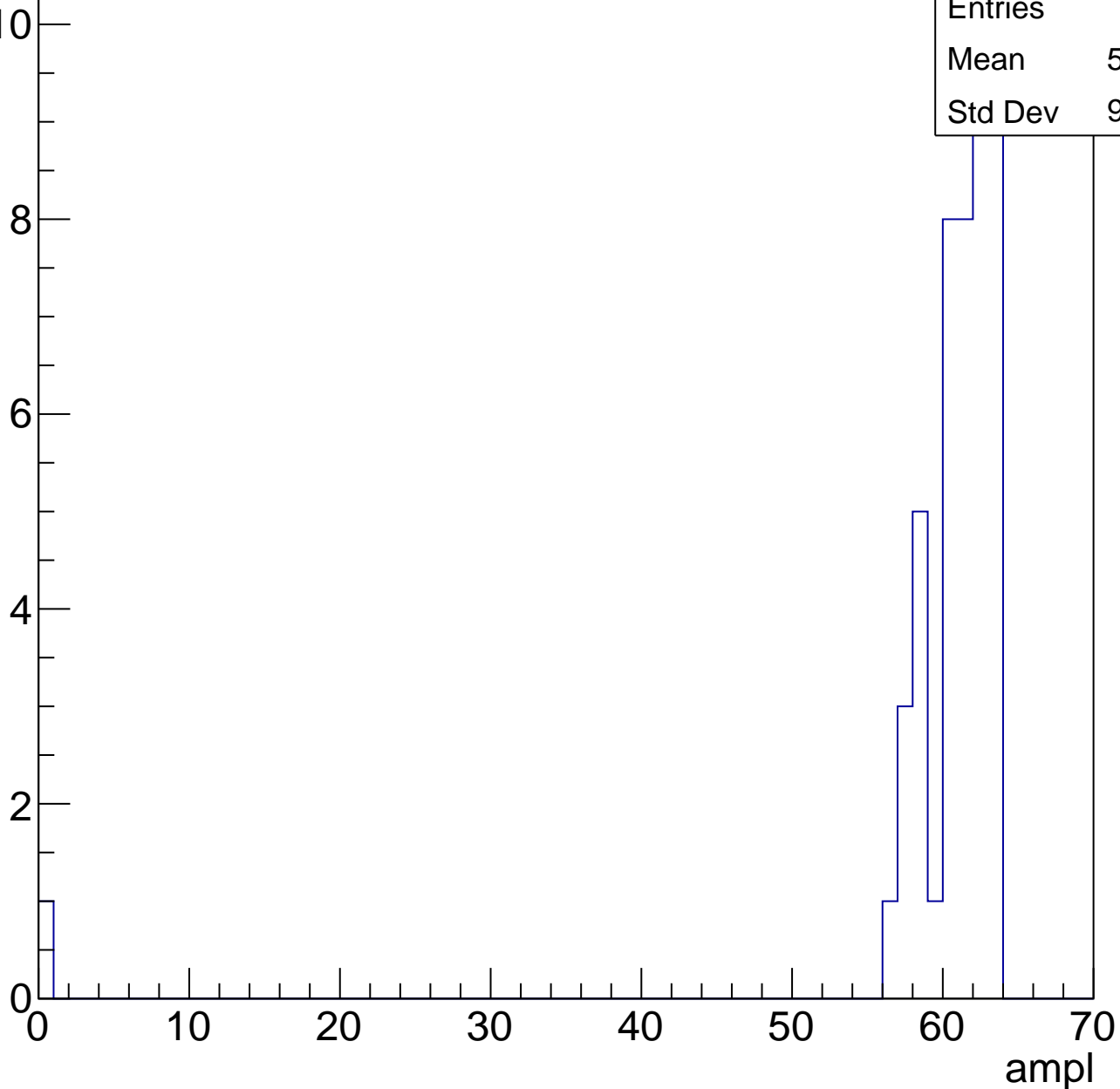


B1L100S, U13-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.37
Std Dev	9.059



B1L100S, U13-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

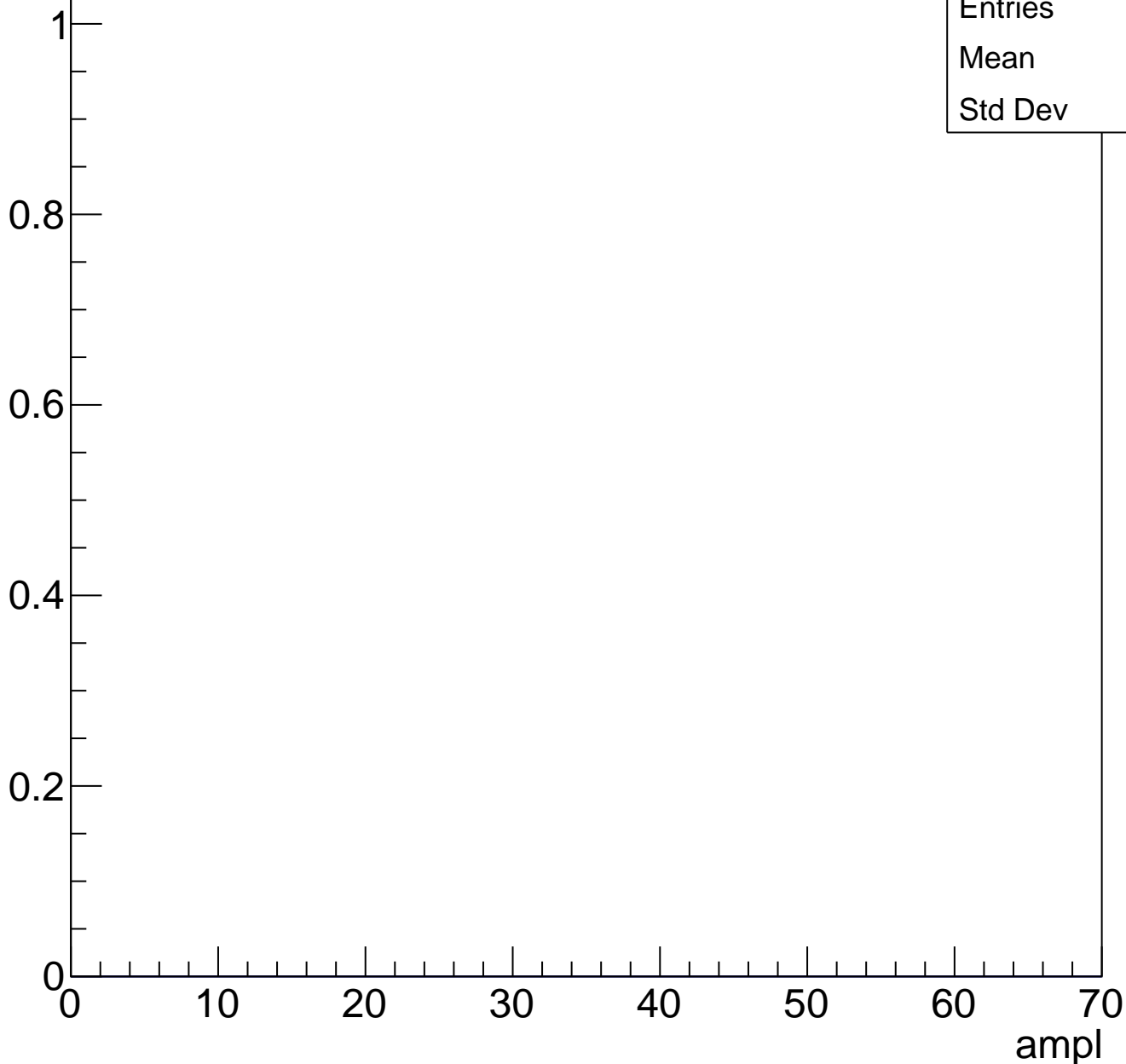


Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch7, adc0

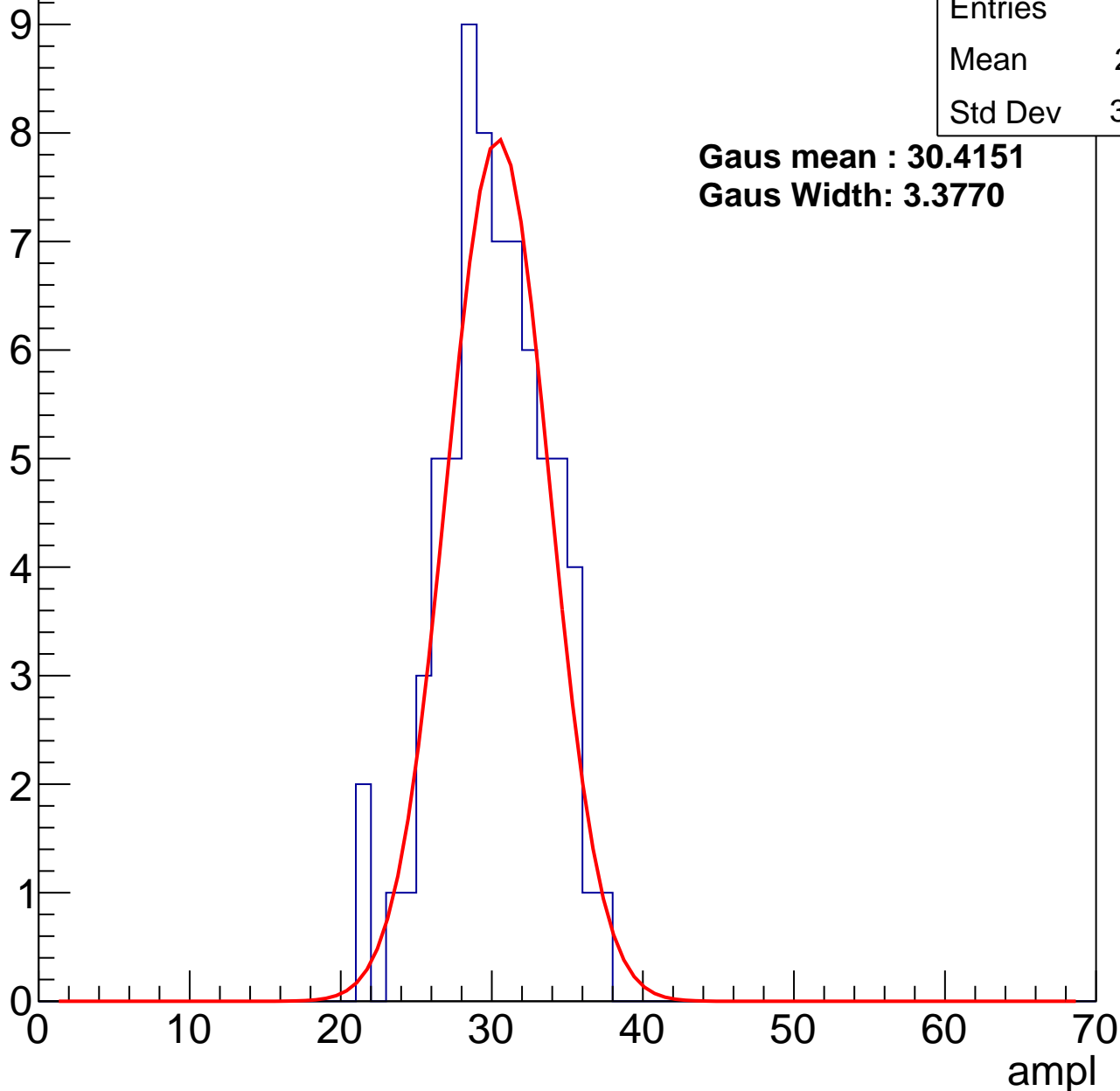
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.71
Std Dev	3.436

Gaus mean : 30.4151

Gaus Width: 3.3770



B1L100S, U13-ch7, adc1

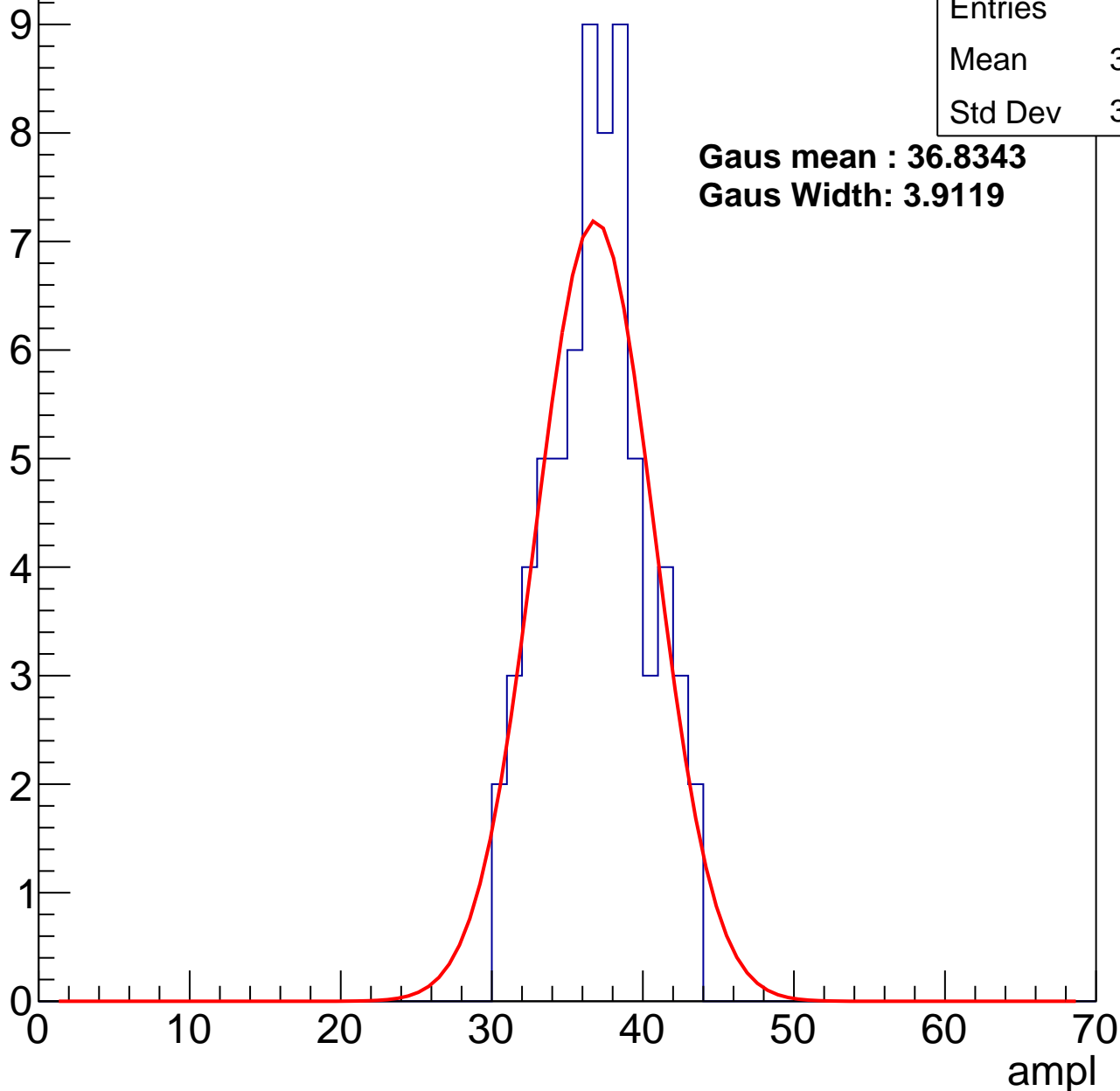
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.46
Std Dev	3.233

Gaus mean : 36.8343

Gaus Width: 3.9119



B1L100S, U13-ch7, adc2

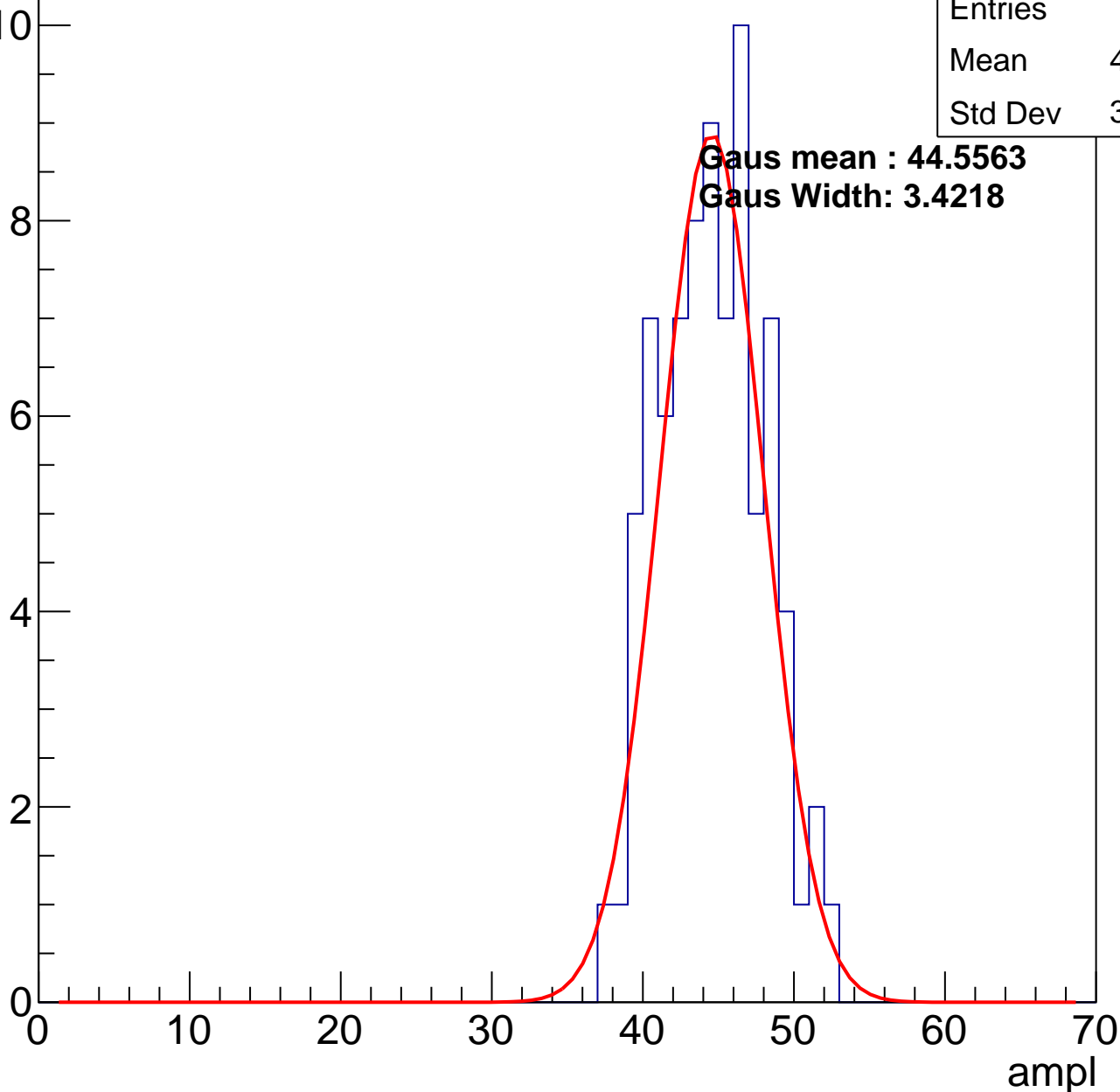
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	44.15
Std Dev	3.356

Gaus mean : 44.5563

Gaus Width: 3.4218



B1L100S, U13-ch7, adc3

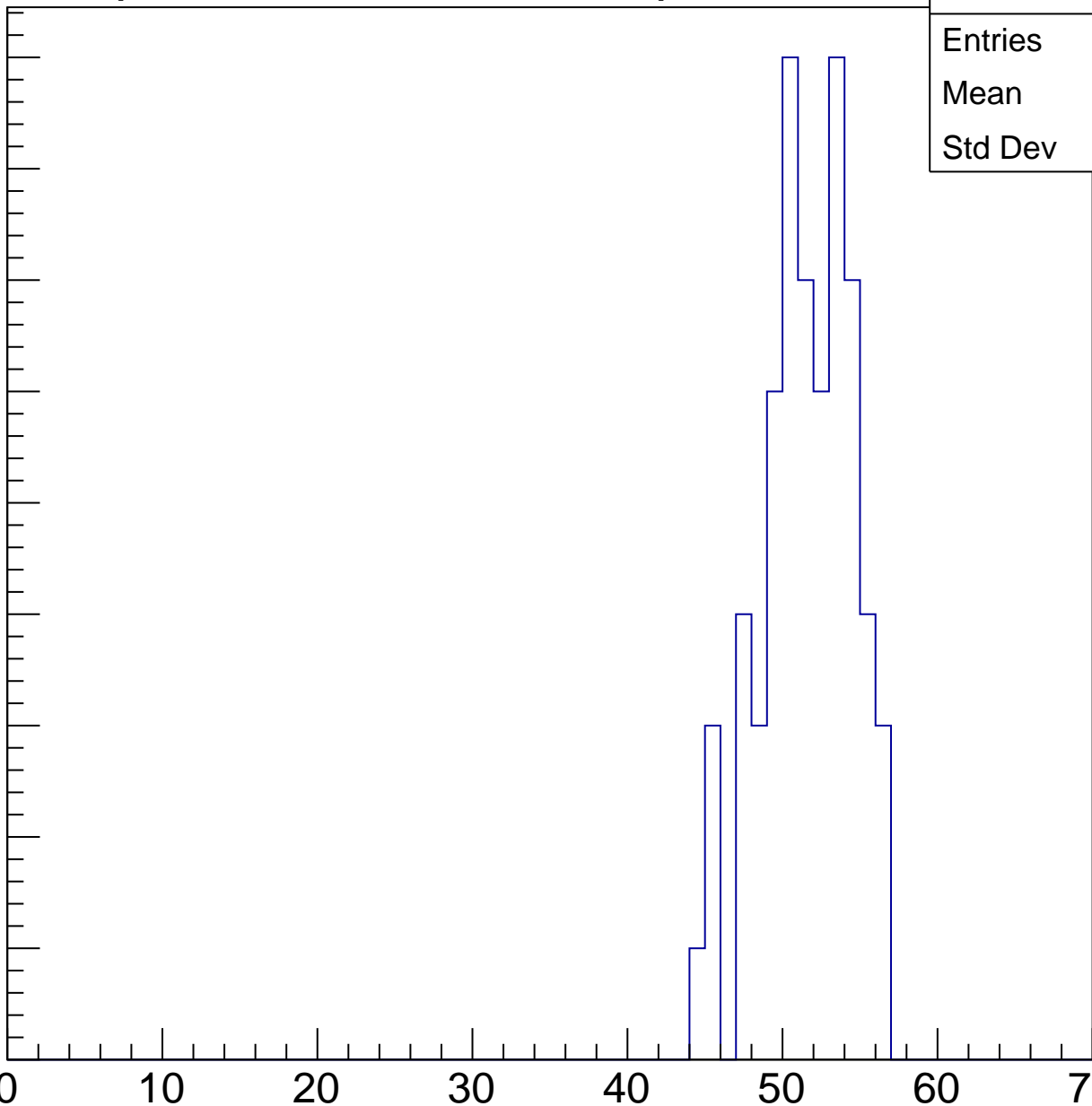
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

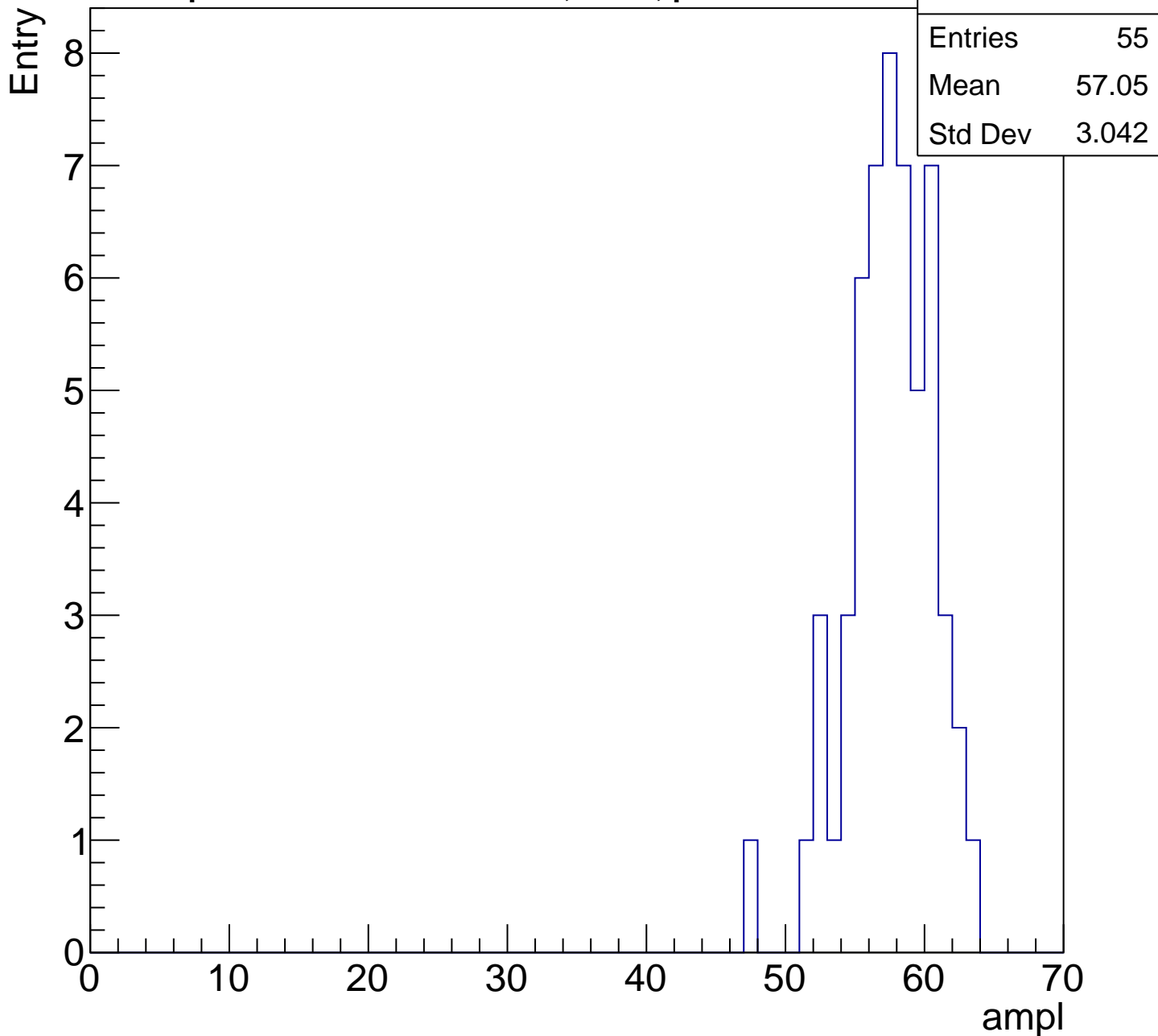
Entries	62
Mean	51.08
Std Dev	2.909

ampl



B1L100S, U13-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

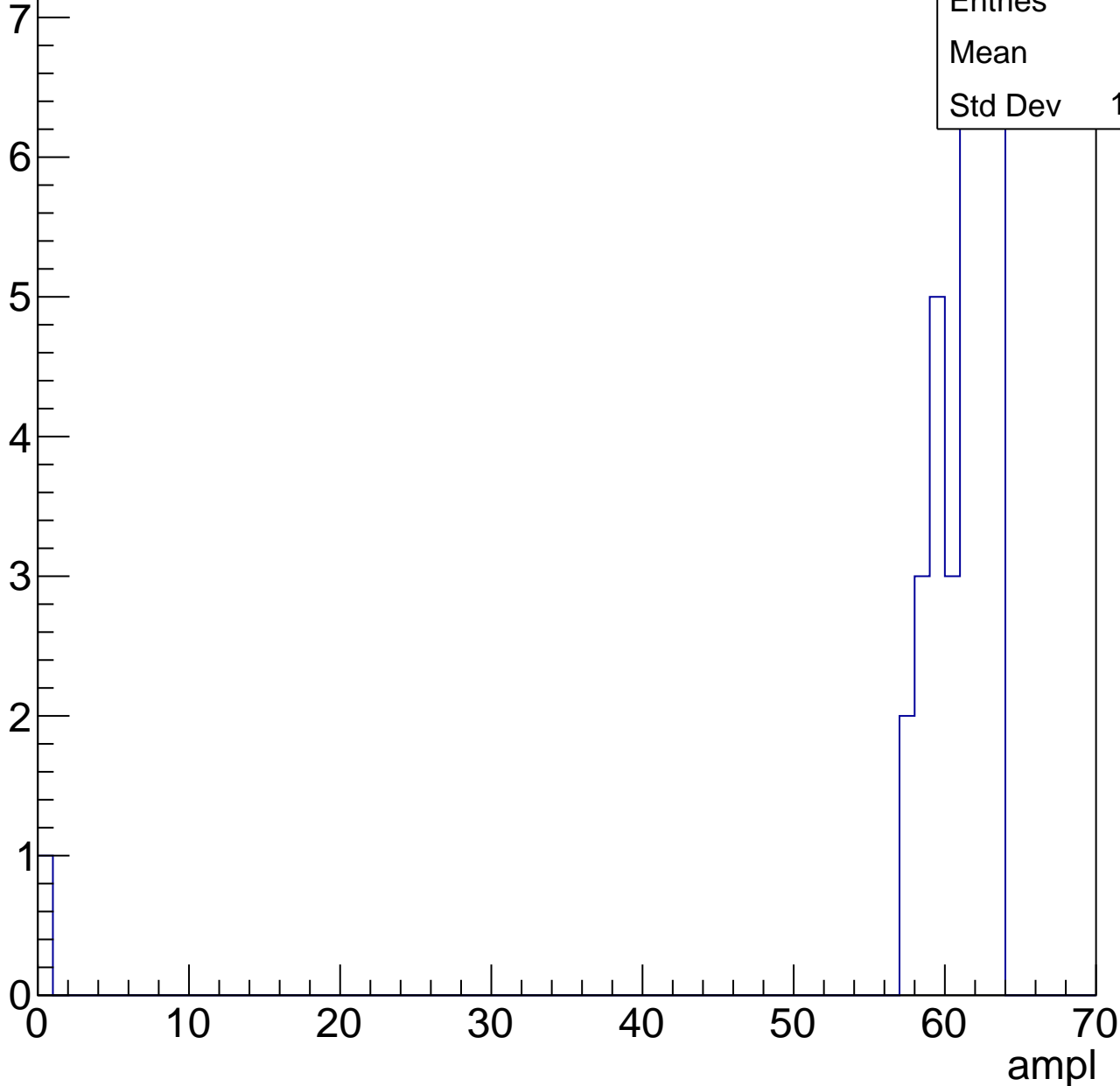


B1L100S, U13-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	59
Std Dev	10.28



B1L100S, U13-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U13-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch8, adc0

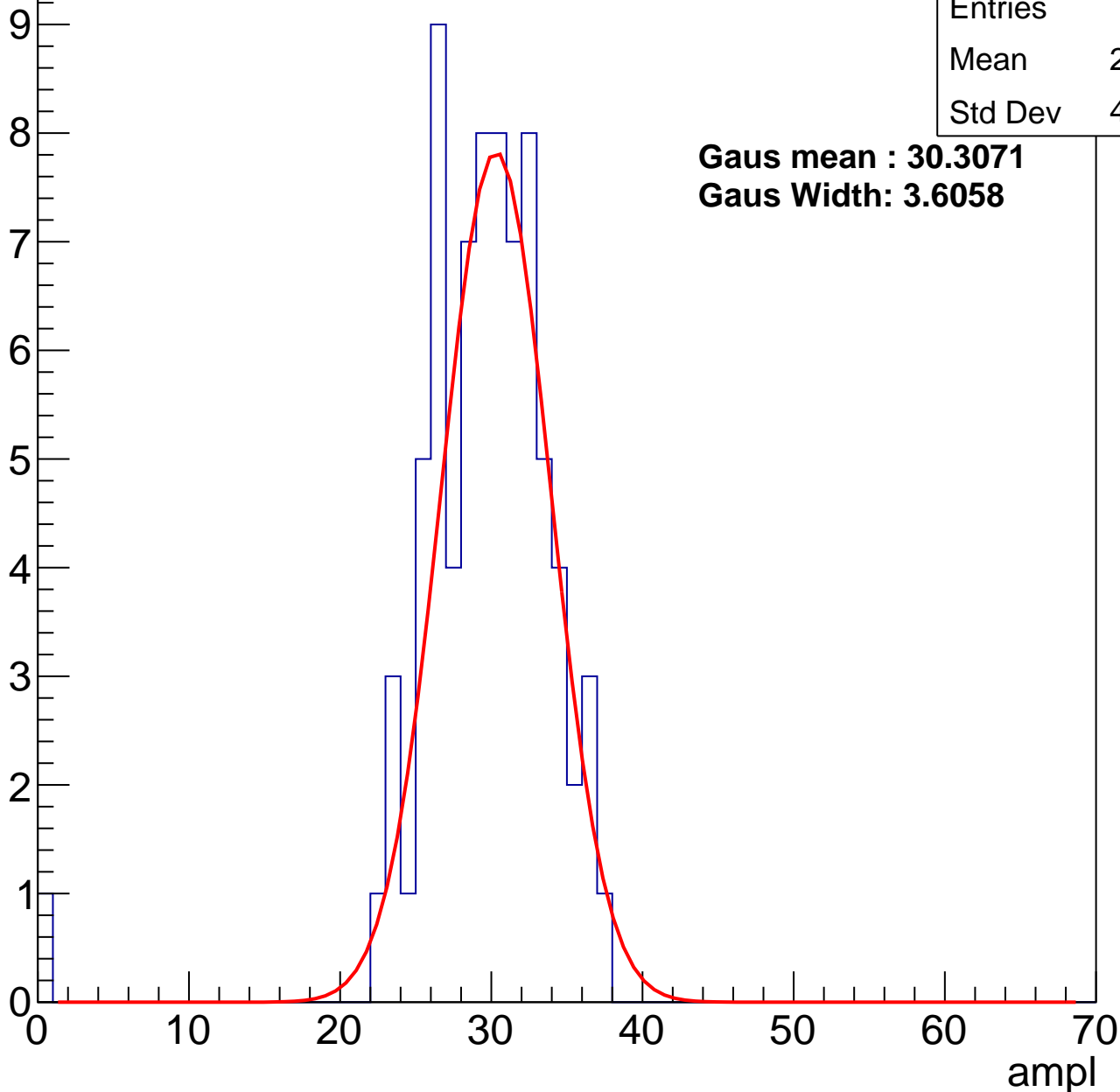
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.08
Std Dev	4.805

Gaus mean : 30.3071

Gaus Width: 3.6058



B1L100S, U13-ch8, adc1

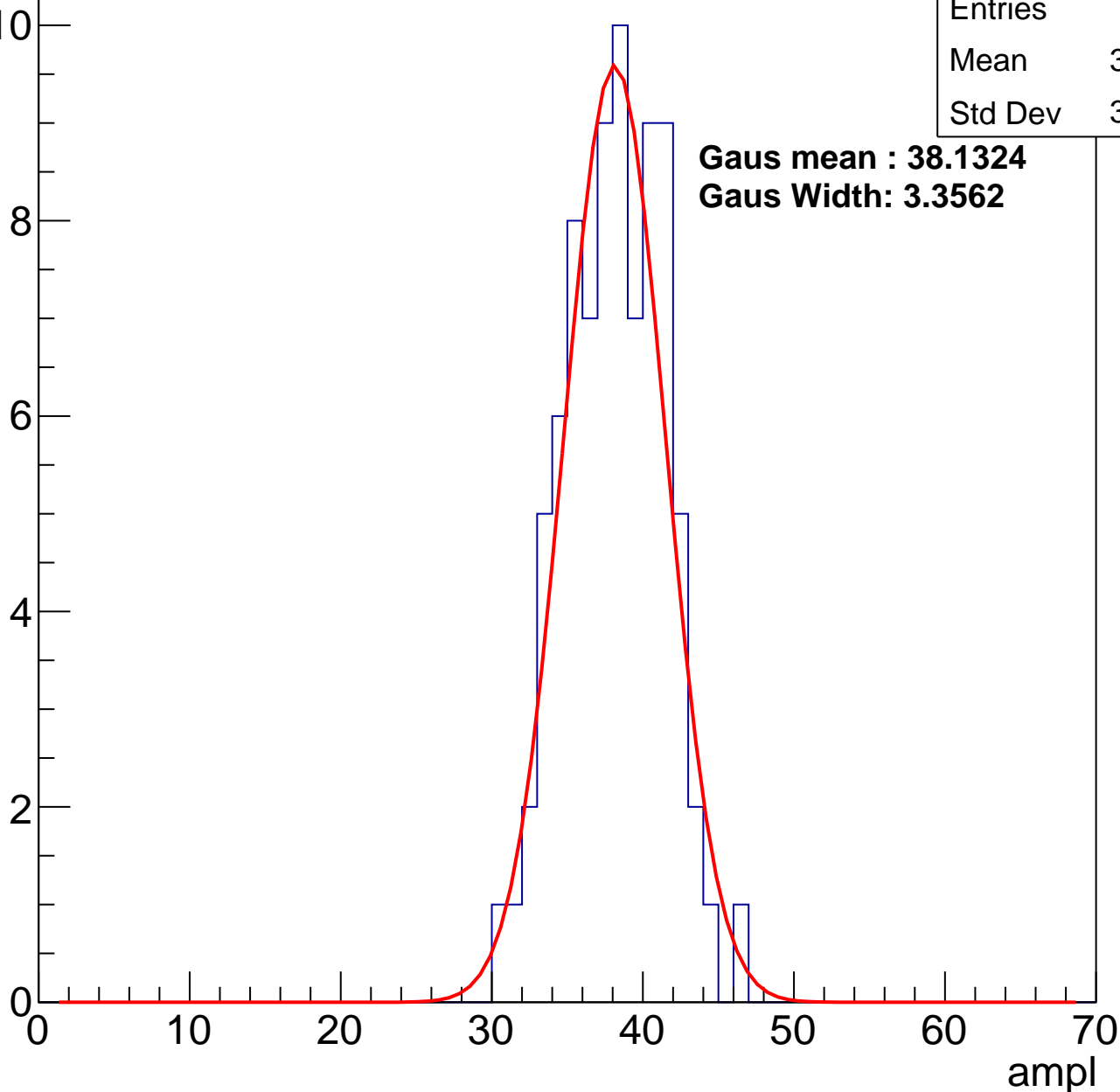
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	37.67
Std Dev	3.219

Gaus mean : 38.1324

Gaus Width: 3.3562



B1L100S, U13-ch8, adc2

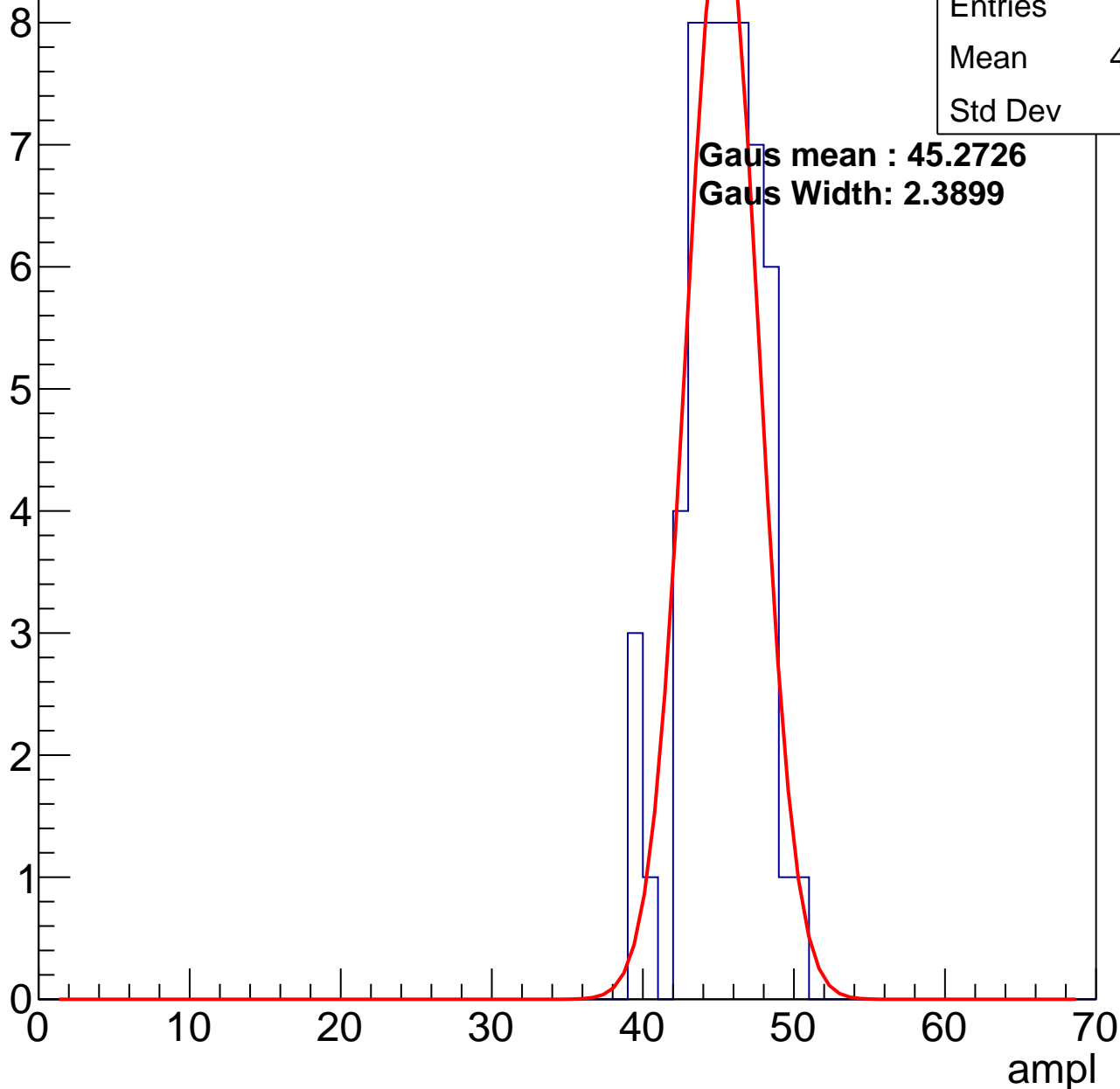
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	44.82
Std Dev	2.48

Gaus mean : 45.2726

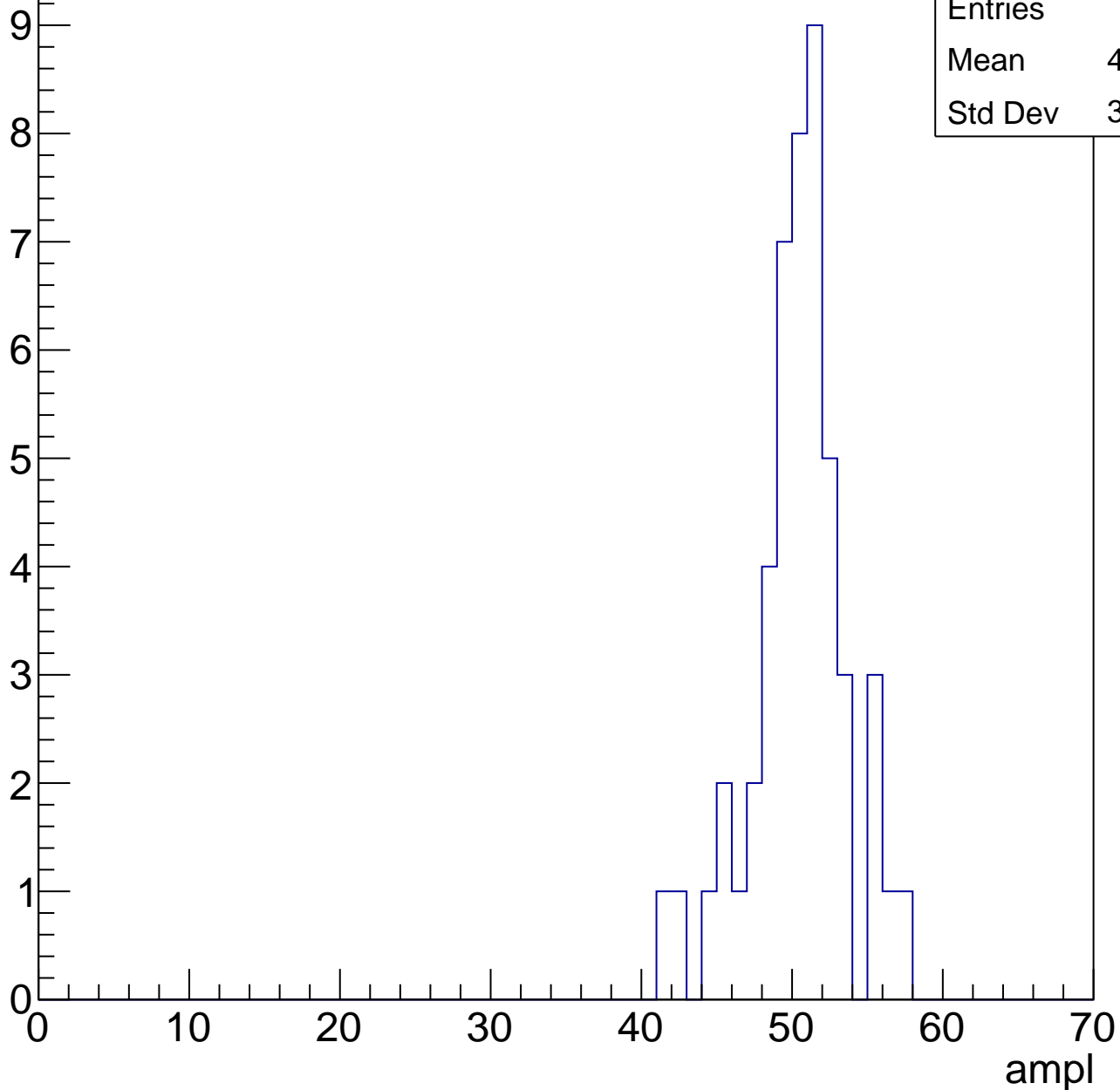
Gaus Width: 2.3899



B1L100S, U13-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

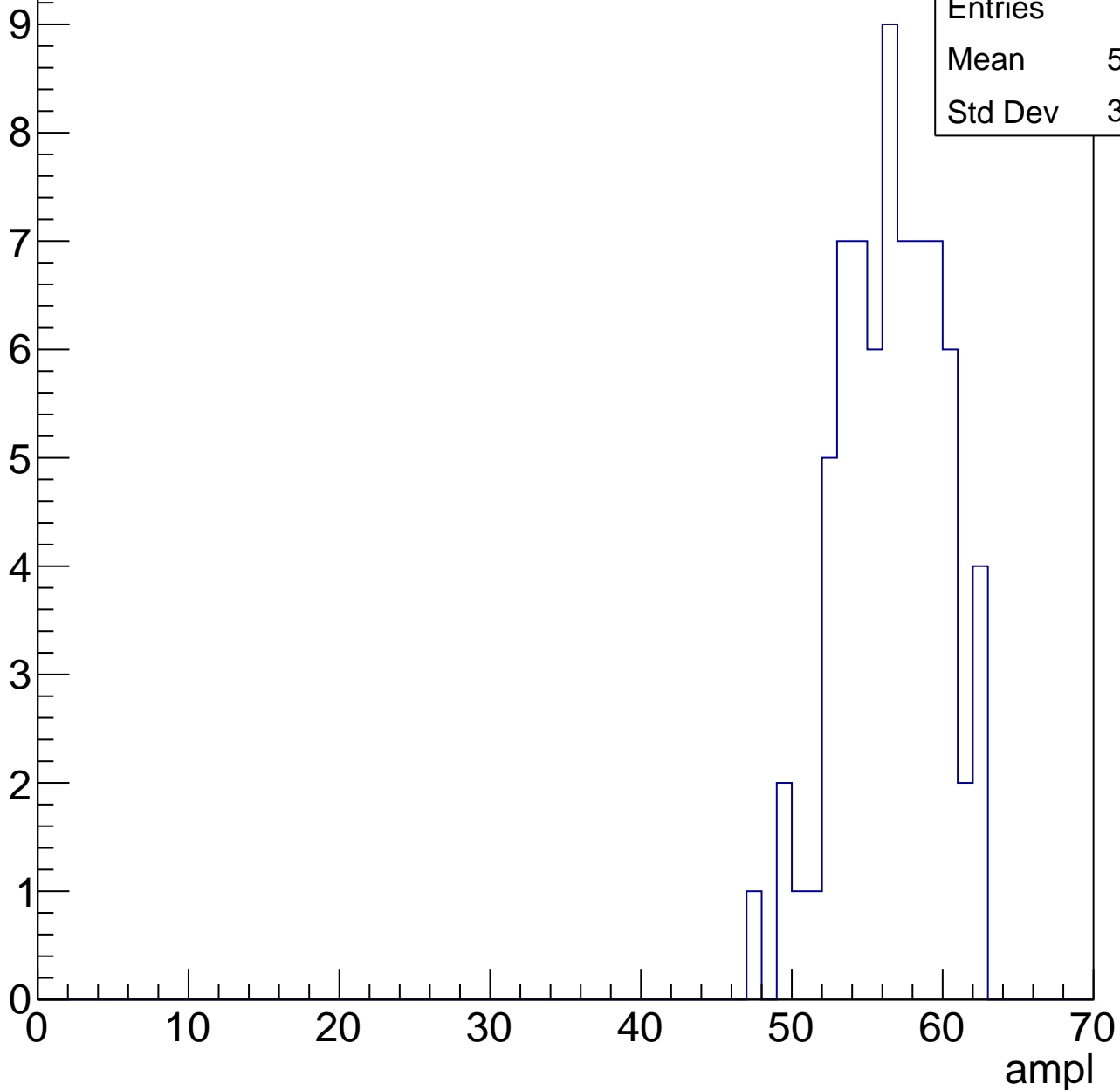
Entry



B1L100S, U13-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



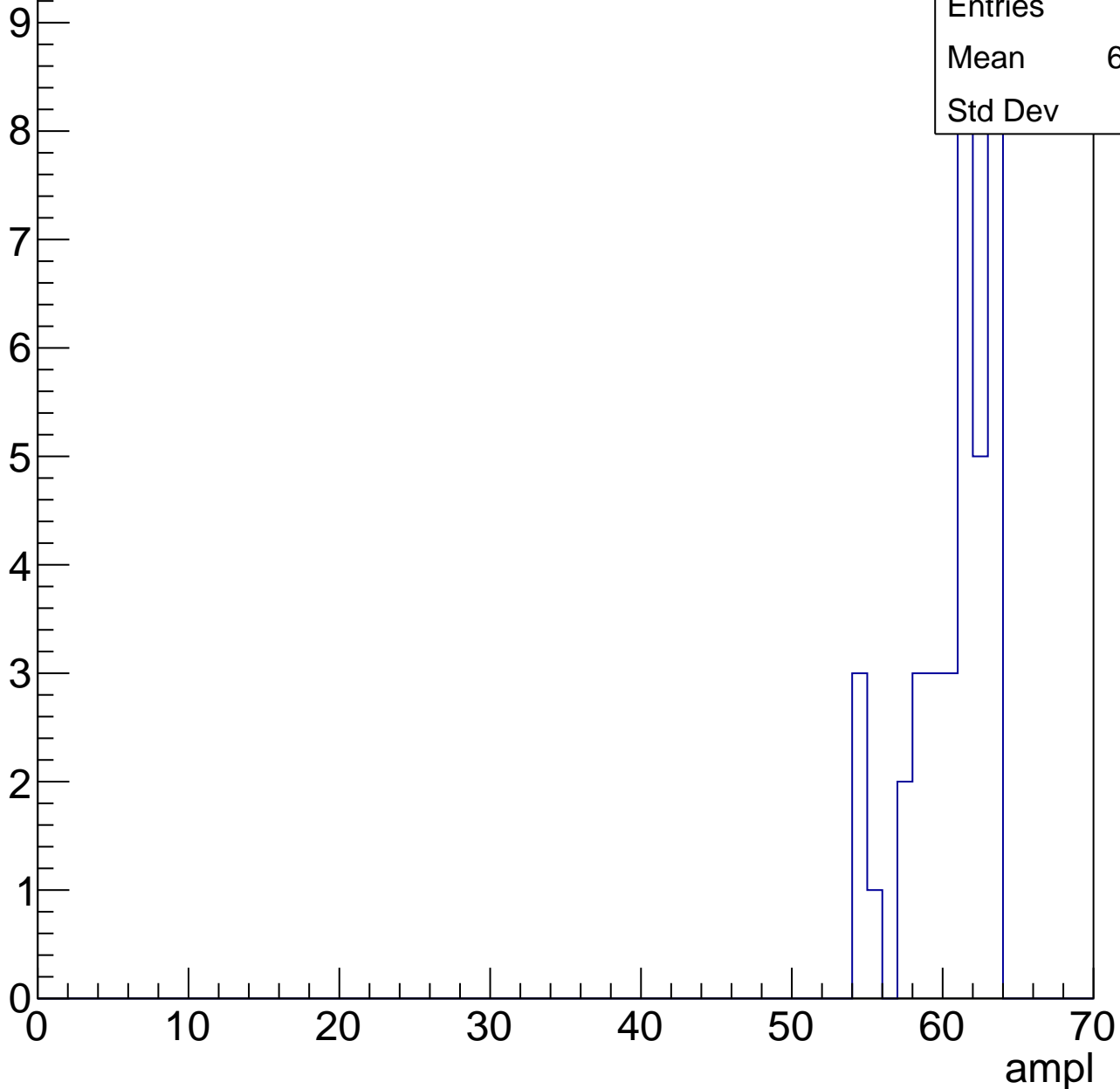
Entries	72
Mean	56.07
Std Dev	3.343

B1L100S, U13-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

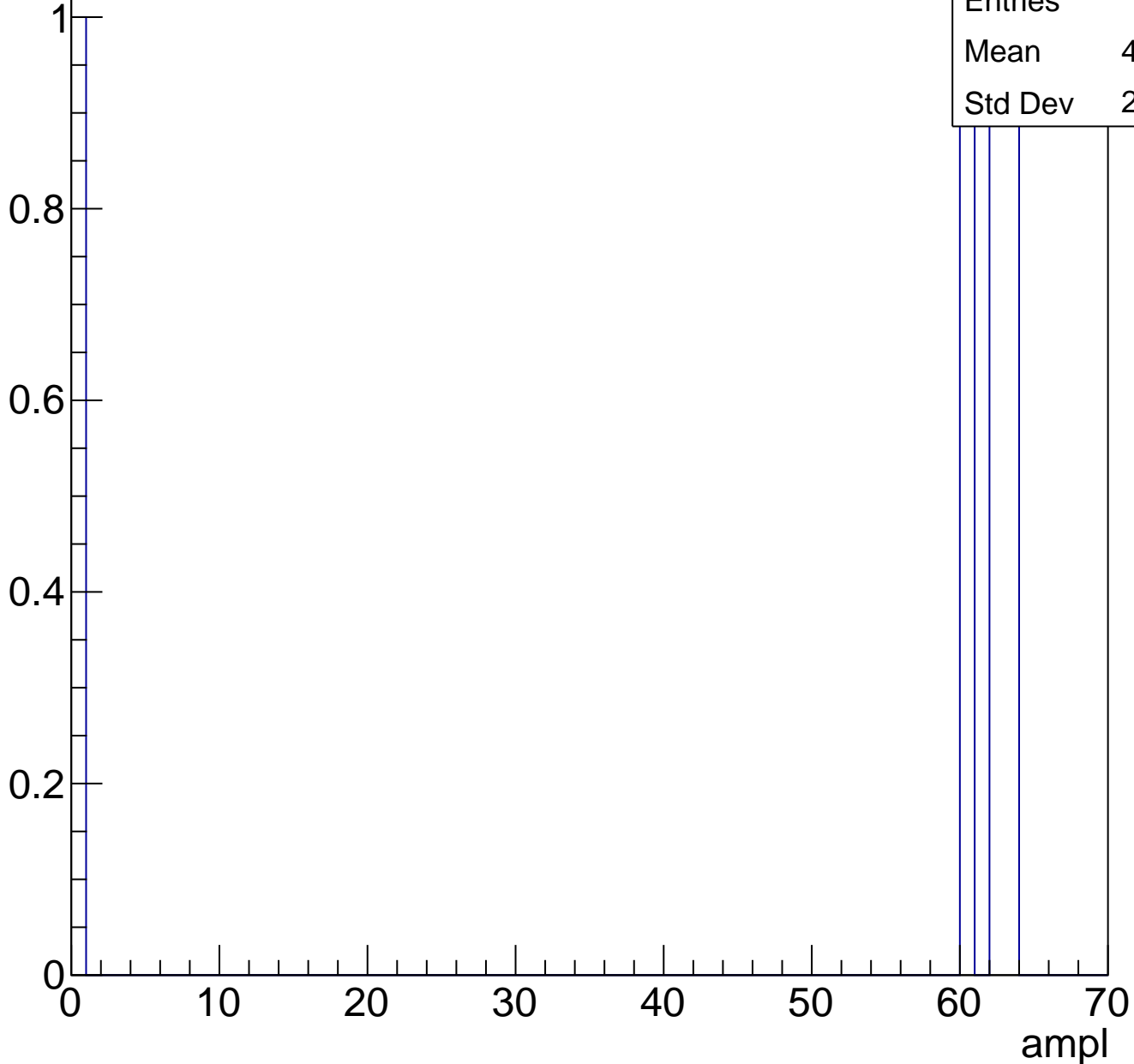
Entries	37
Mean	60.19
Std Dev	2.72



B1L100S, U13-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch9, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	31.21
Std Dev	3.642

Gaus mean : 31.6608

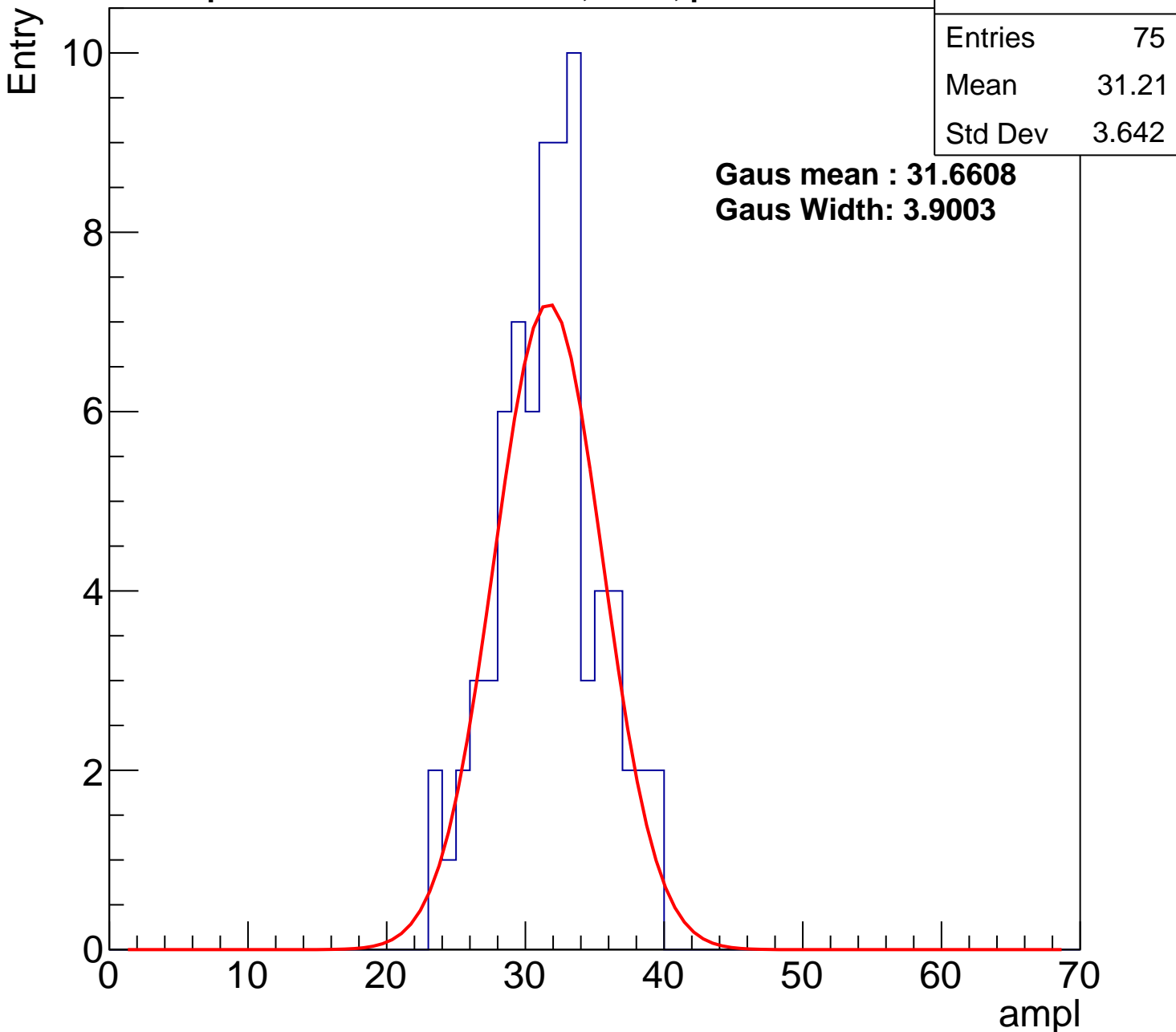
Gaus Width: 3.9003

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch9, adc1

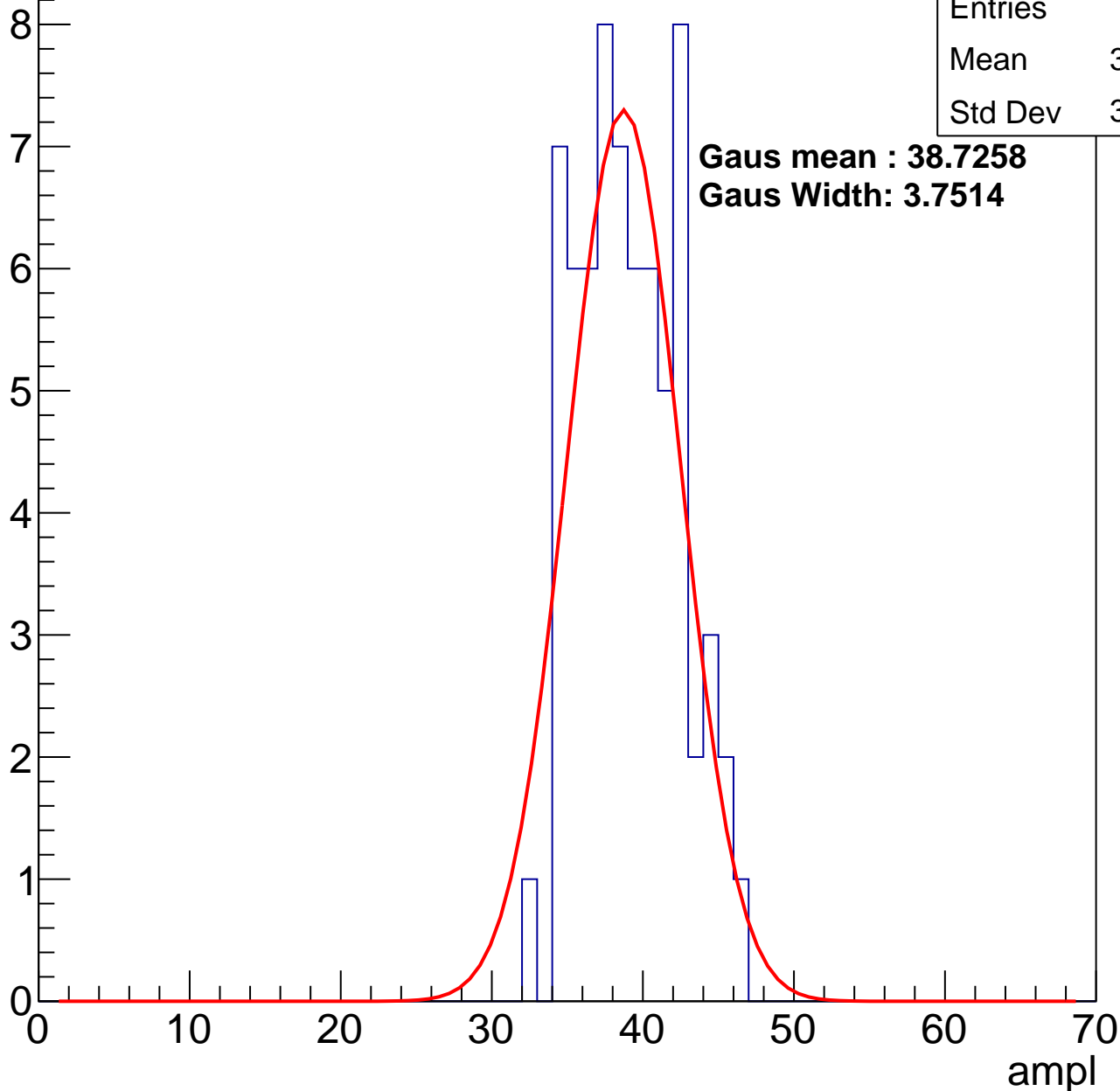
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.63
Std Dev	3.276

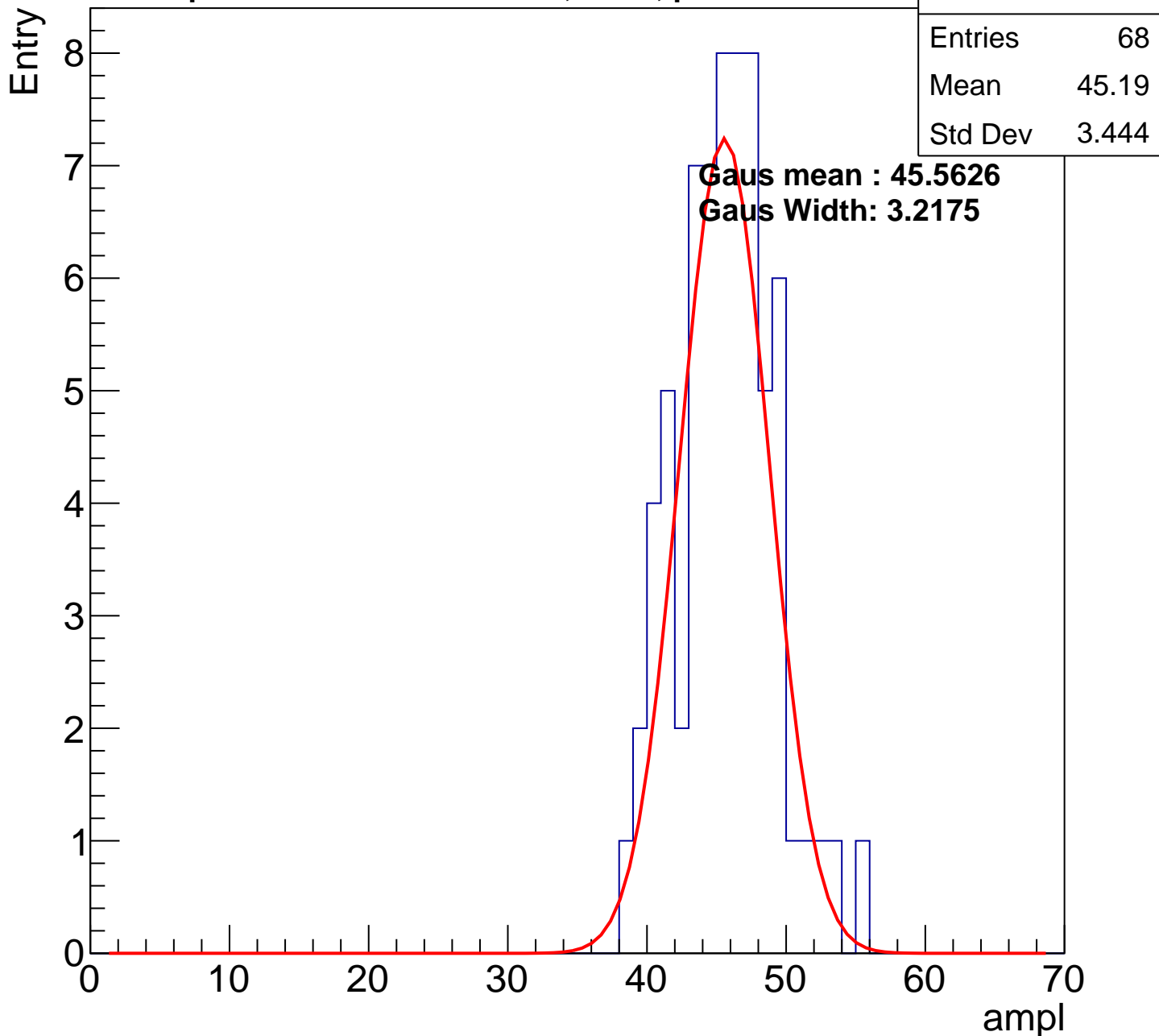
Gaus mean : 38.7258

Gaus Width: 3.7514



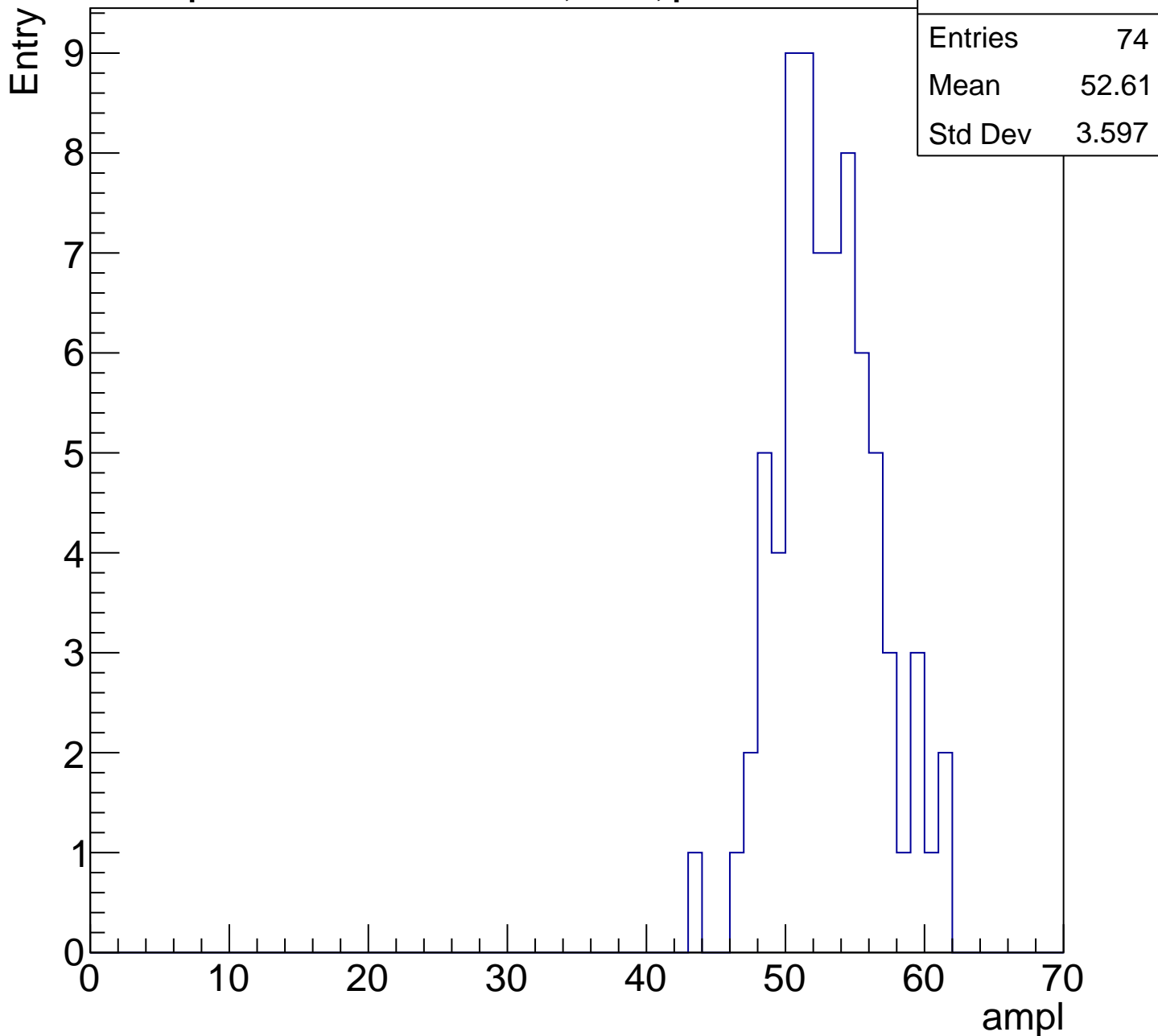
B1L100S, U13-ch9, adc2

calib_packv5_042523_0143.root, FC#4, port A2



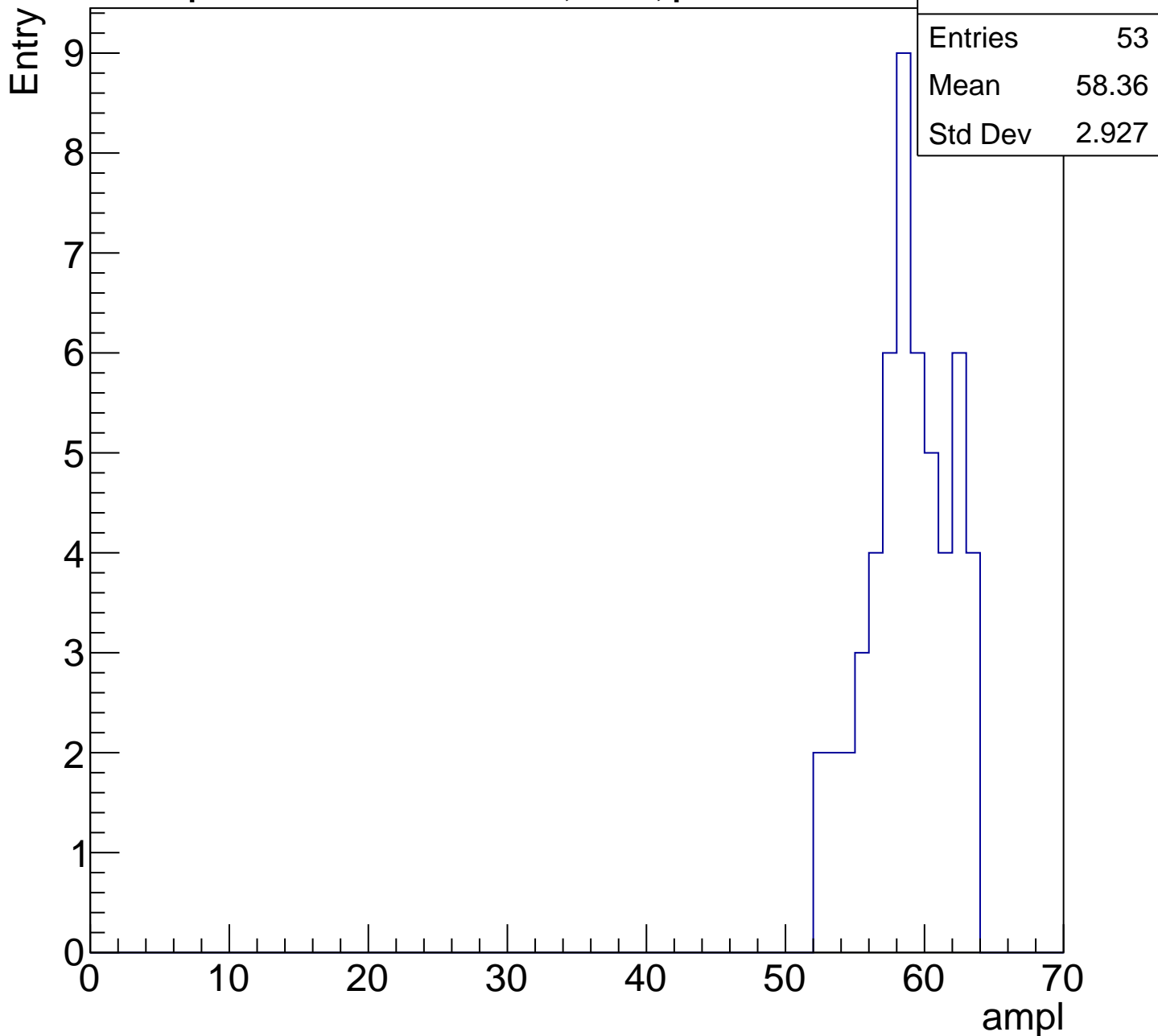
B1L100S, U13-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

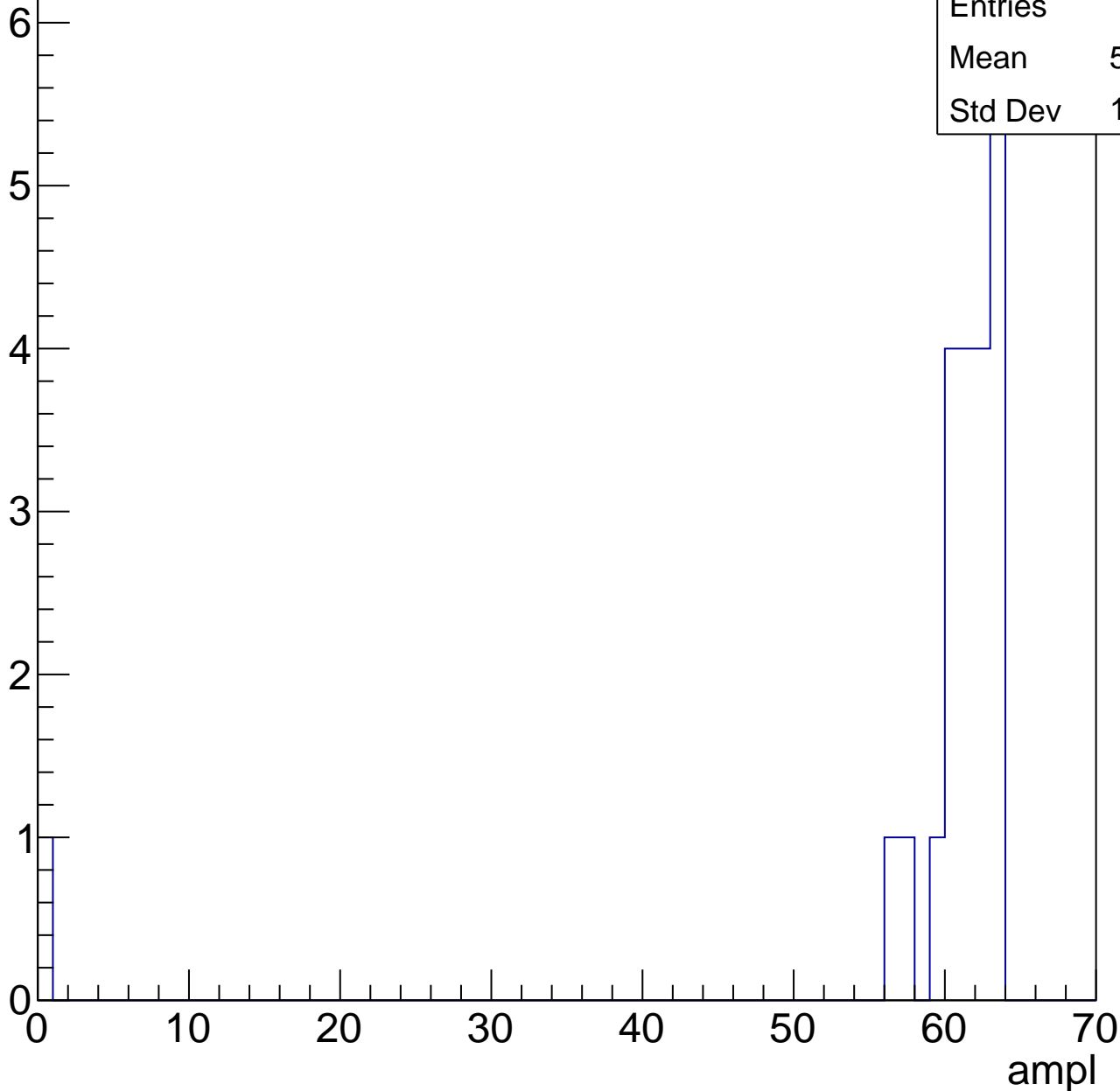


B1L100S, U13-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.27
Std Dev	12.85



B1L100S, U13-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch10, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	31.62
Std Dev	5.057

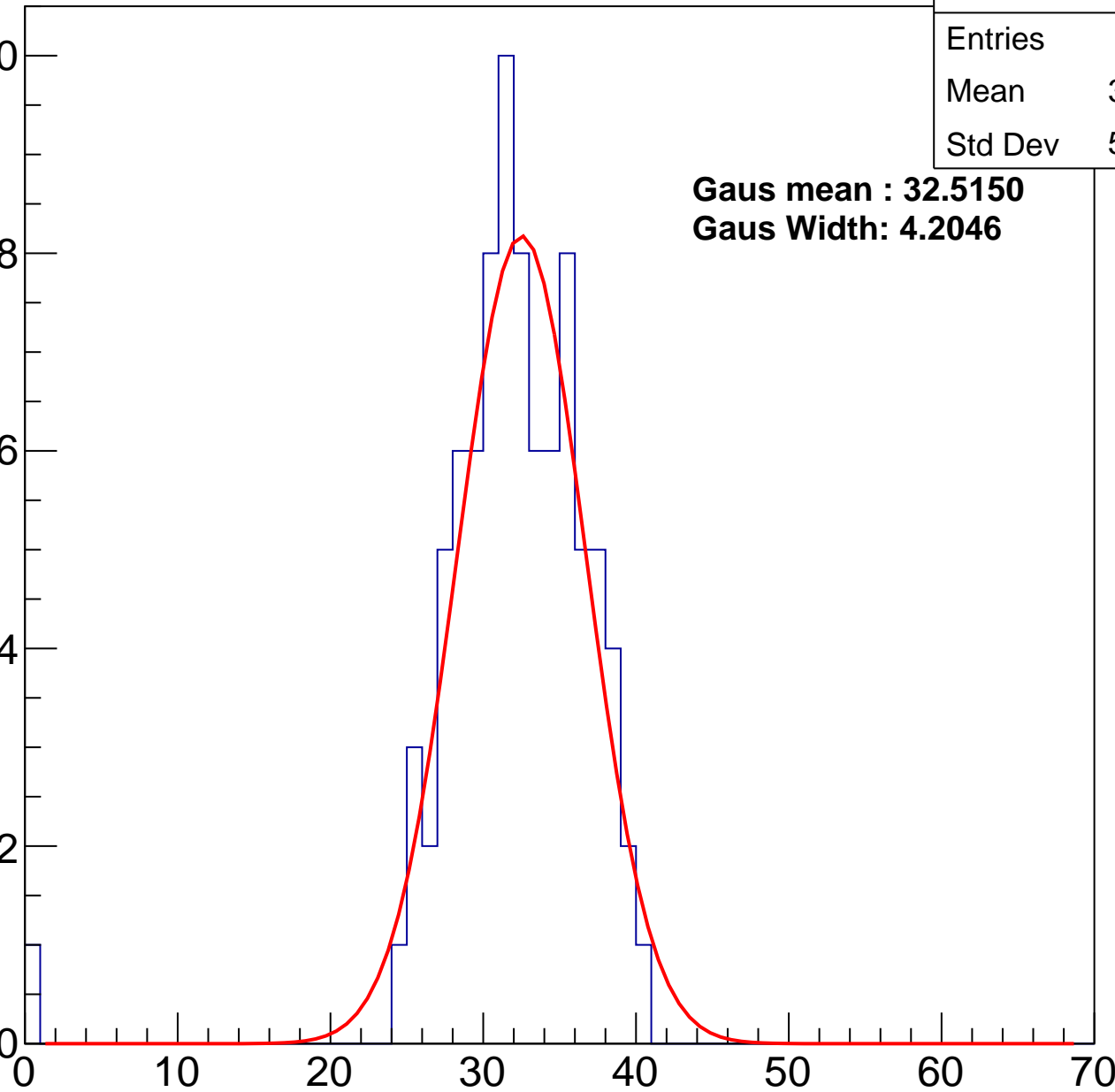
Gaus mean : 32.5150

Gaus Width: 4.2046

Entry

10
8
6
4
2
0

ampl



B1L100S, U13-ch10, adc1

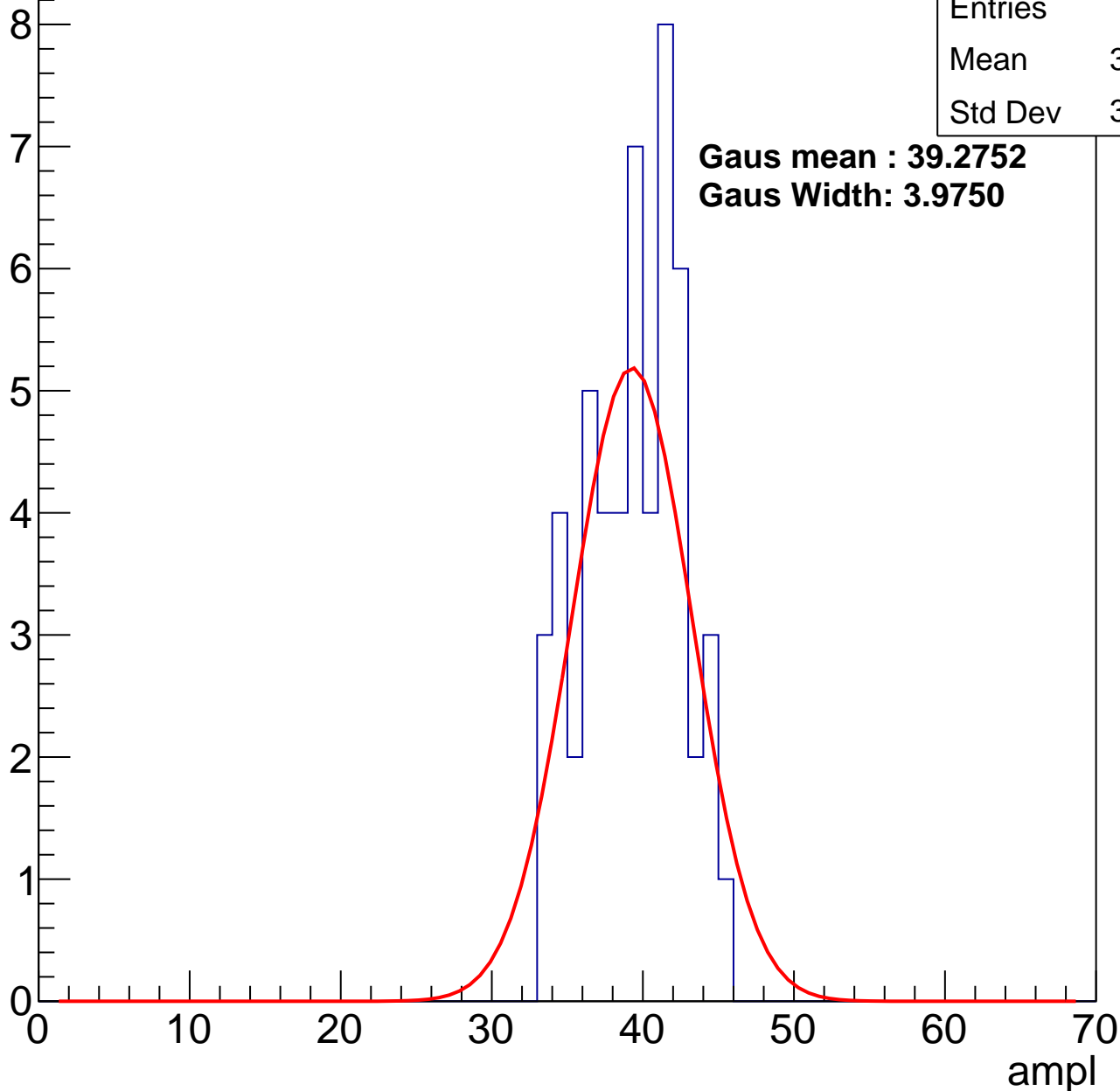
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	38.89
Std Dev	3.184

Gaus mean : 39.2752

Gaus Width: 3.9750



B1L100S, U13-ch10, adc2

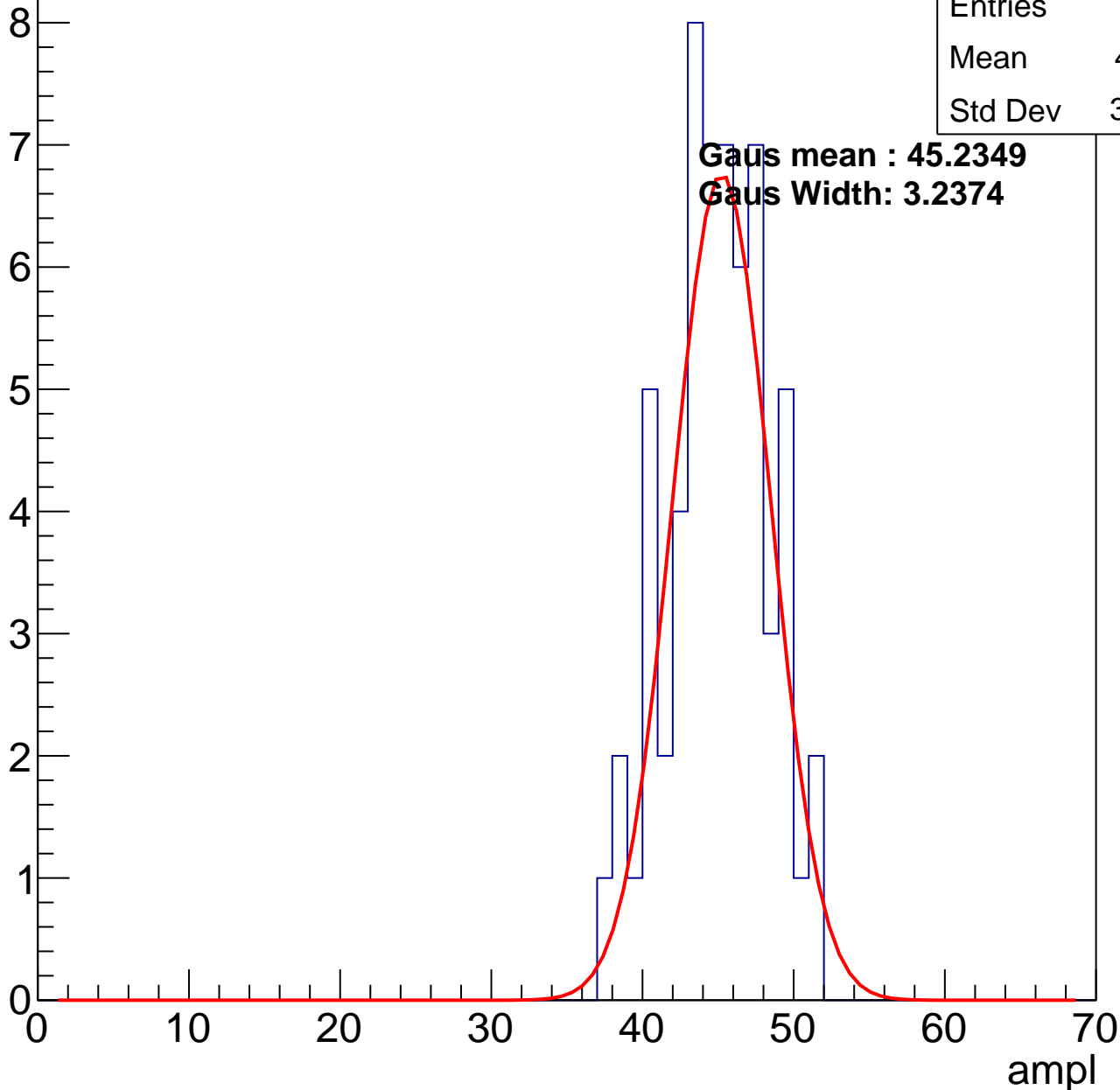
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.51
Std Dev	3.272

Gaus mean : 45.2349

Gaus Width: 3.2374

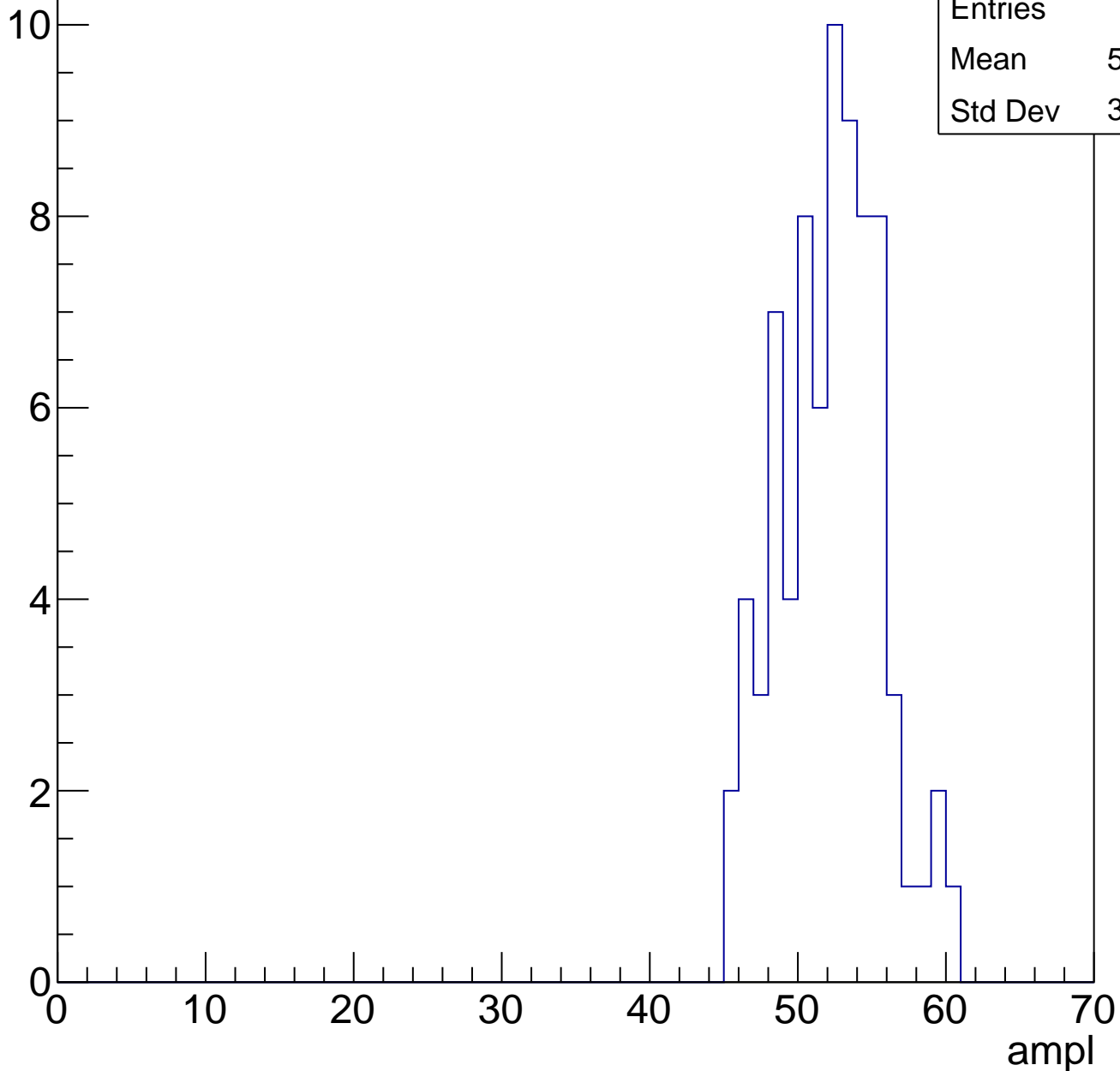


B1L100S, U13-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

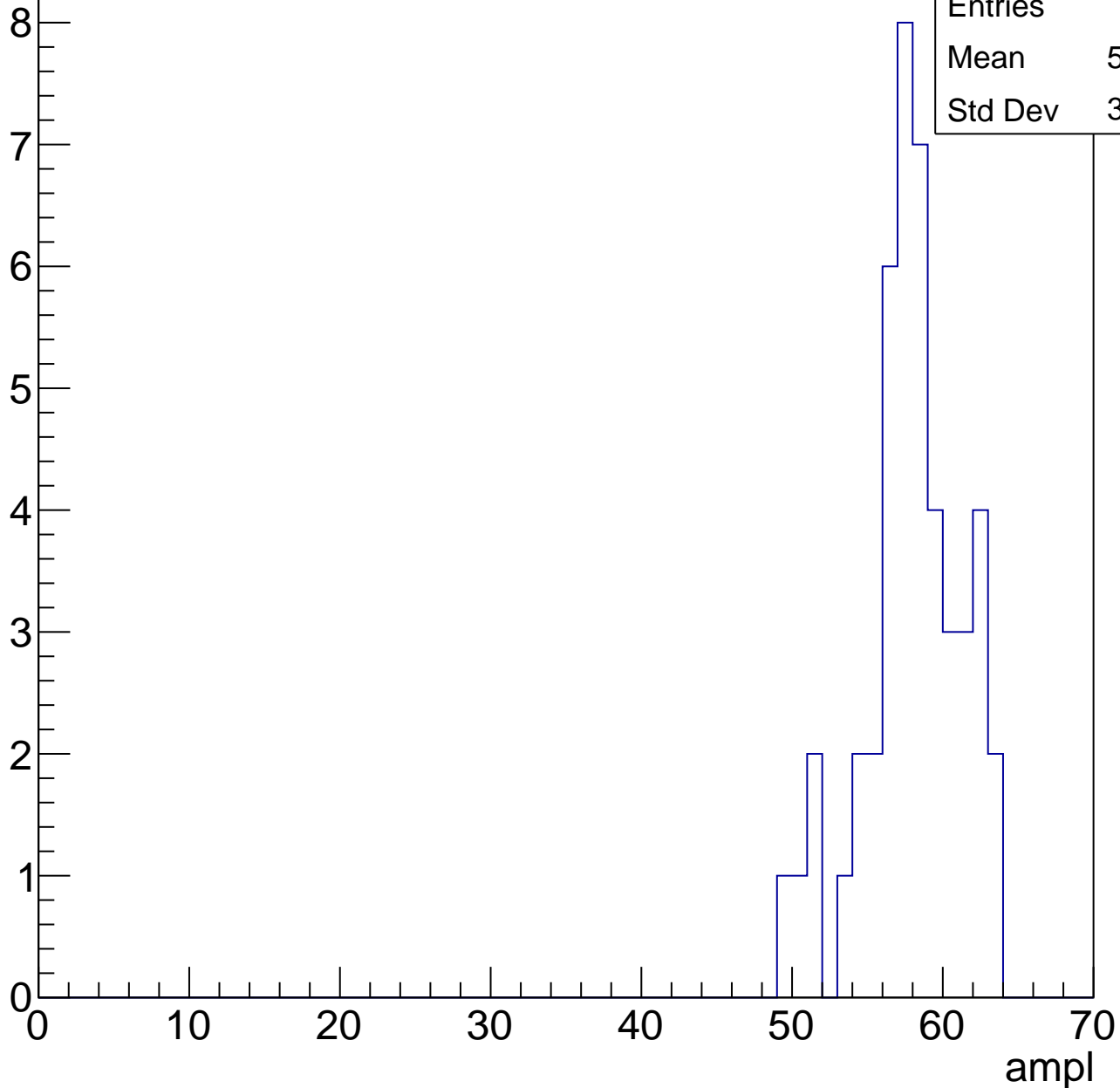
Entries	77
Mean	51.73
Std Dev	3.383



B1L100S, U13-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

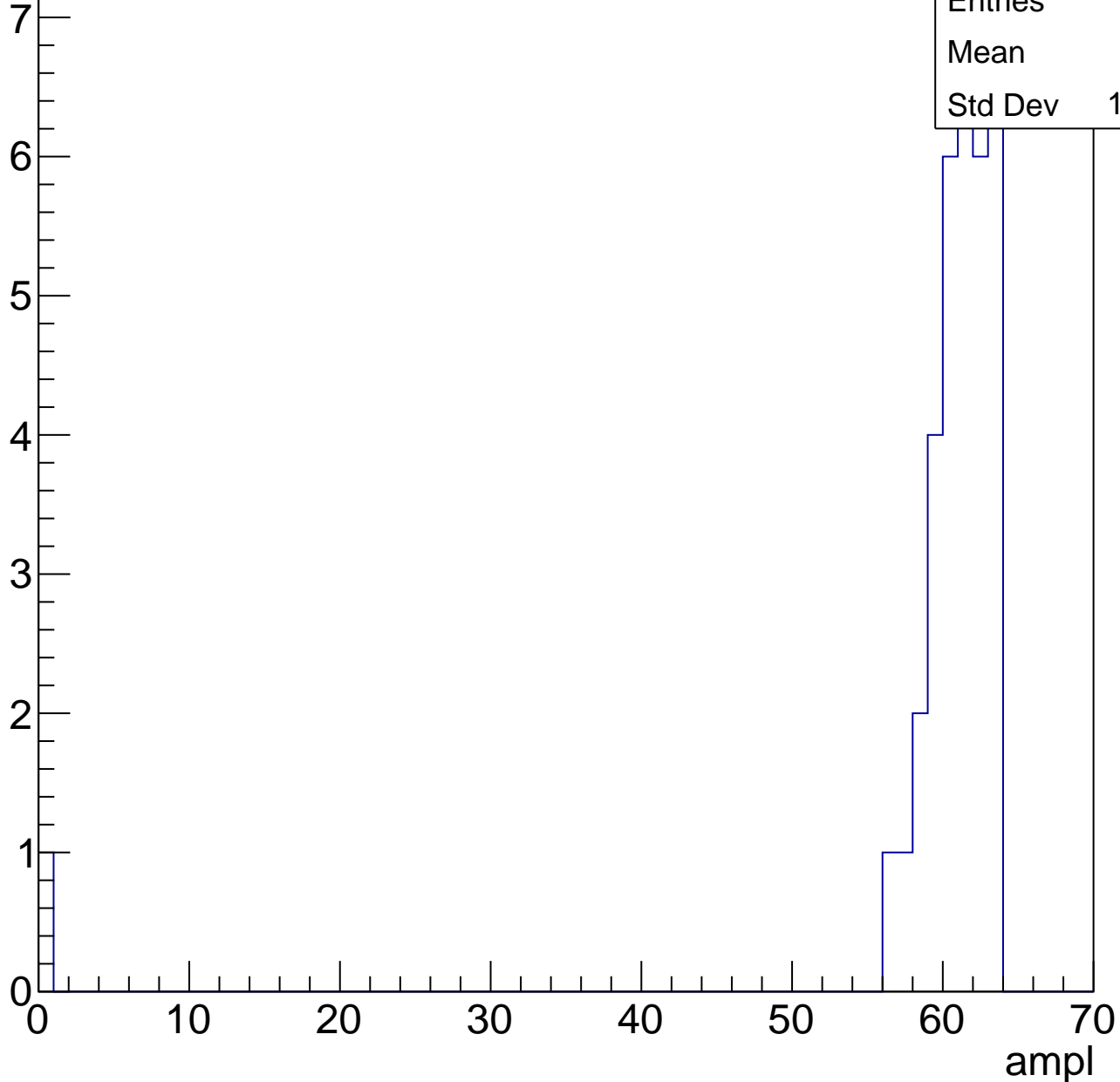
Entry



B1L100S, U13-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

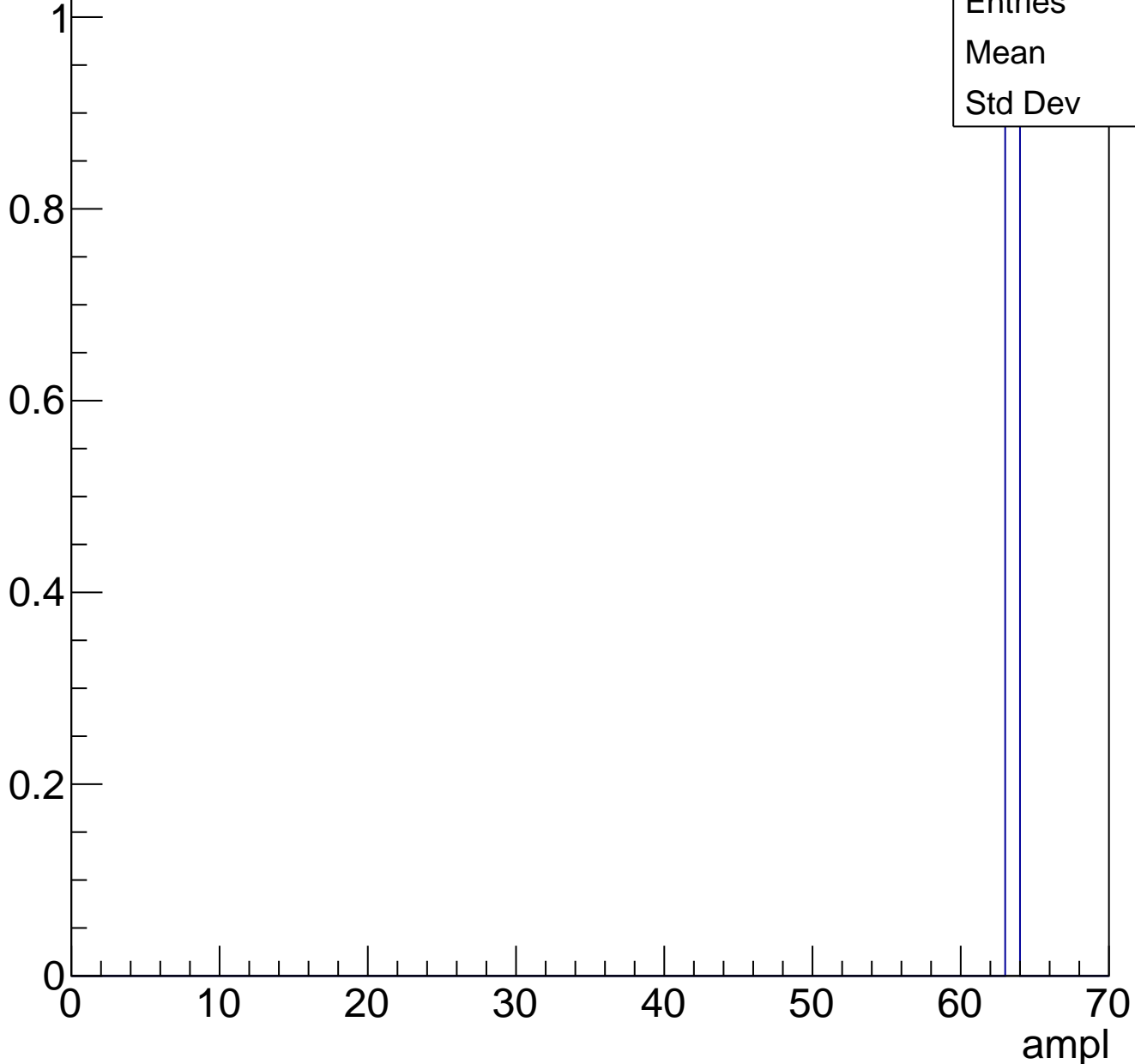
Entry



B1L100S, U13-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U13-ch11, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	31.21
Std Dev	3.46

Gaus mean : 31.9352

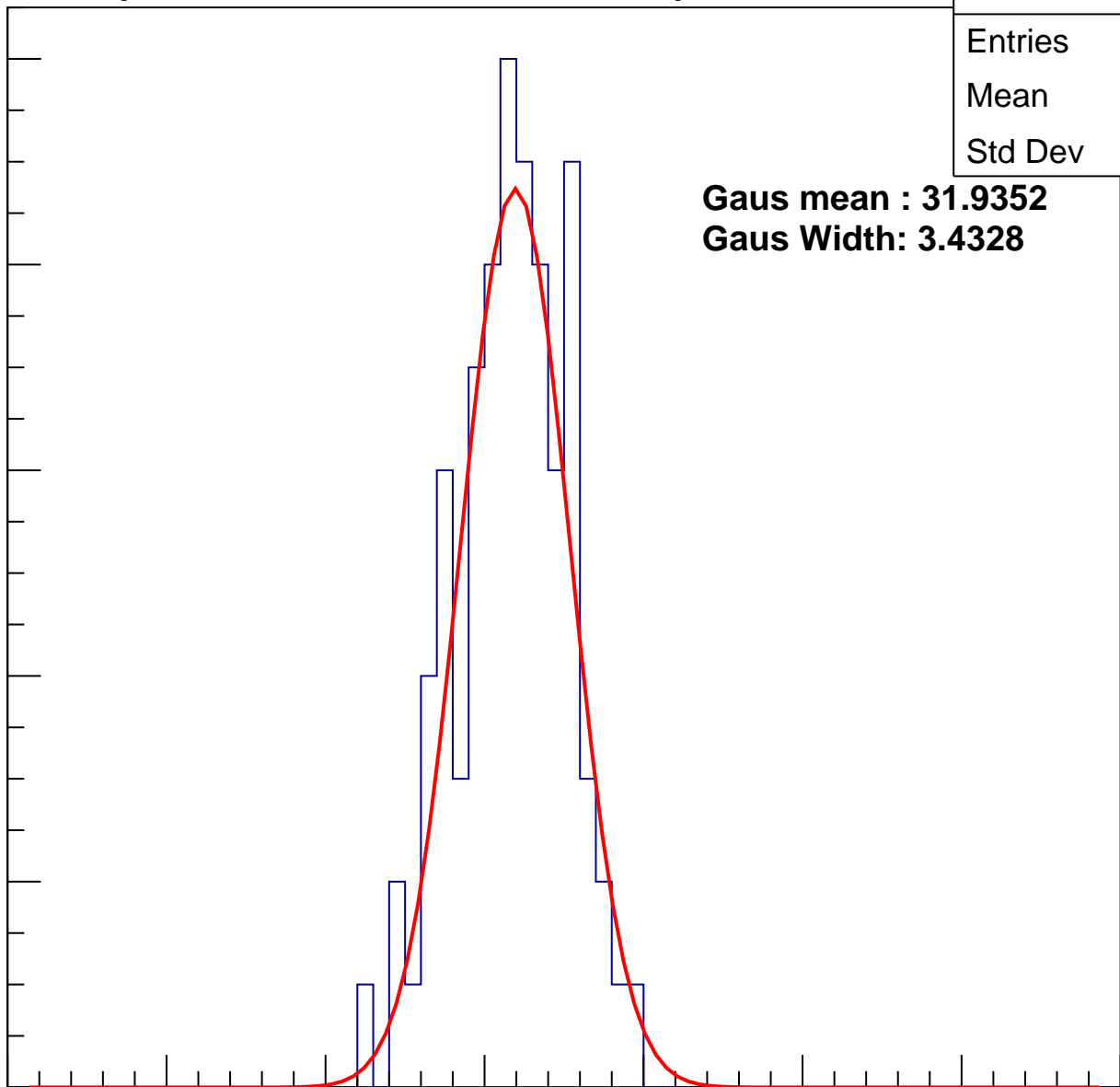
Gaus Width: 3.4328

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



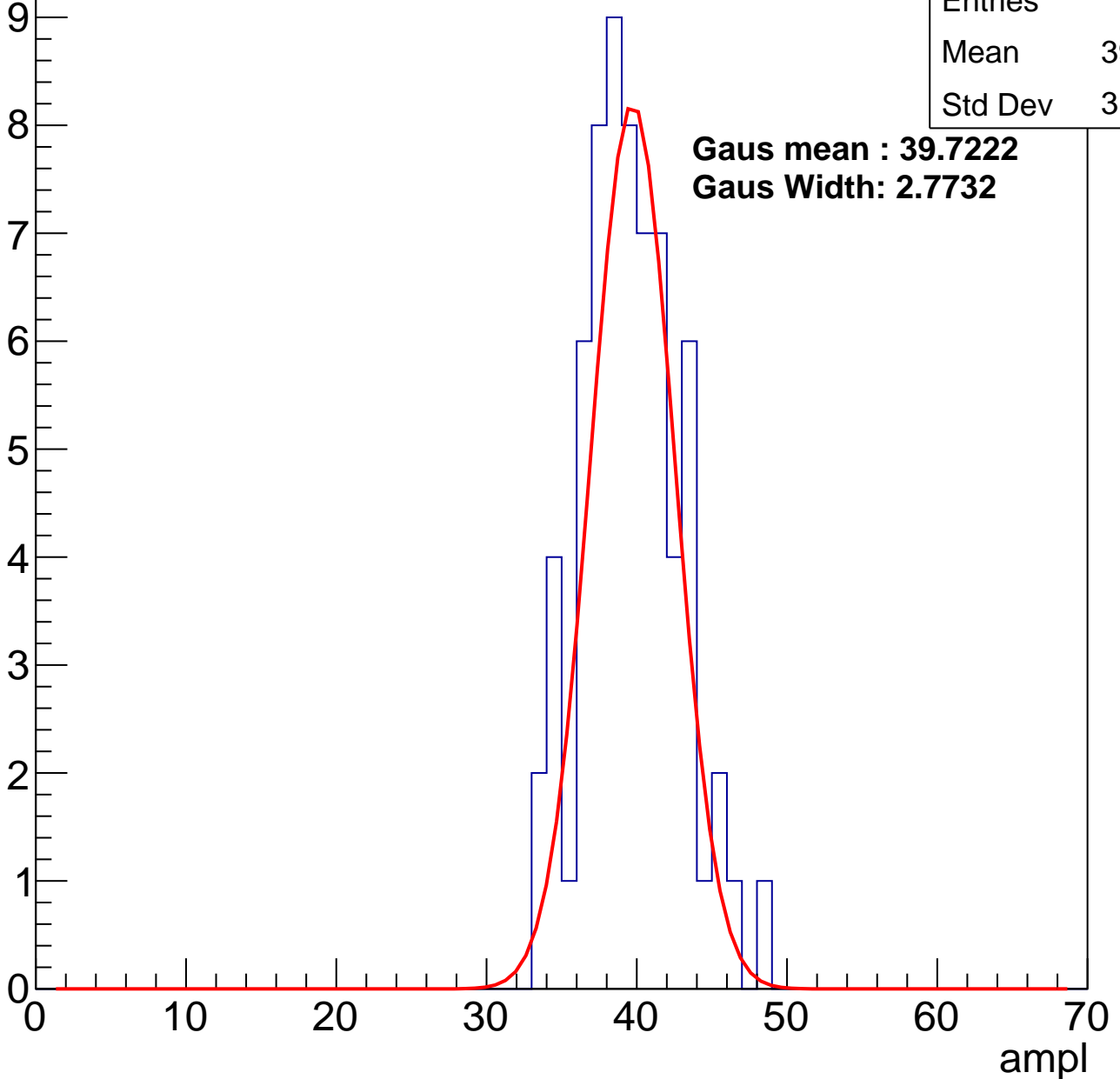
B1L100S, U13-ch11, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	39.16
Std Dev	3.175

Gaus mean : 39.7222
Gaus Width: 2.7732



B1L100S, U13-ch11, adc2

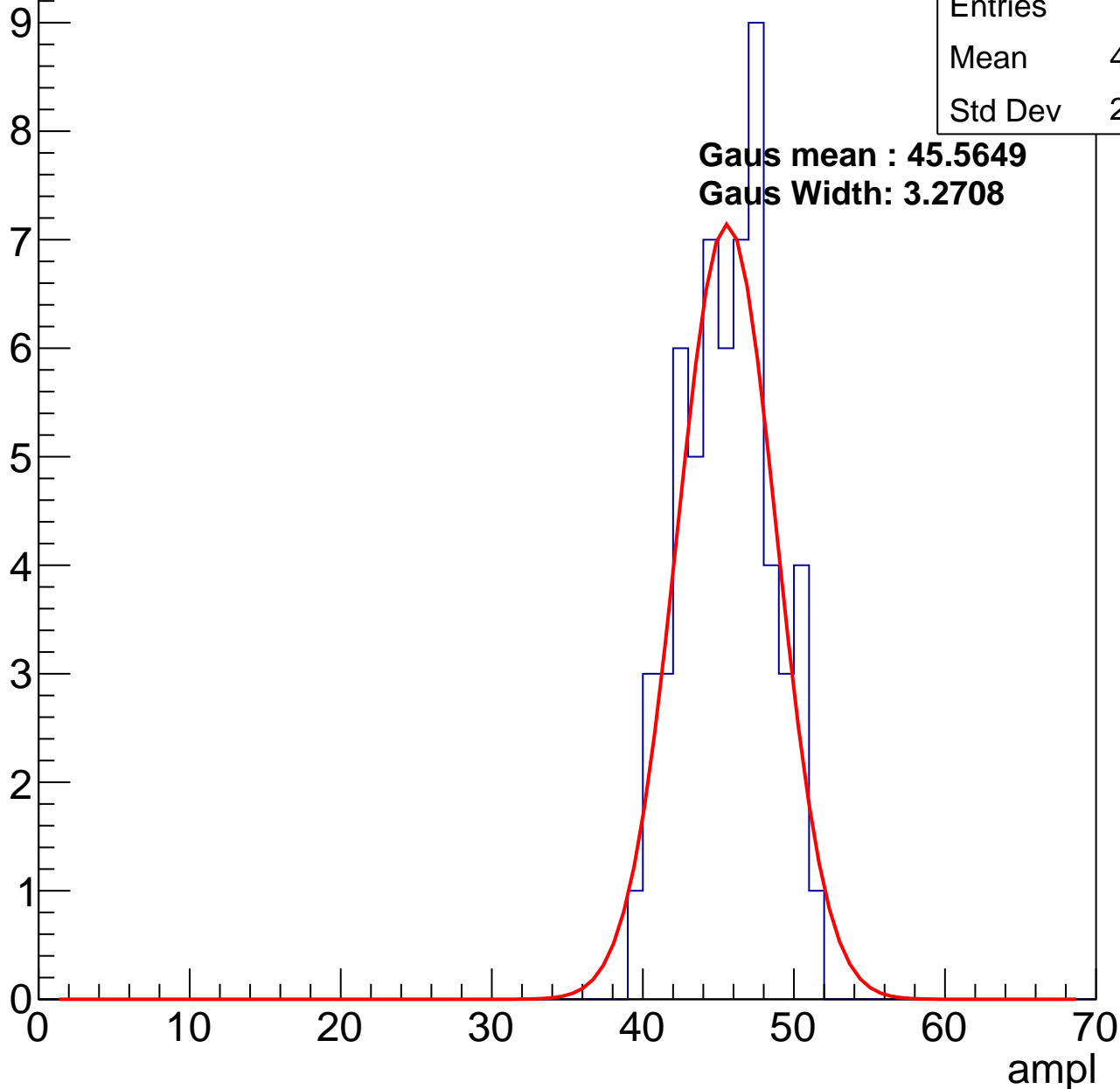
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	45.12
Std Dev	2.917

Gaus mean : 45.5649

Gaus Width: 3.2708

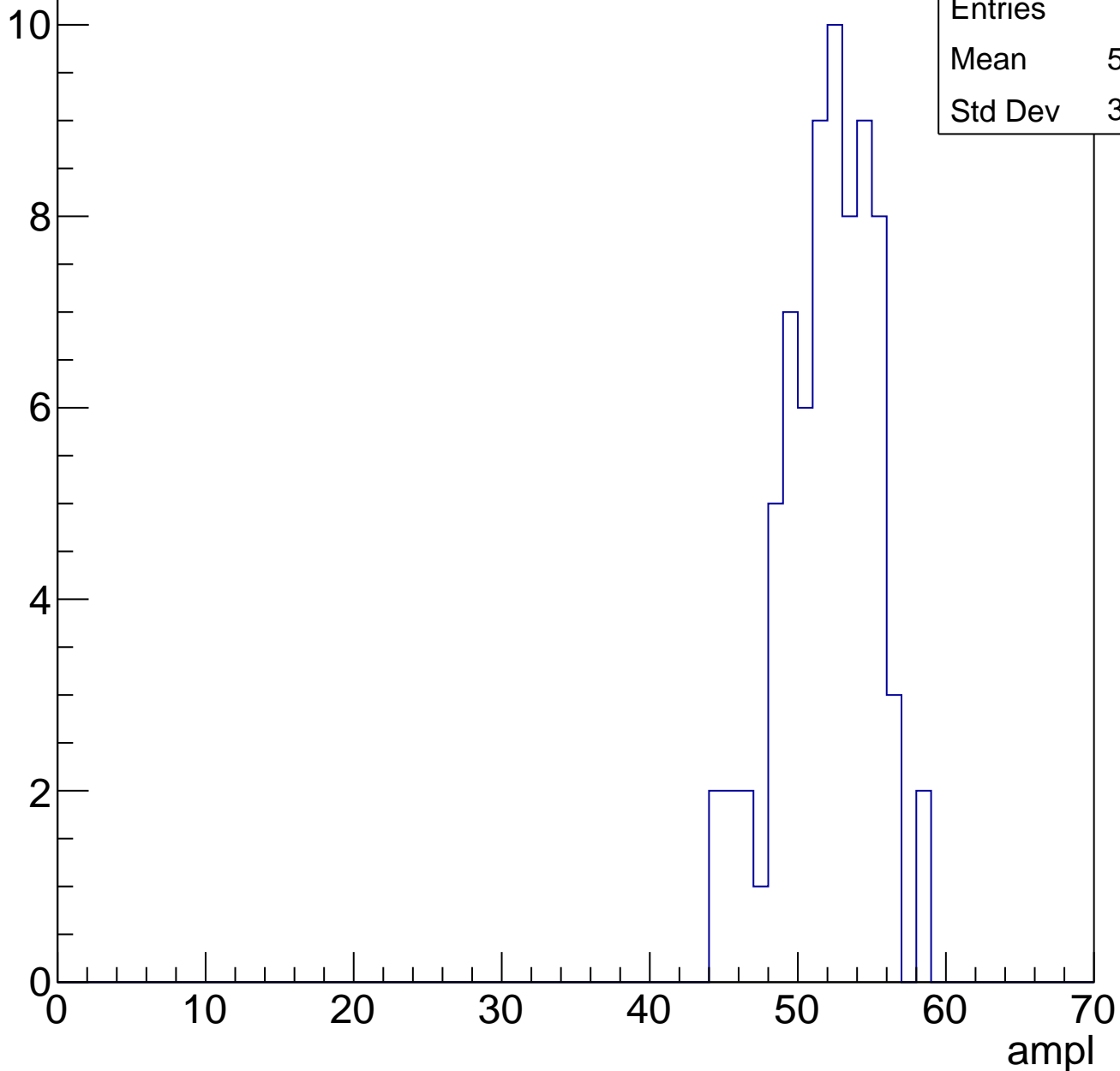


B1L100S, U13-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

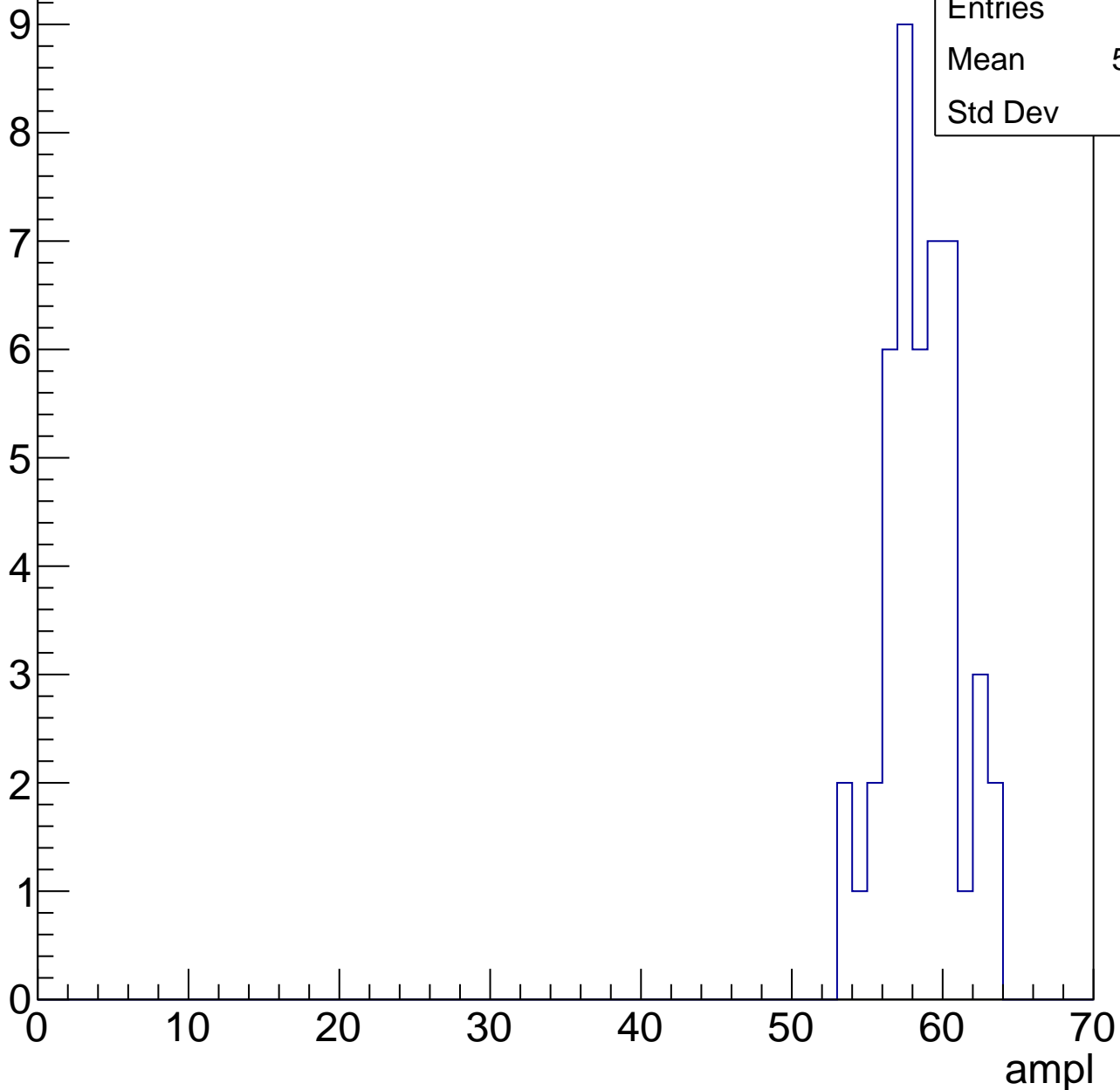
Entries	74
Mean	51.53
Std Dev	3.116



B1L100S, U13-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

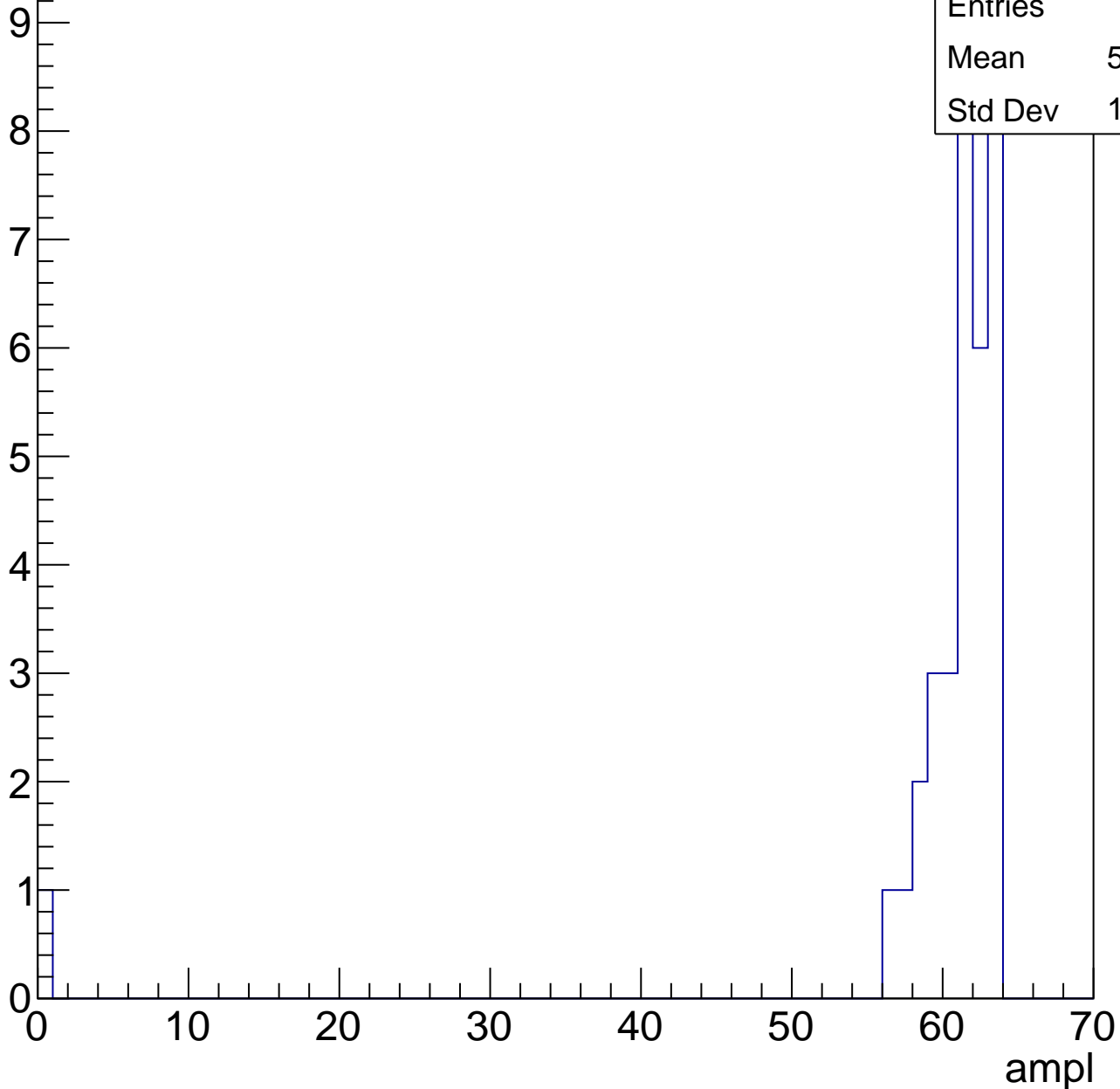


B1L100S, U13-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59.15
Std Dev	10.45



B1L100S, U13-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch12, adc0

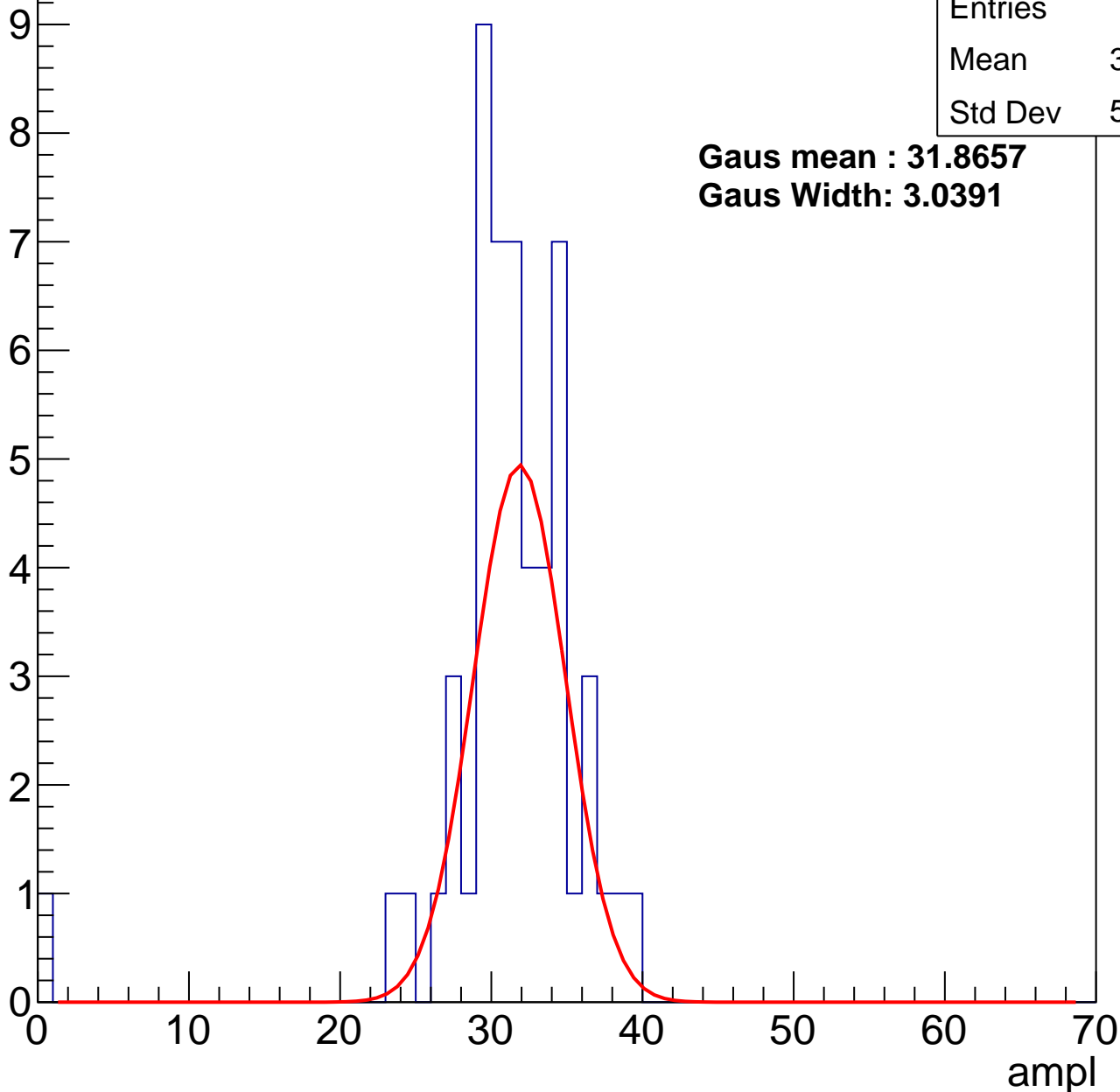
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	30.66
Std Dev	5.348

Gaus mean : 31.8657

Gaus Width: 3.0391



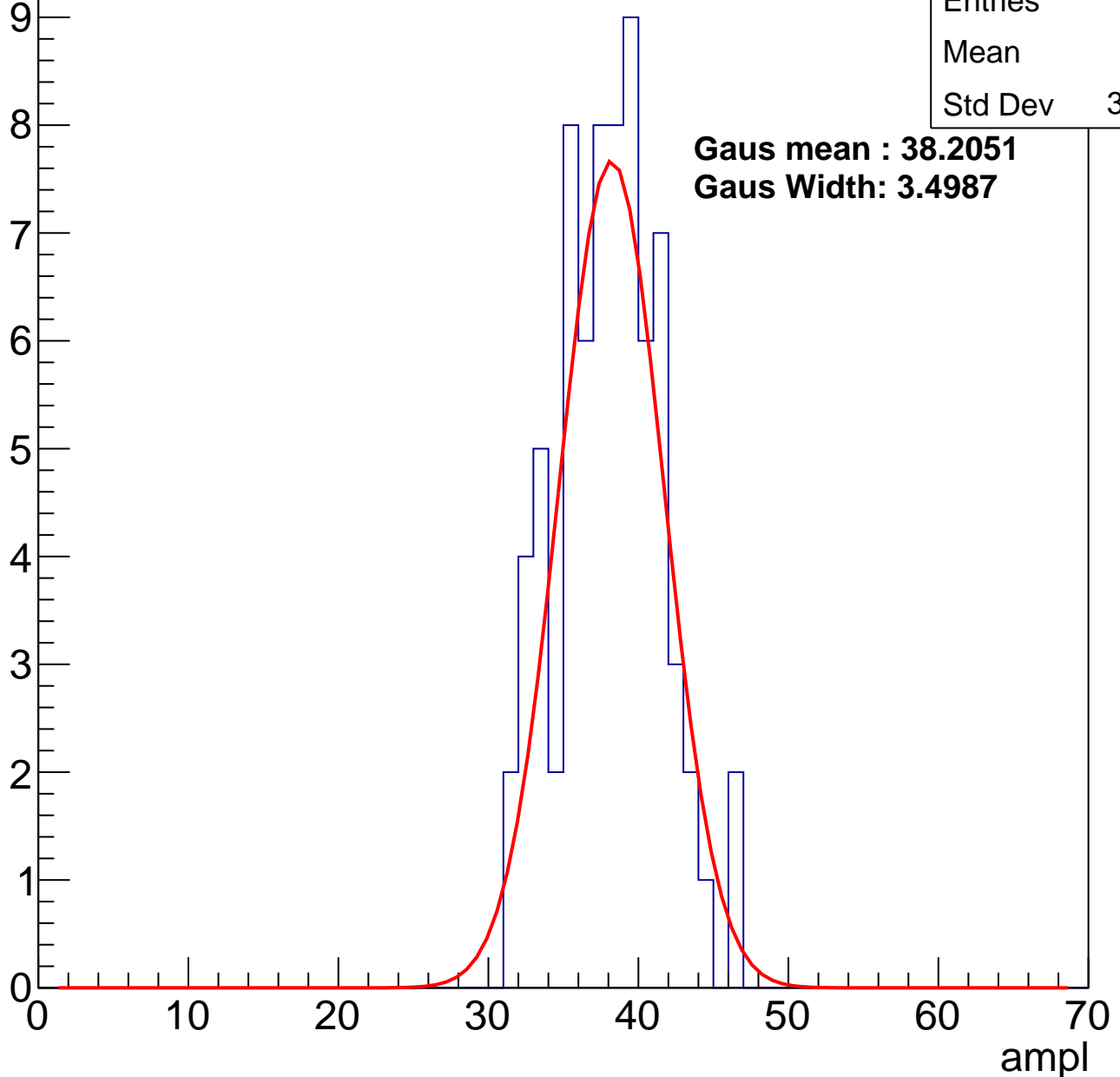
B1L100S, U13-ch12, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.6
Std Dev	3.411

Gaus mean : 38.2051
Gaus Width: 3.4987



B1L100S, U13-ch12, adc2

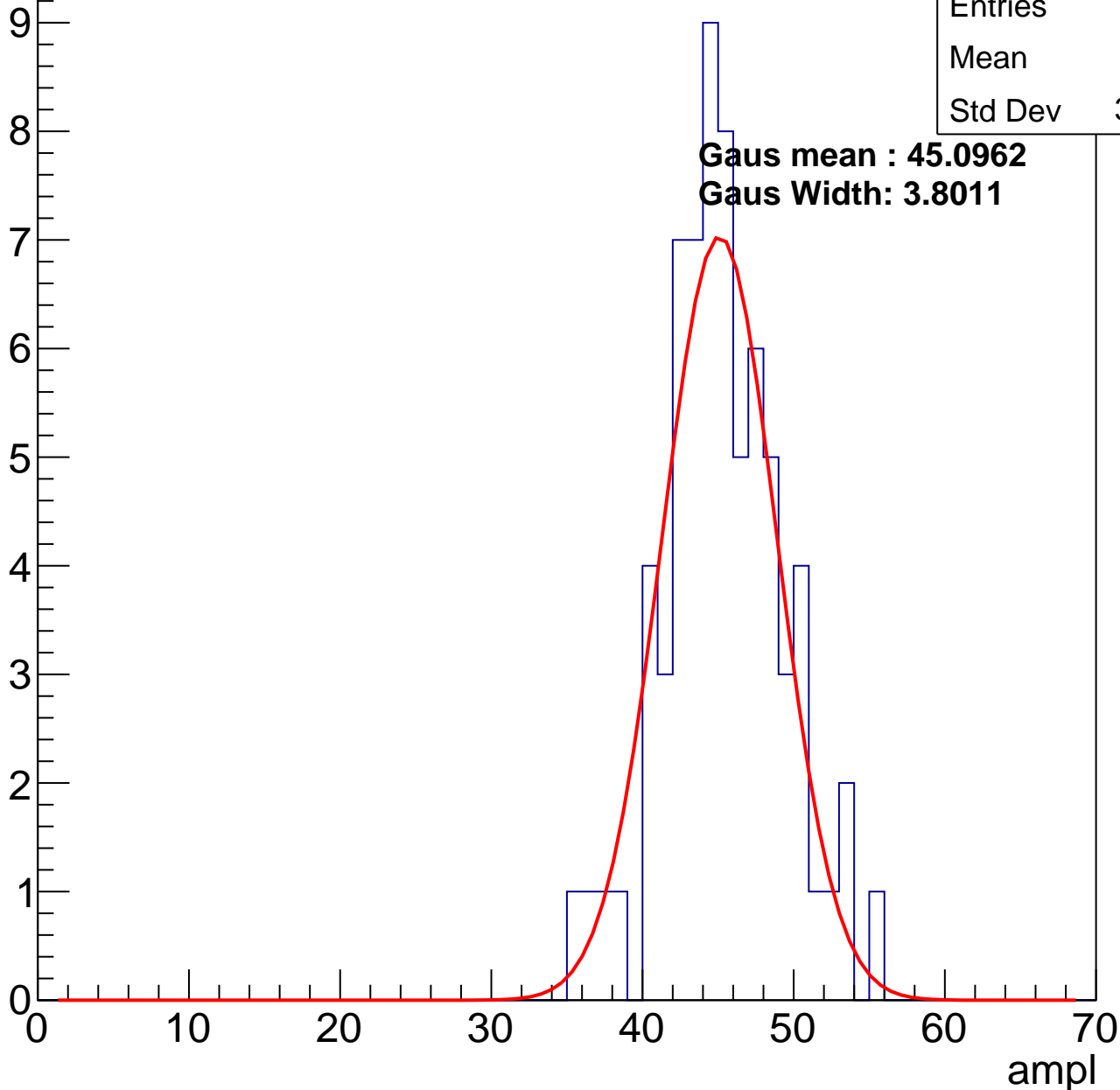
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.9
Std Dev	3.921

Gaus mean : 45.0962

Gaus Width: 3.8011



B1L100S, U13-ch12, adc3

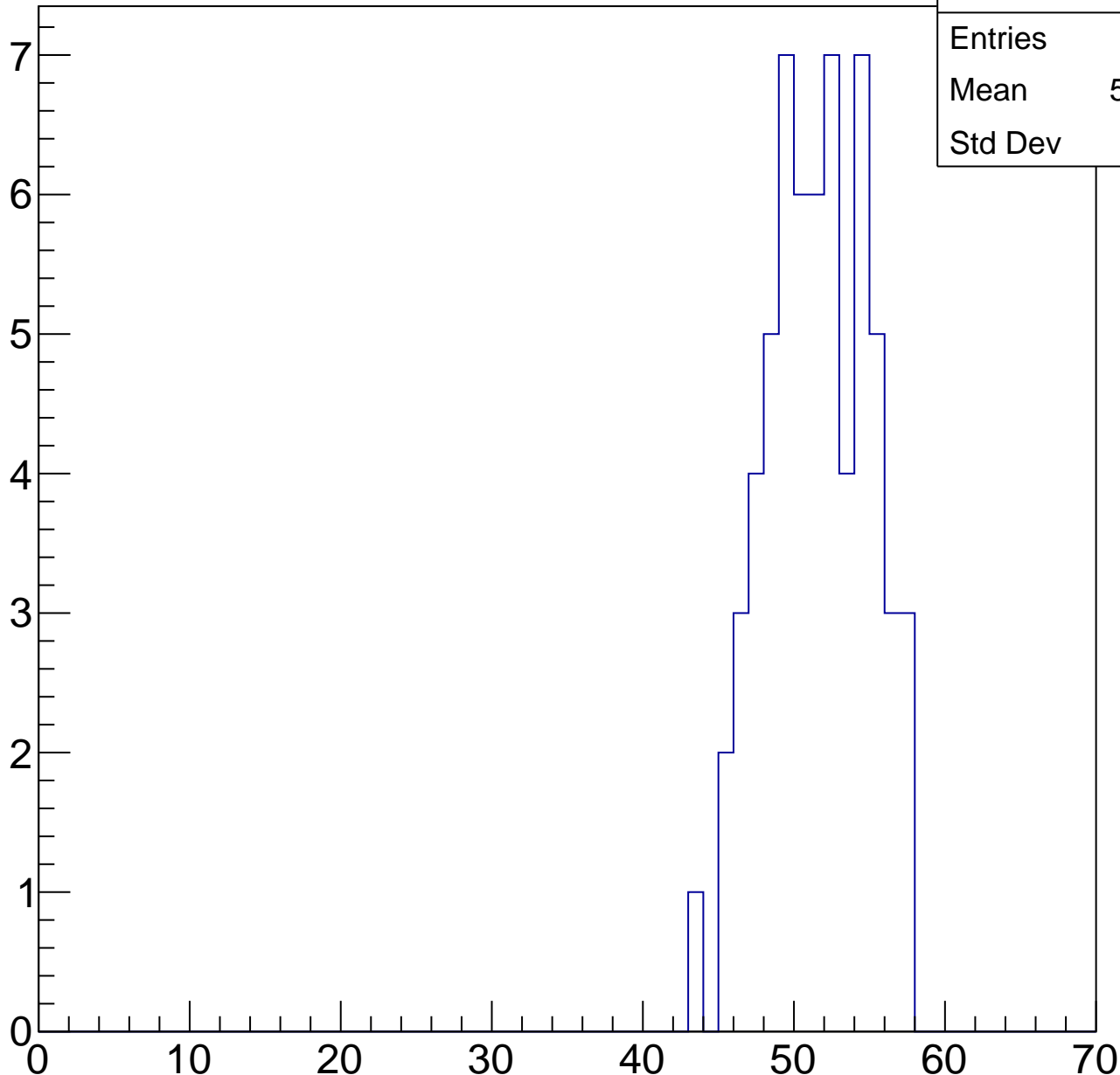
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	63
Mean	51.05
Std Dev	3.34

ampl



B1L100S, U13-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	57.39
Std Dev	3.102

Entry

10

8

6

4

2

0

0

10

20

30

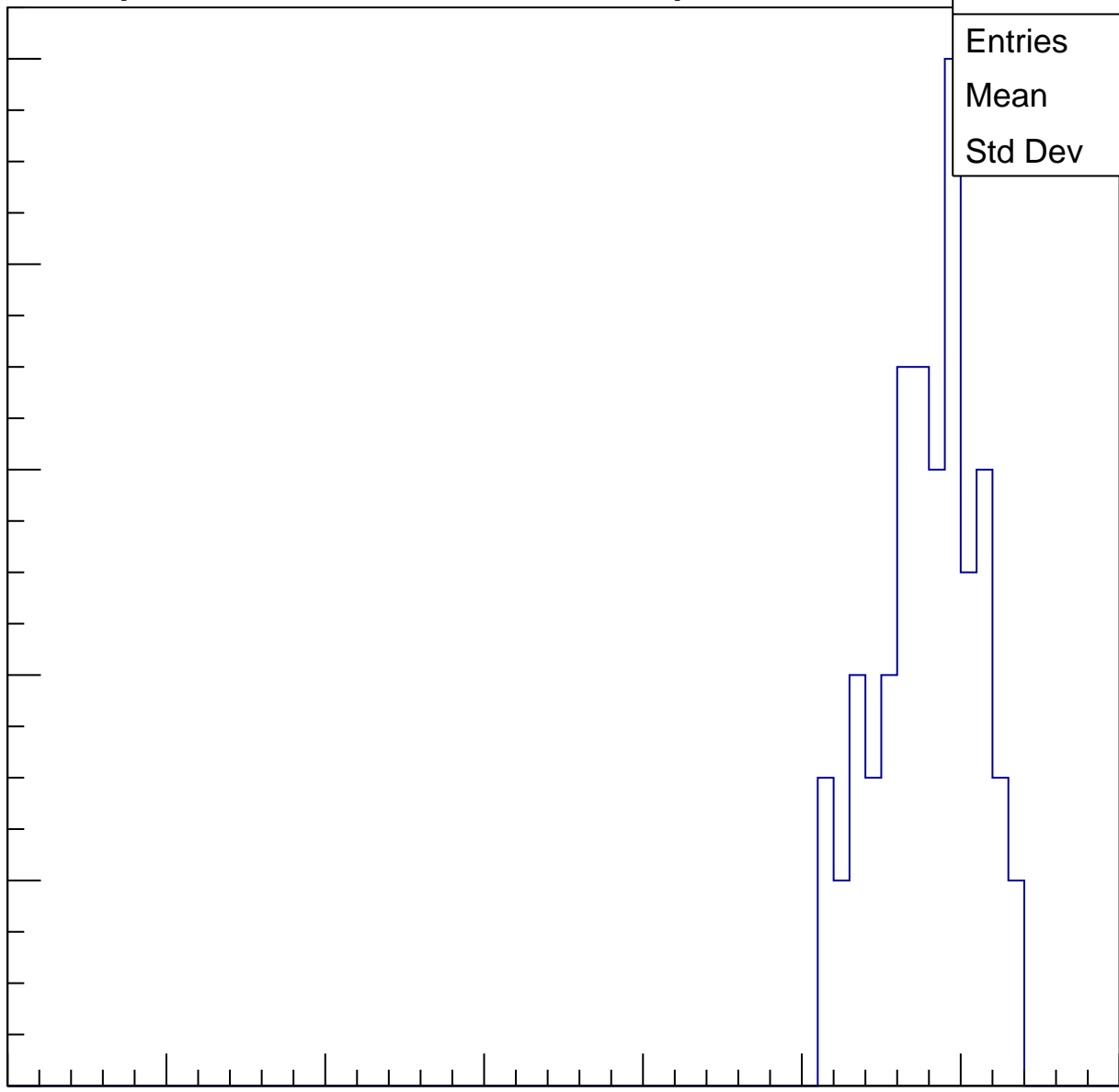
40

50

60

70

ampl

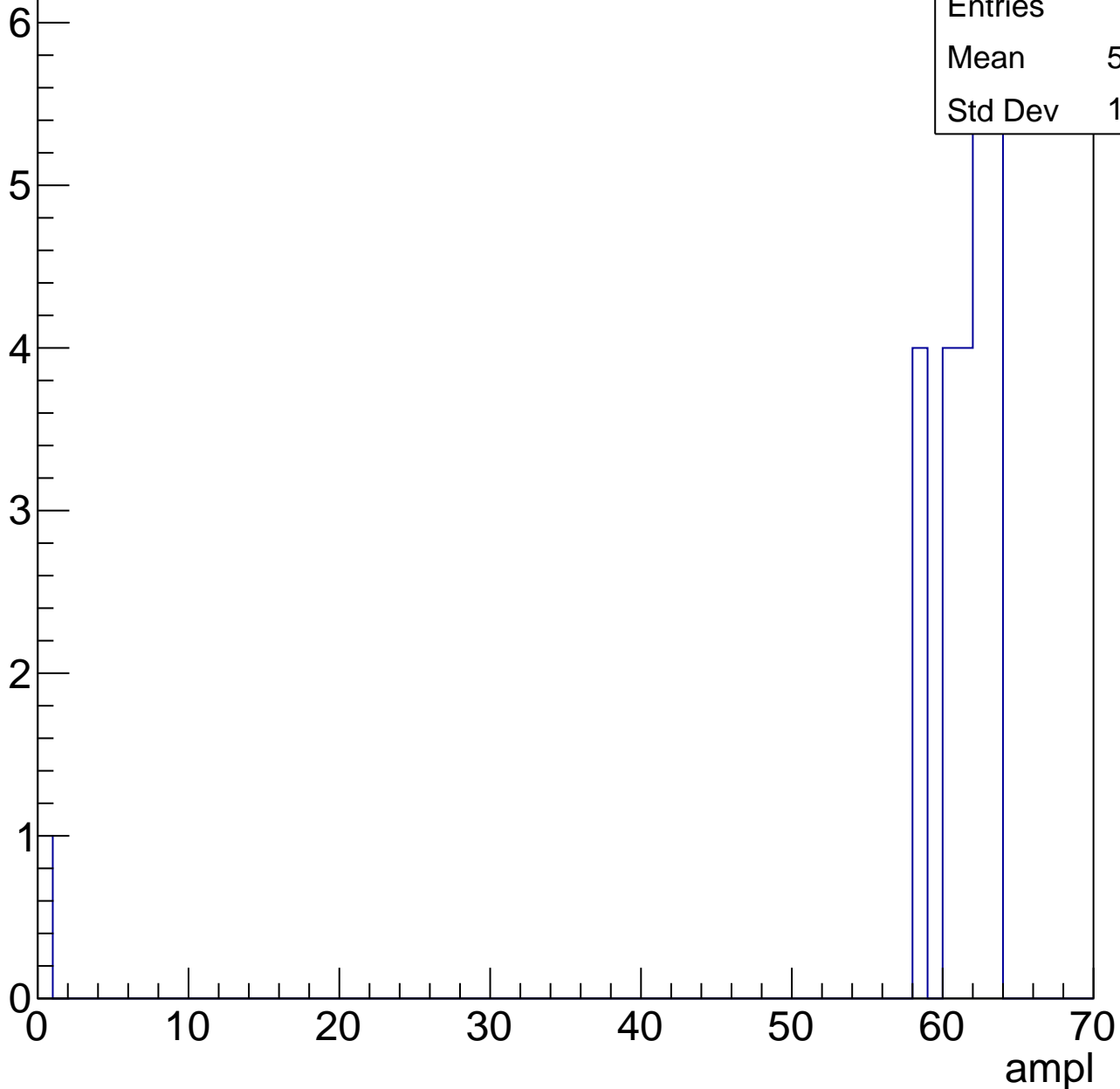


B1L100S, U13-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.64
Std Dev	12.09



B1L100S, U13-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch13, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	30.77
Std Dev	3.321

Gaus mean : 30.8306

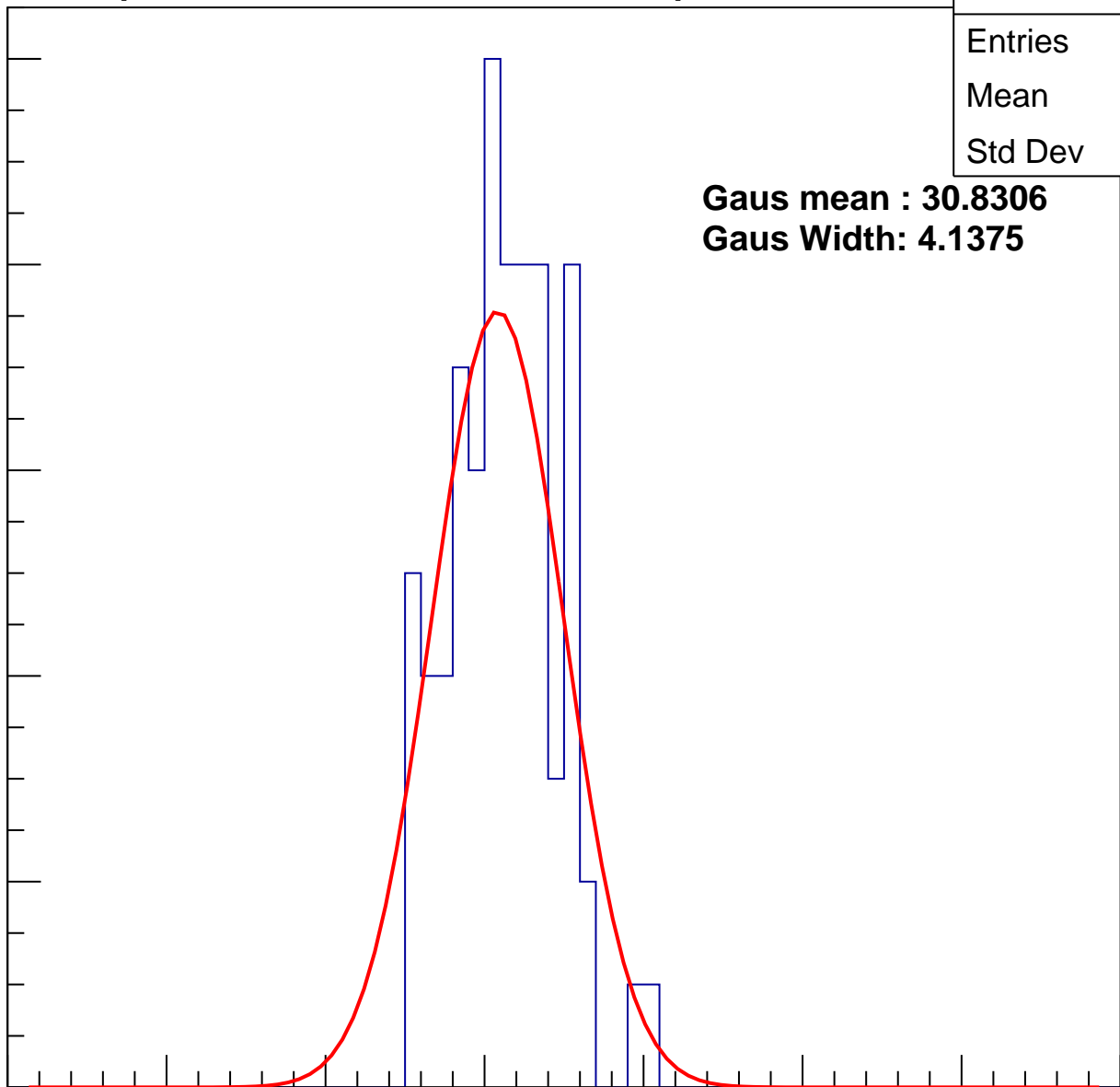
Gaus Width: 4.1375

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch13, adc1

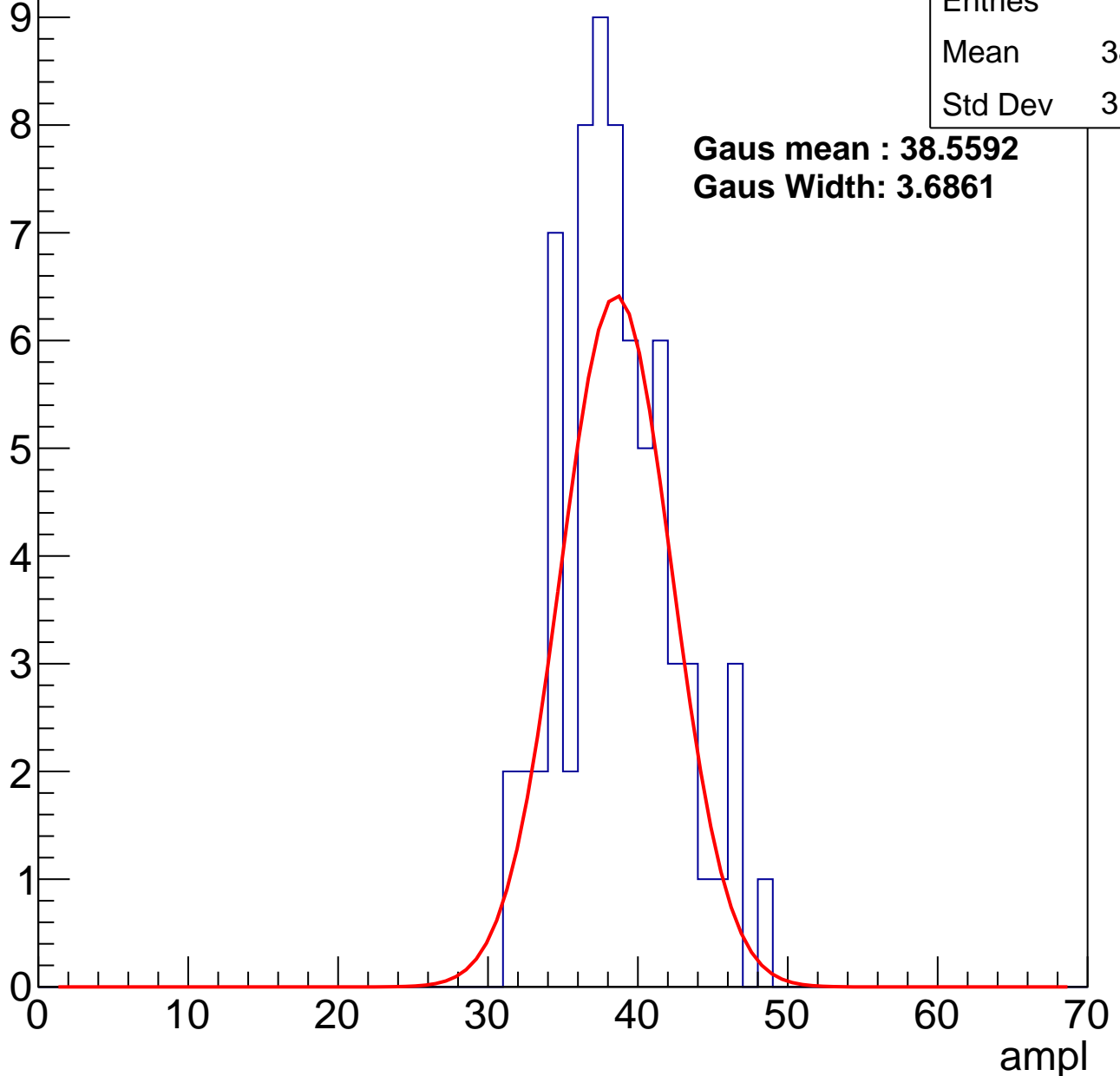
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	38.19
Std Dev	3.747

Gaus mean : 38.5592

Gaus Width: 3.6861



B1L100S, U13-ch13, adc2

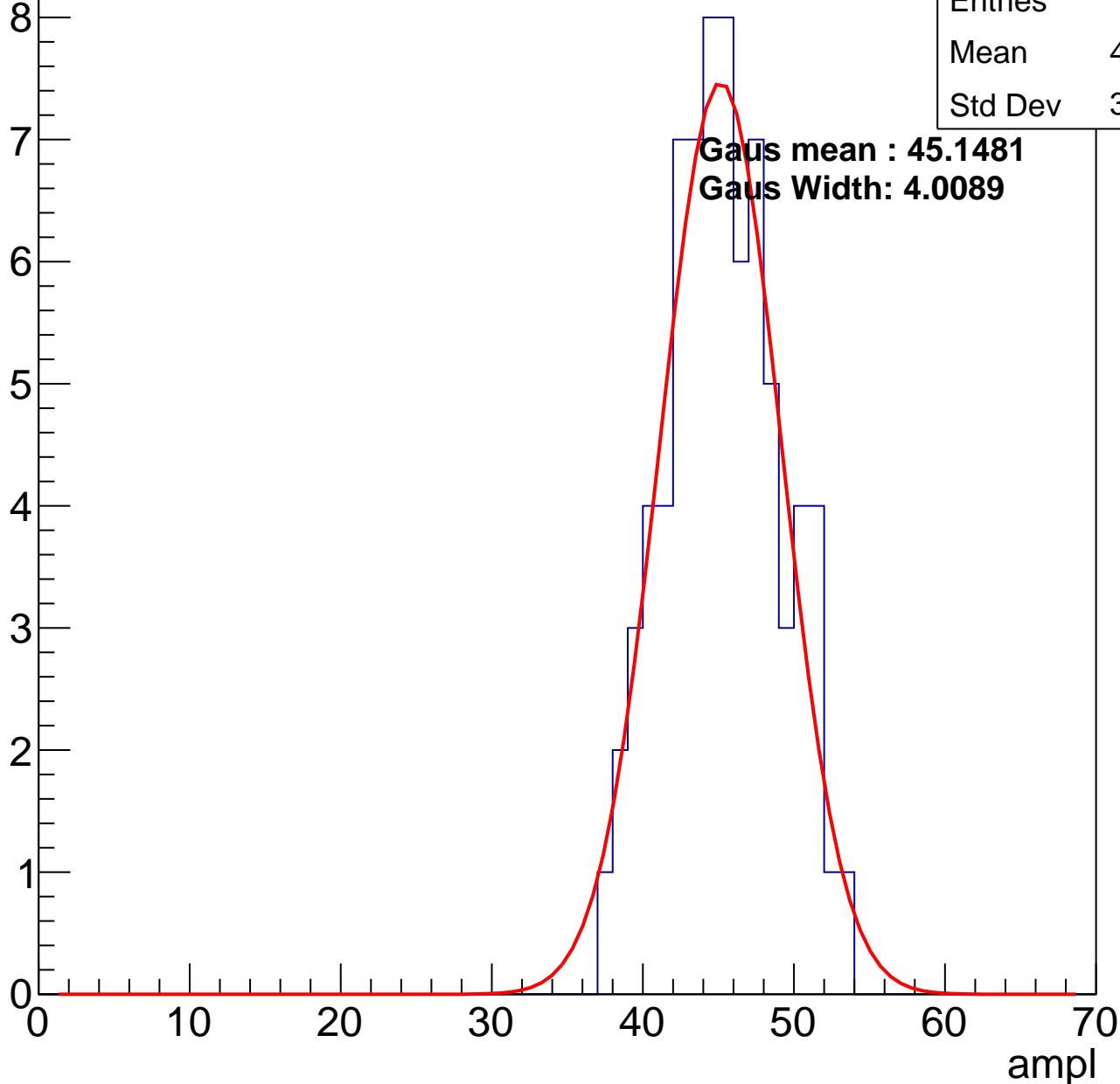
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.83
Std Dev	3.678

Gaus mean : 45.1481

Gaus Width: 4.0089

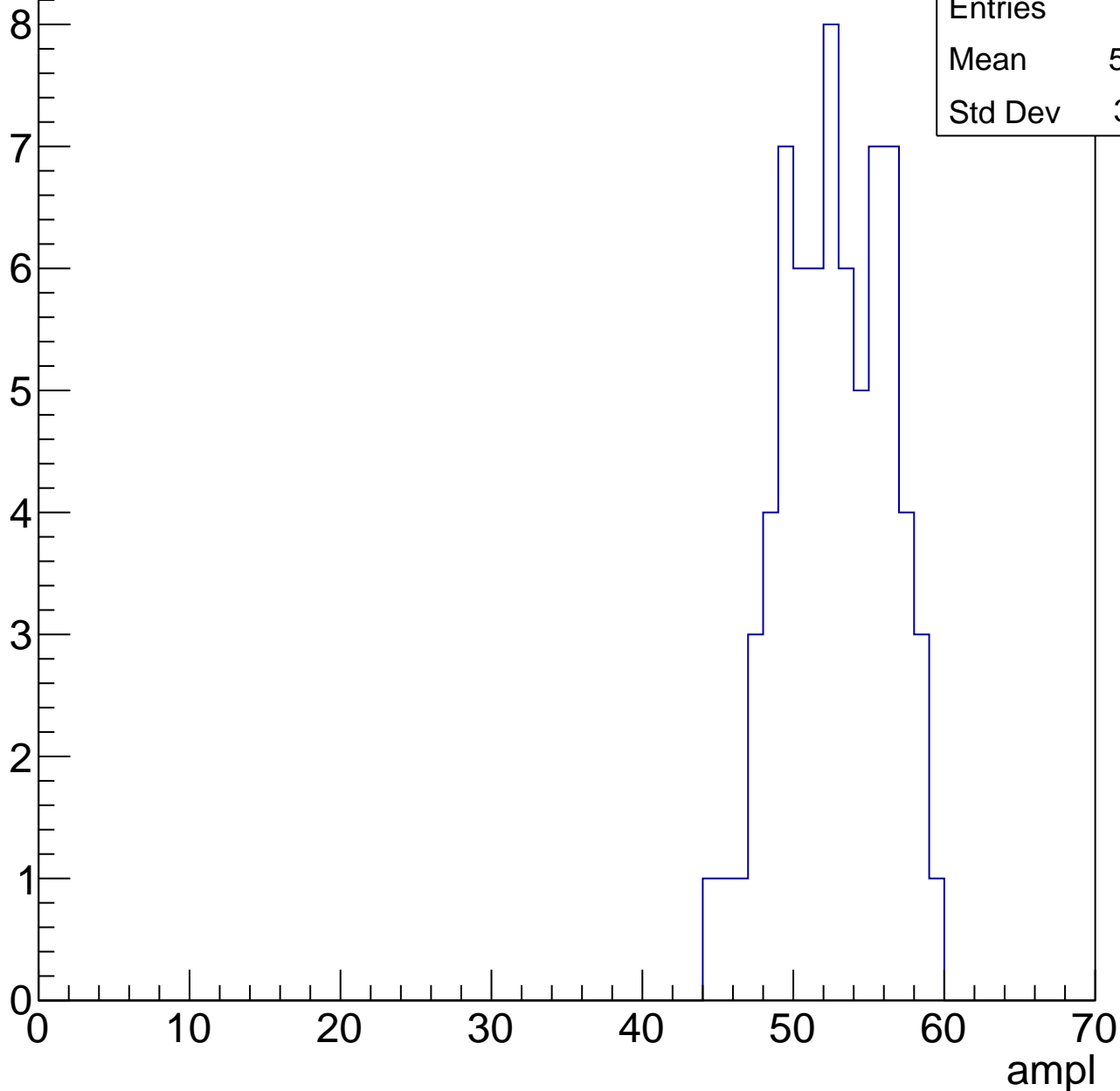


B1L100S, U13-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	52.27
Std Dev	3.451

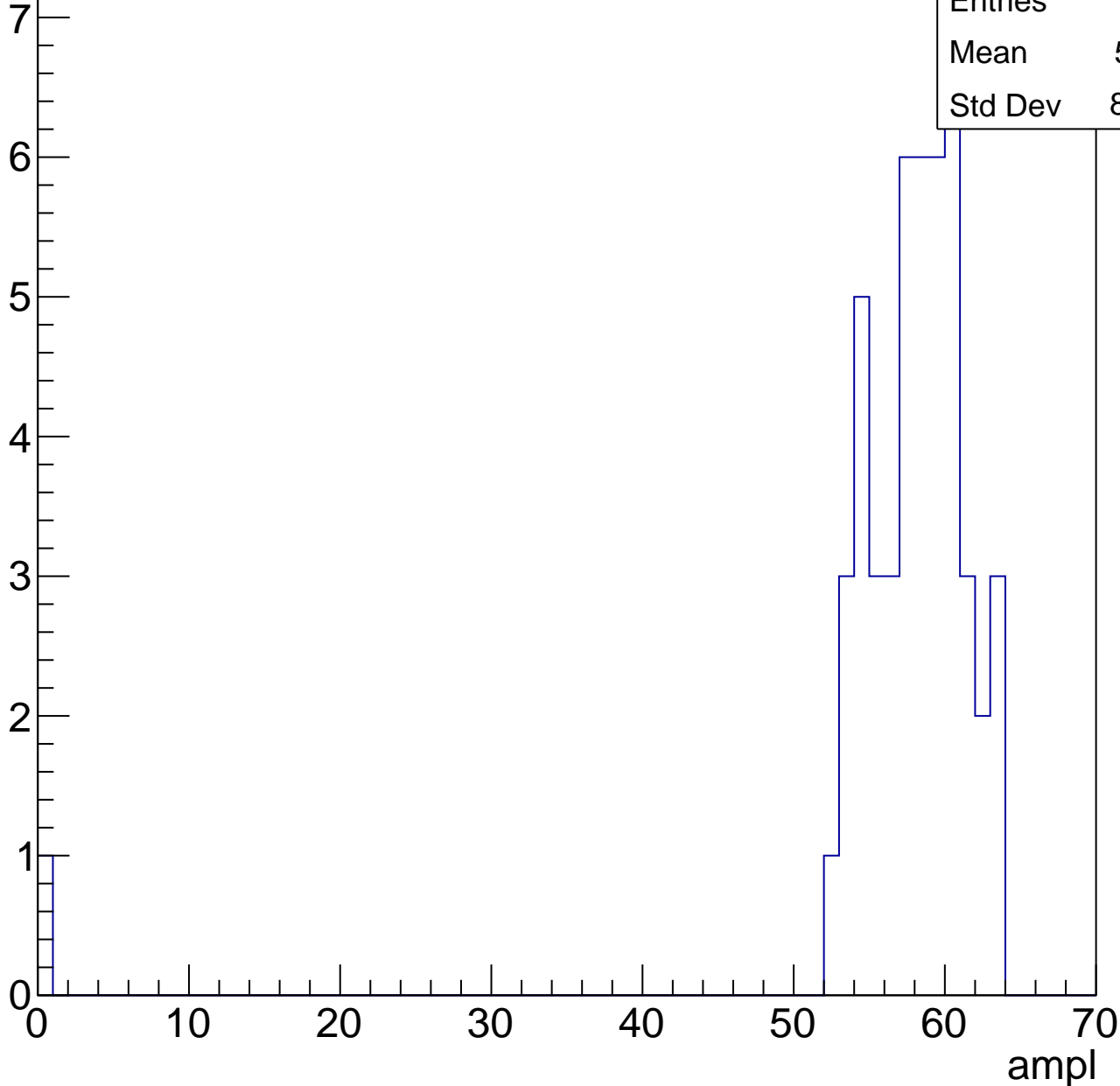


B1L100S, U13-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	56.61
Std Dev	8.659

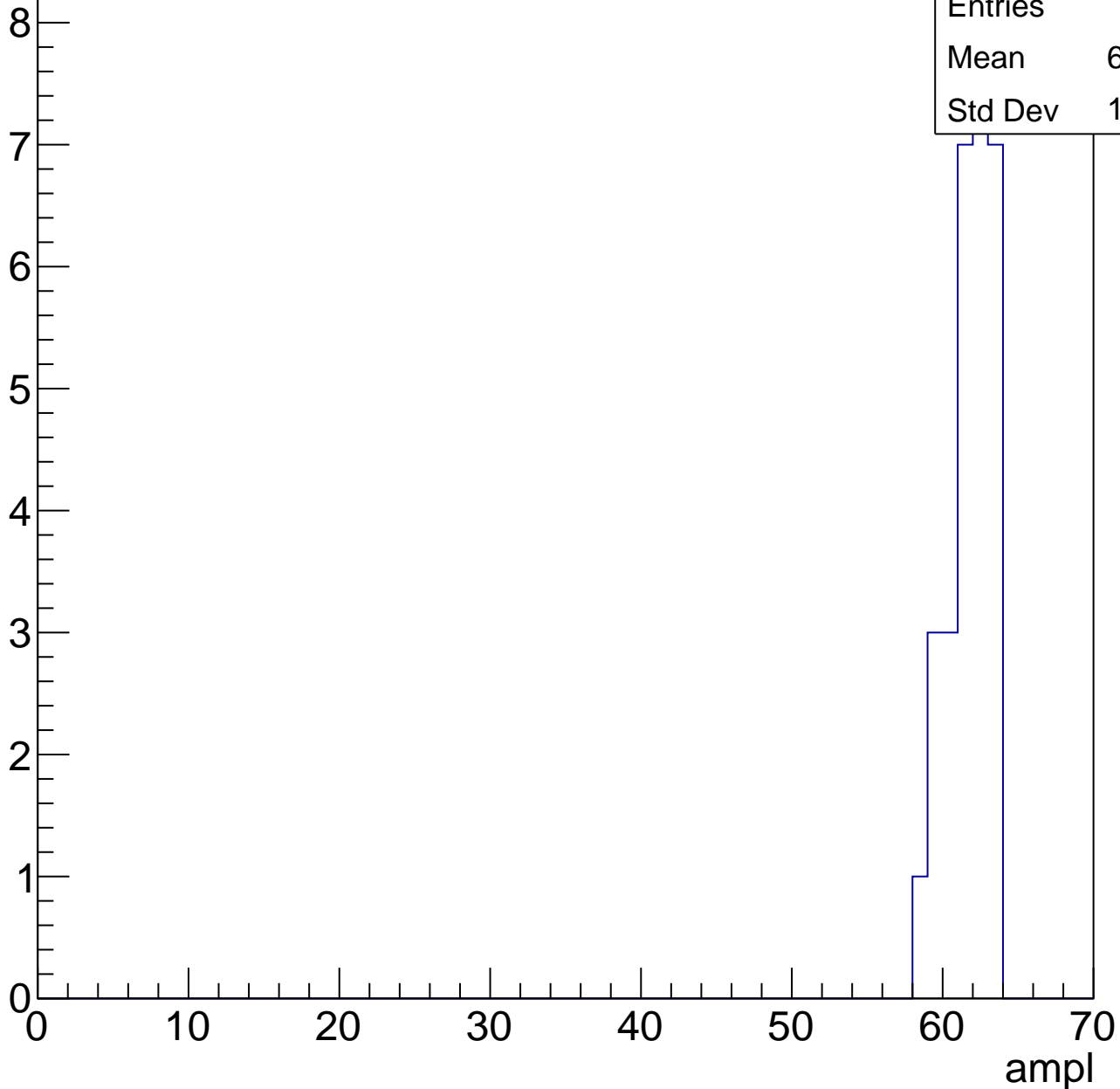


B1L100S, U13-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	61.34
Std Dev	1.396



B1L100S, U13-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch13, adc7

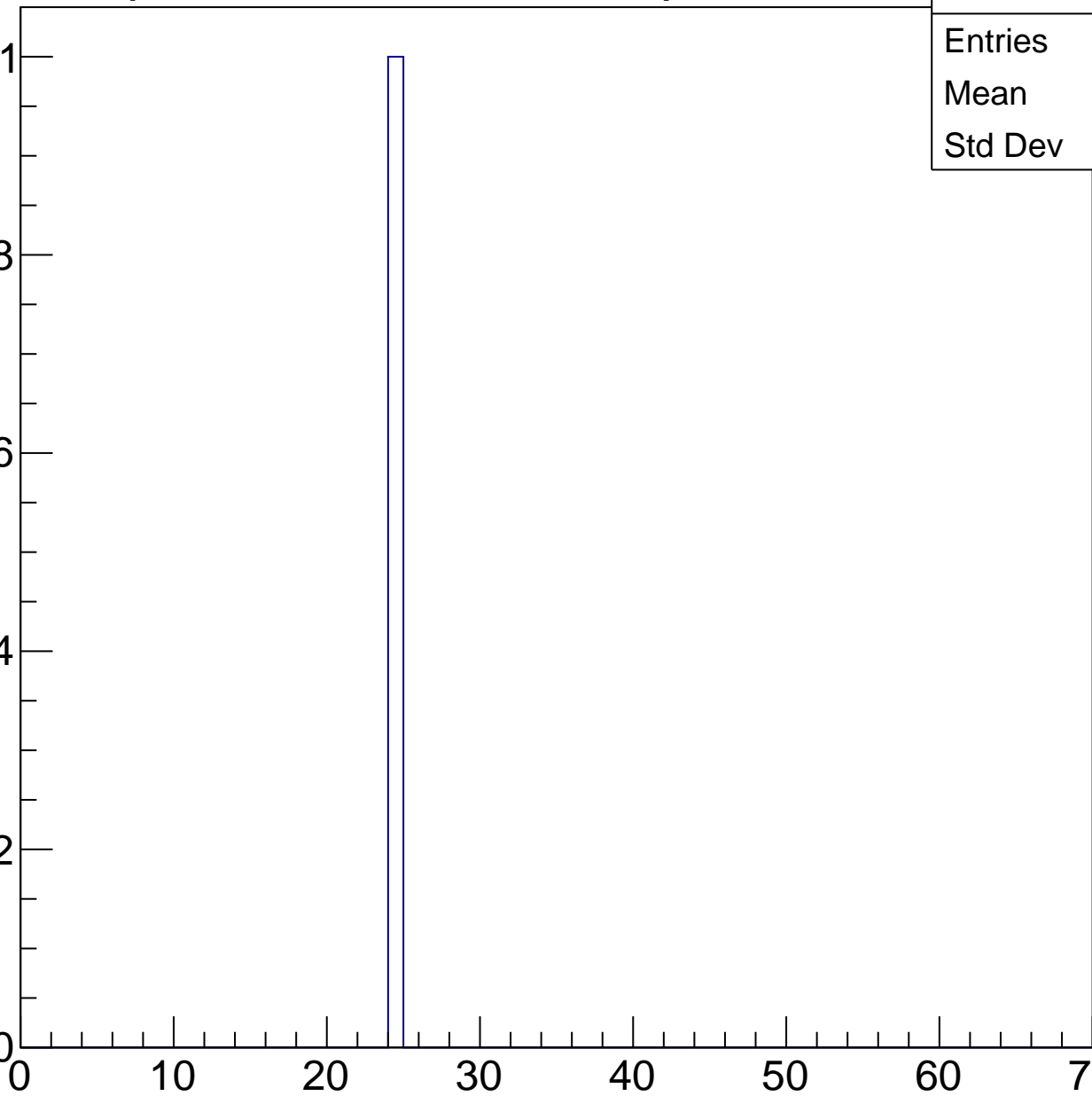
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L100S, U13-ch14, adc0

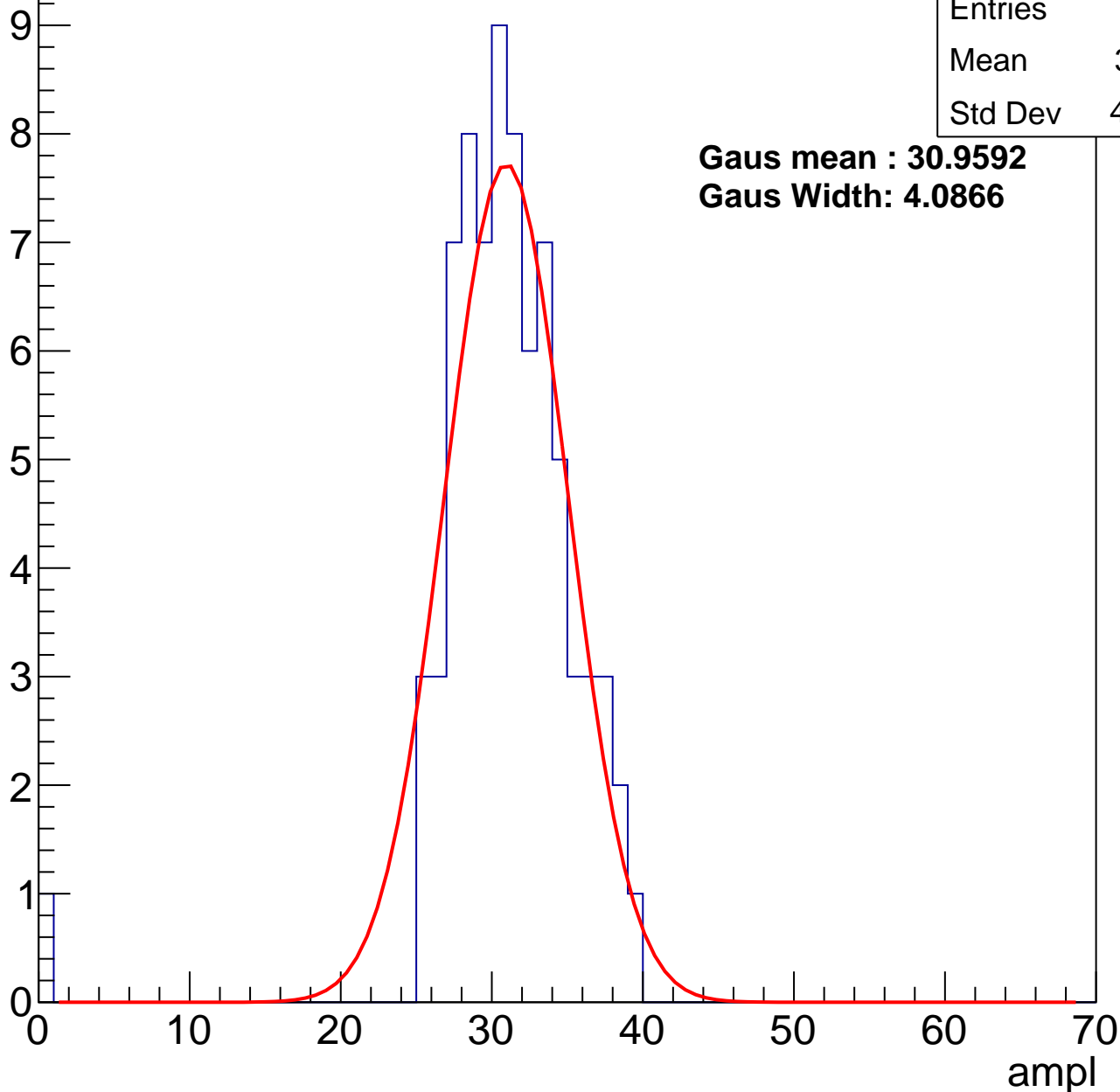
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.51
Std Dev	4.892

Gaus mean : 30.9592

Gaus Width: 4.0866



B1L100S, U13-ch14, adc1

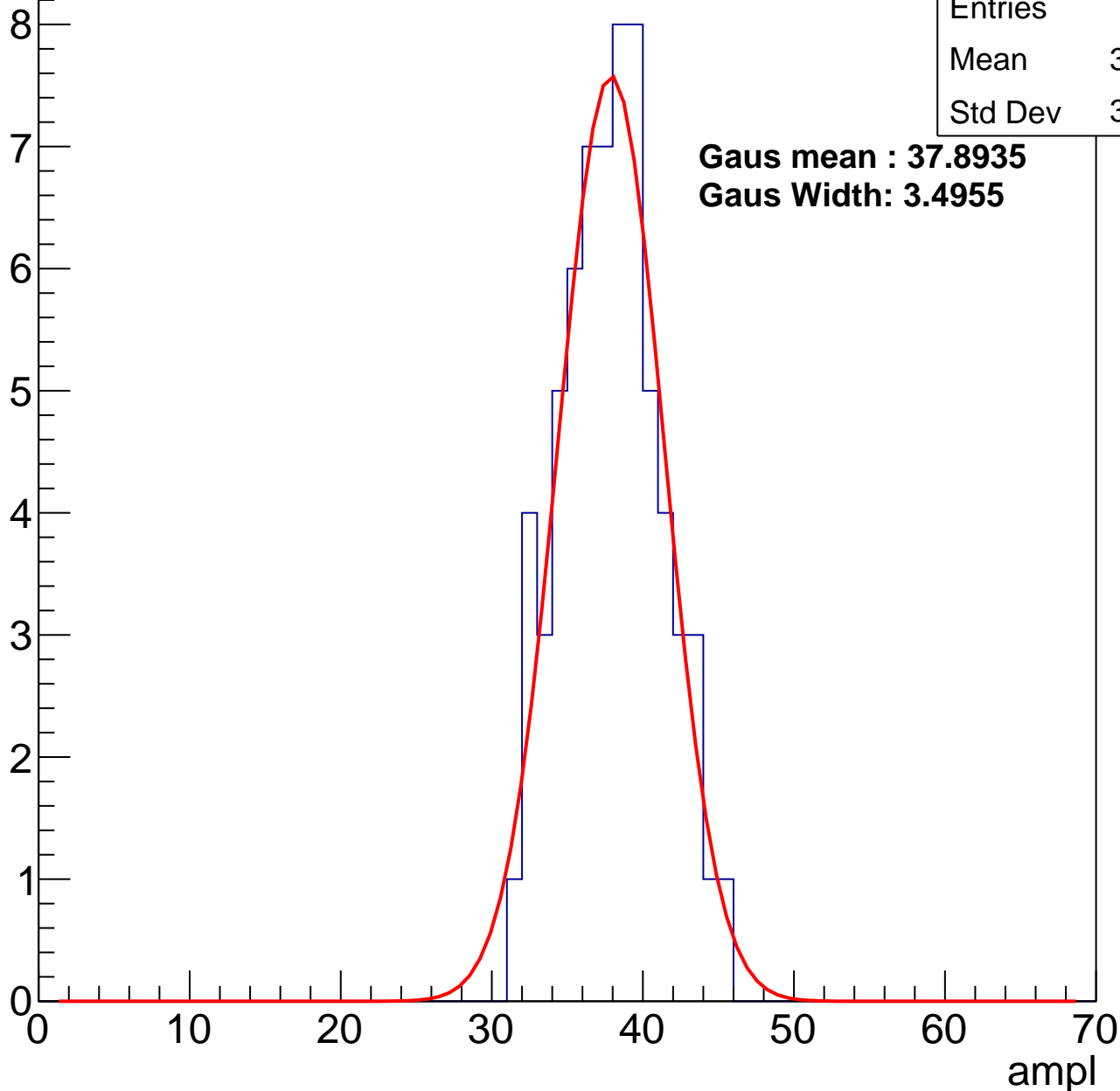
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.47
Std Dev	3.234

Gaus mean : 37.8935

Gaus Width: 3.4955



B1L100S, U13-ch14, adc2

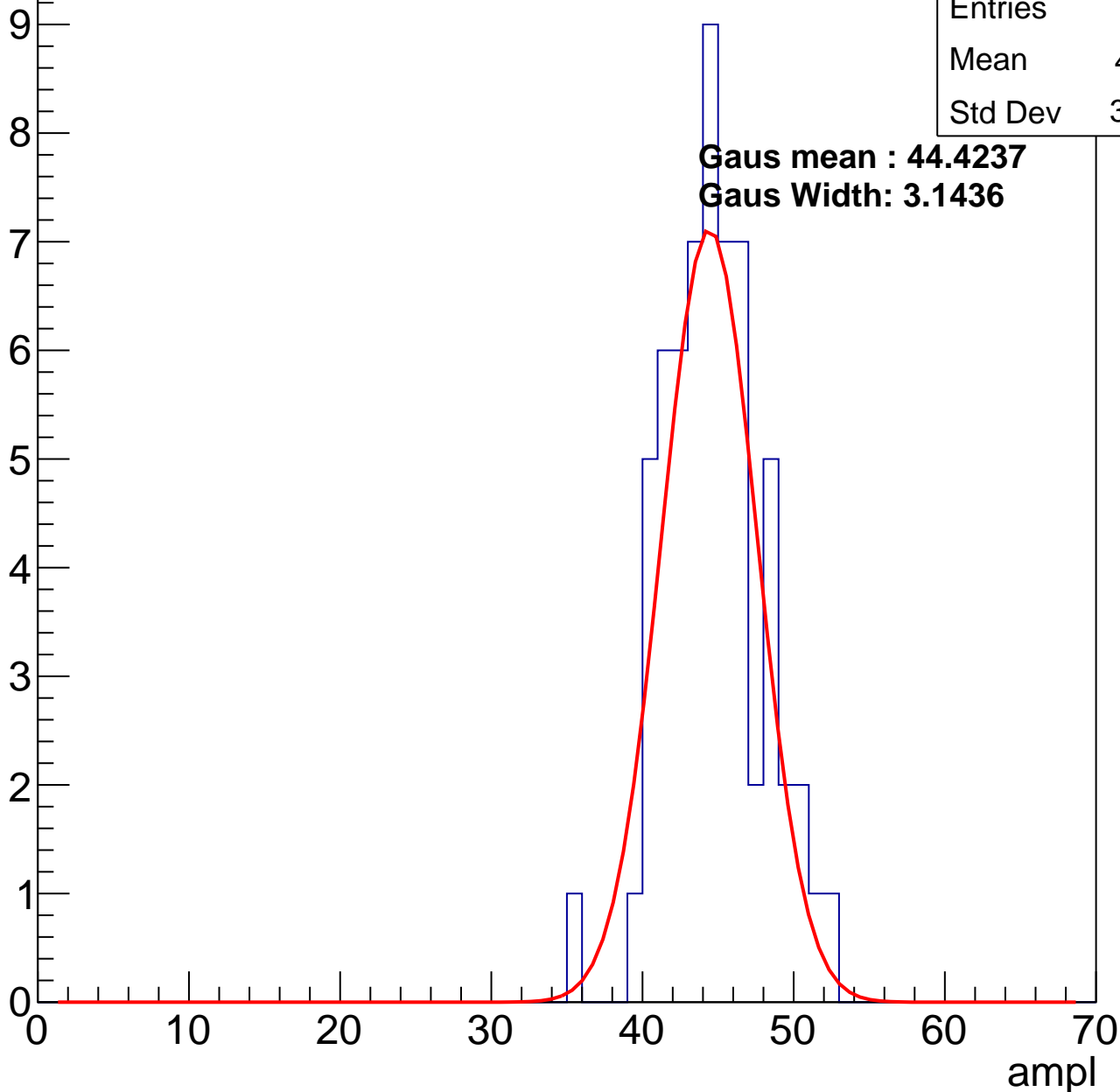
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.21
Std Dev	3.204

Gaus mean : 44.4237

Gaus Width: 3.1436

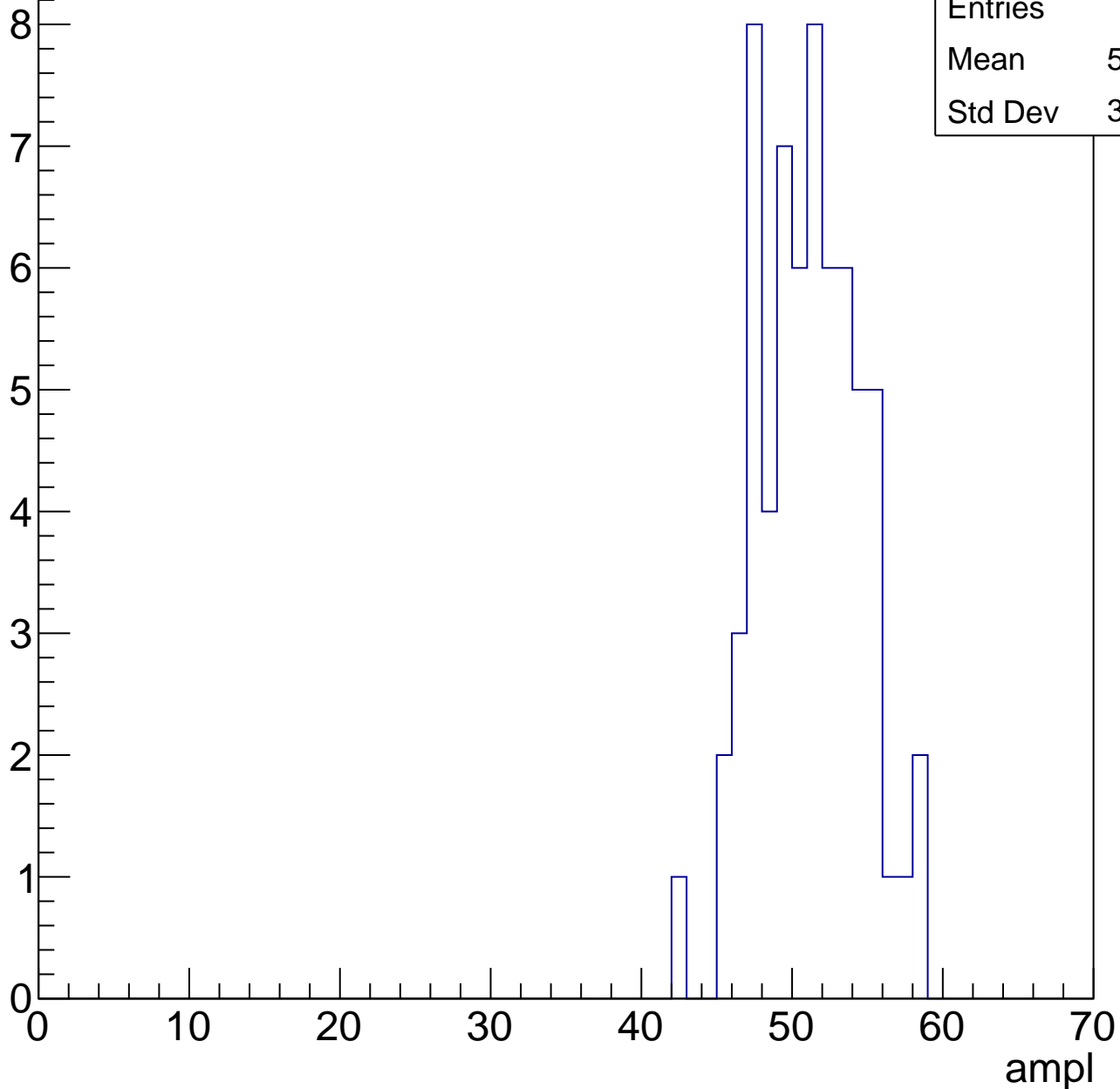


B1L100S, U13-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

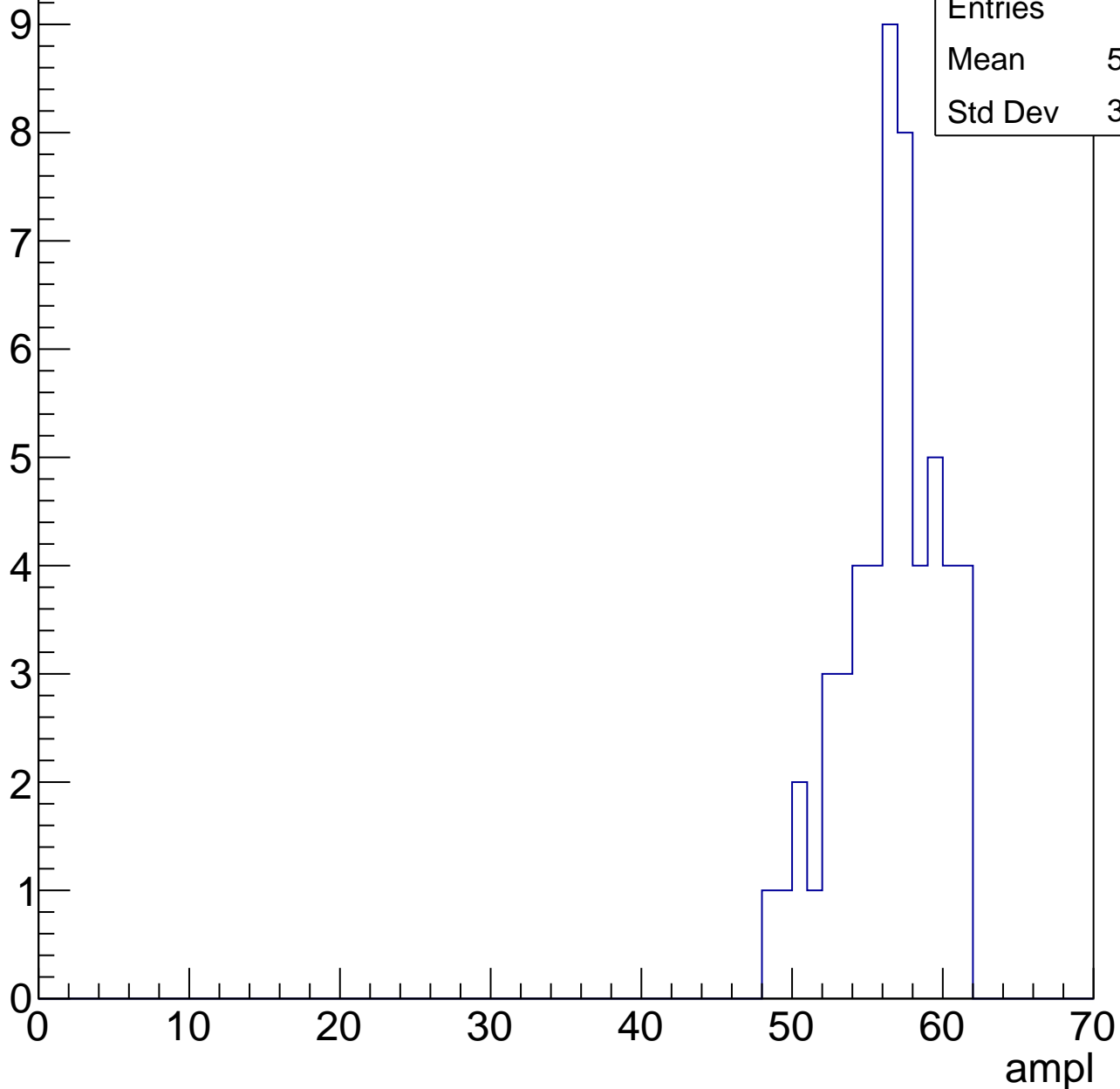
Entries	65
Mean	50.66
Std Dev	3.357



B1L100S, U13-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



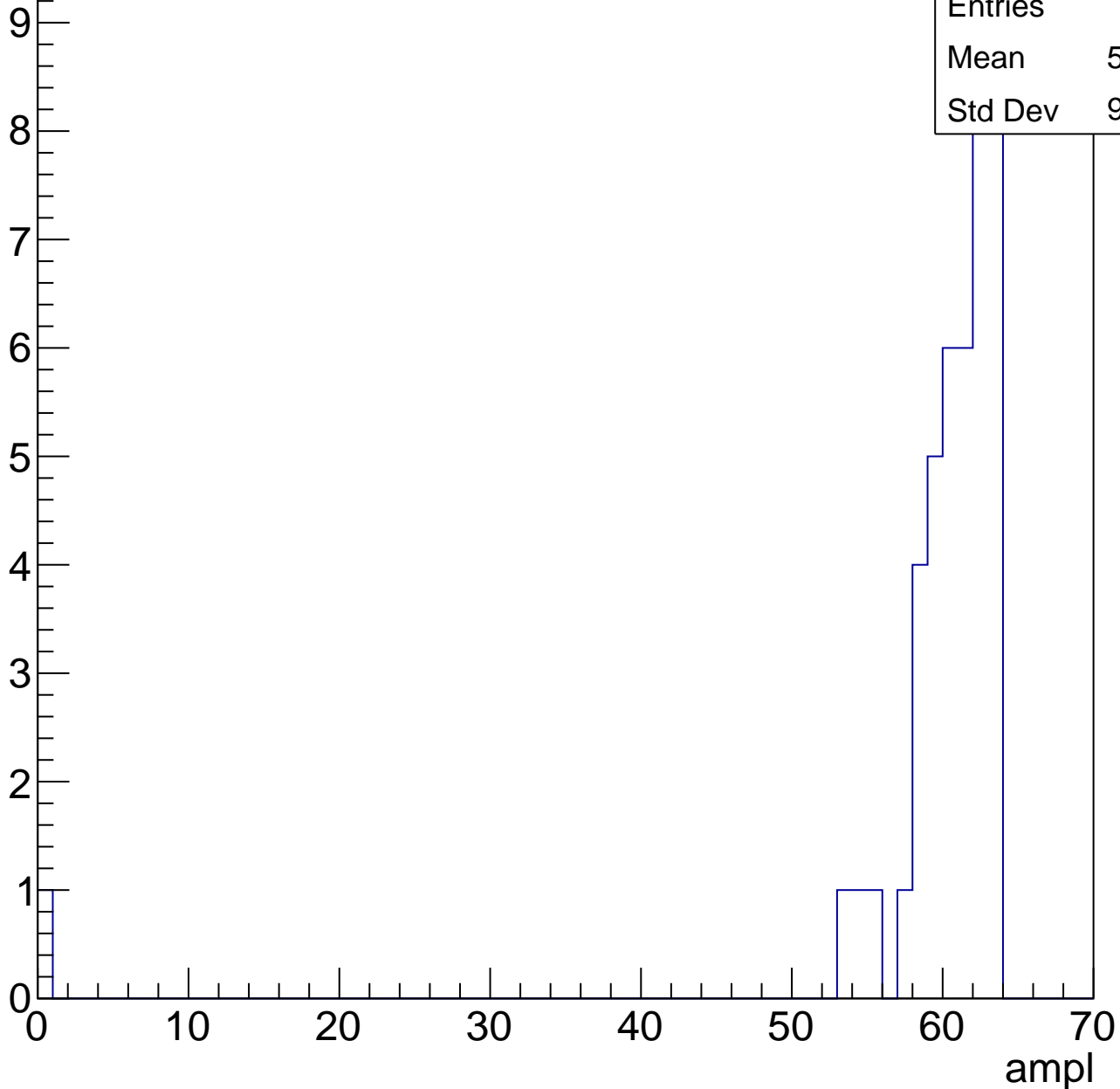
Entries	53
Mean	56.04
Std Dev	3.186

B1L100S, U13-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	59.02
Std Dev	9.319



B1L100S, U13-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch15, adc0

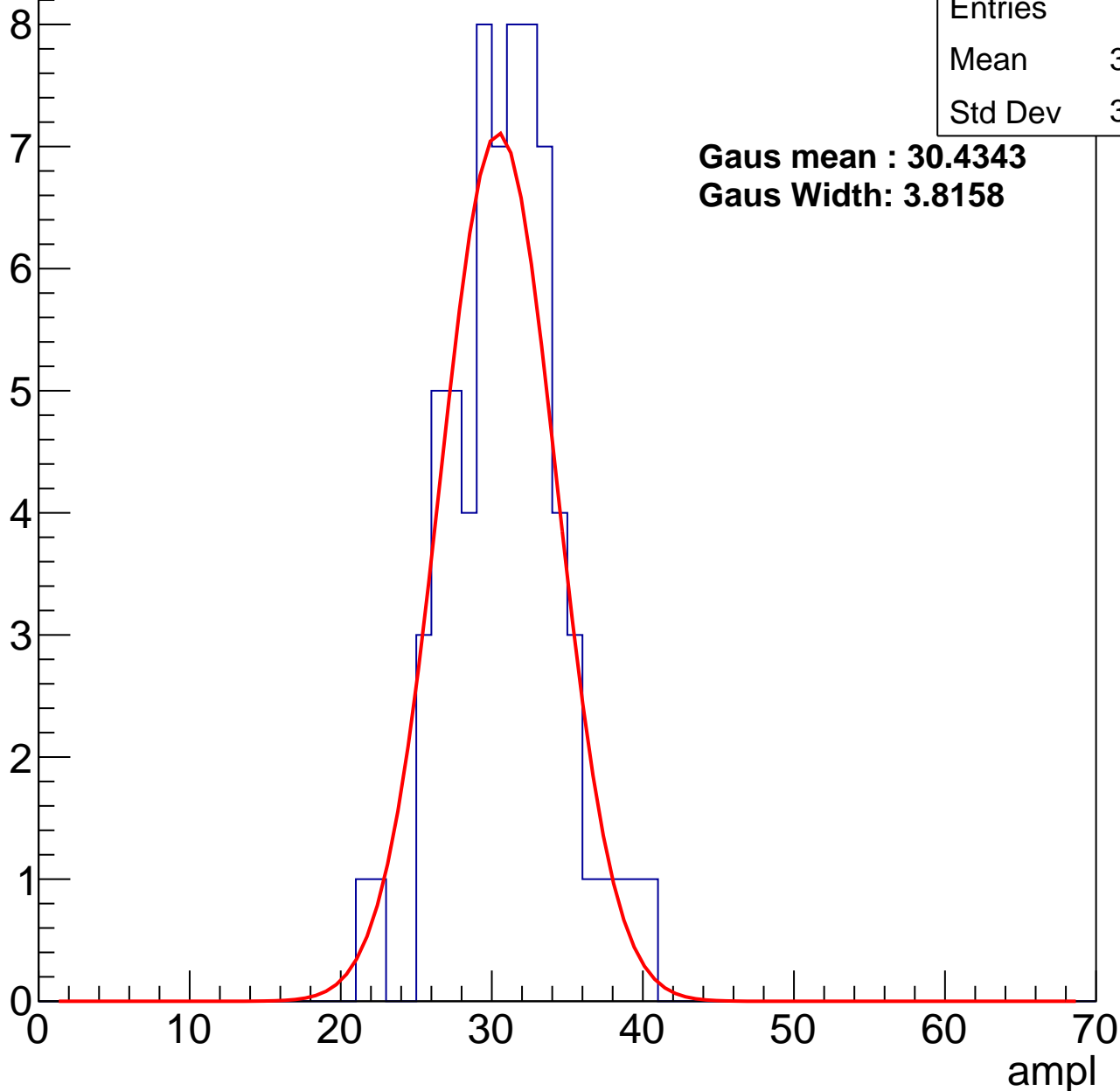
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.48
Std Dev	3.662

Gaus mean : 30.4343

Gaus Width: 3.8158



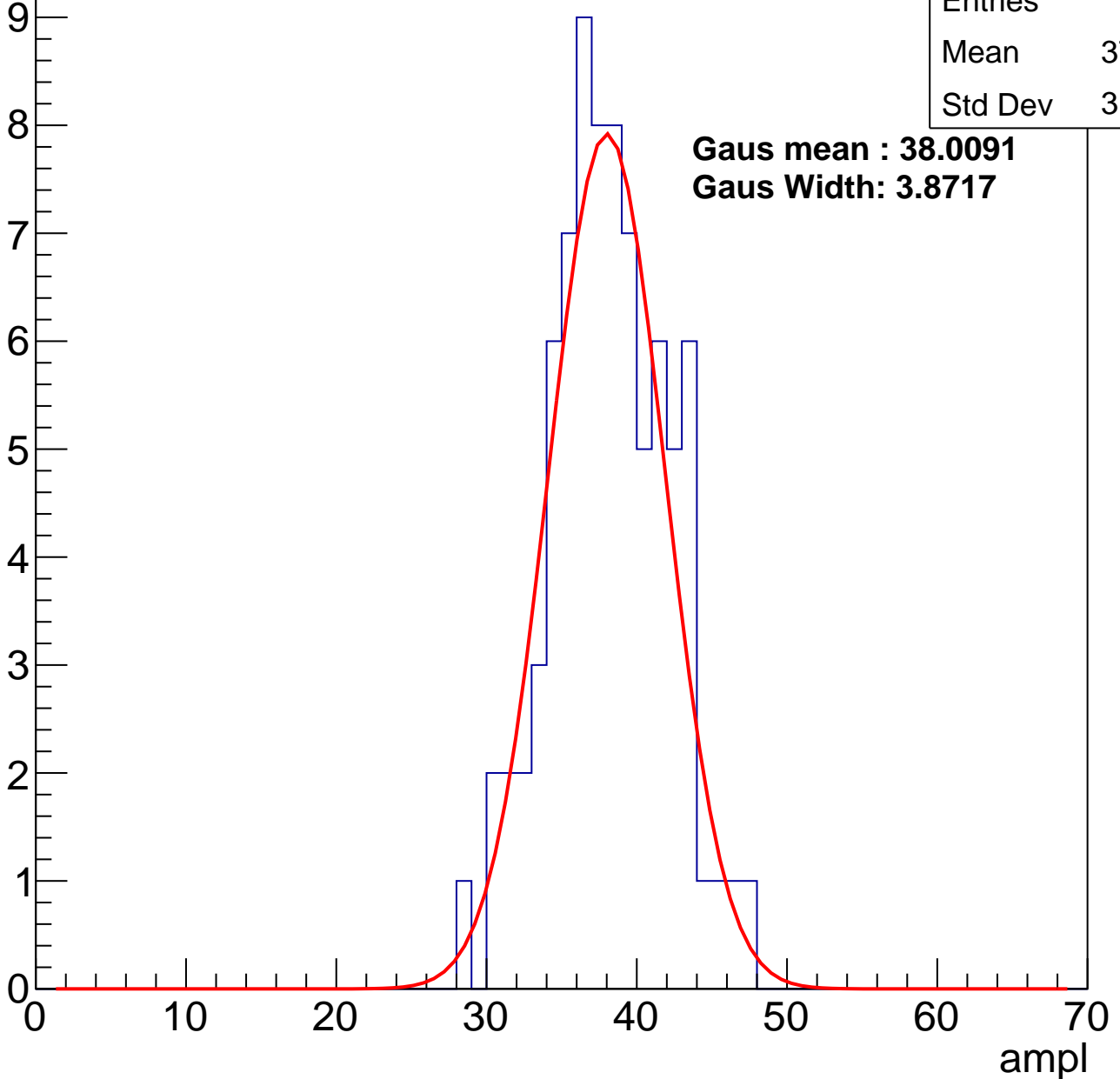
B1L100S, U13-ch15, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	37.72
Std Dev	3.866

Gaus mean : 38.0091
Gaus Width: 3.8717



B1L100S, U13-ch15, adc2

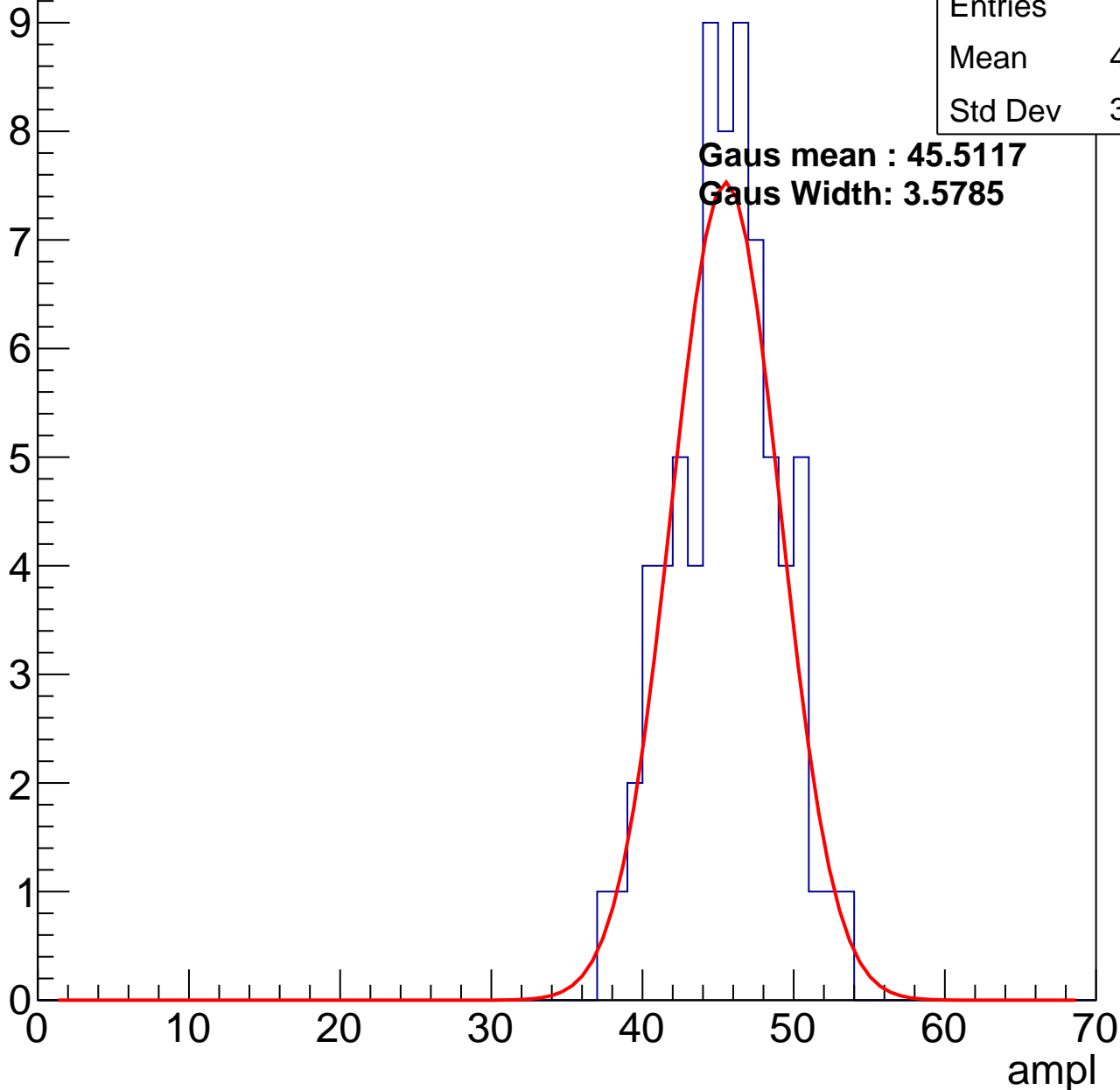
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	45.07
Std Dev	3.437

Gaus mean : 45.5117

Gaus Width: 3.5785

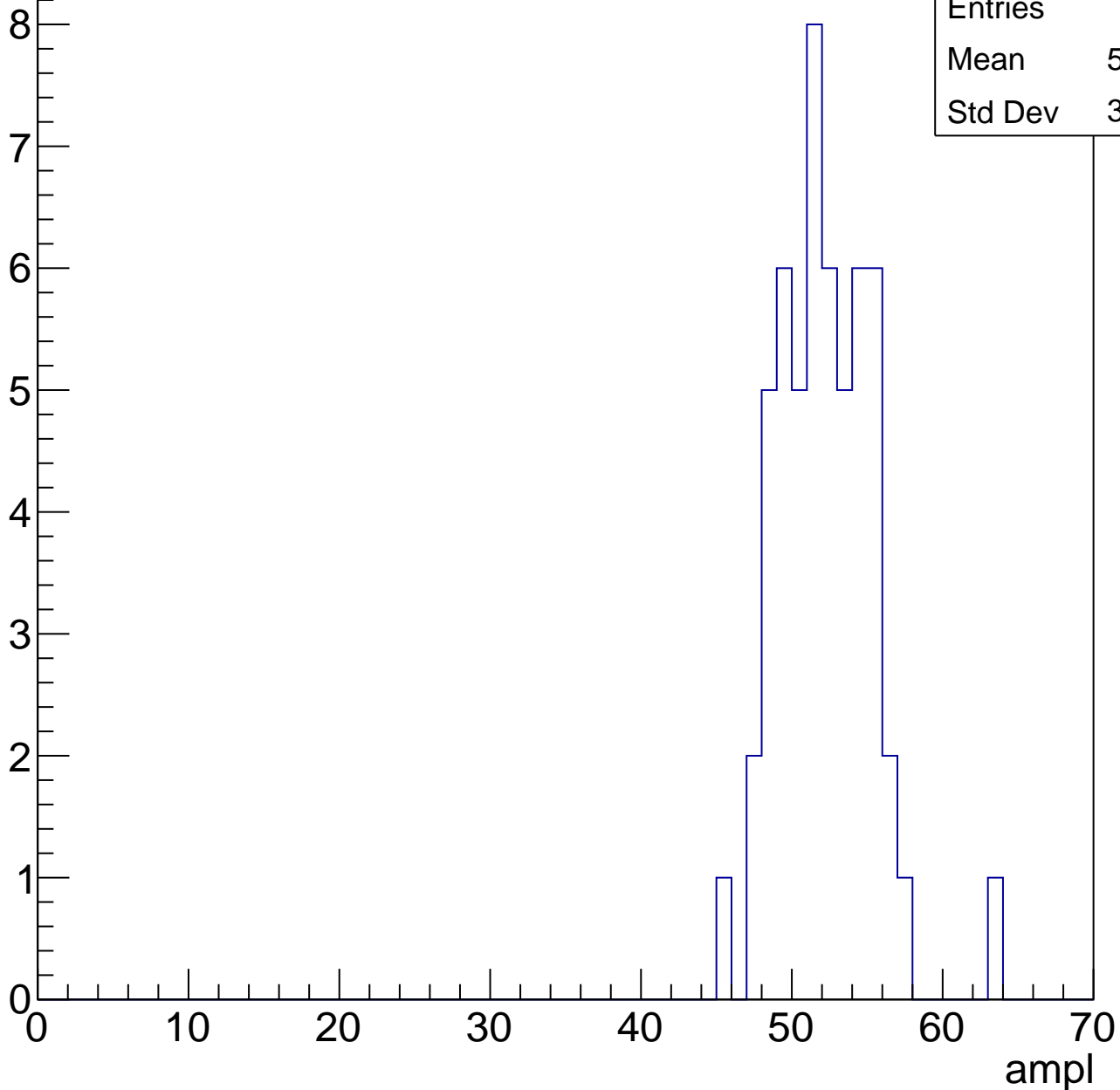


B1L100S, U13-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	51.74
Std Dev	3.098

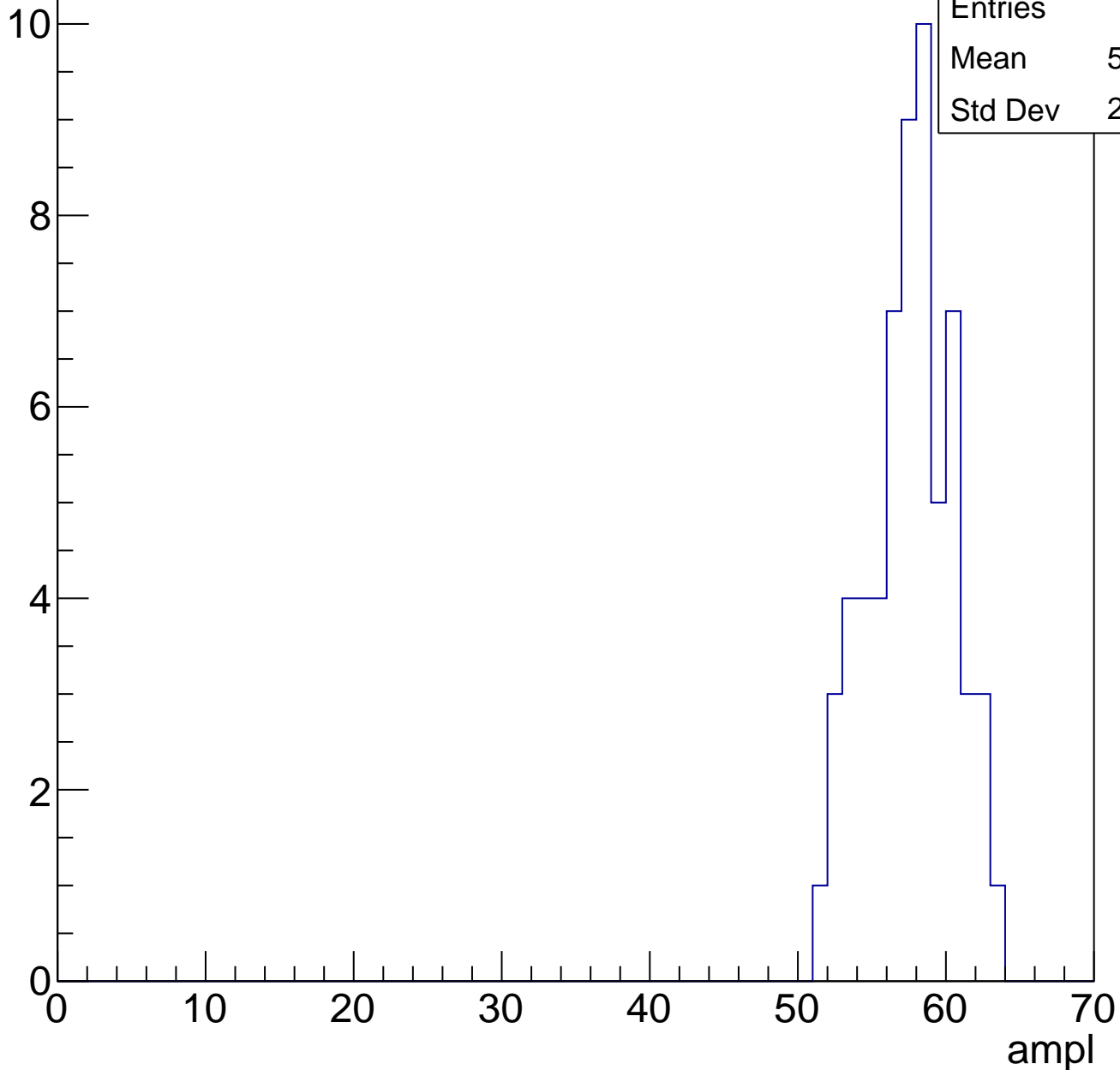


B1L100S, U13-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	57.16
Std Dev	2.818

Entry

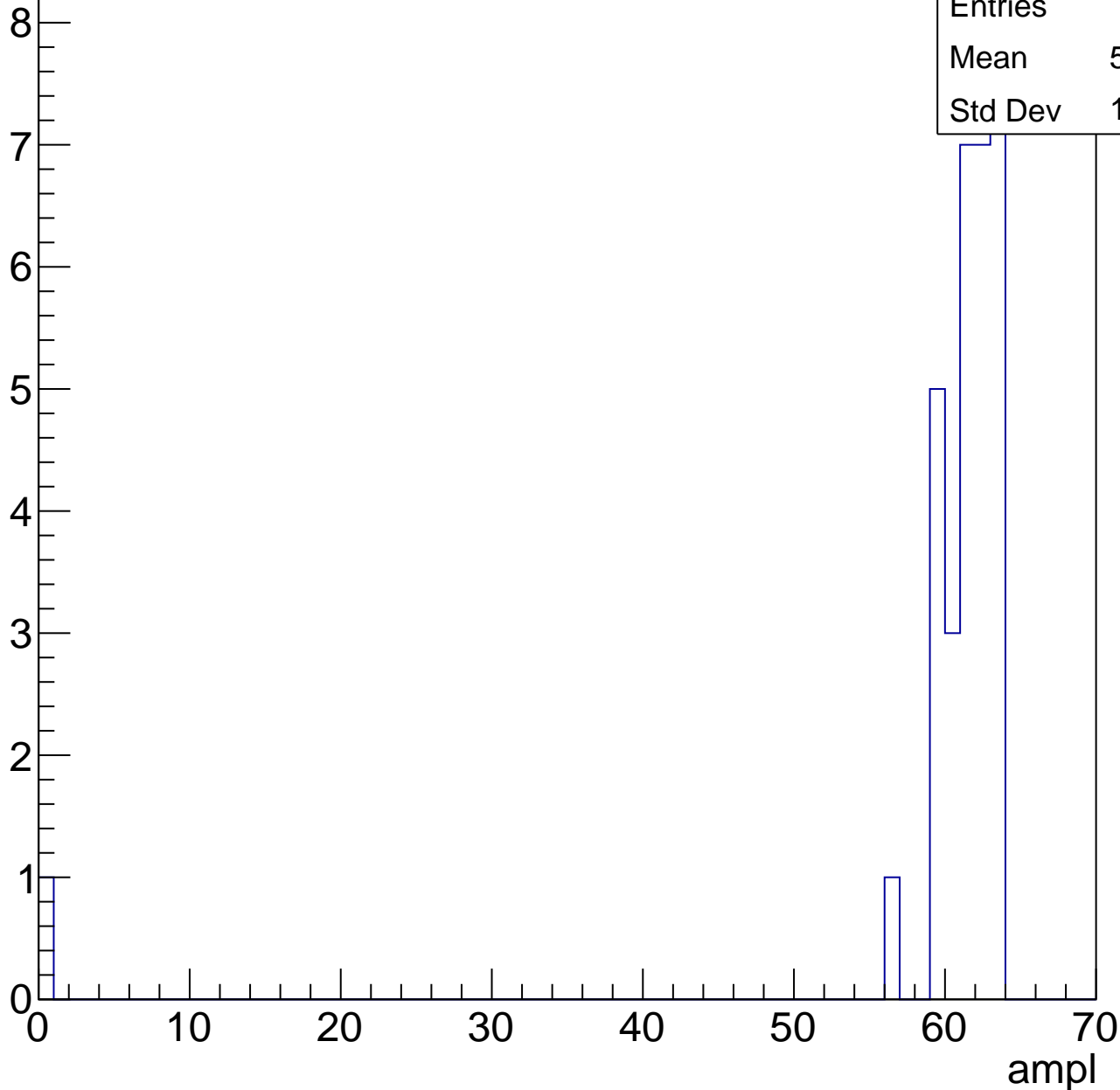


B1L100S, U13-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	59.25
Std Dev	10.77



B1L100S, U13-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch16, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	29.36
Std Dev	4.82

Gaus mean : 30.2671

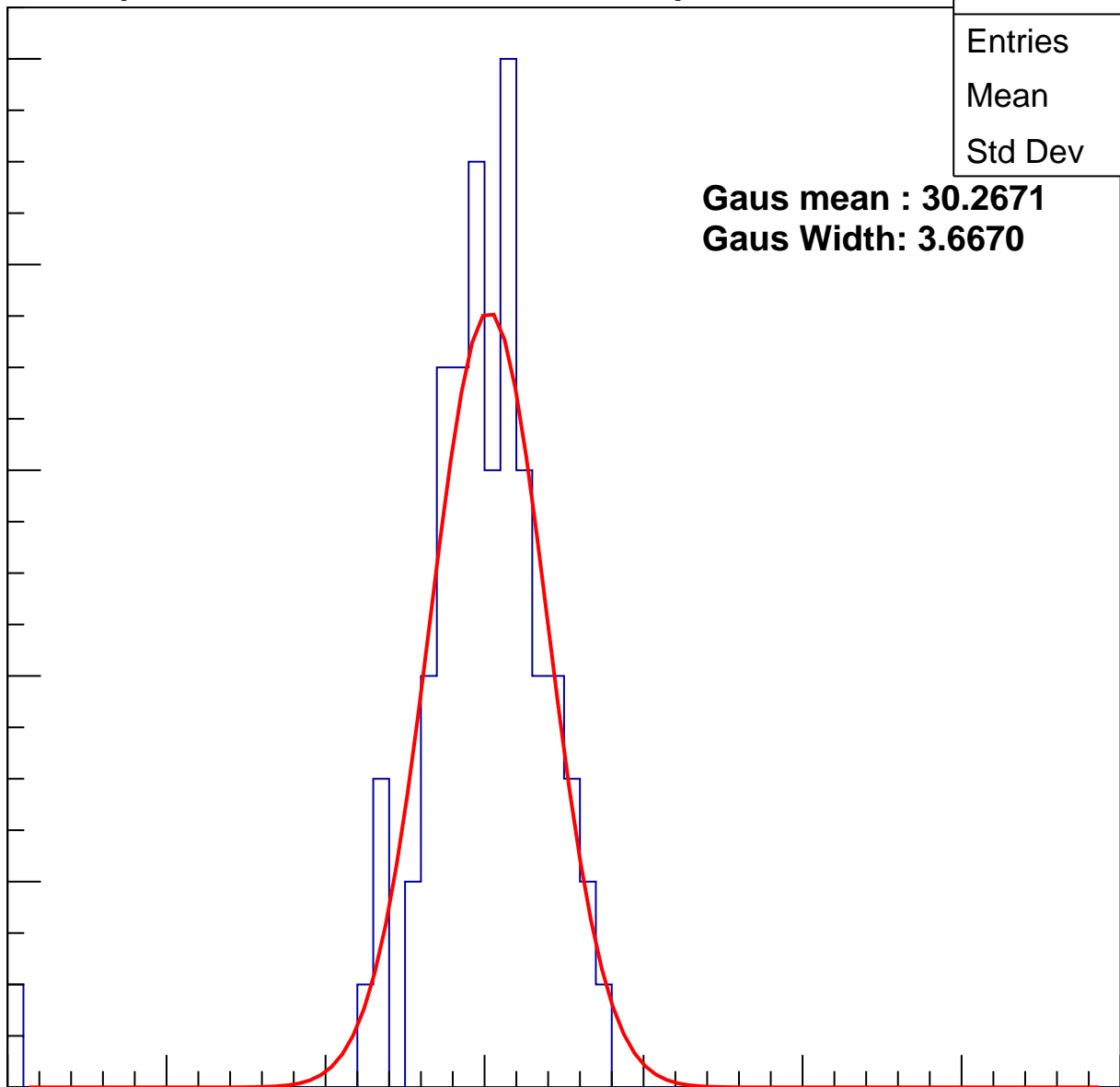
Gaus Width: 3.6670

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch16, adc1

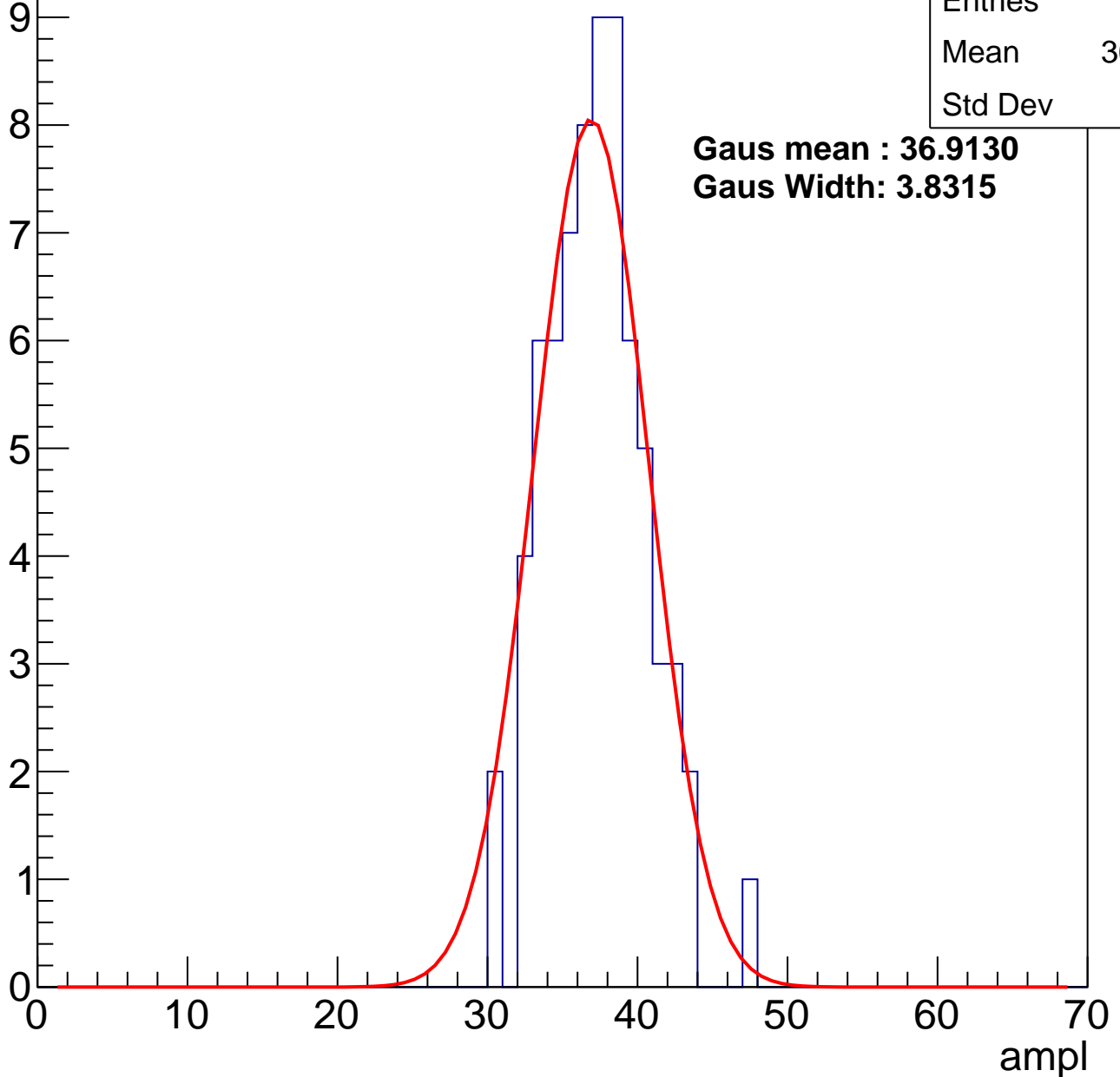
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.82
Std Dev	3.26

Gaus mean : 36.9130

Gaus Width: 3.8315



B1L100S, U13-ch16, adc2

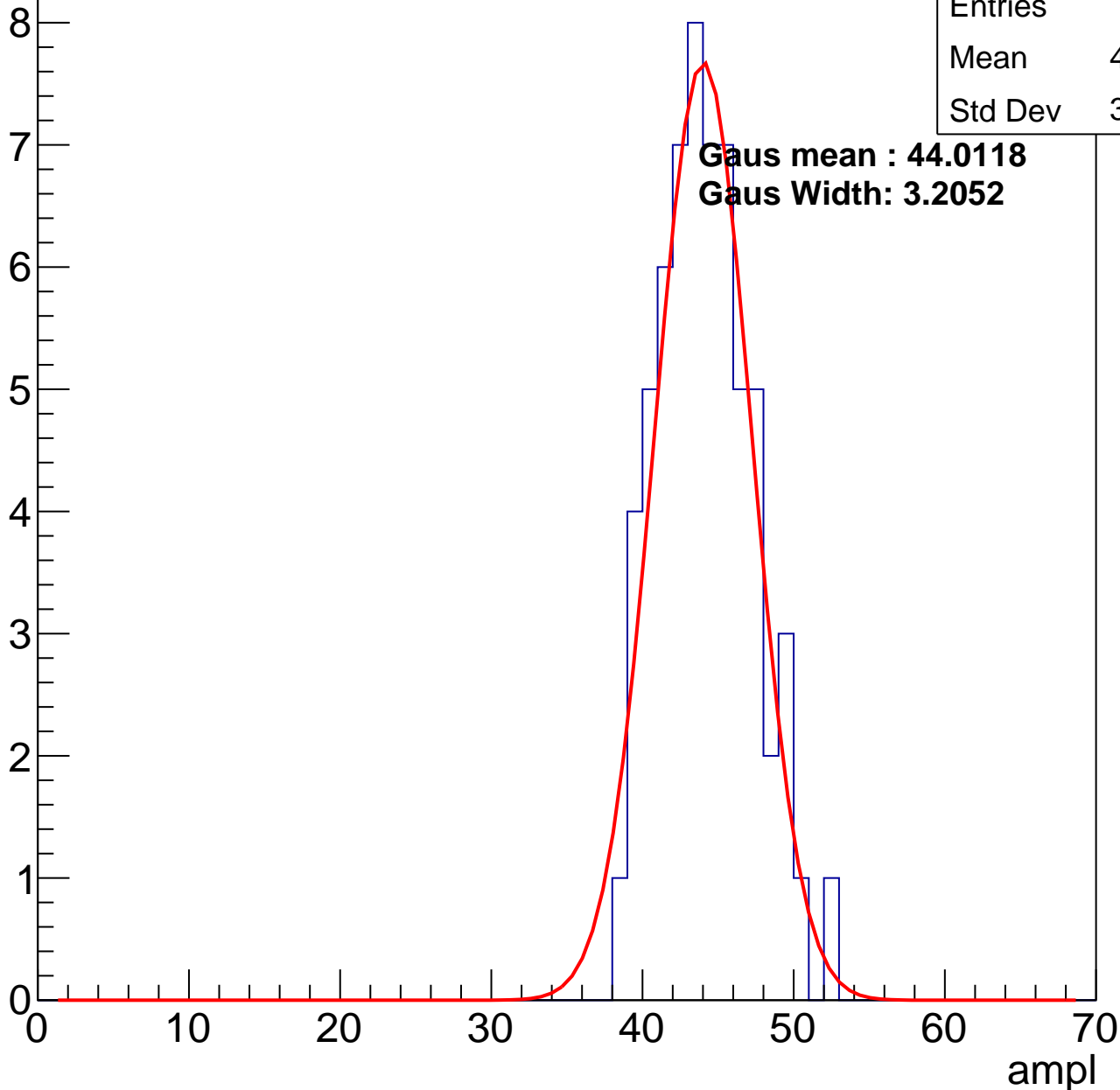
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.73
Std Dev	3.059

Gaus mean : 44.0118

Gaus Width: 3.2052

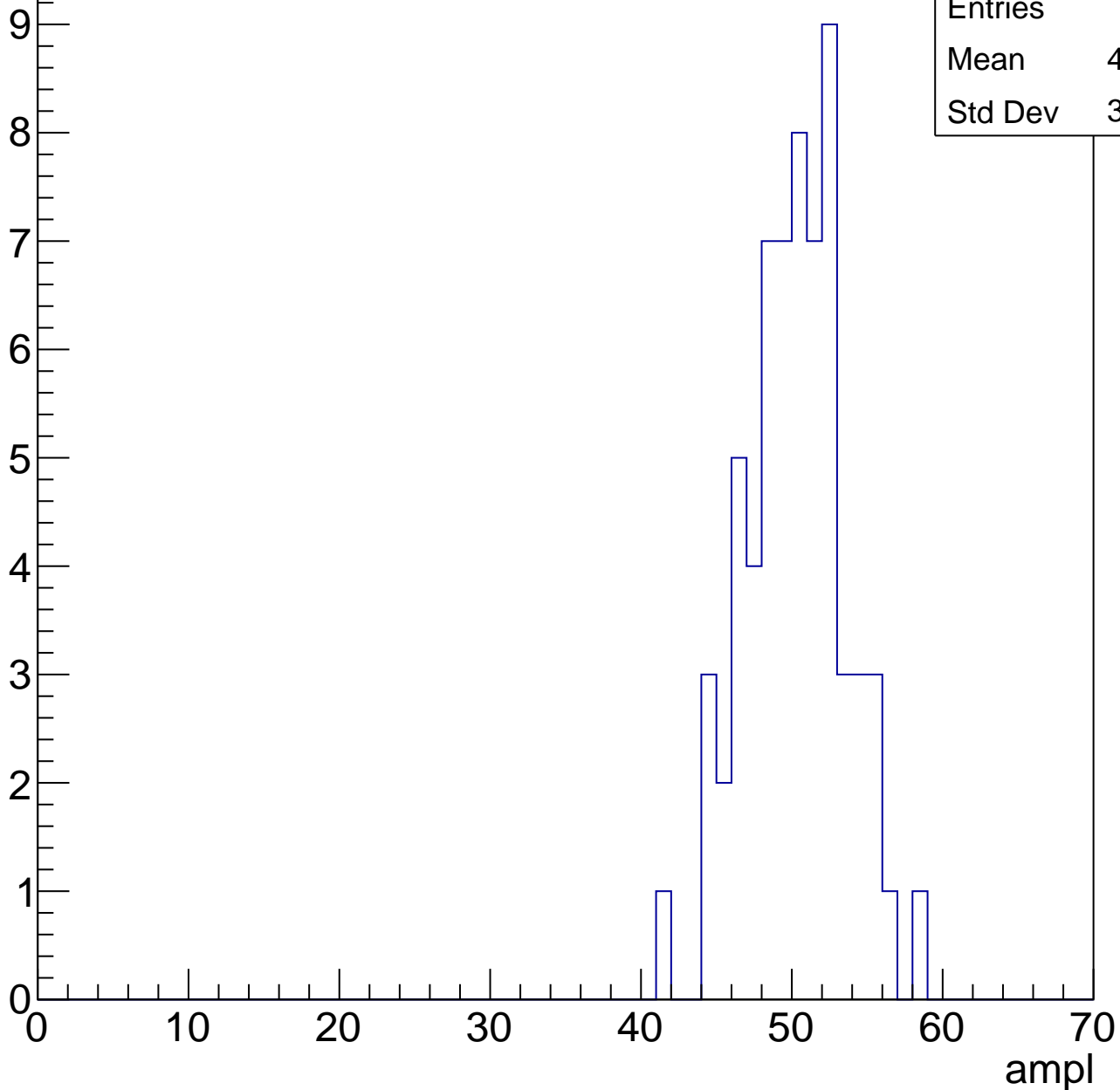


B1L100S, U13-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.77
Std Dev	3.258

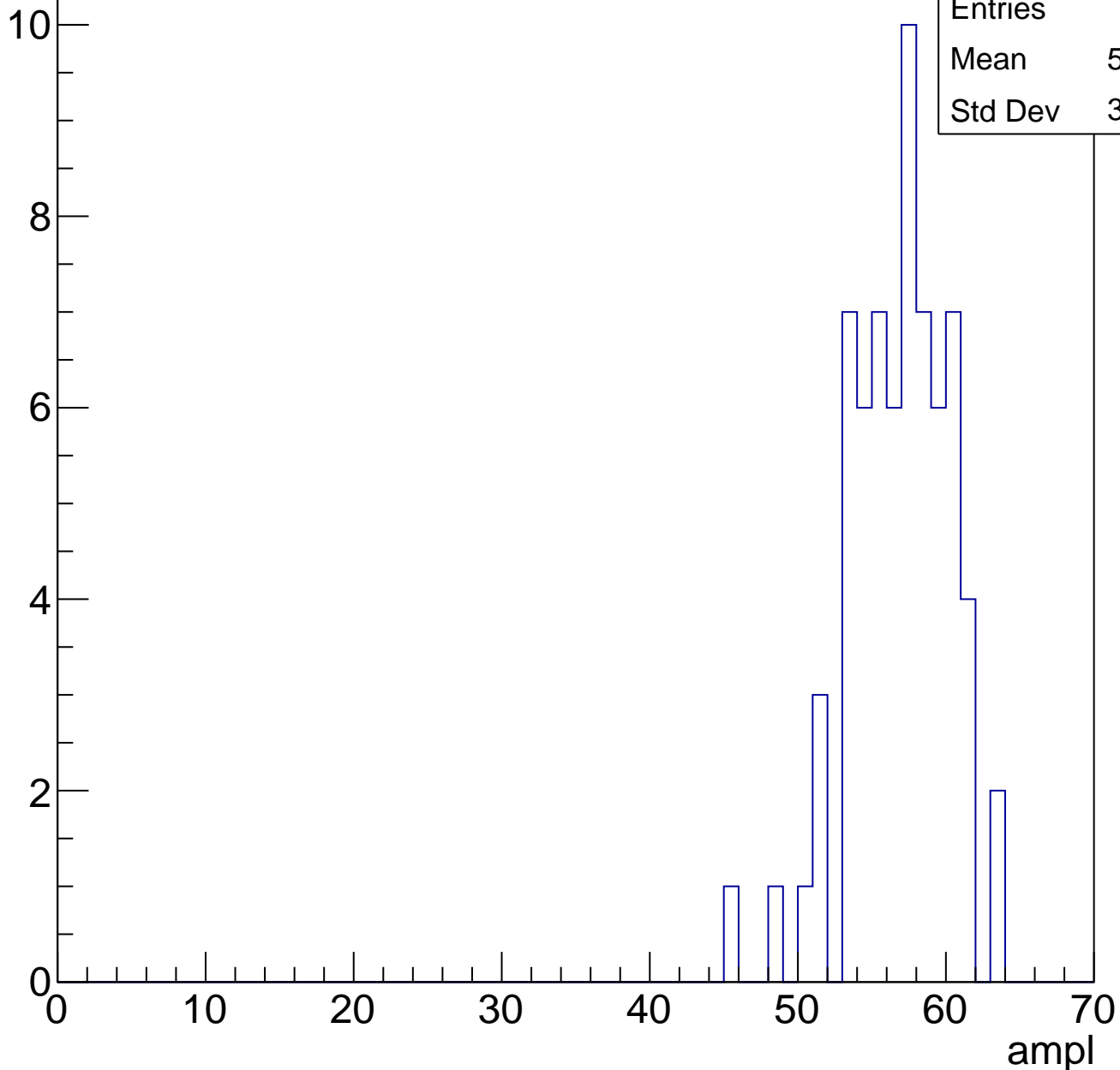


B1L100S, U13-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	56.35
Std Dev	3.394

Entry

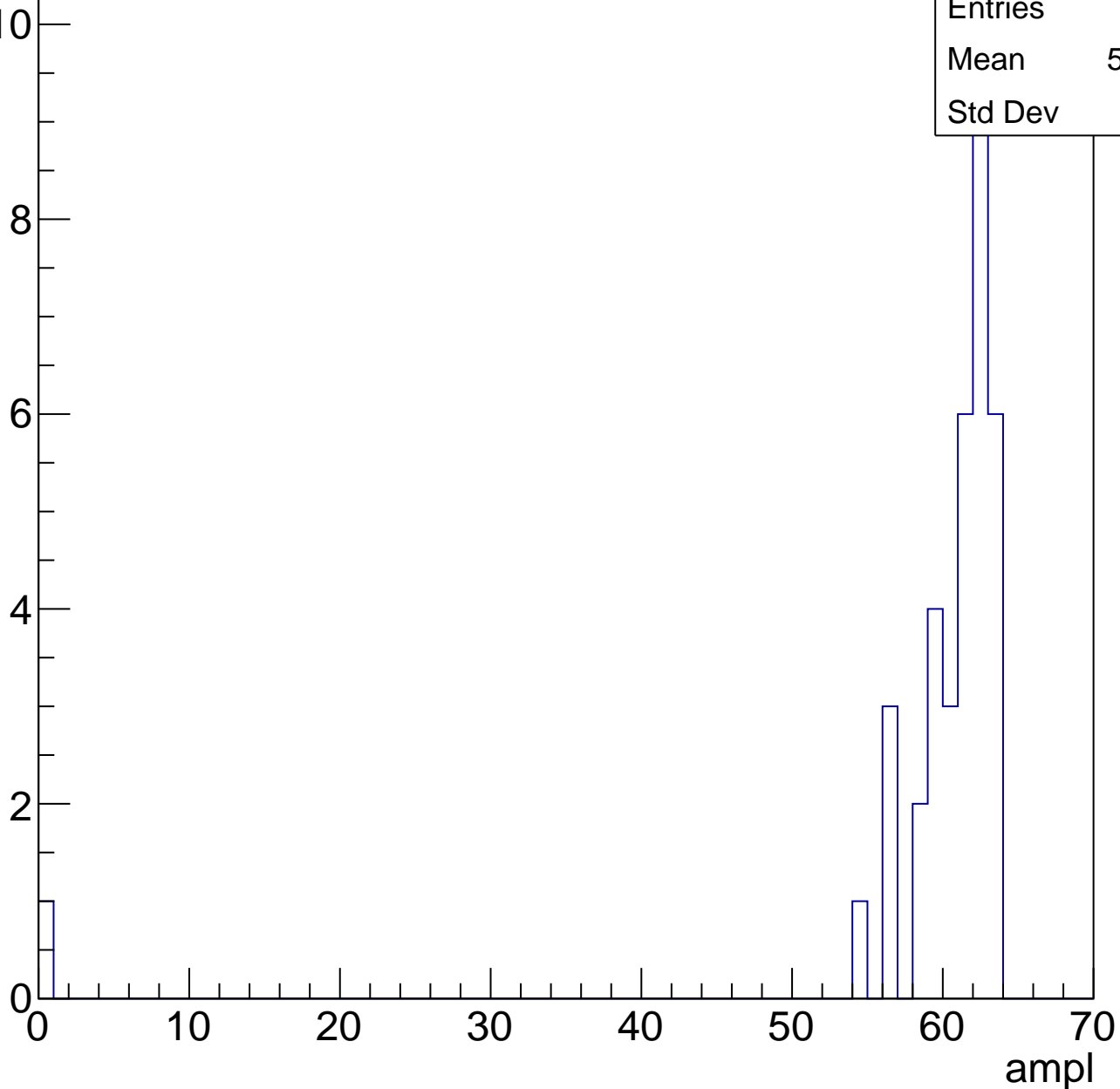


B1L100S, U13-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.83
Std Dev	10.2



B1L100S, U13-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U13-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch17, adc0

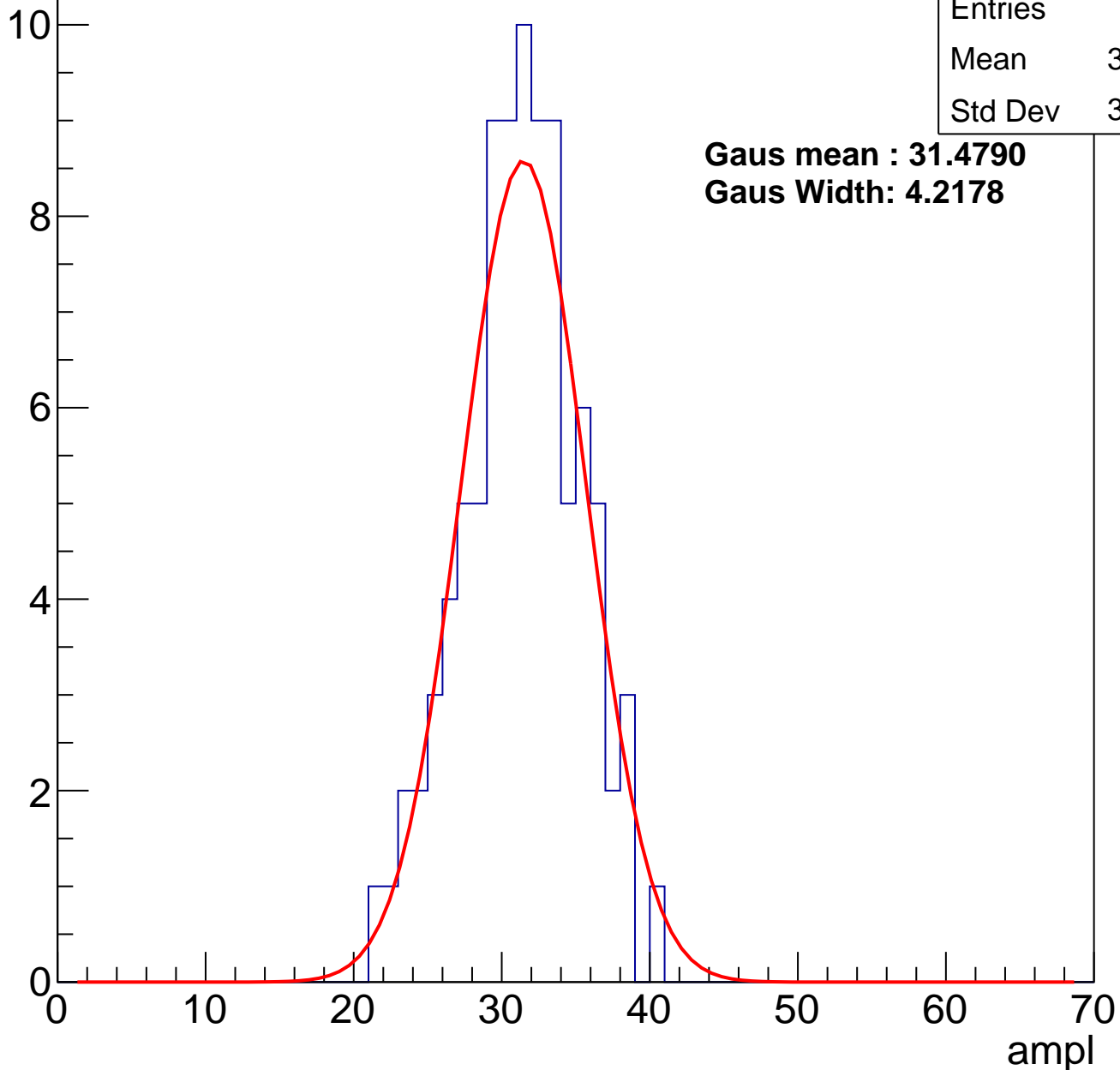
calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	30.82
Std Dev	3.913

Gaus mean : 31.4790

Gaus Width: 4.2178

Entry



B1L100S, U13-ch17, adc1

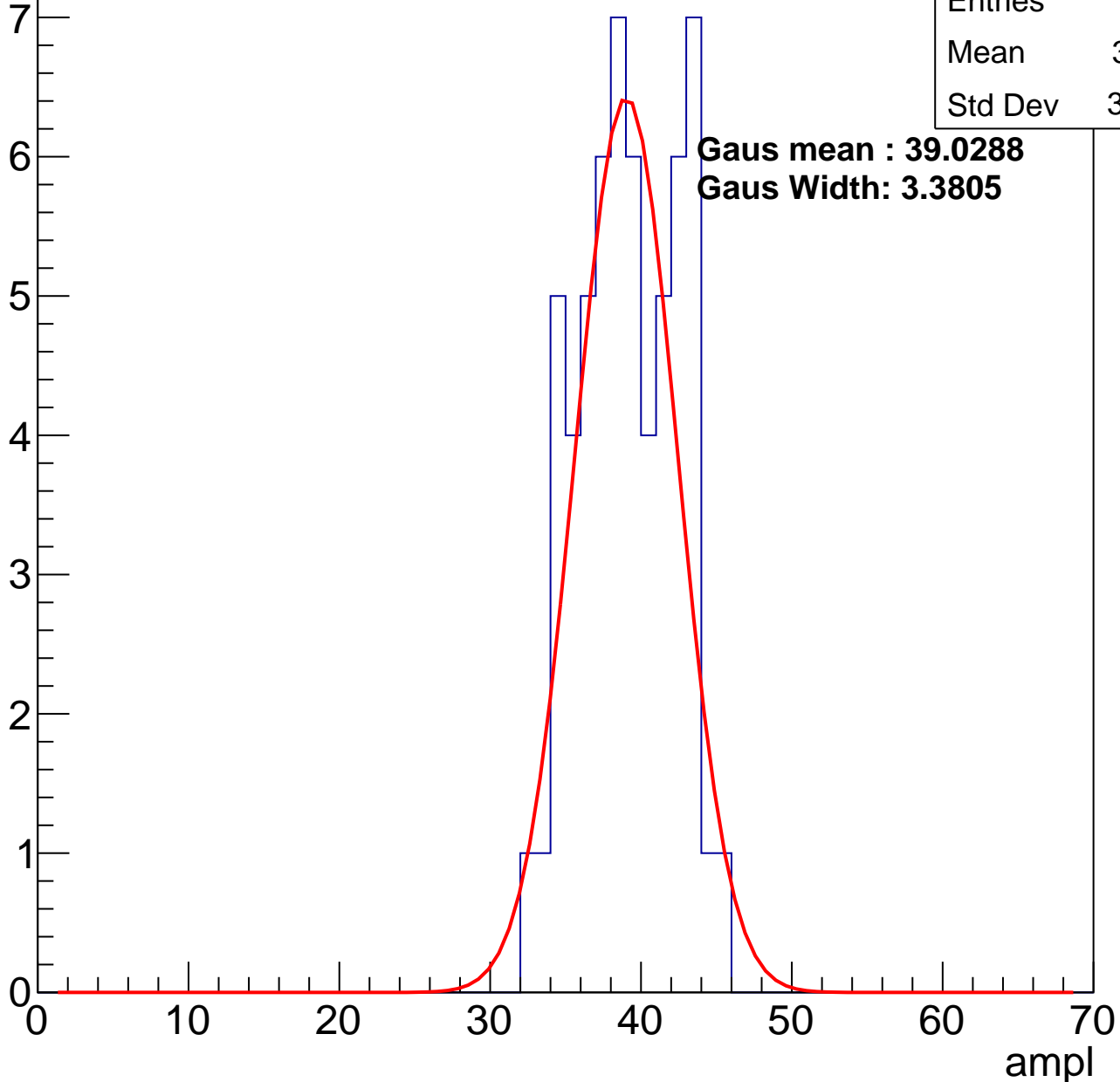
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	38.71
Std Dev	3.179

Gaus mean : 39.0288

Gaus Width: 3.3805



B1L100S, U13-ch17, adc2

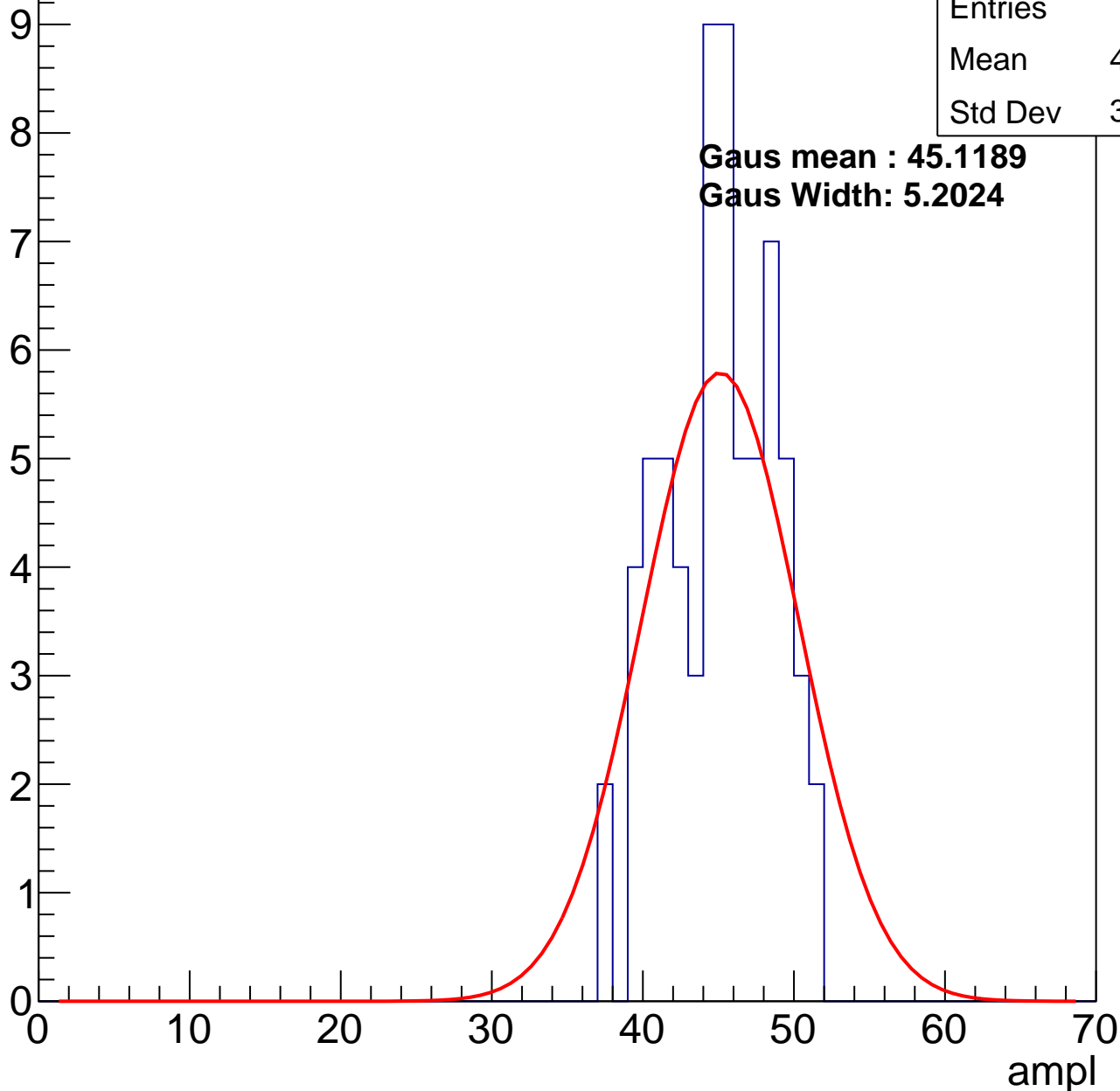
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.57
Std Dev	3.508

Gaus mean : 45.1189

Gaus Width: 5.2024

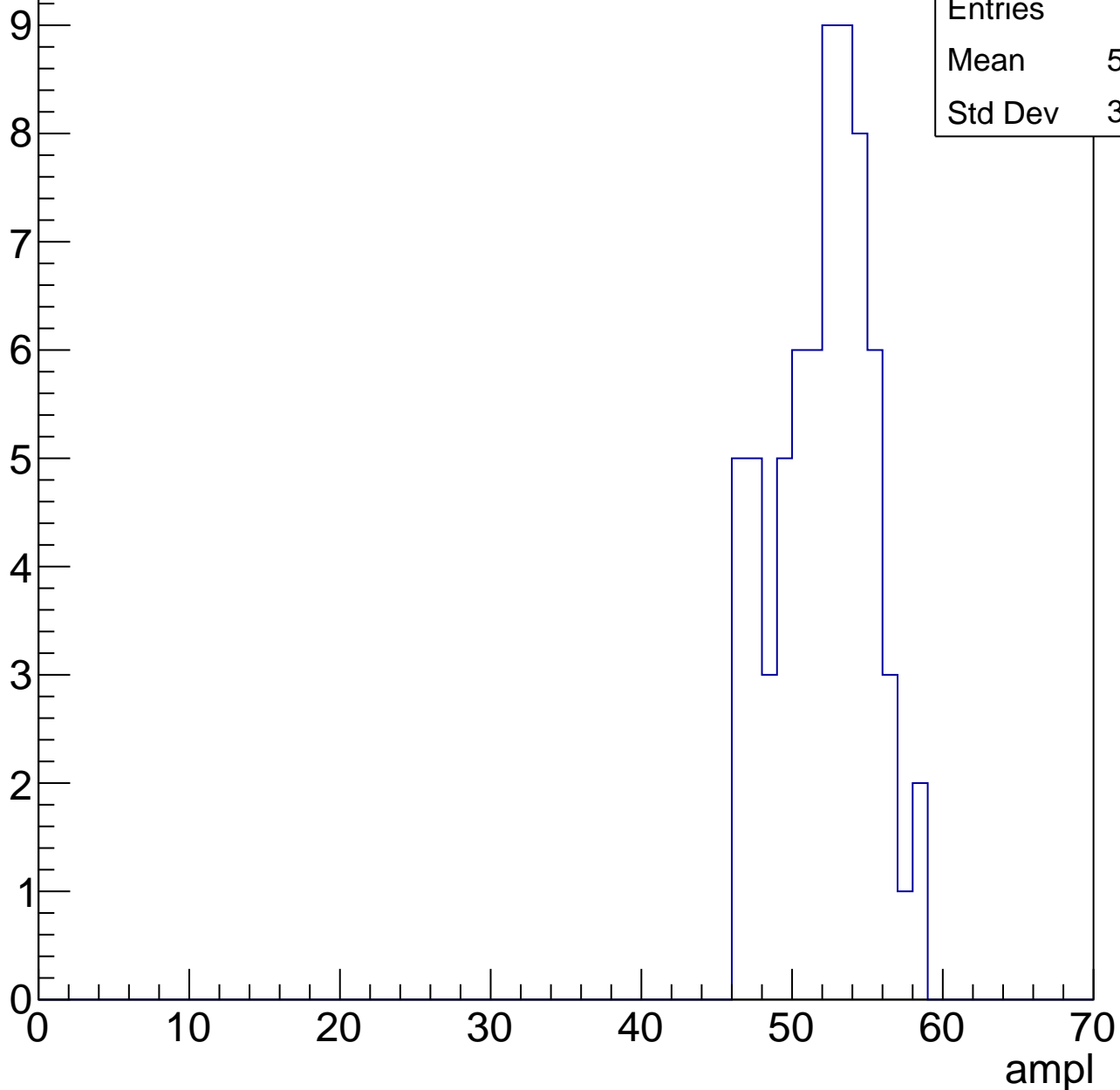


B1L100S, U13-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	51.59
Std Dev	3.107



B1L100S, U13-ch17, adc4

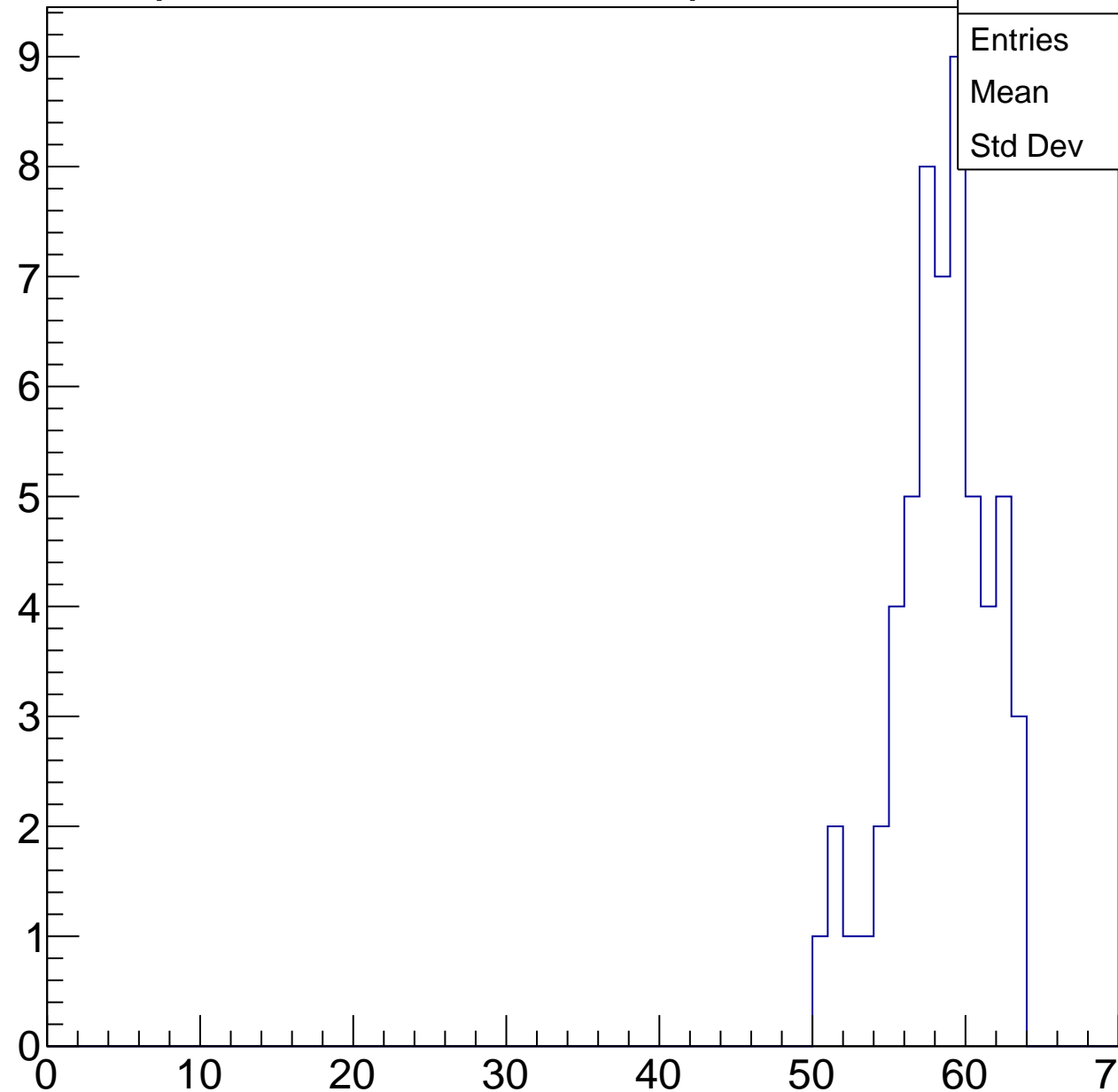
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	57.91
Std Dev	3.074

ampl

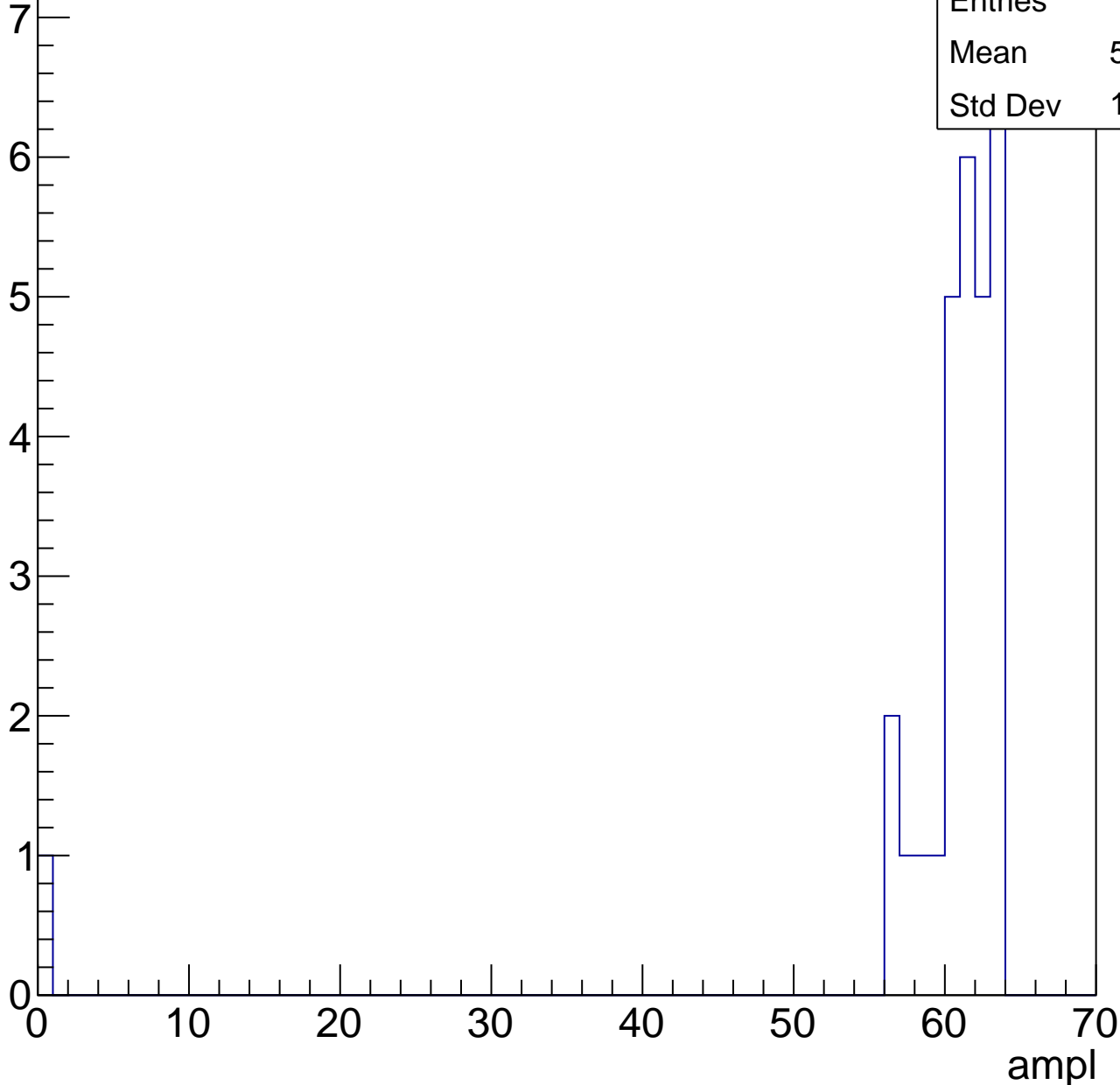


B1L100S, U13-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.72
Std Dev	11.28



B1L100S, U13-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch18, adc0

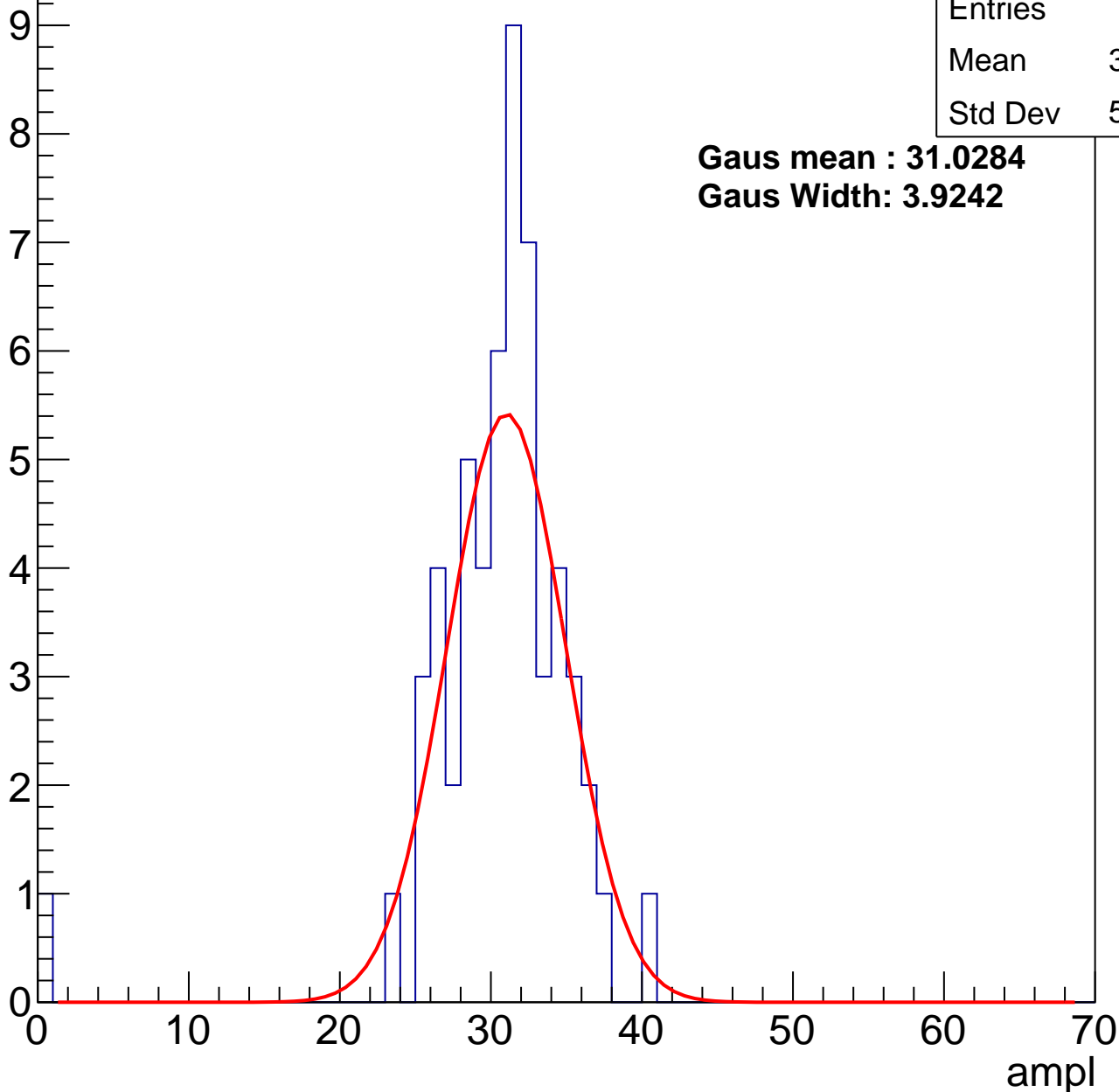
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.07
Std Dev	5.267

Gaus mean : 31.0284

Gaus Width: 3.9242



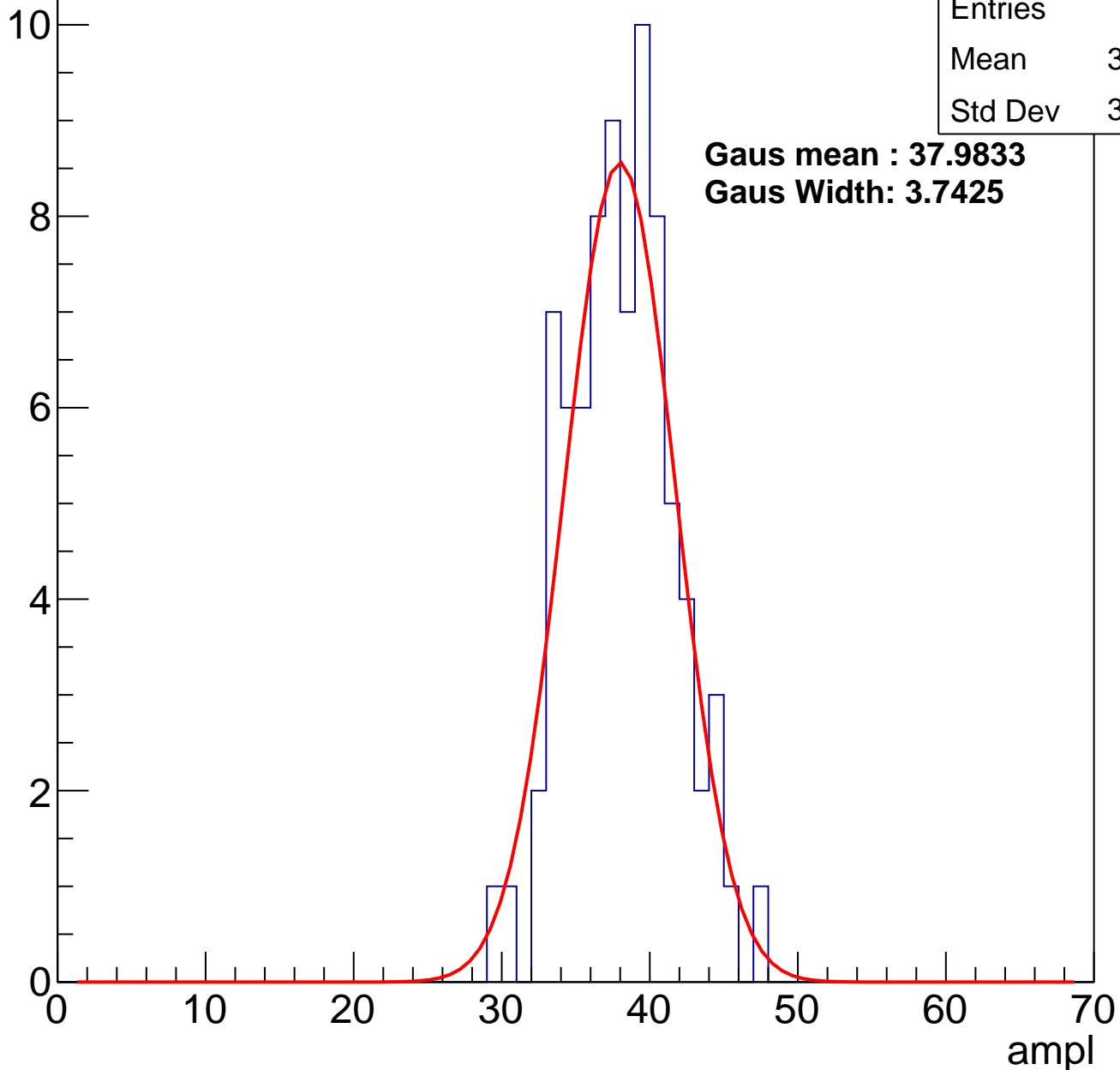
B1L100S, U13-ch18, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	37.63
Std Dev	3.533

Gaus mean : 37.9833
Gaus Width: 3.7425

Entry



B1L100S, U13-ch18, adc2

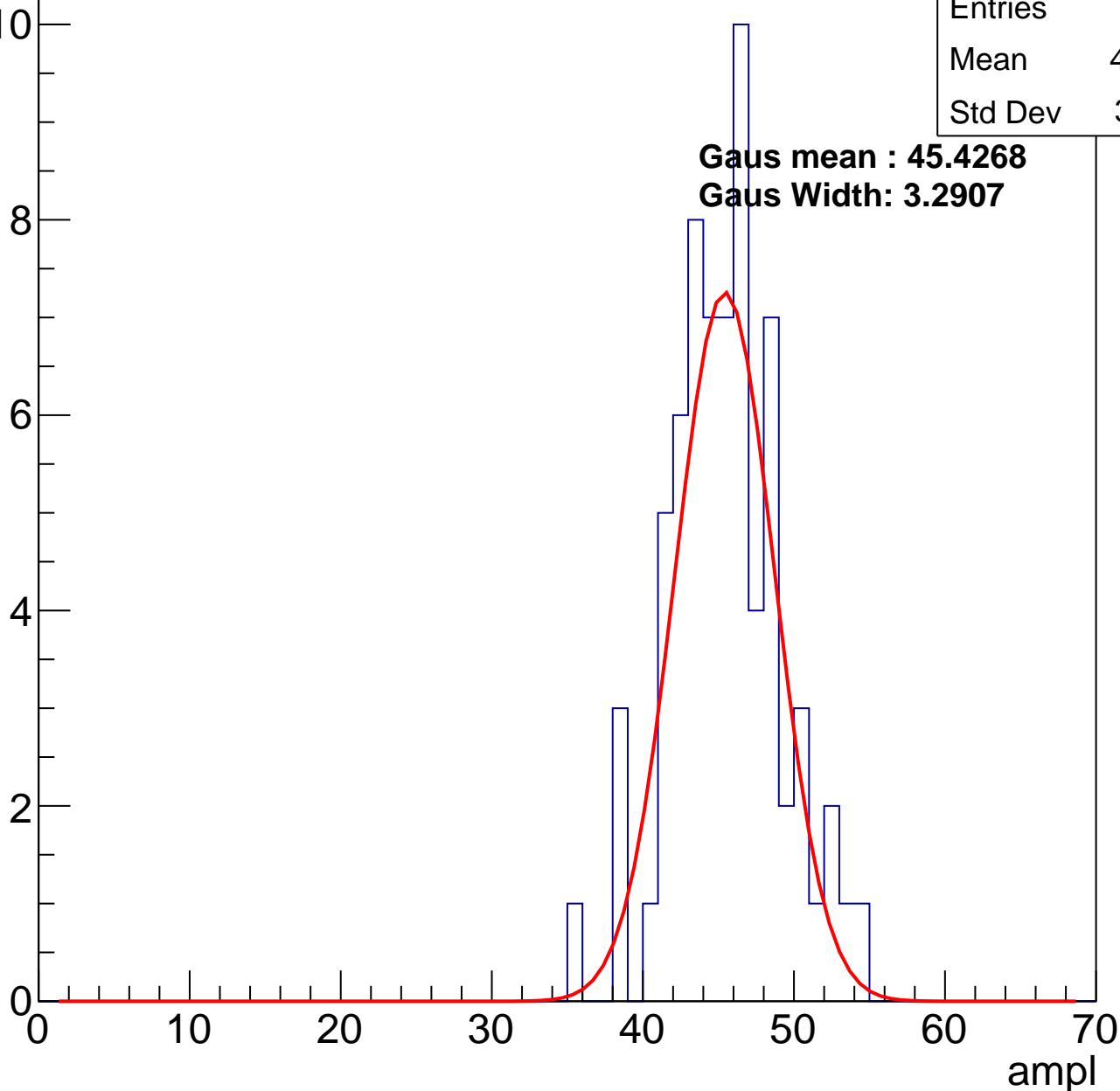
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	45.03
Std Dev	3.651

Gaus mean : 45.4268

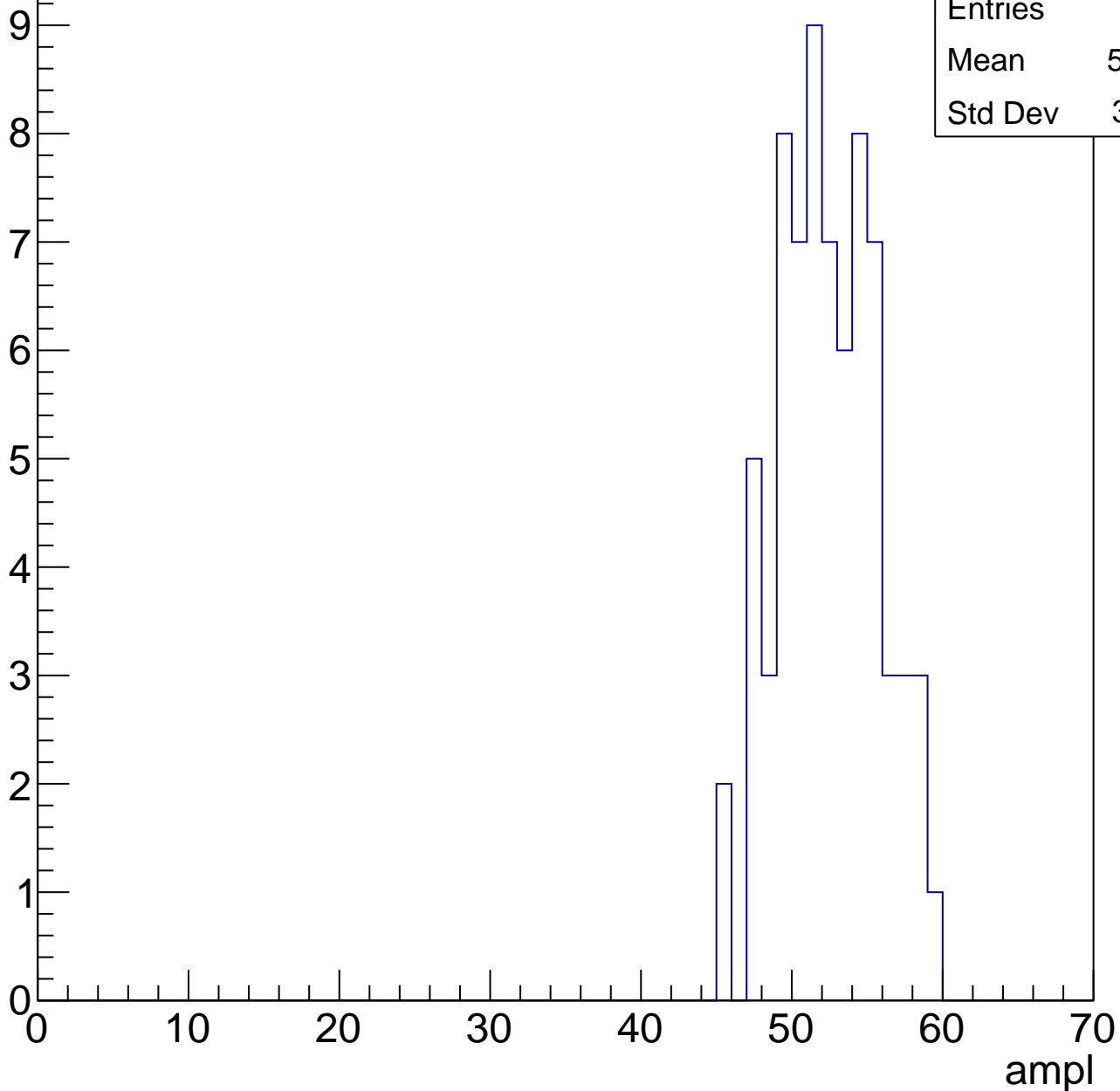
Gaus Width: 3.2907



B1L100S, U13-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

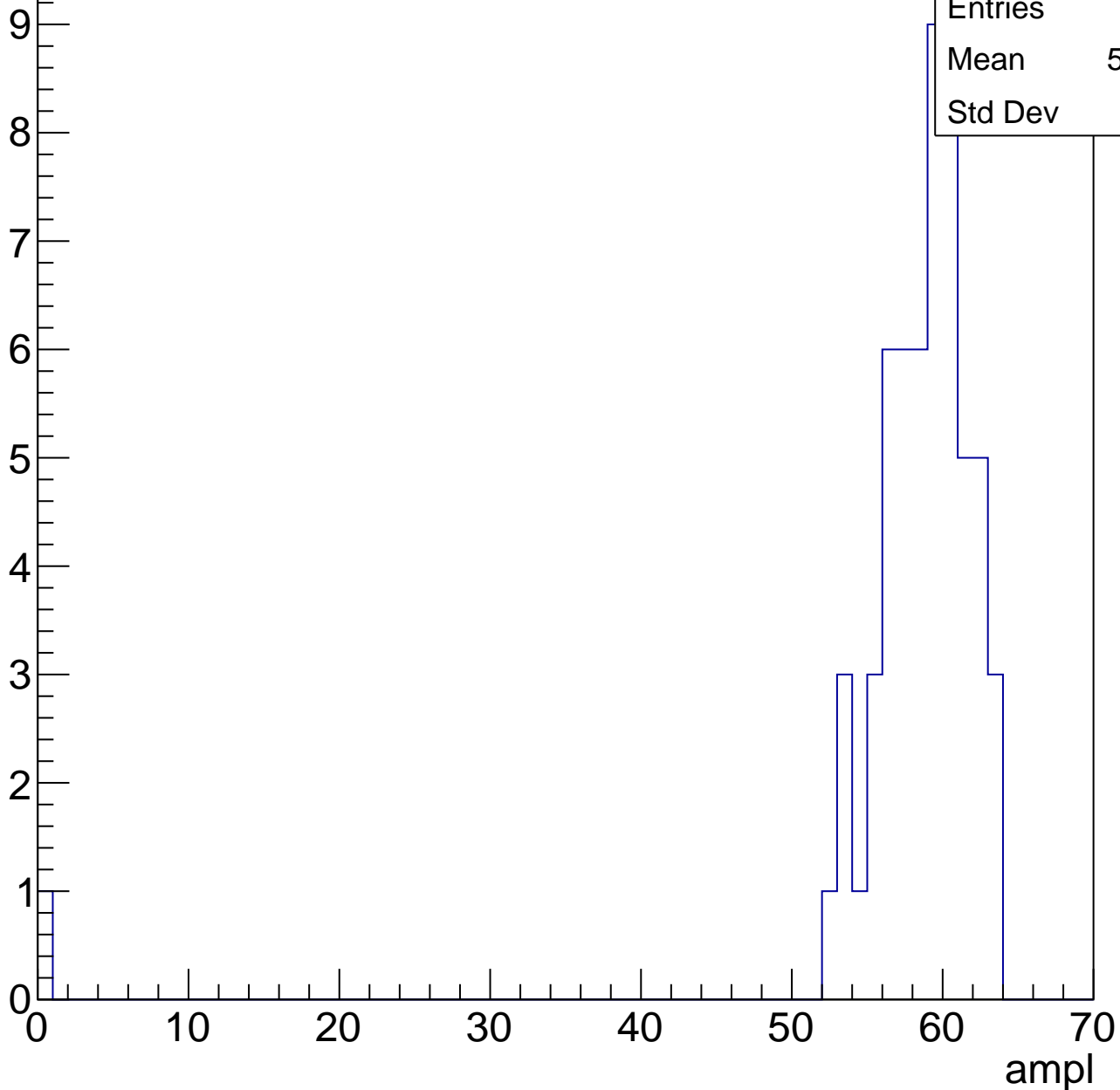


B1L100S, U13-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	57.39
Std Dev	8.13

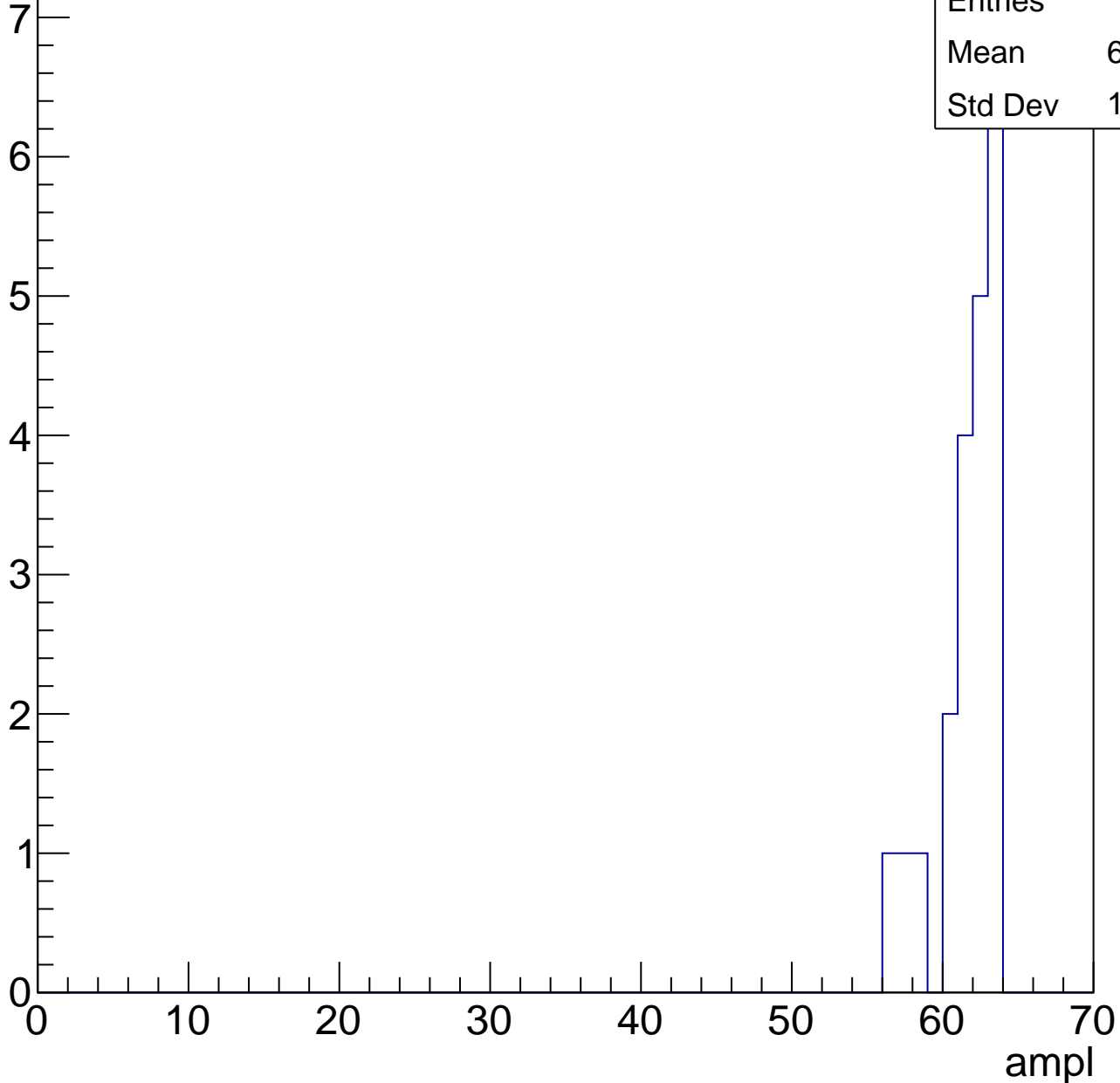


B1L100S, U13-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	61.24
Std Dev	1.998



B1L100S, U13-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch19, adc0

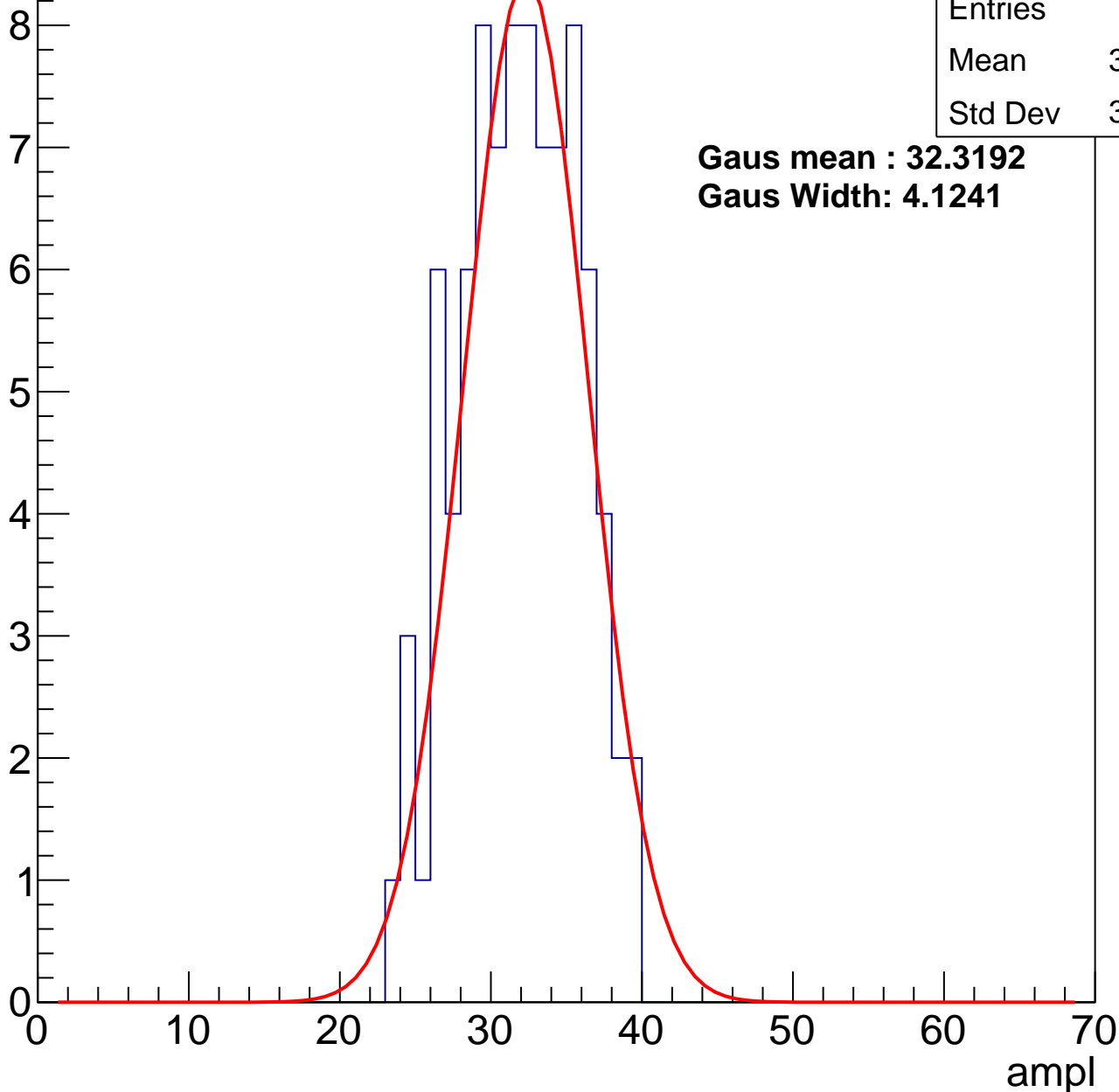
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	31.42
Std Dev	3.822

Gaus mean : 32.3192

Gaus Width: 4.1241



B1L100S, U13-ch19, adc1

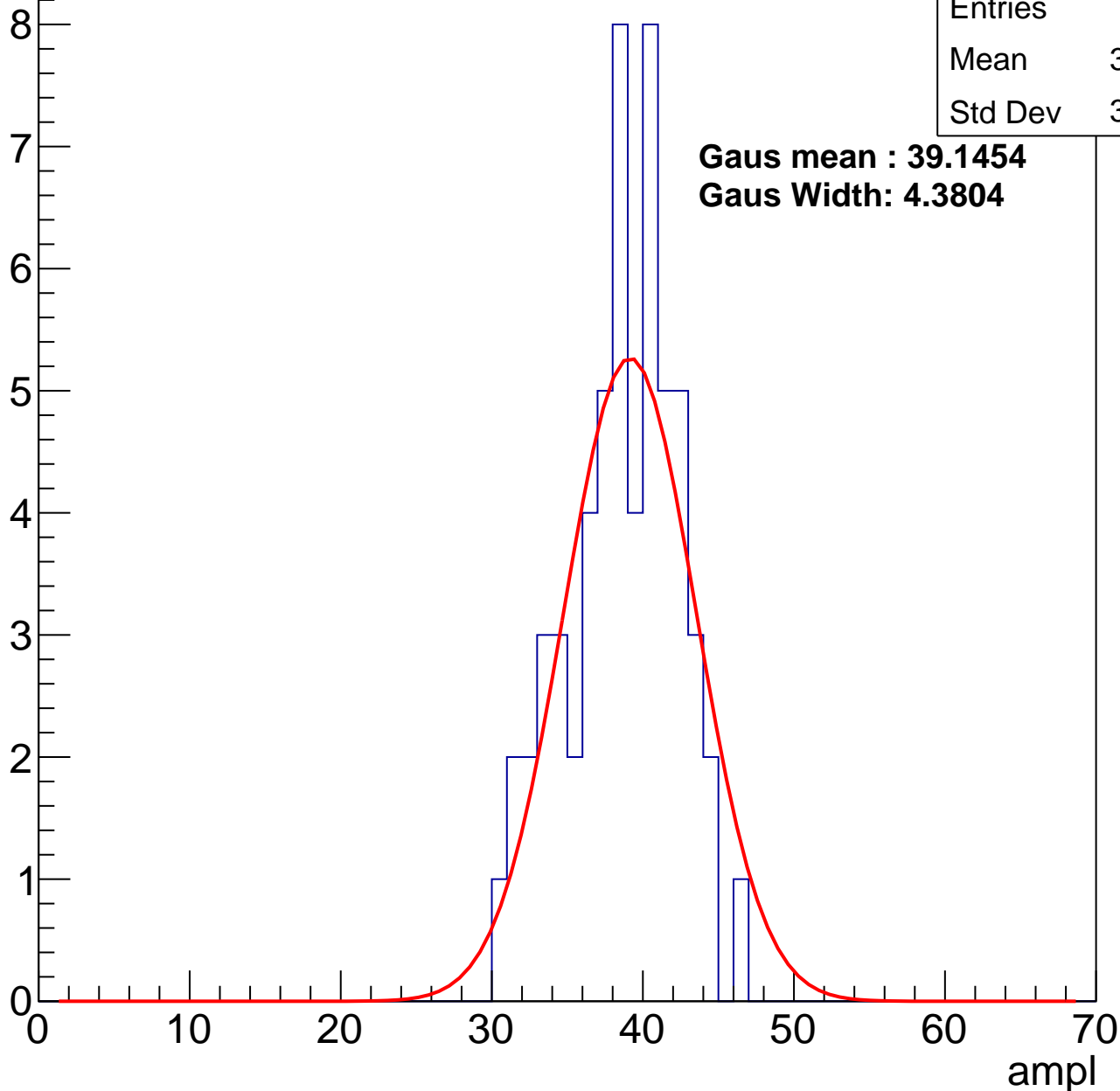
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.17
Std Dev	3.635

Gaus mean : 39.1454

Gaus Width: 4.3804



B1L100S, U13-ch19, adc2

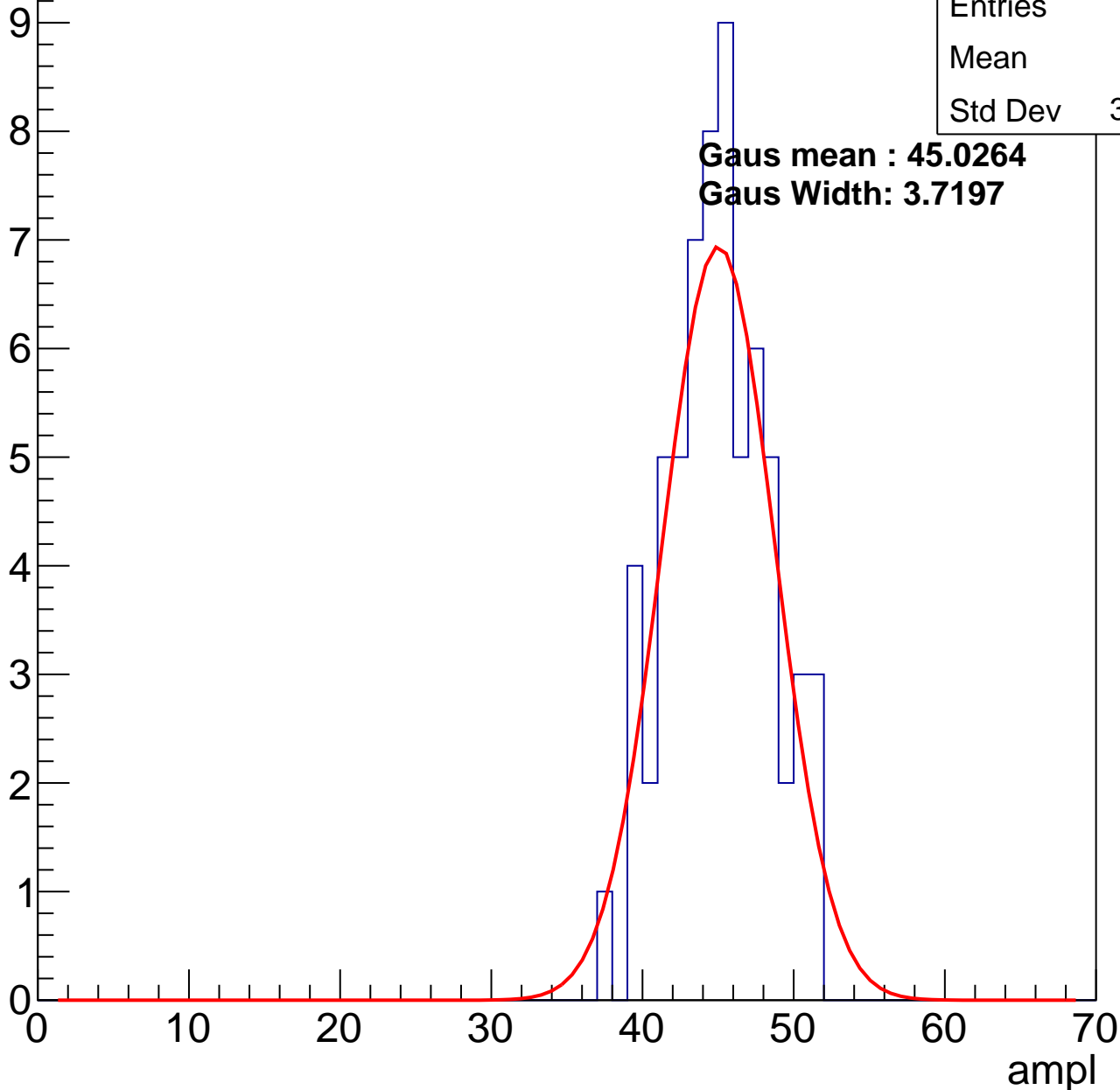
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.6
Std Dev	3.276

Gaus mean : 45.0264

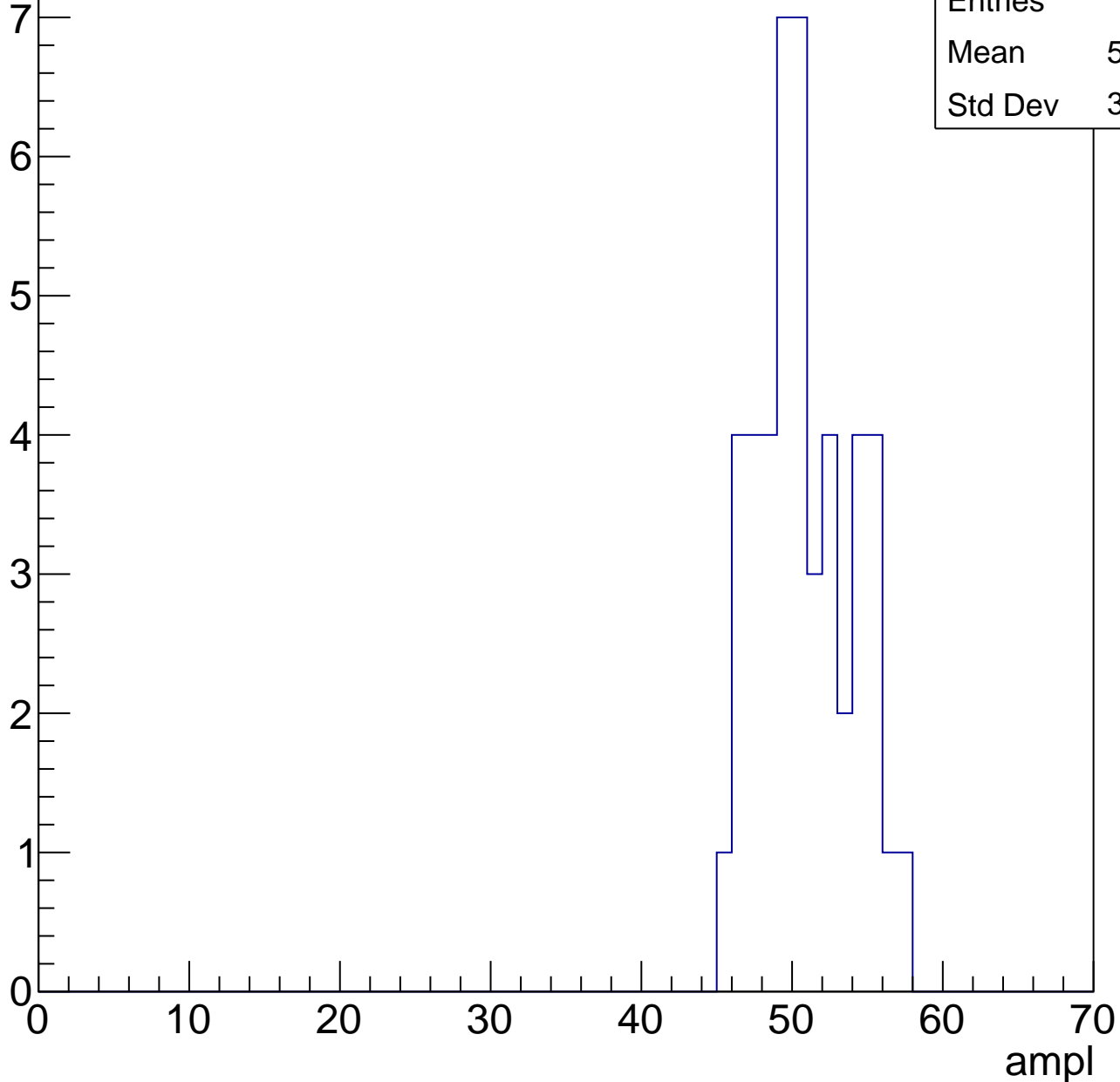
Gaus Width: 3.7197



B1L100S, U13-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

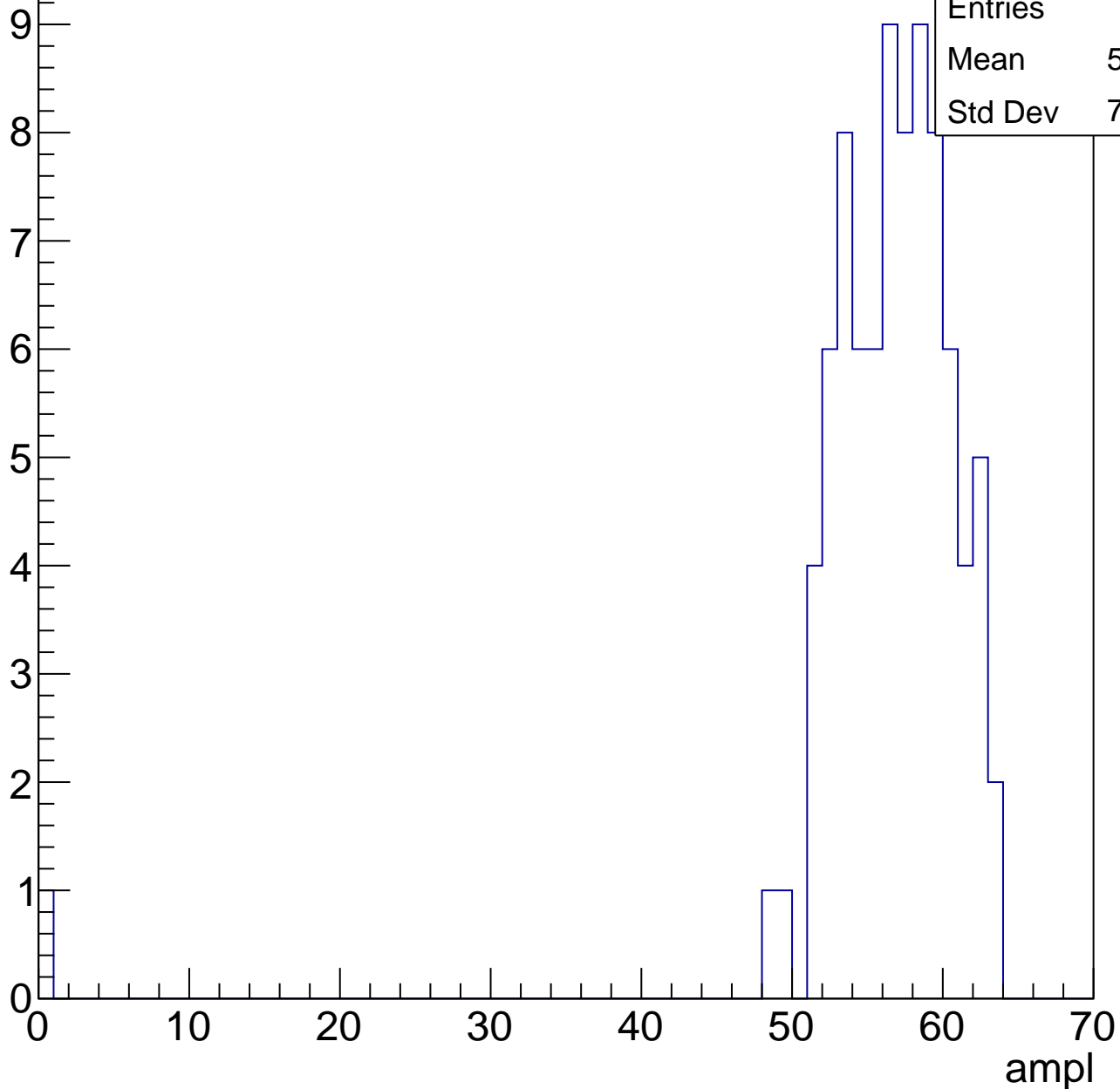


Entries	46
Mean	50.39
Std Dev	3.047

B1L100S, U13-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

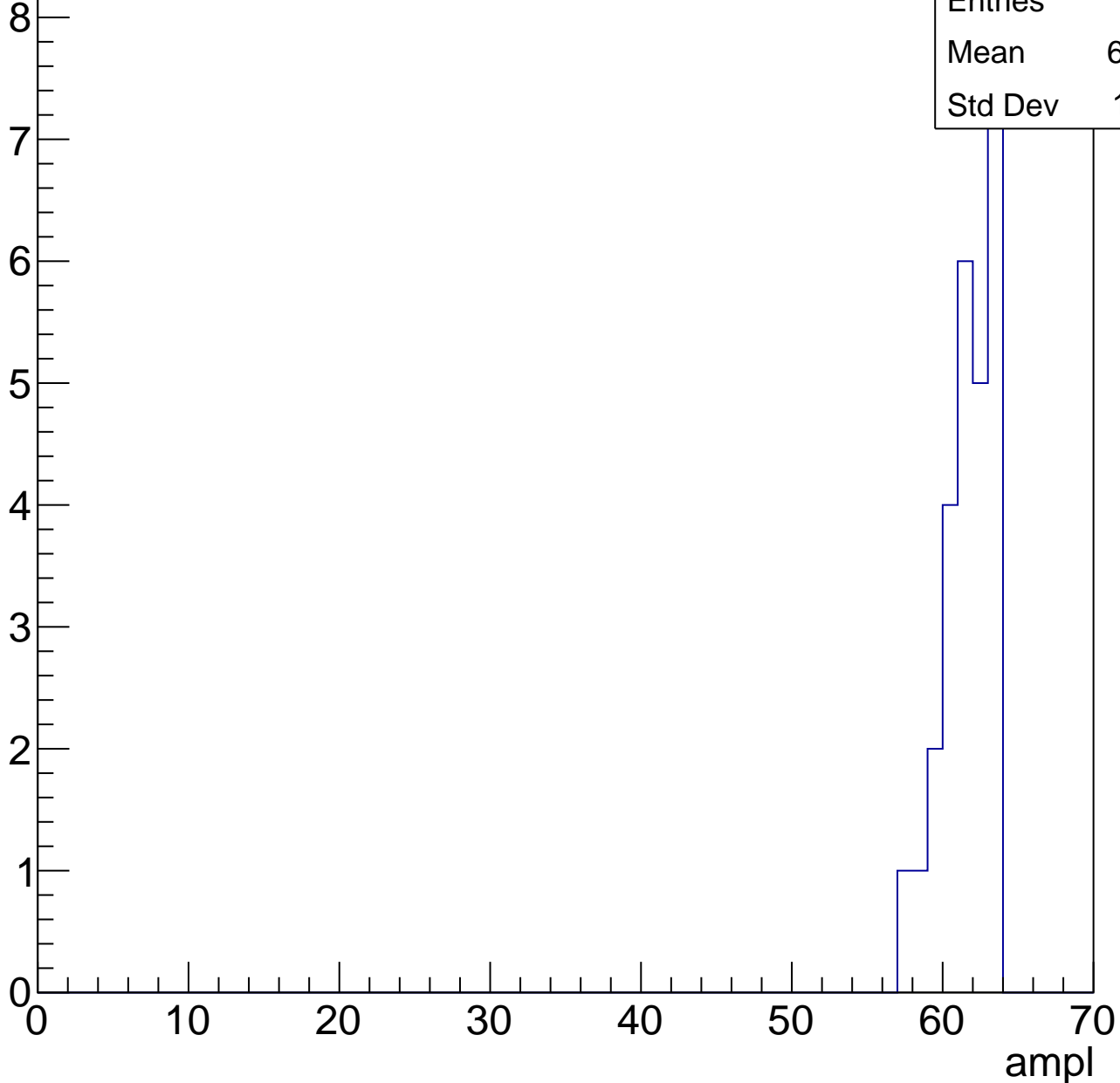


B1L100S, U13-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	61.22
Std Dev	1.641



B1L100S, U13-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	1
Mean	23
Std Dev	0

B1L100S, U13-ch20, adc0

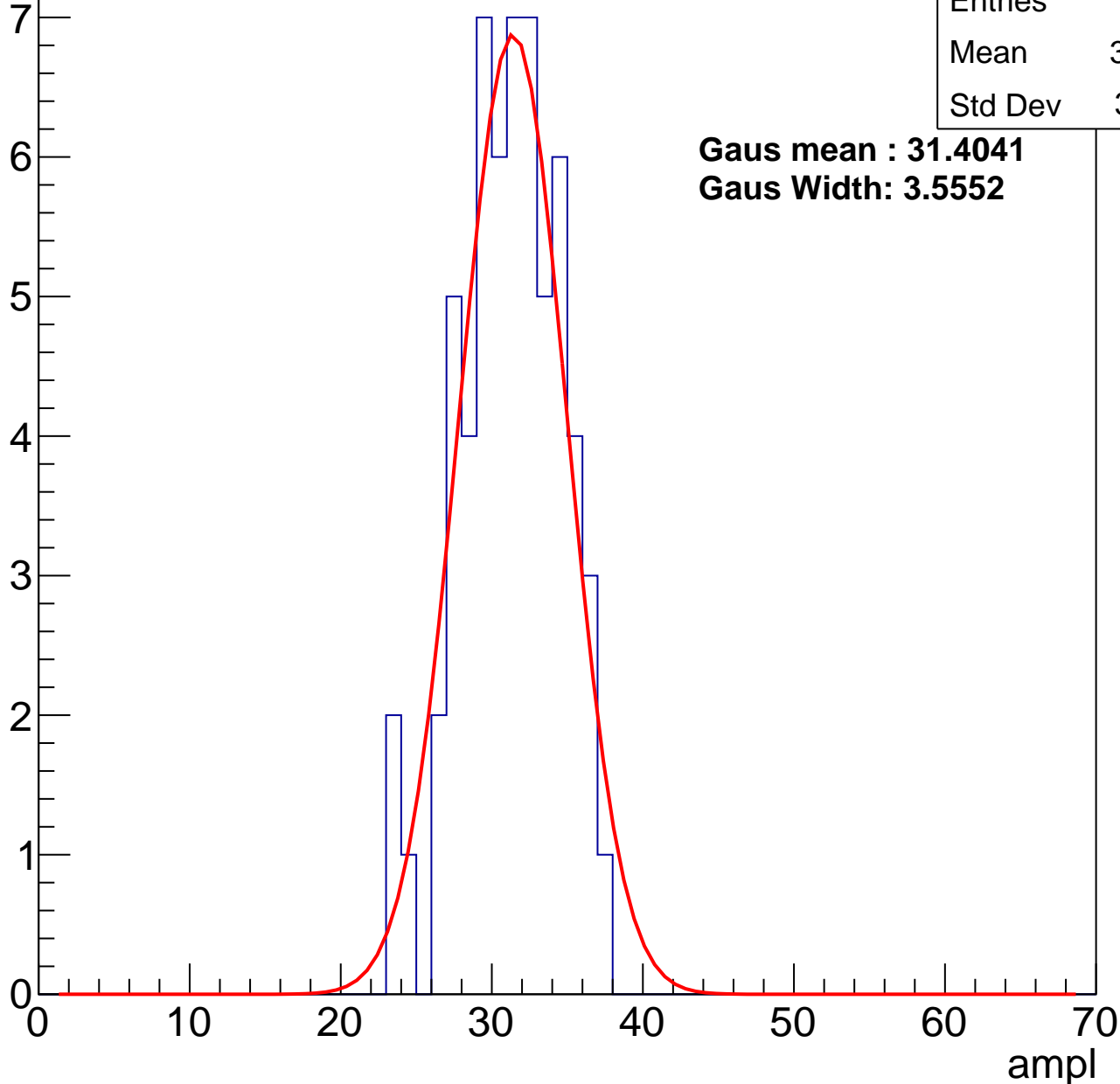
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	30.78
Std Dev	3.241

Gaus mean : 31.4041

Gaus Width: 3.5552



B1L100S, U13-ch20, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	37.7
Std Dev	3.609

Gaus mean : 38.2690

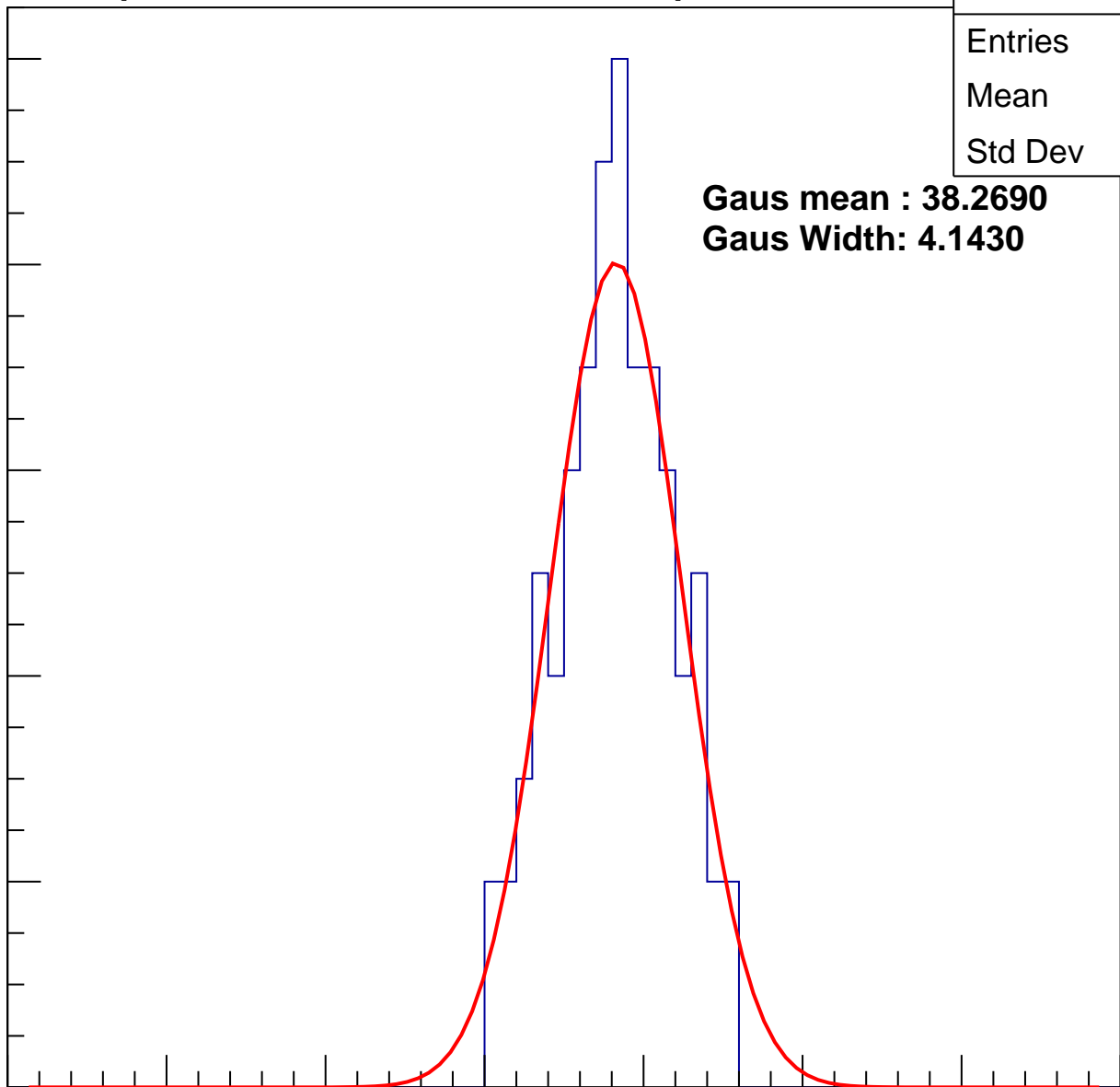
Gaus Width: 4.1430

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch20, adc2

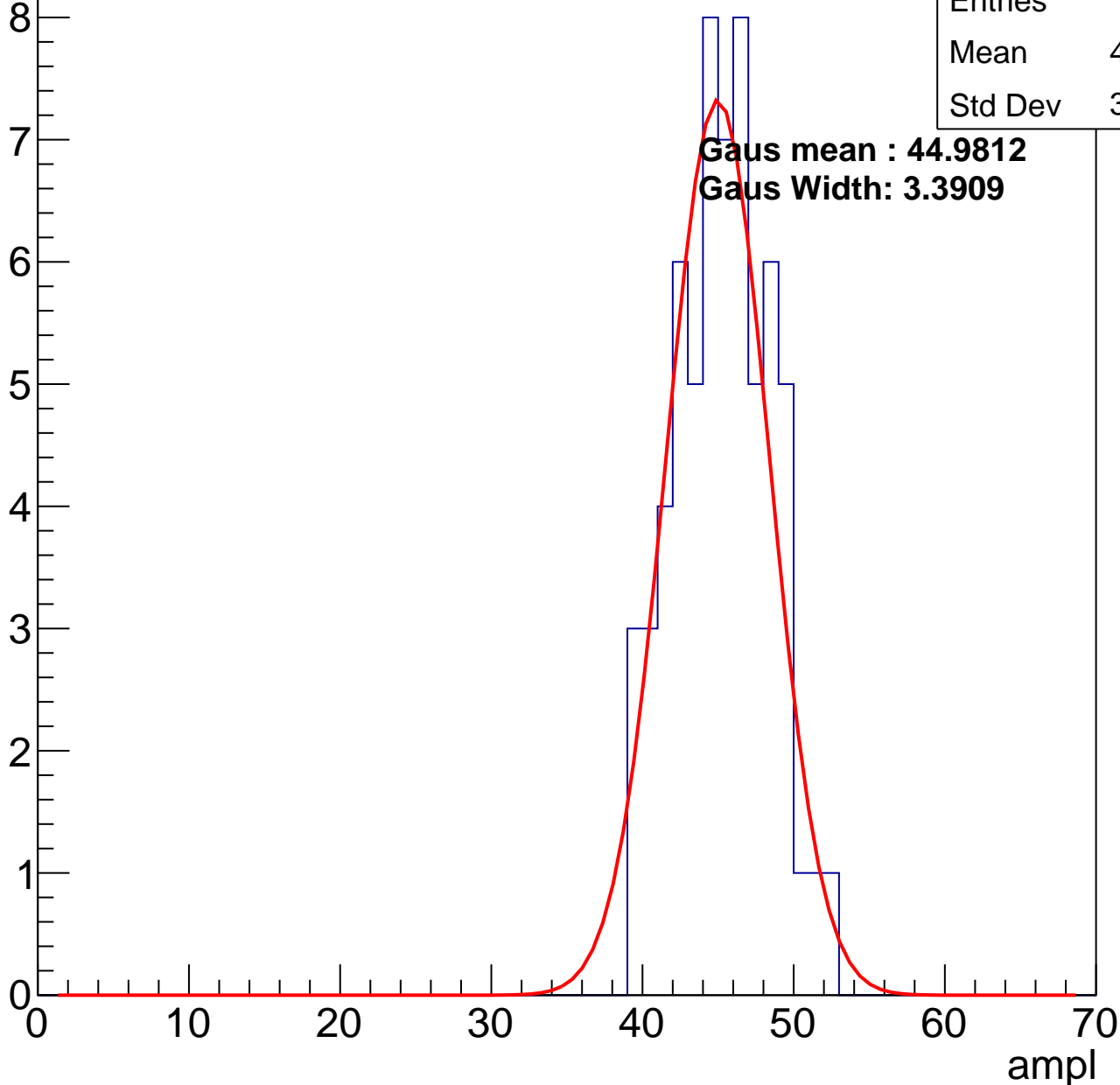
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.83
Std Dev	3.084

Gaus mean : 44.9812

Gaus Width: 3.3909



B1L100S, U13-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	51.73
Std Dev	3.026

Entry

10

8

6

4

2

0

0

10

20

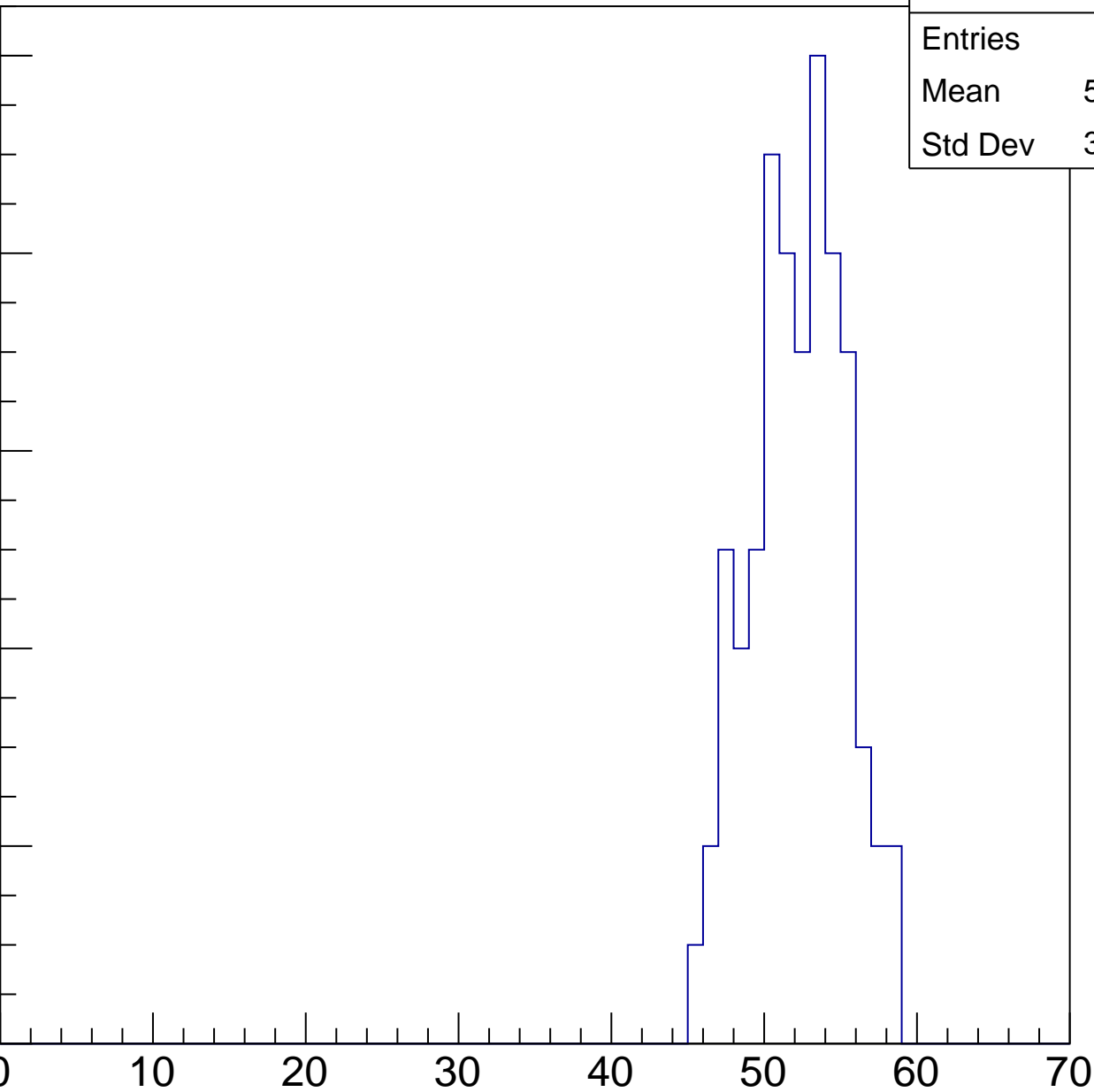
30

40

50

60

ampl



B1L100S, U13-ch20, adc4

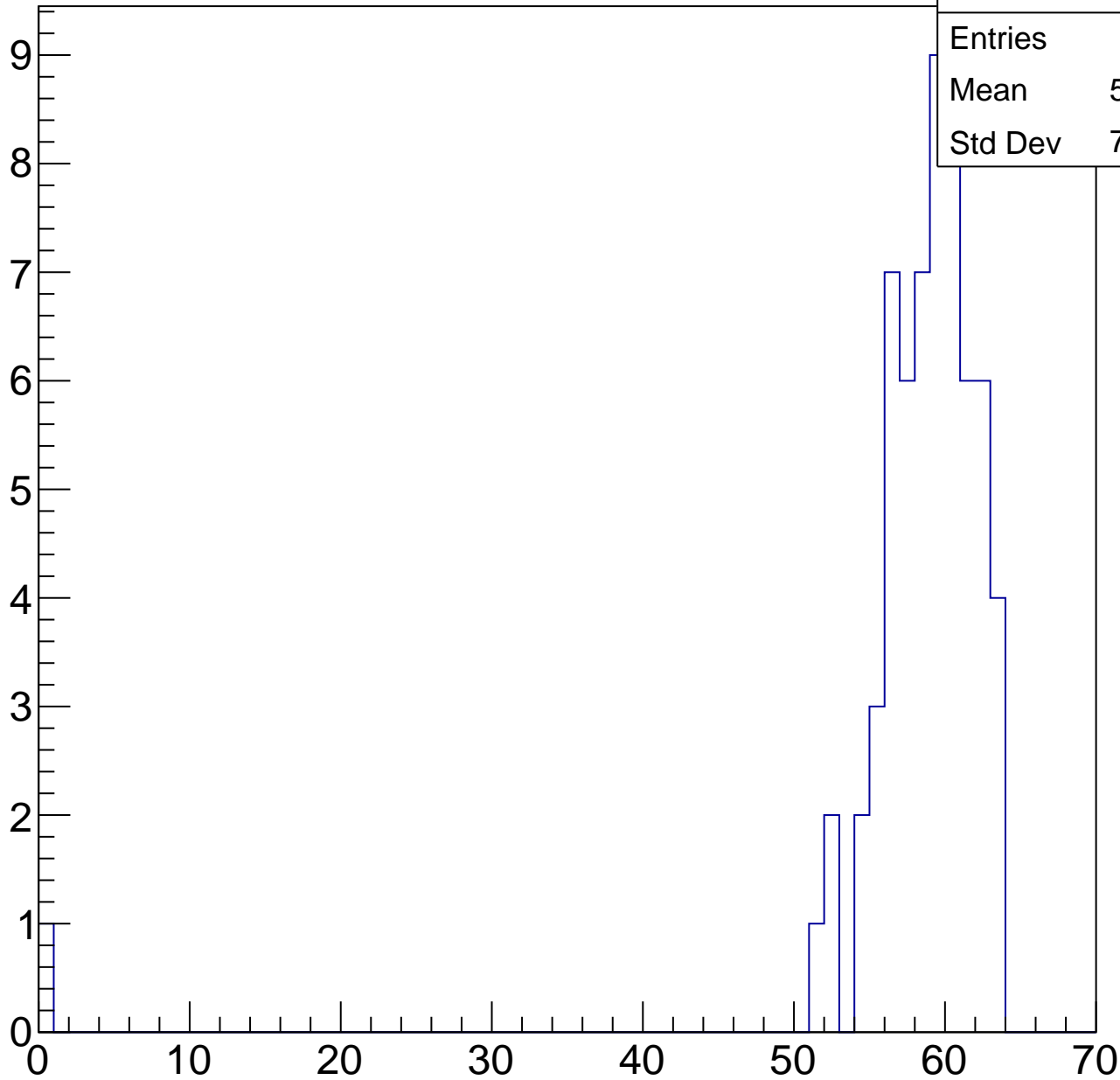
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	57.56
Std Dev	7.888

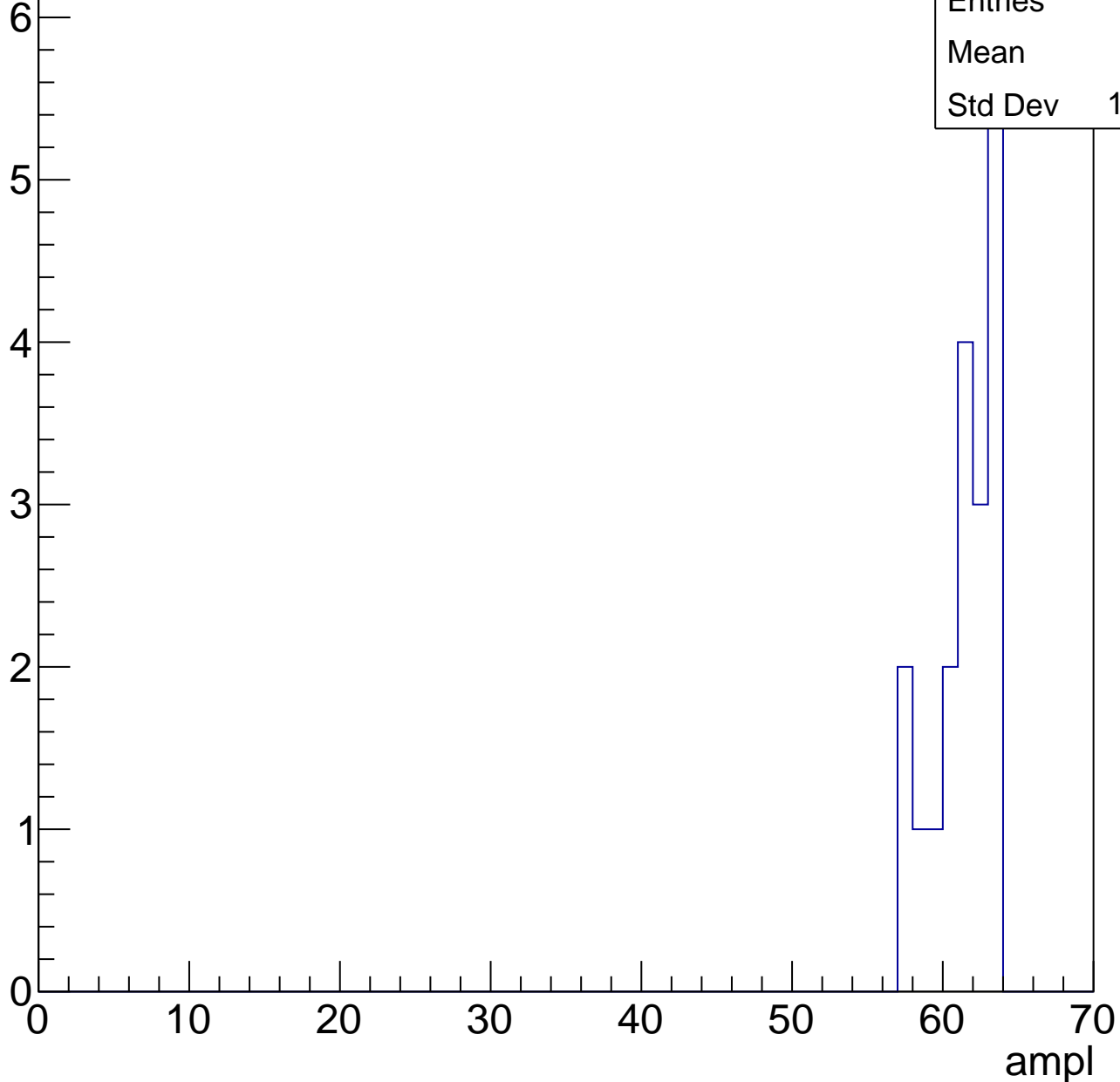
ampl



B1L100S, U13-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	19
Mean	61
Std Dev	1.974

B1L100S, U13-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch21, adc0

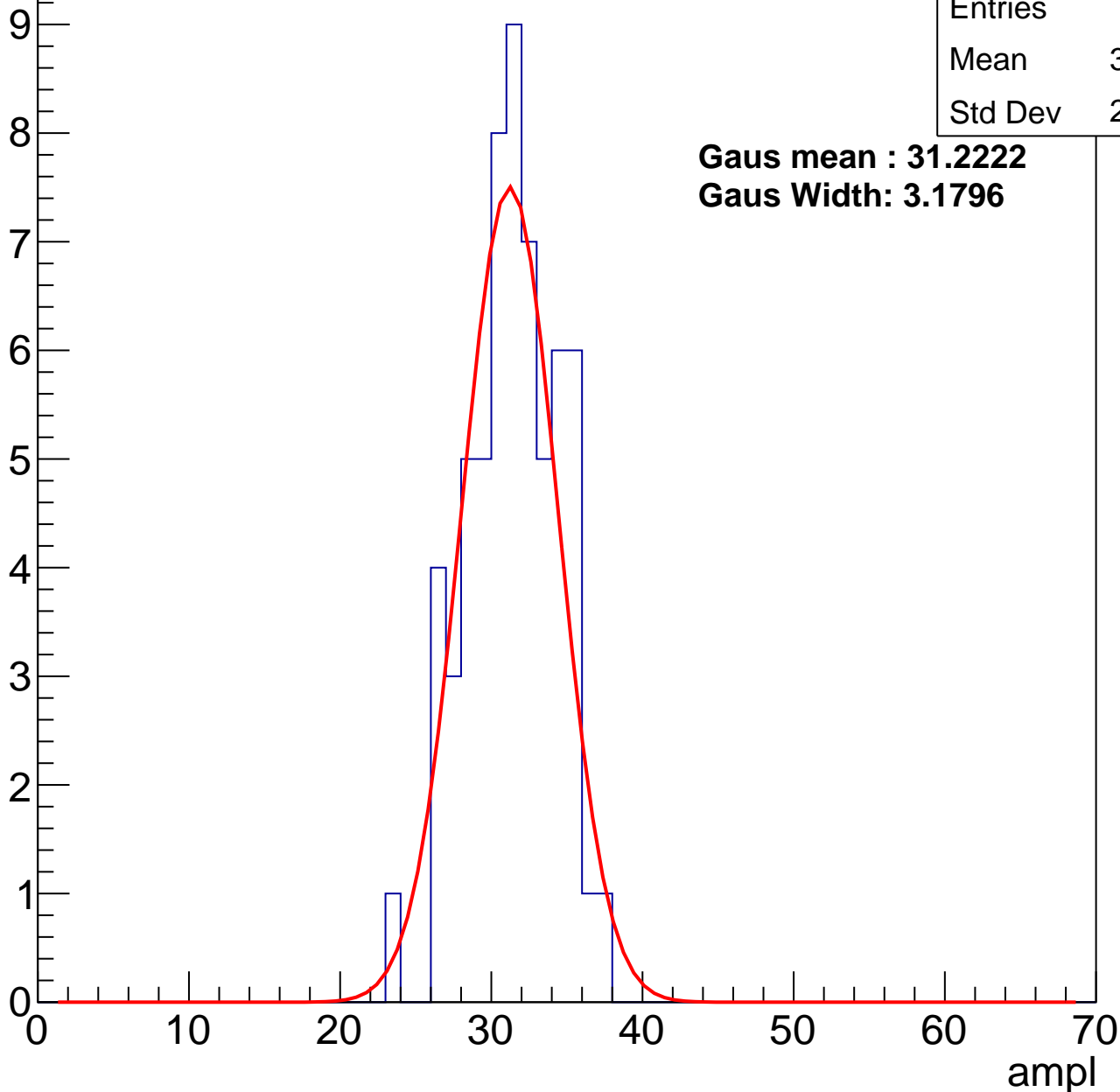
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.95
Std Dev	2.928

Gaus mean : 31.2222

Gaus Width: 3.1796



B1L100S, U13-ch21, adc1

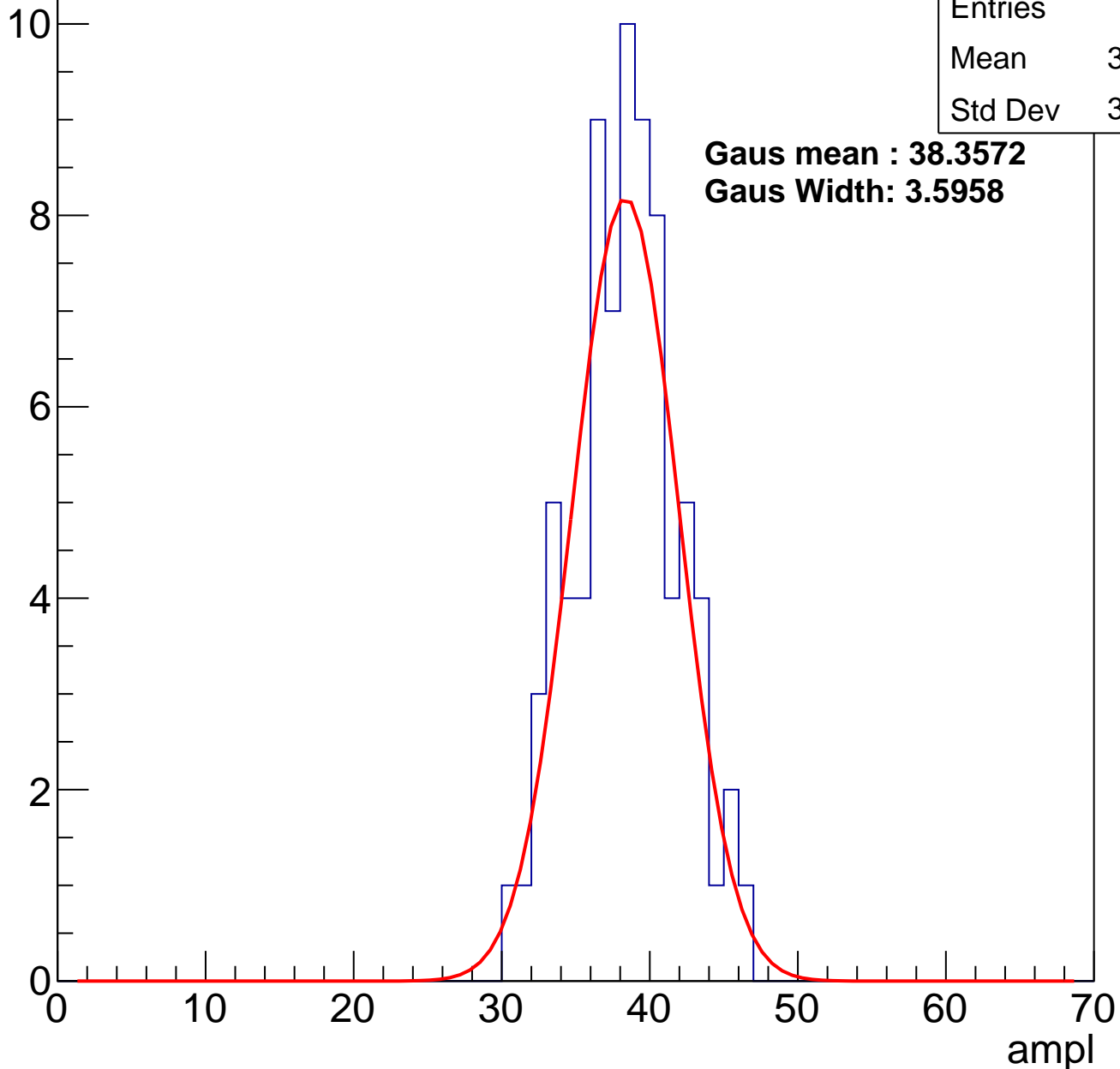
calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	37.92
Std Dev	3.478

Gaus mean : 38.3572

Gaus Width: 3.5958

Entry



B1L100S, U13-ch21, adc2

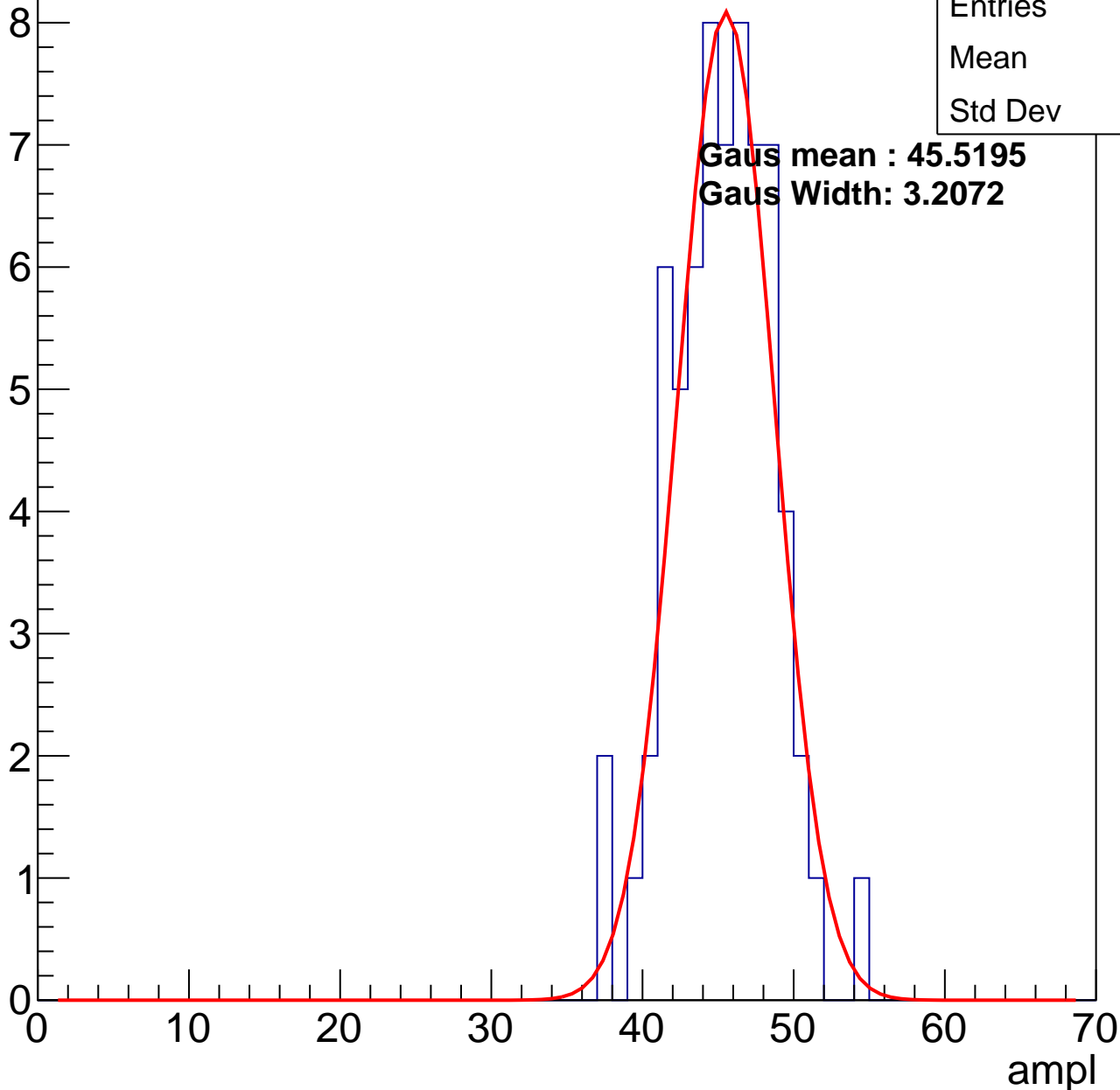
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.9
Std Dev	3.27

Gaus mean : 45.5195

Gaus Width: 3.2072

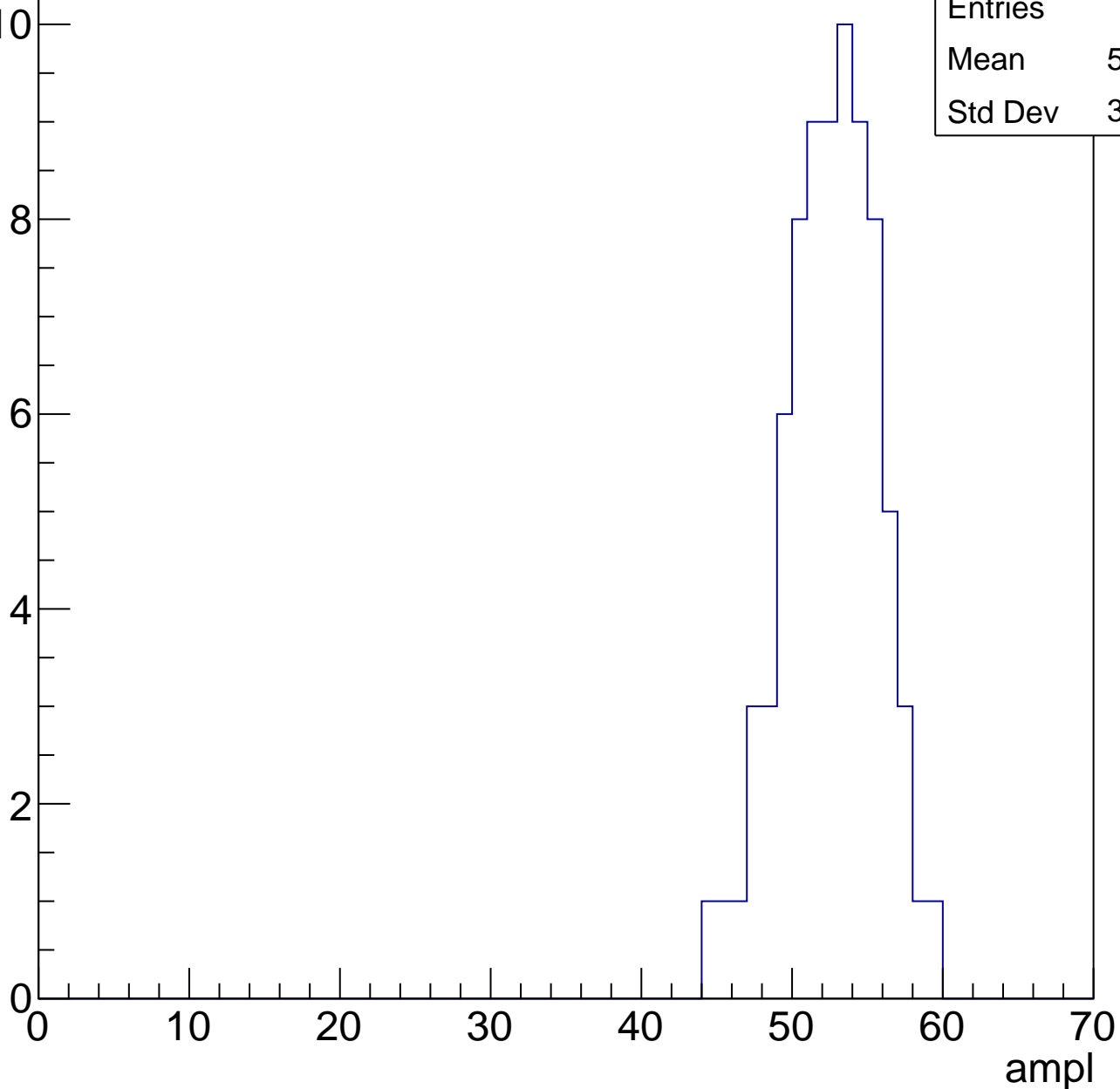


B1L100S, U13-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	52.12
Std Dev	3.047

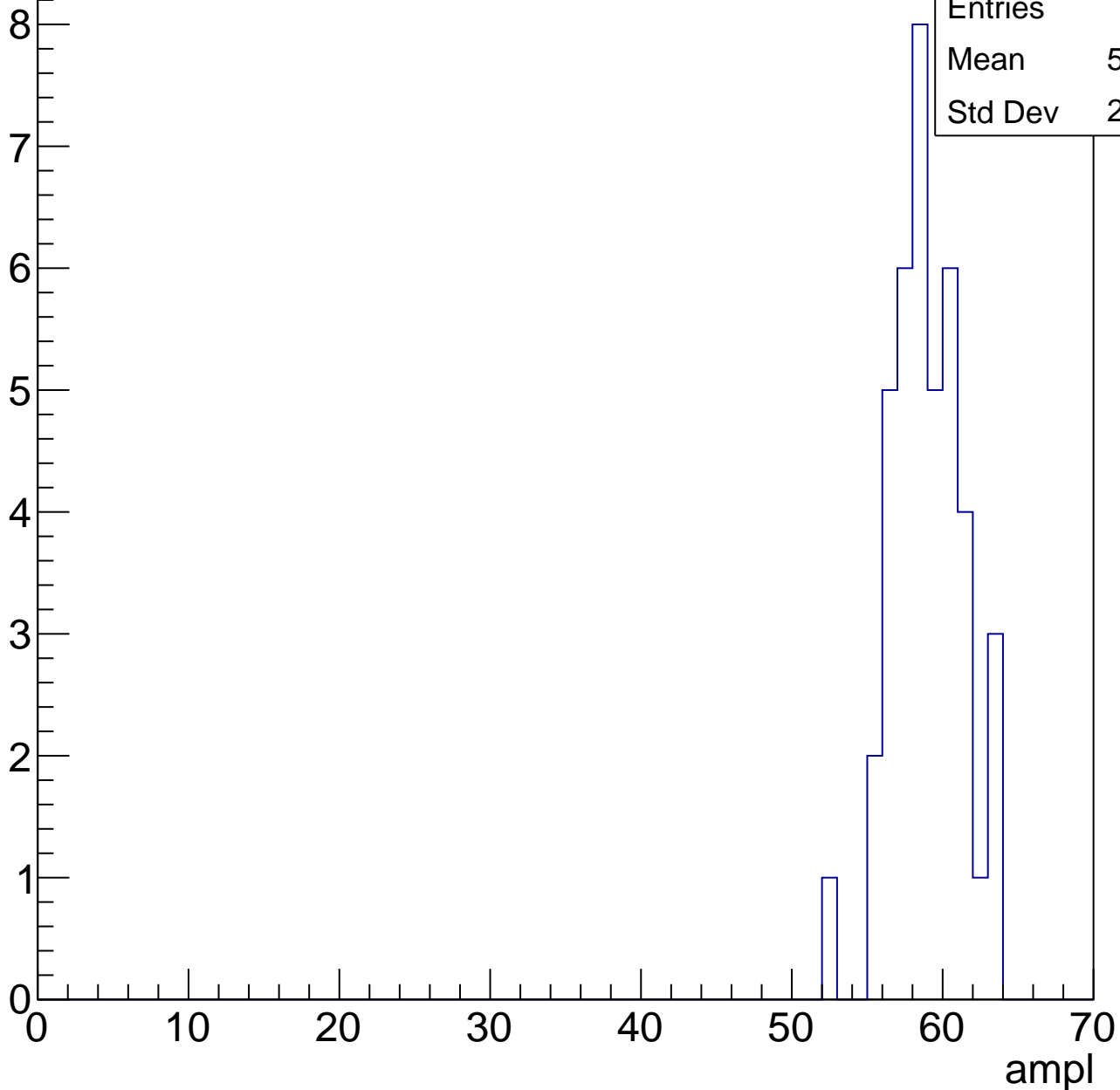


B1L100S, U13-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	58.49
Std Dev	2.349

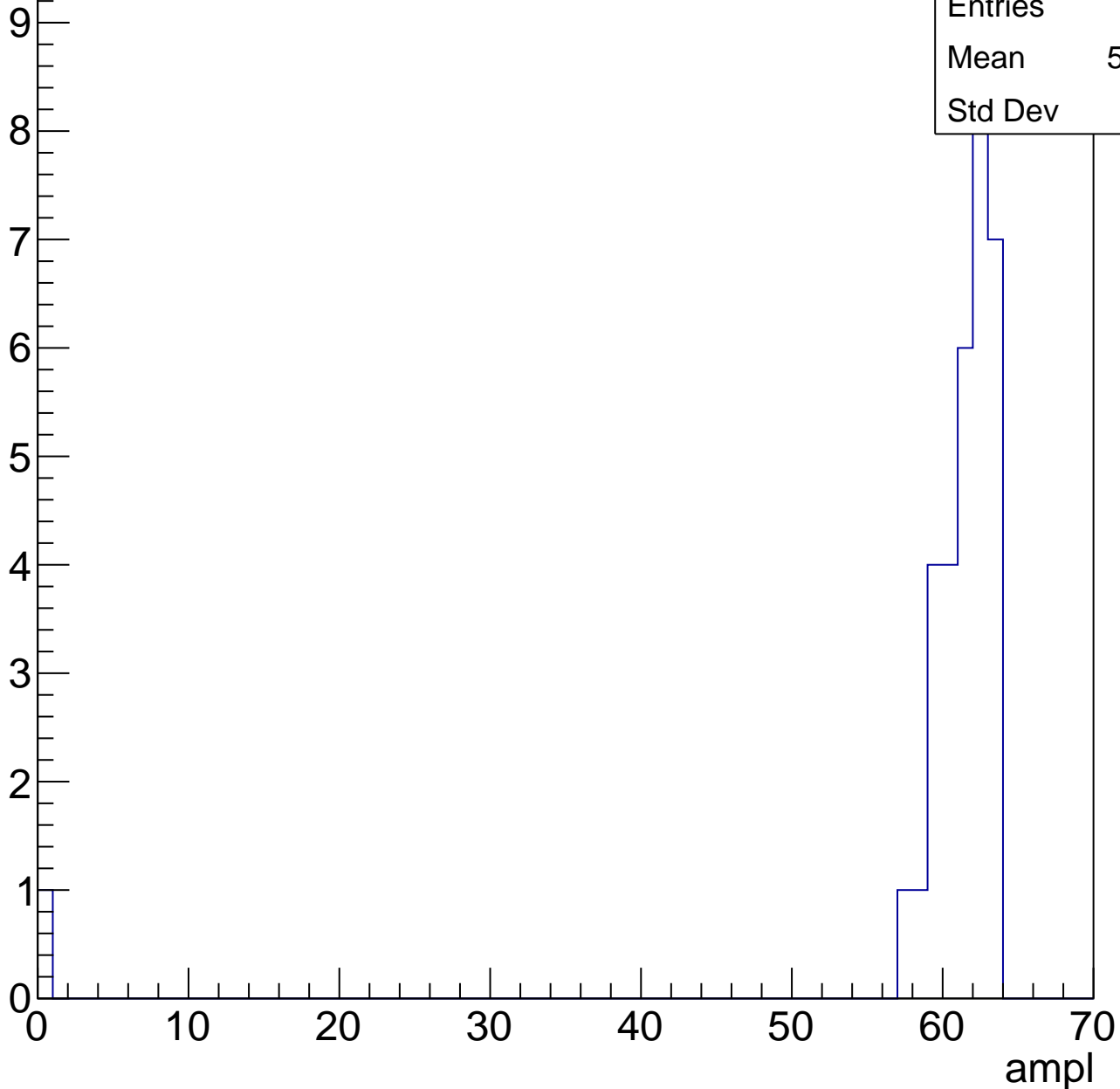


B1L100S, U13-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	59.27
Std Dev	10.6



B1L100S, U13-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch22, adc0

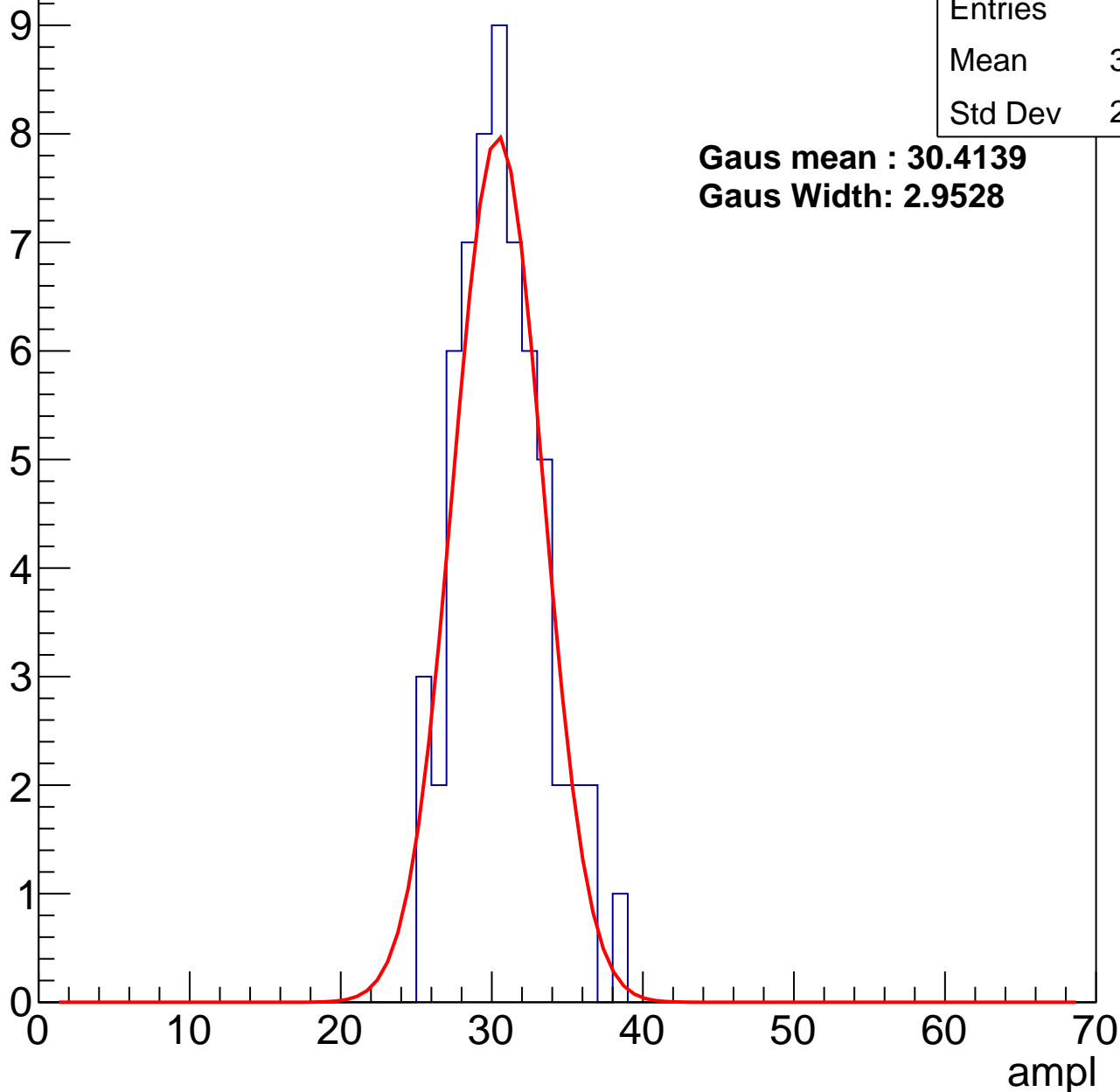
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	30.15
Std Dev	2.857

Gaus mean : 30.4139

Gaus Width: 2.9528



B1L100S, U13-ch22, adc1

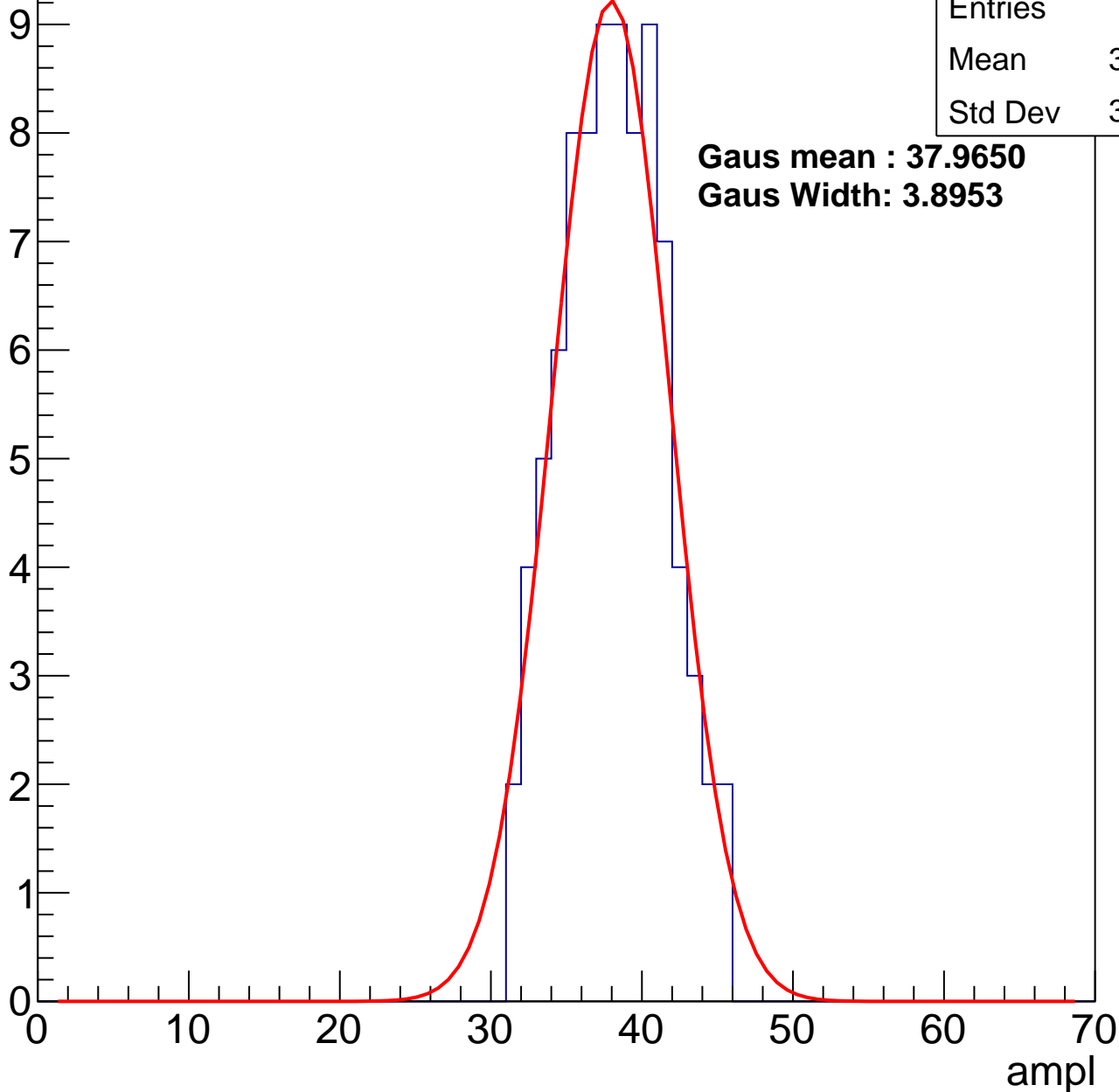
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	37.63
Std Dev	3.376

Gaus mean : 37.9650

Gaus Width: 3.8953



B1L100S, U13-ch22, adc2

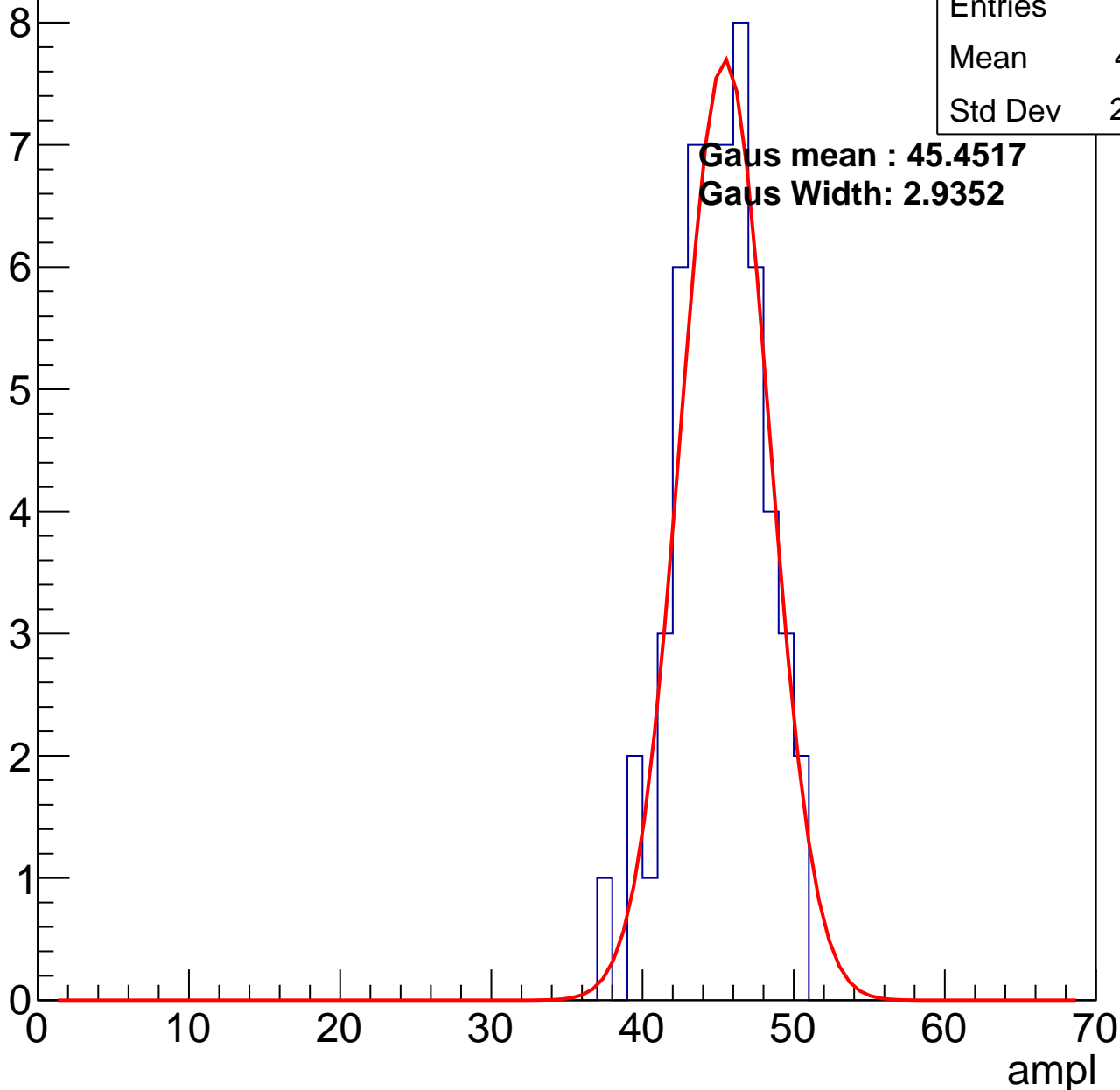
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.61
Std Dev	2.827

Gaus mean : 45.4517

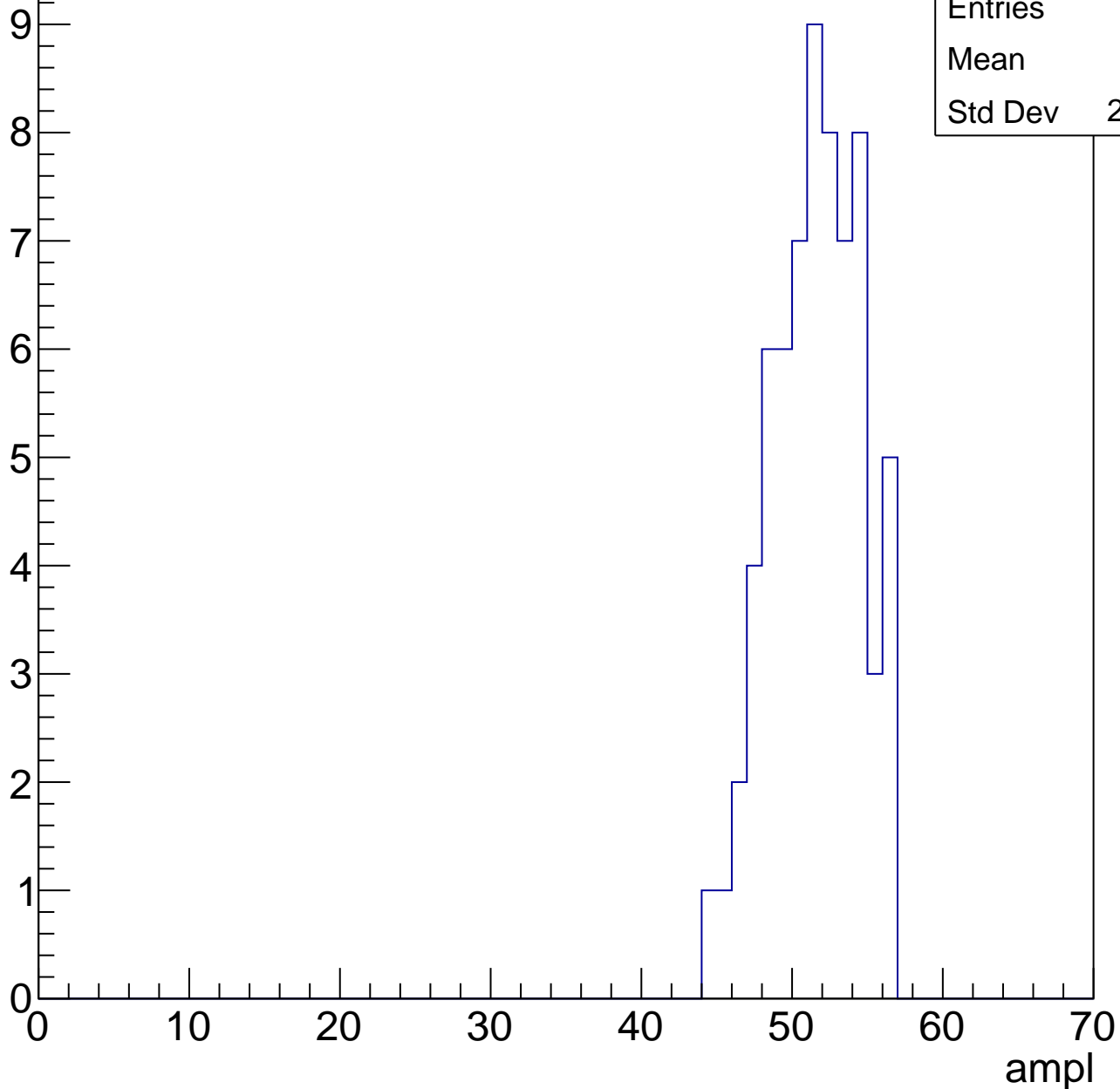
Gaus Width: 2.9352



B1L100S, U13-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



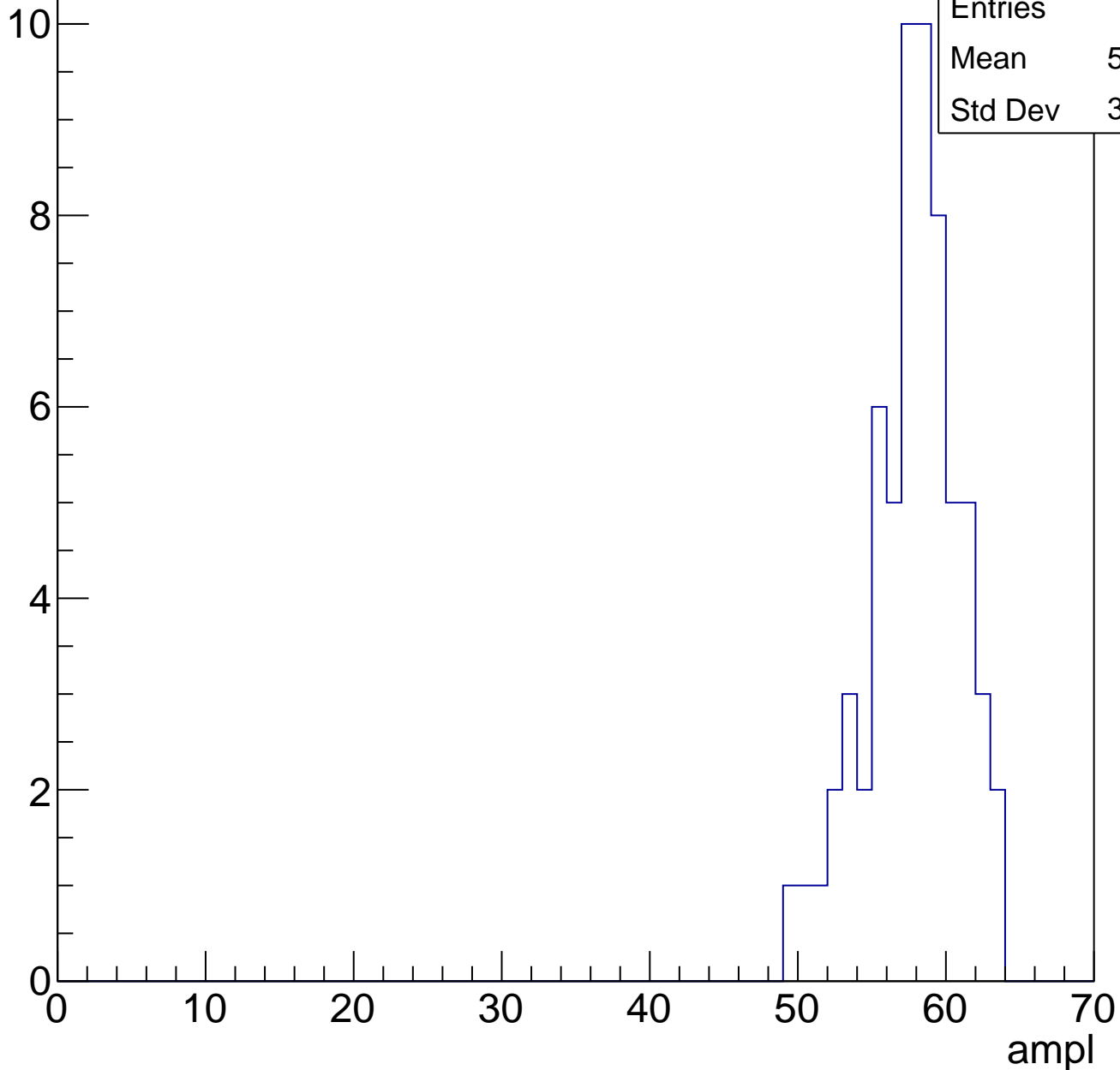
Entries	67
Mean	51.1
Std Dev	2.902

B1L100S, U13-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	57.34
Std Dev	3.063

Entry

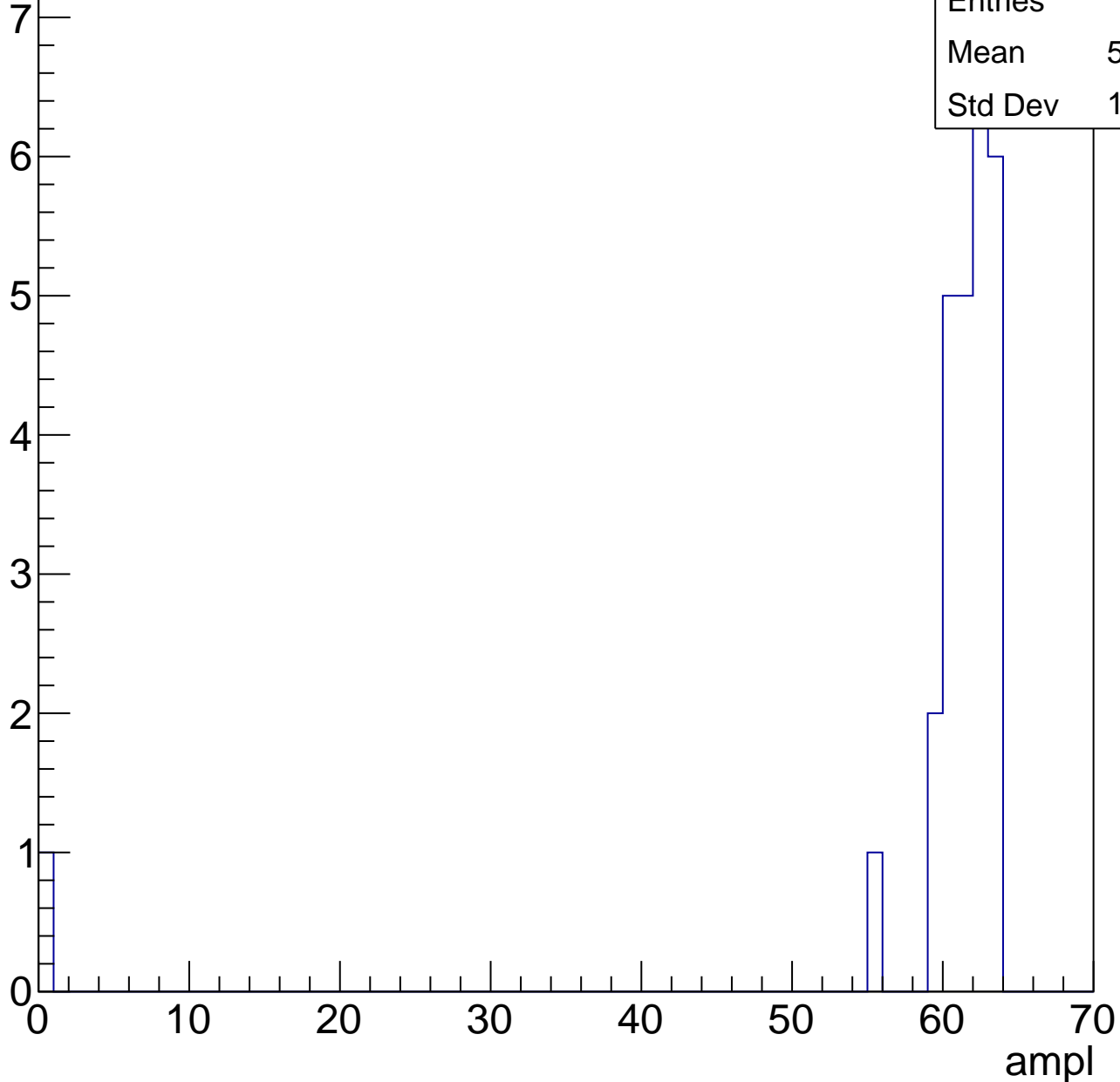


B1L100S, U13-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.89
Std Dev	11.68



B1L100S, U13-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

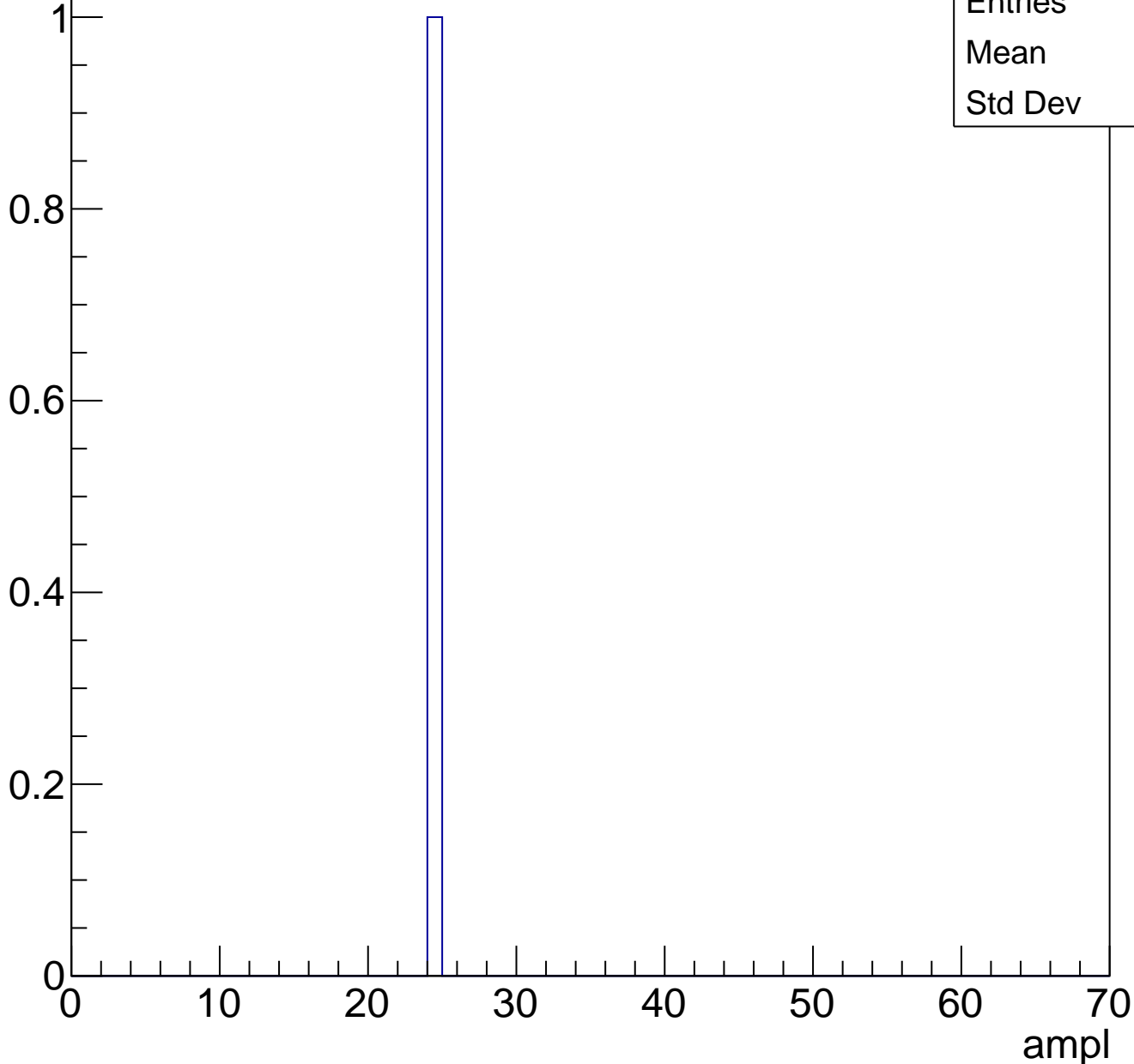
Entry



B1L100S, U13-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	24
Std Dev	0

B1L100S, U13-ch23, adc0

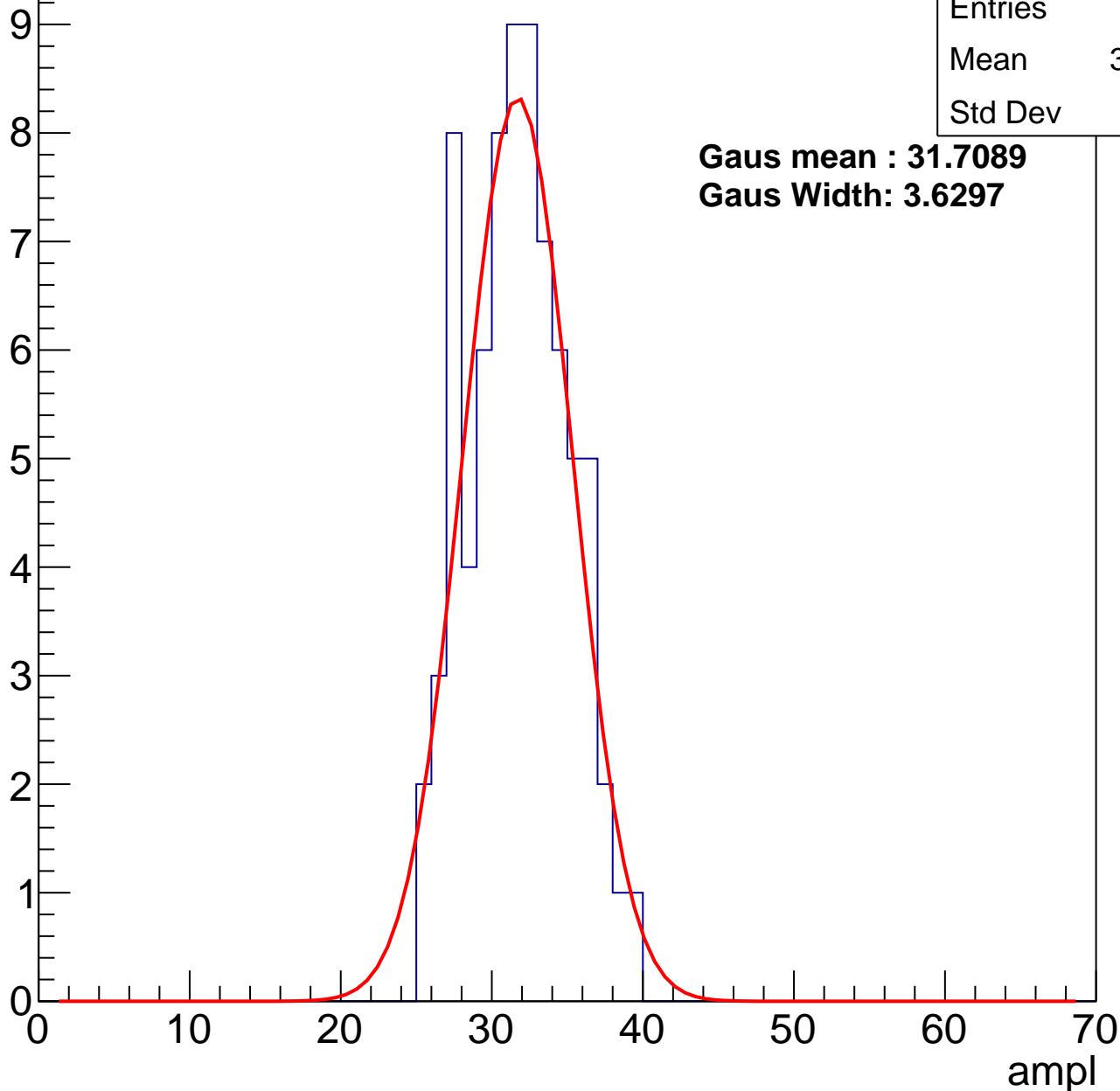
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	31.29
Std Dev	3.28

Gaus mean : 31.7089

Gaus Width: 3.6297



B1L100S, U13-ch23, adc1

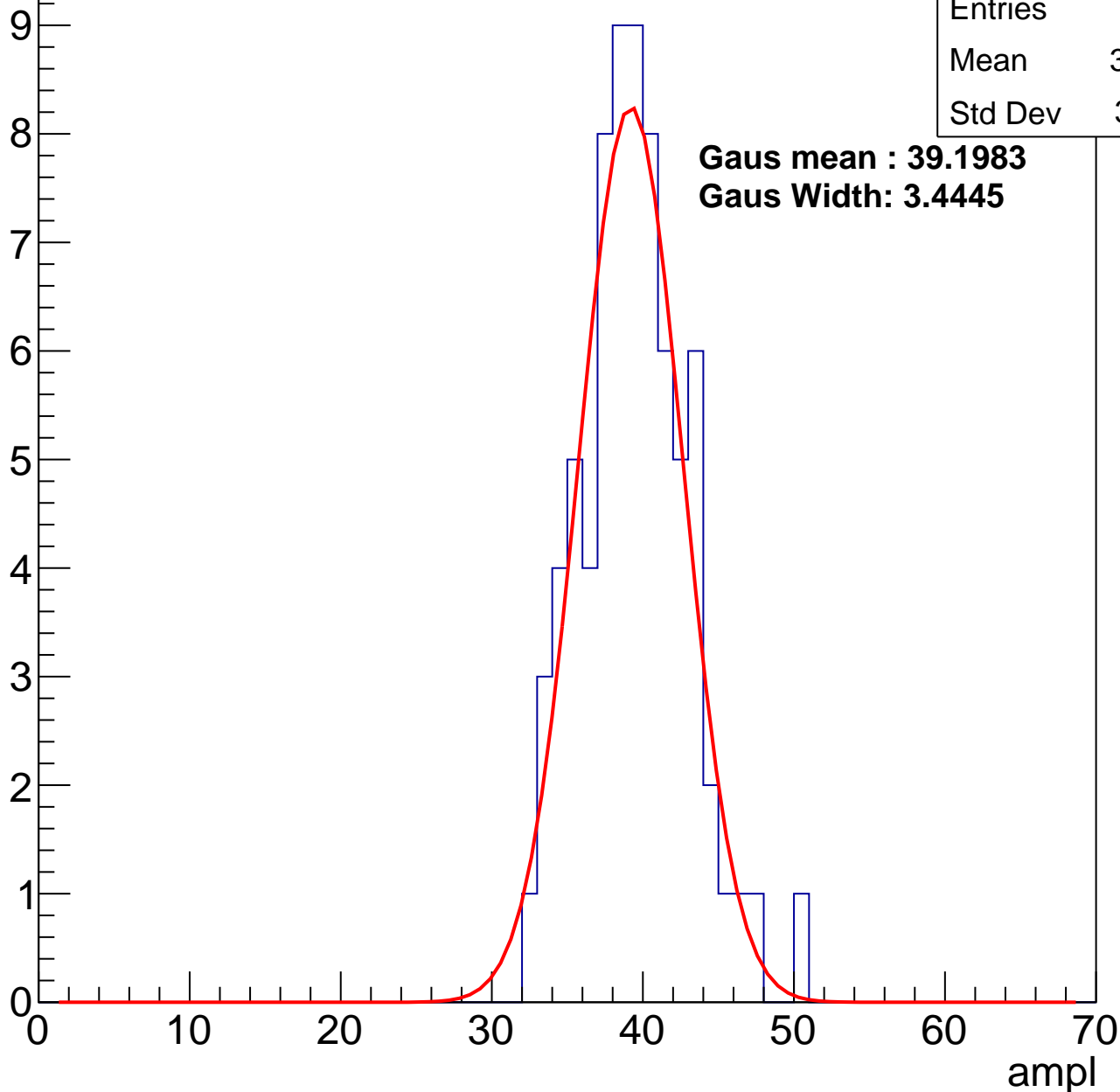
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	38.99
Std Dev	3.501

Gaus mean : 39.1983

Gaus Width: 3.4445



B1L100S, U13-ch23, adc2

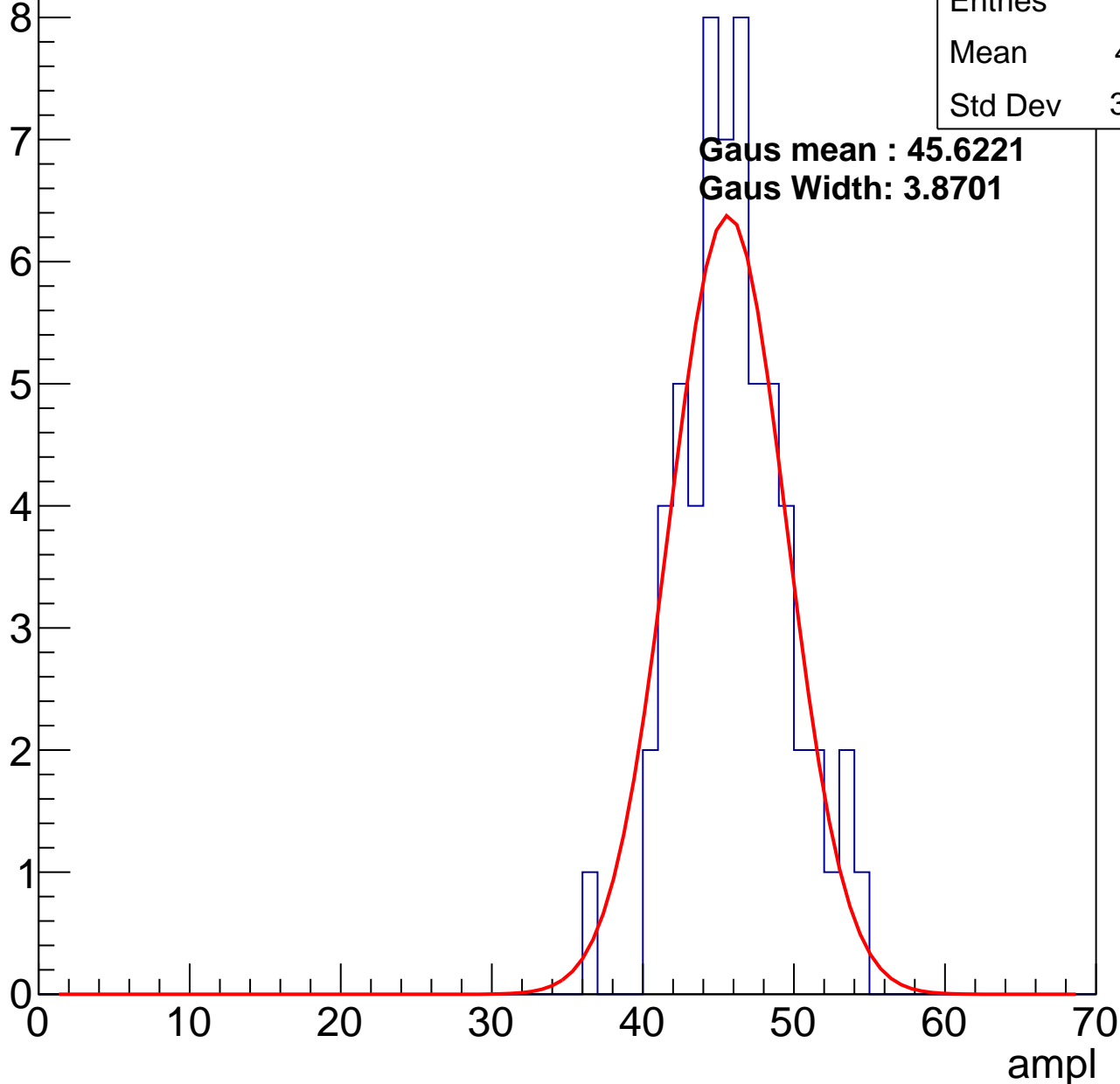
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	45.61
Std Dev	3.526

Gaus mean : 45.6221

Gaus Width: 3.8701

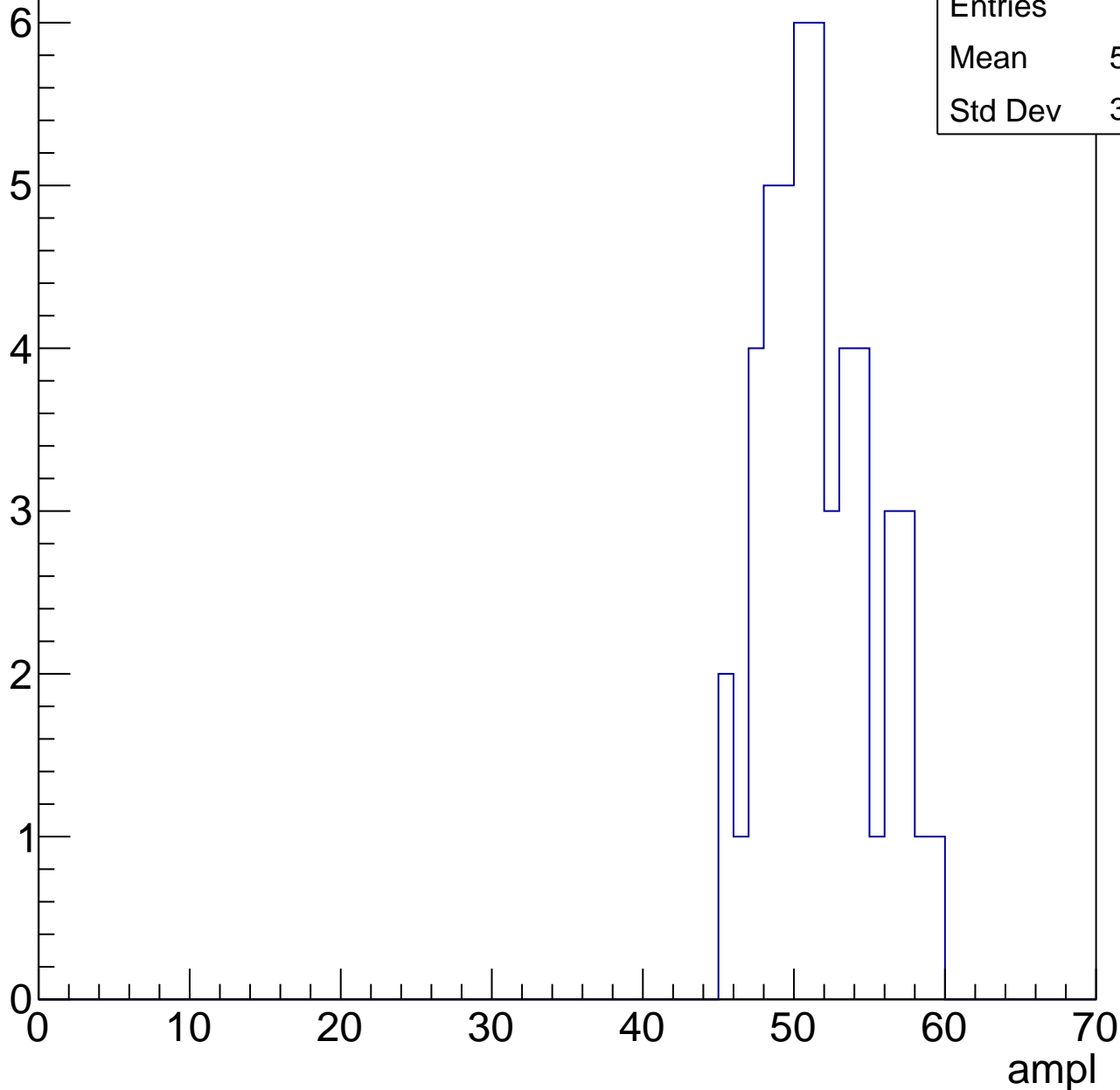


B1L100S, U13-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

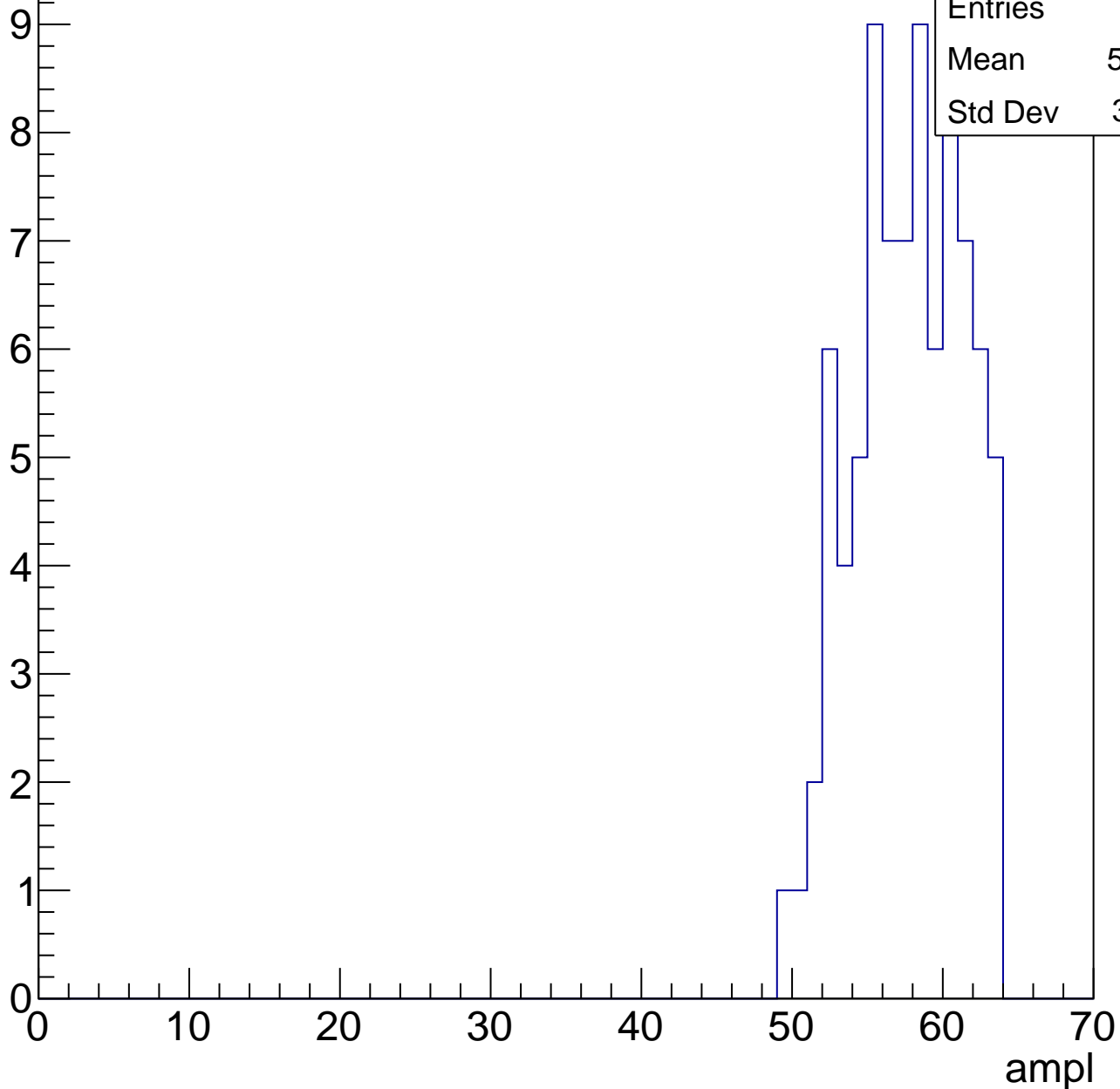
Entries	49
Mean	51.22
Std Dev	3.489



B1L100S, U13-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



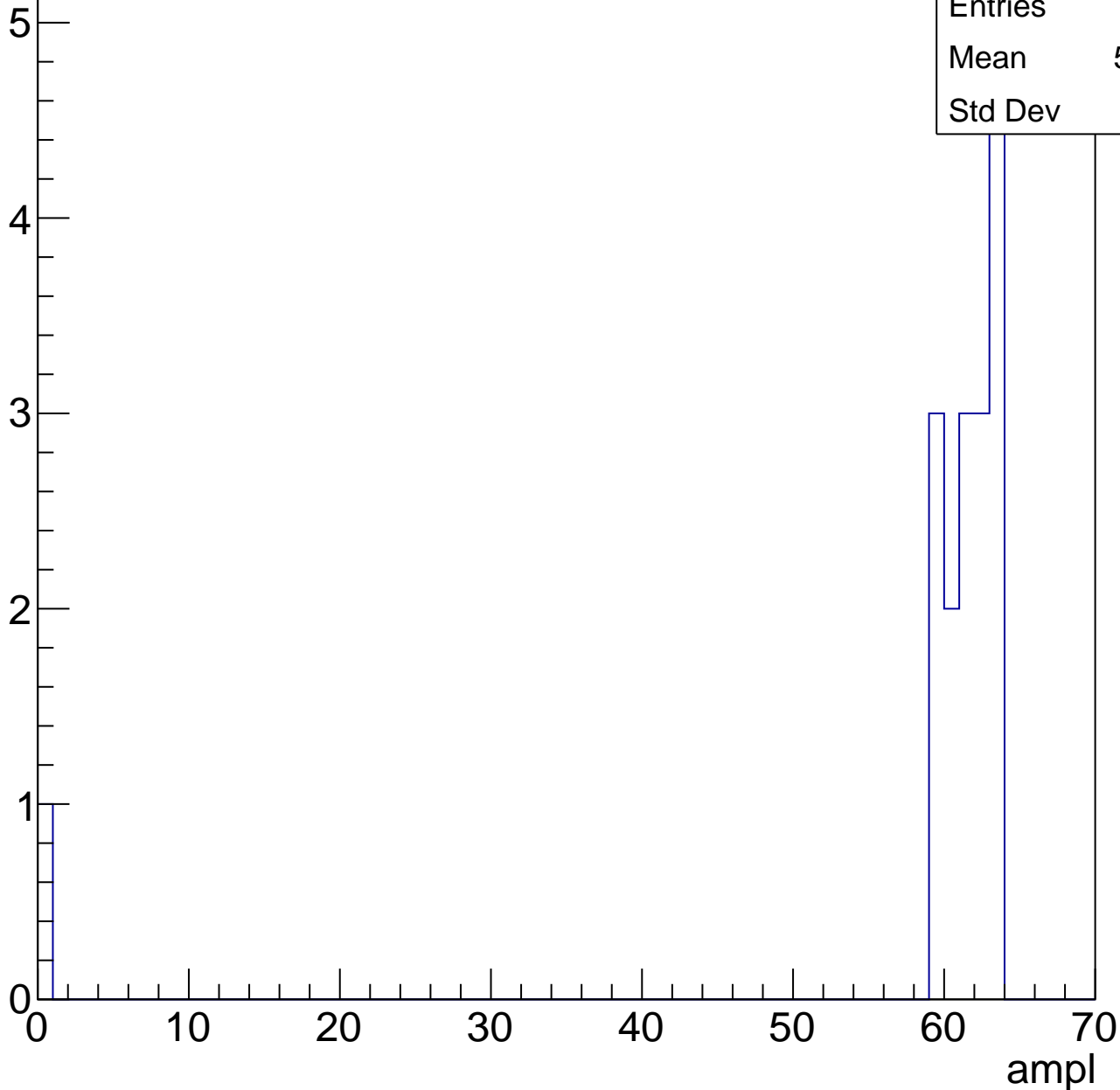
Entries	83
Mean	57.24
Std Dev	3.521

B1L100S, U13-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.71
Std Dev	14.5



B1L100S, U13-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch24, adc0

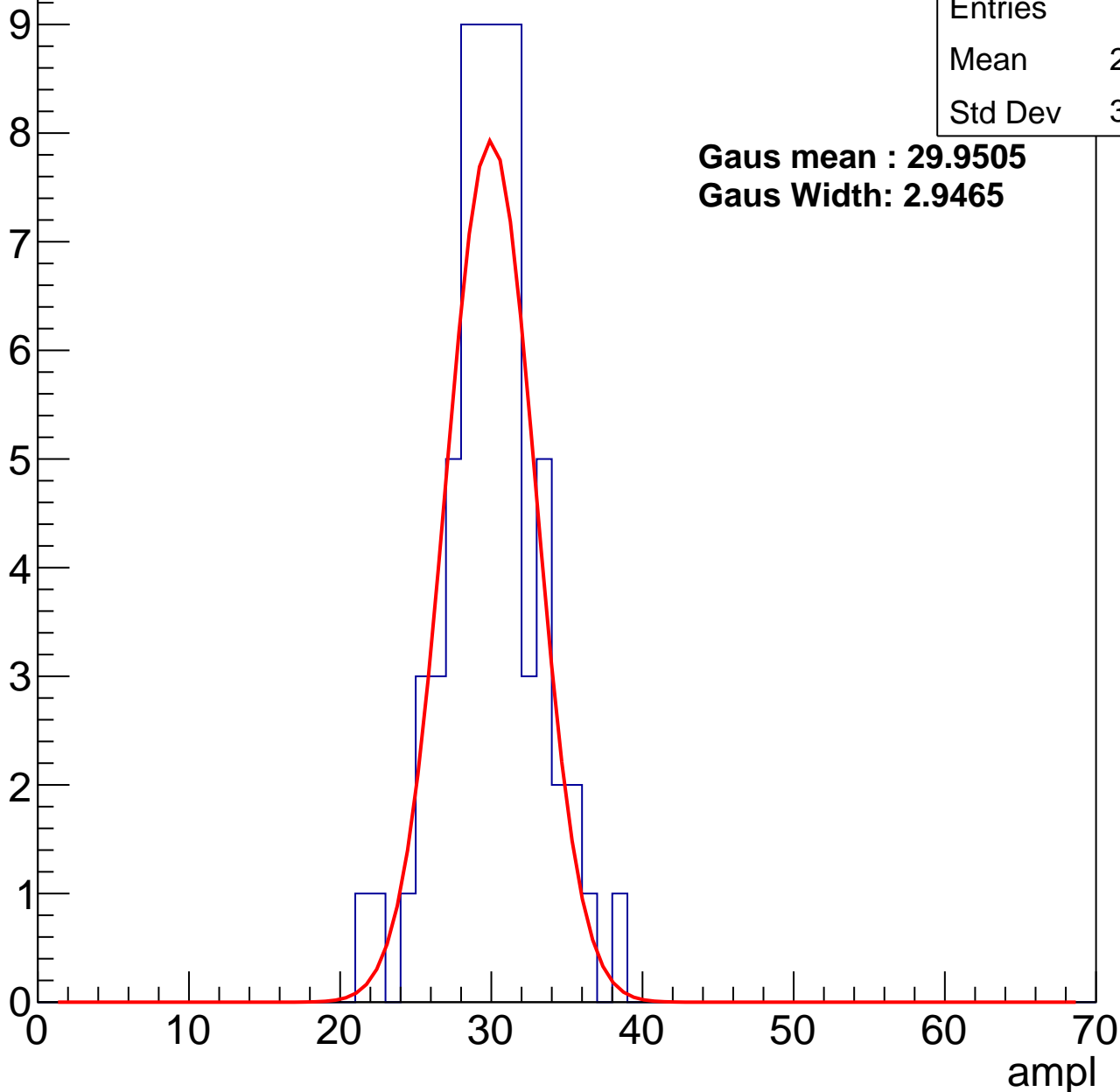
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.53
Std Dev	3.132

Gaus mean : 29.9505

Gaus Width: 2.9465



B1L100S, U13-ch24, adc1

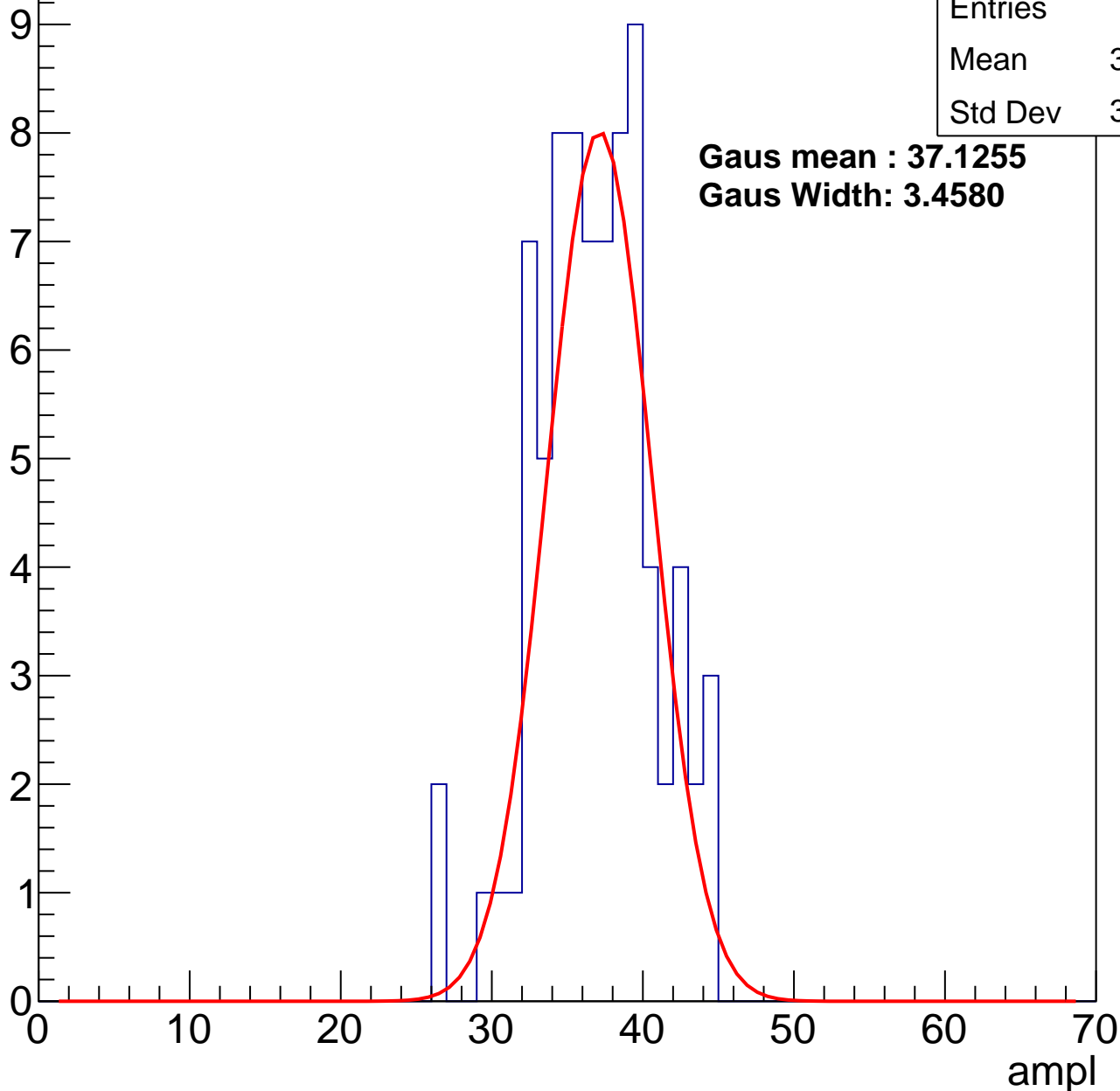
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	36.42
Std Dev	3.834

Gaus mean : 37.1255

Gaus Width: 3.4580

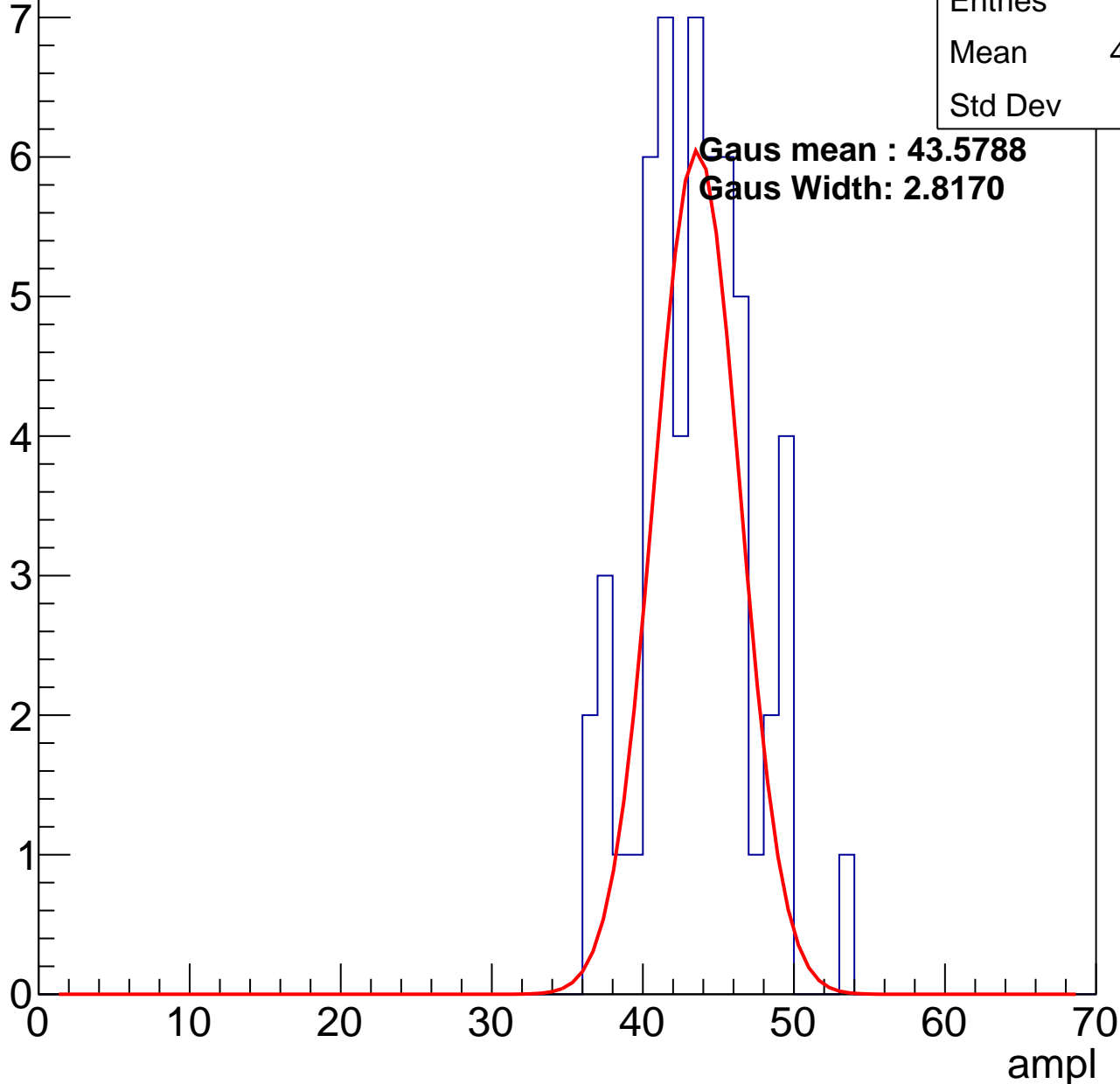


B1L100S, U13-ch24, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.07
Std Dev	3.58

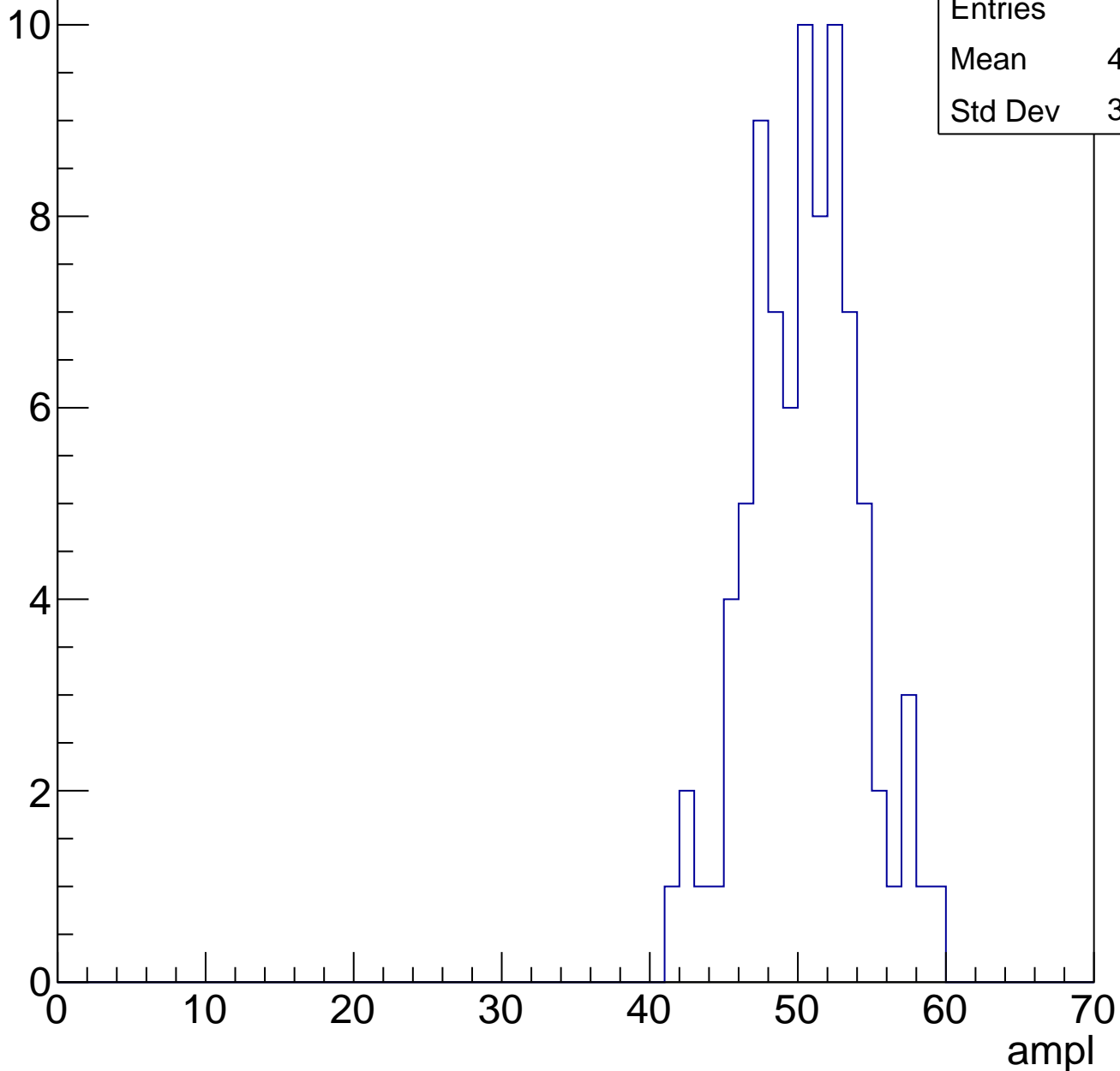


B1L100S, U13-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	49.98
Std Dev	3.713

Entry



B1L100S, U13-ch24, adc4

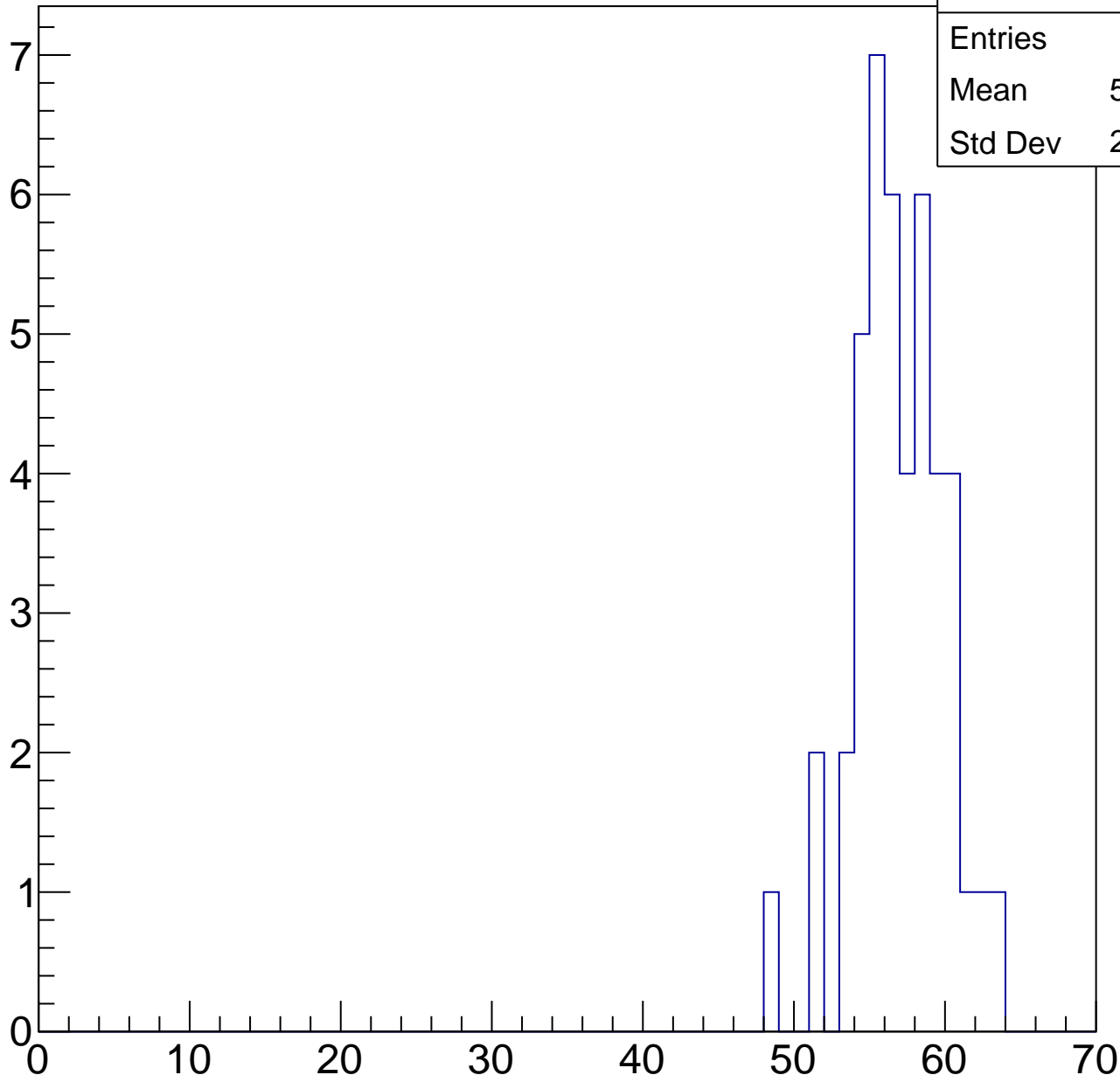
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	56.48
Std Dev	2.966

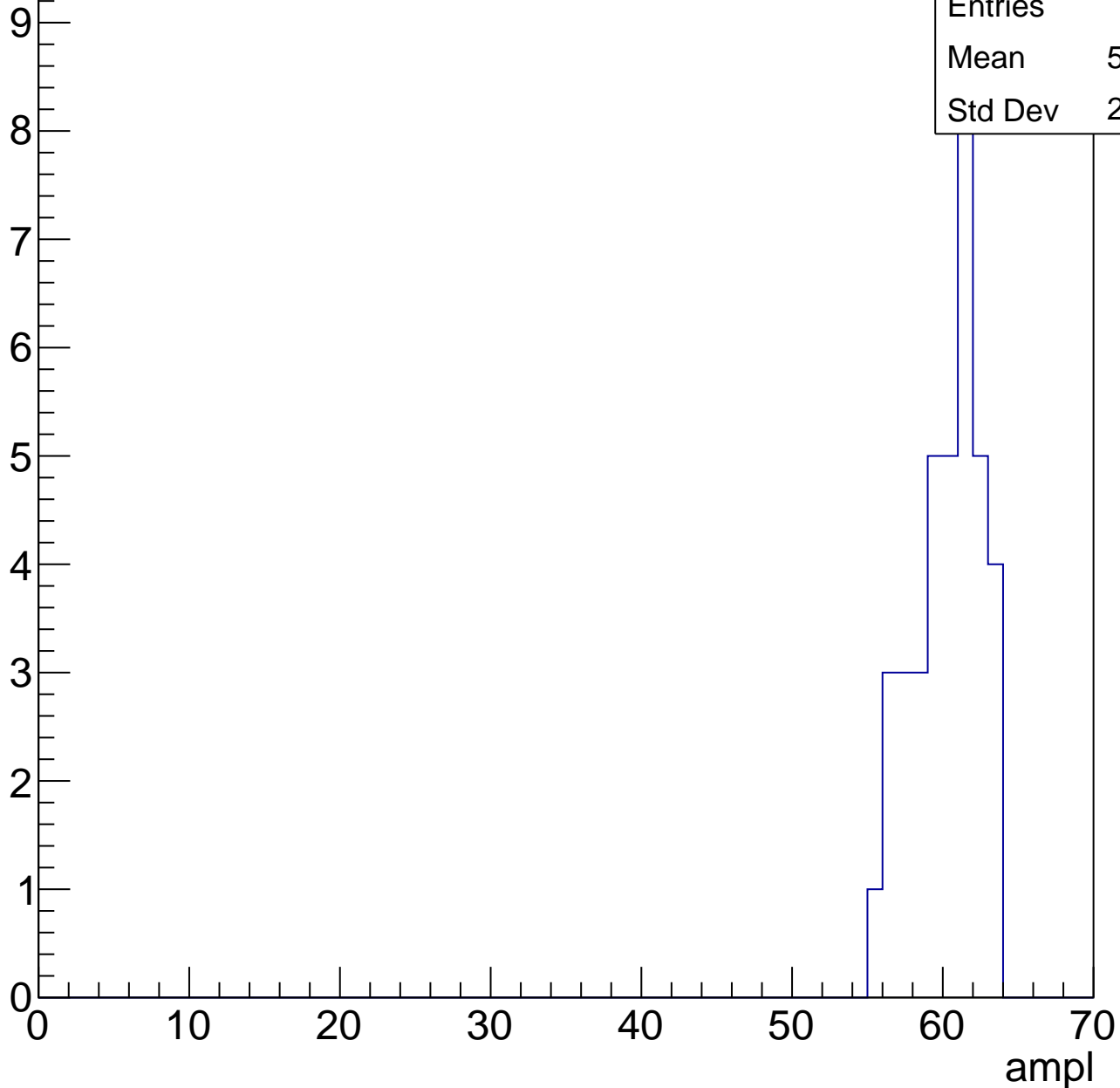
ampl



B1L100S, U13-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

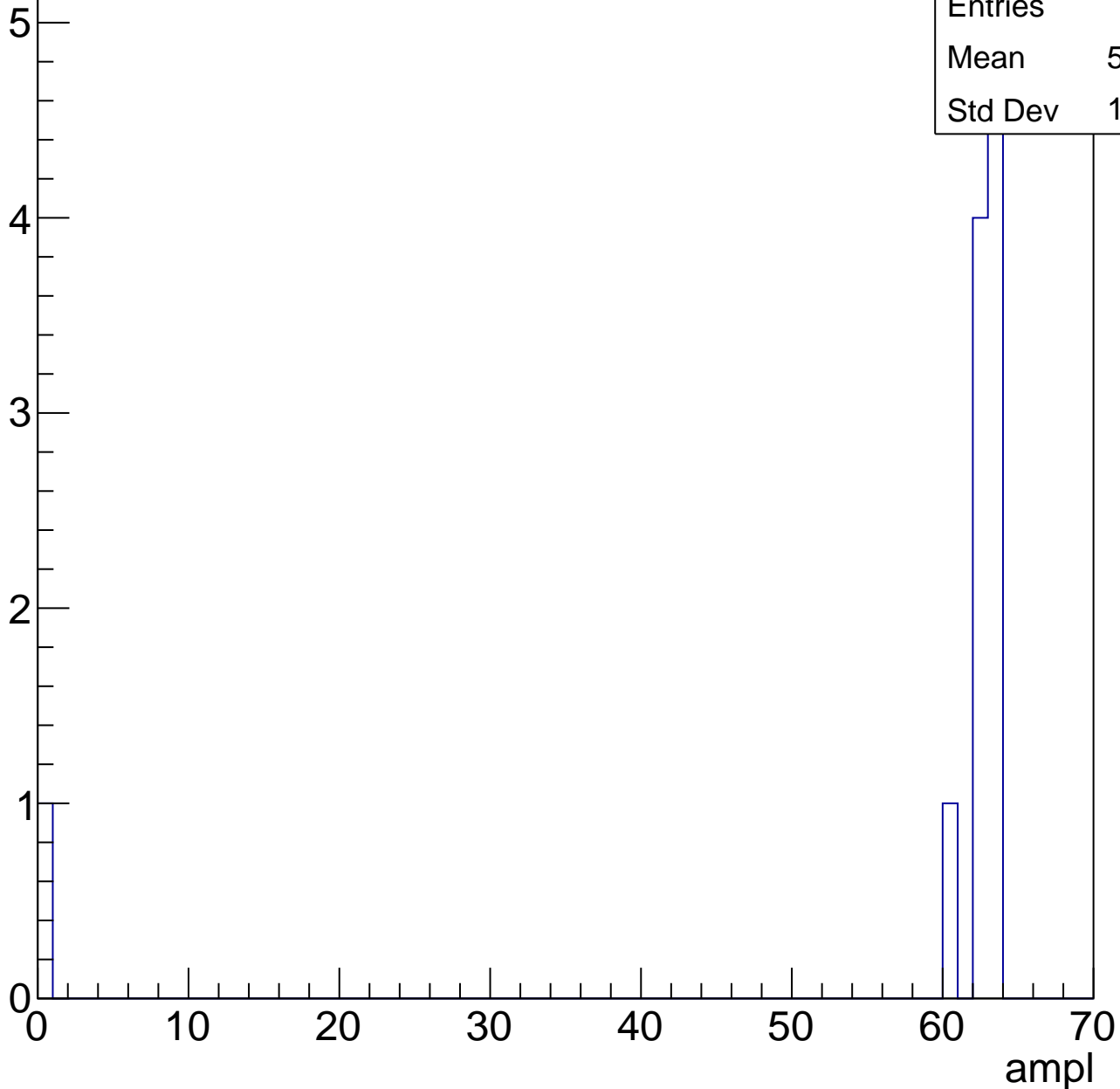


B1L100S, U13-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	56.64
Std Dev	17.93



B1L100S, U13-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch25, adc0

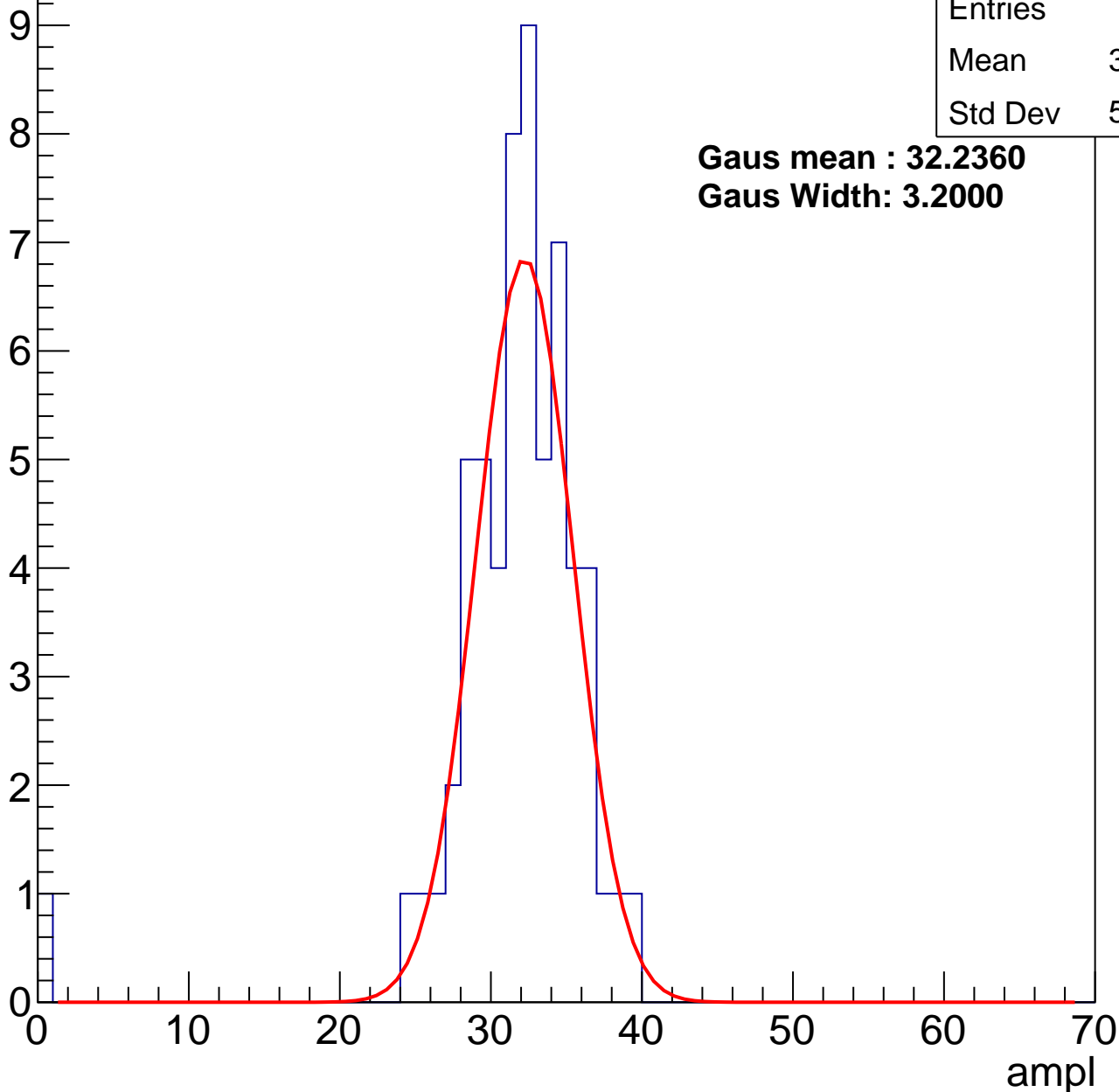
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	31.18
Std Dev	5.123

Gaus mean : 32.2360

Gaus Width: 3.2000



B1L100S, U13-ch25, adc1

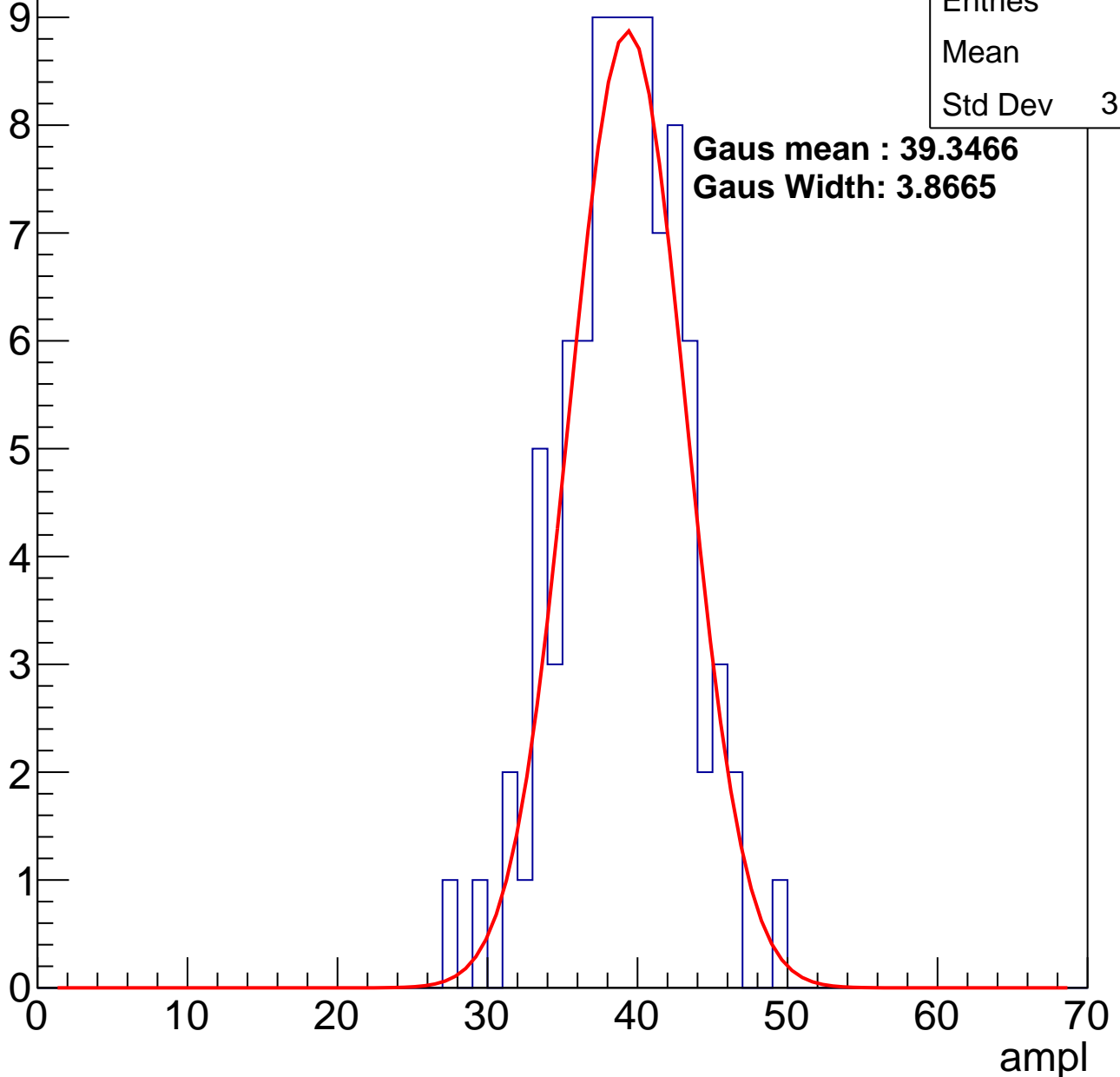
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	38.6
Std Dev	3.969

Gaus mean : 39.3466

Gaus Width: 3.8665



B1L100S, U13-ch25, adc2

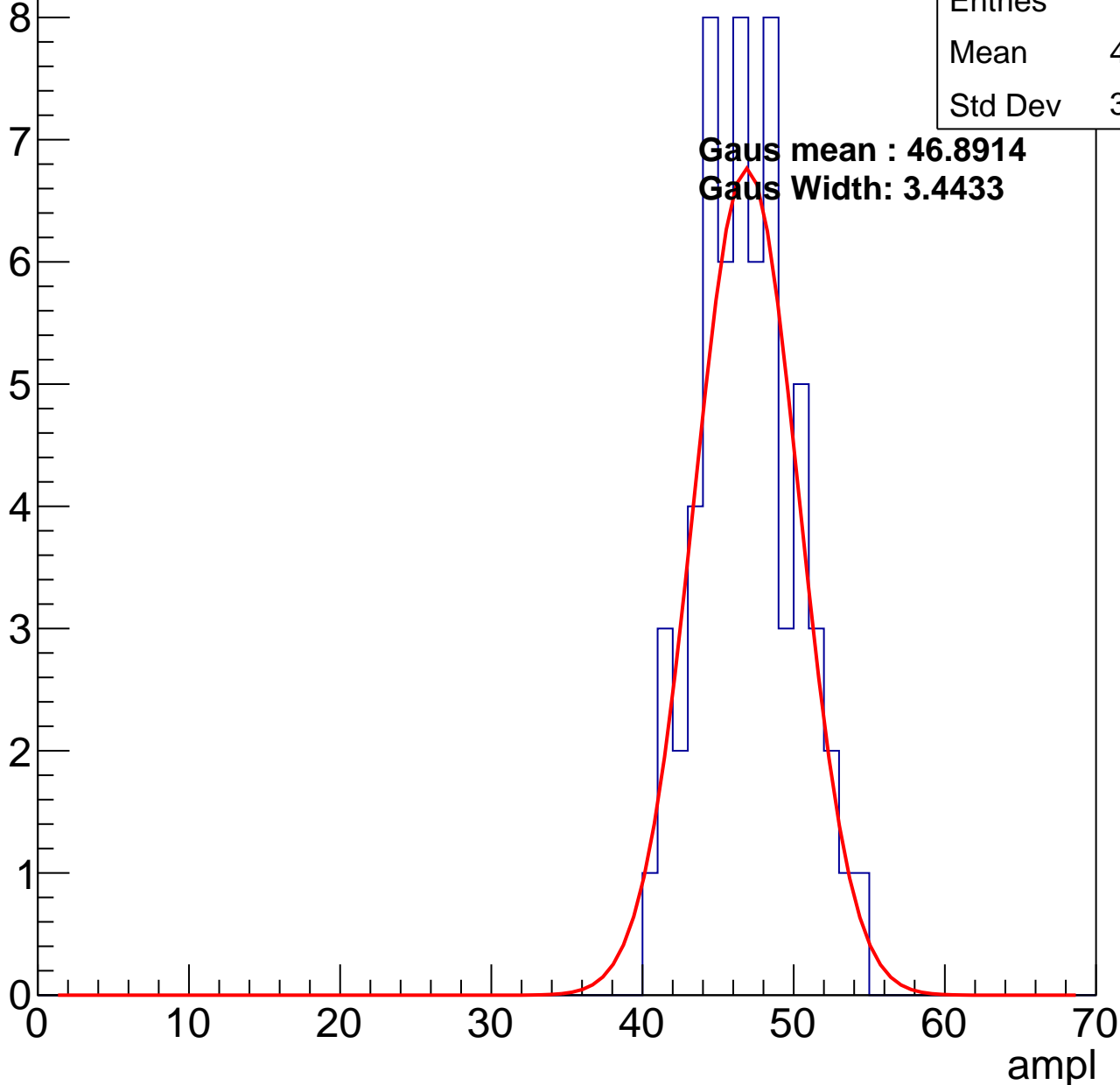
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	46.49
Std Dev	3.155

Gaus mean : 46.8914

Gaus Width: 3.4433

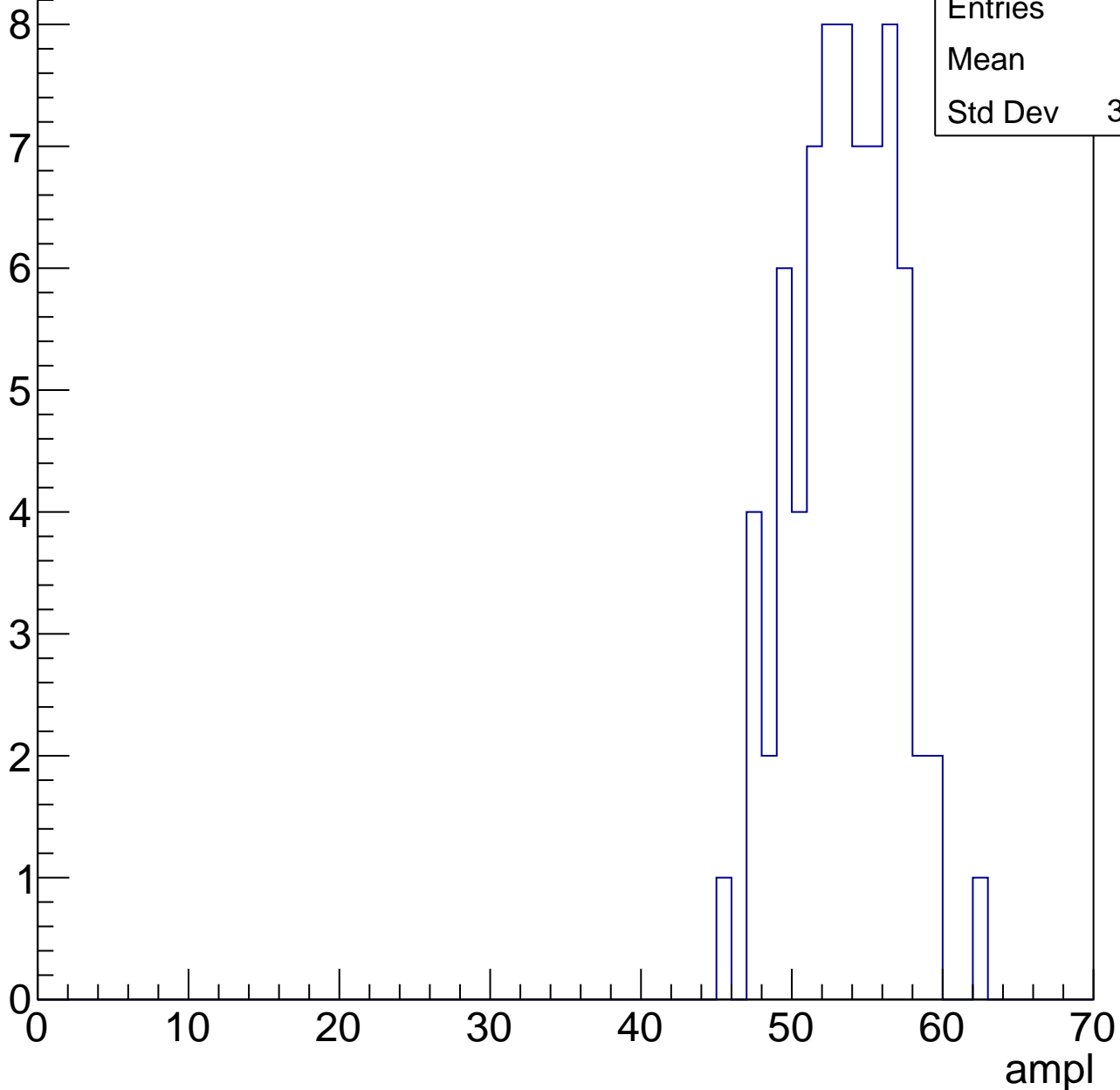


B1L100S, U13-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	53
Std Dev	3.376



B1L100S, U13-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	59.17
Std Dev	2.799

8
7
6
5
4
3
2
1
0

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

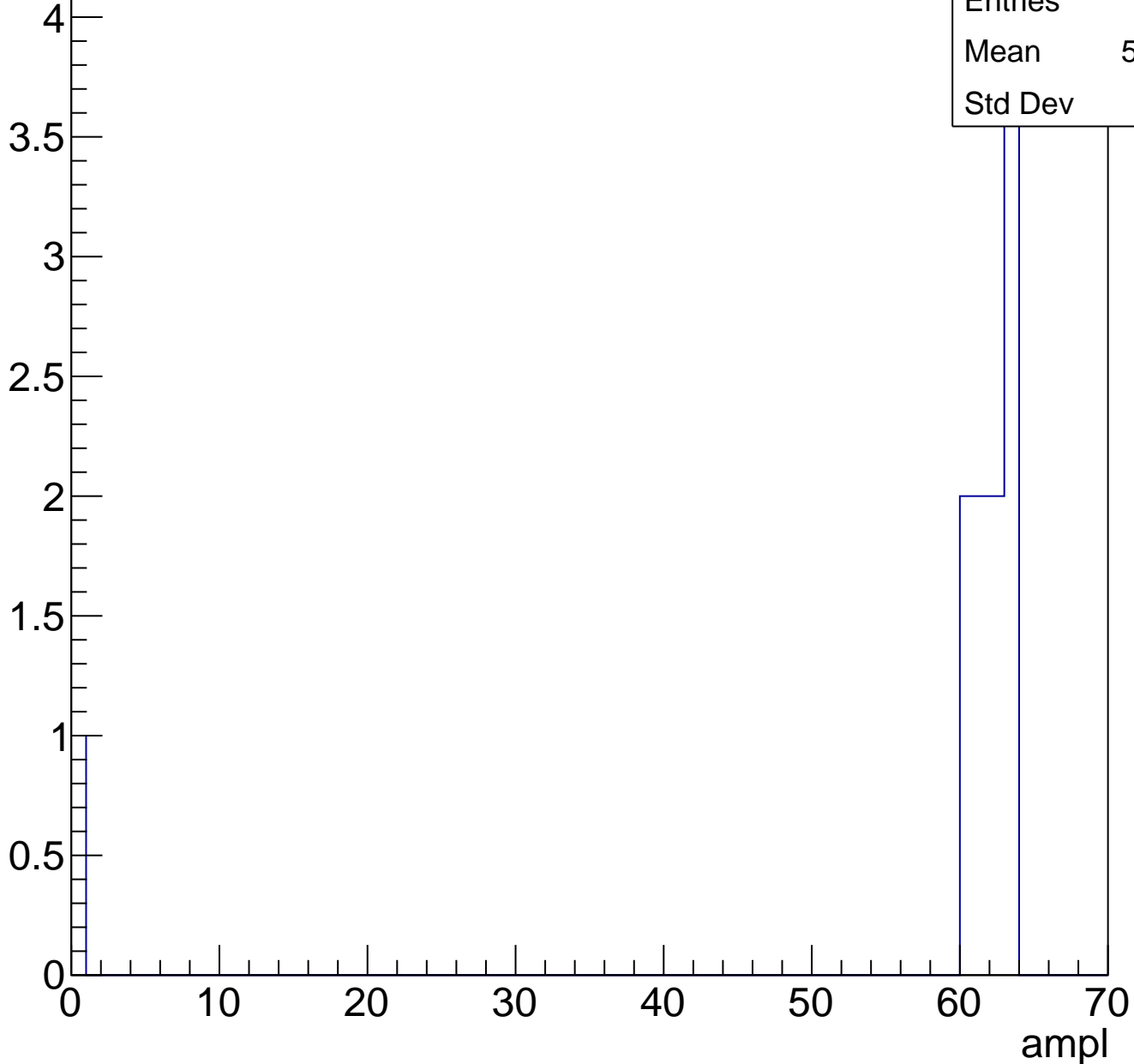
7

8

B1L100S, U13-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch26, adc0

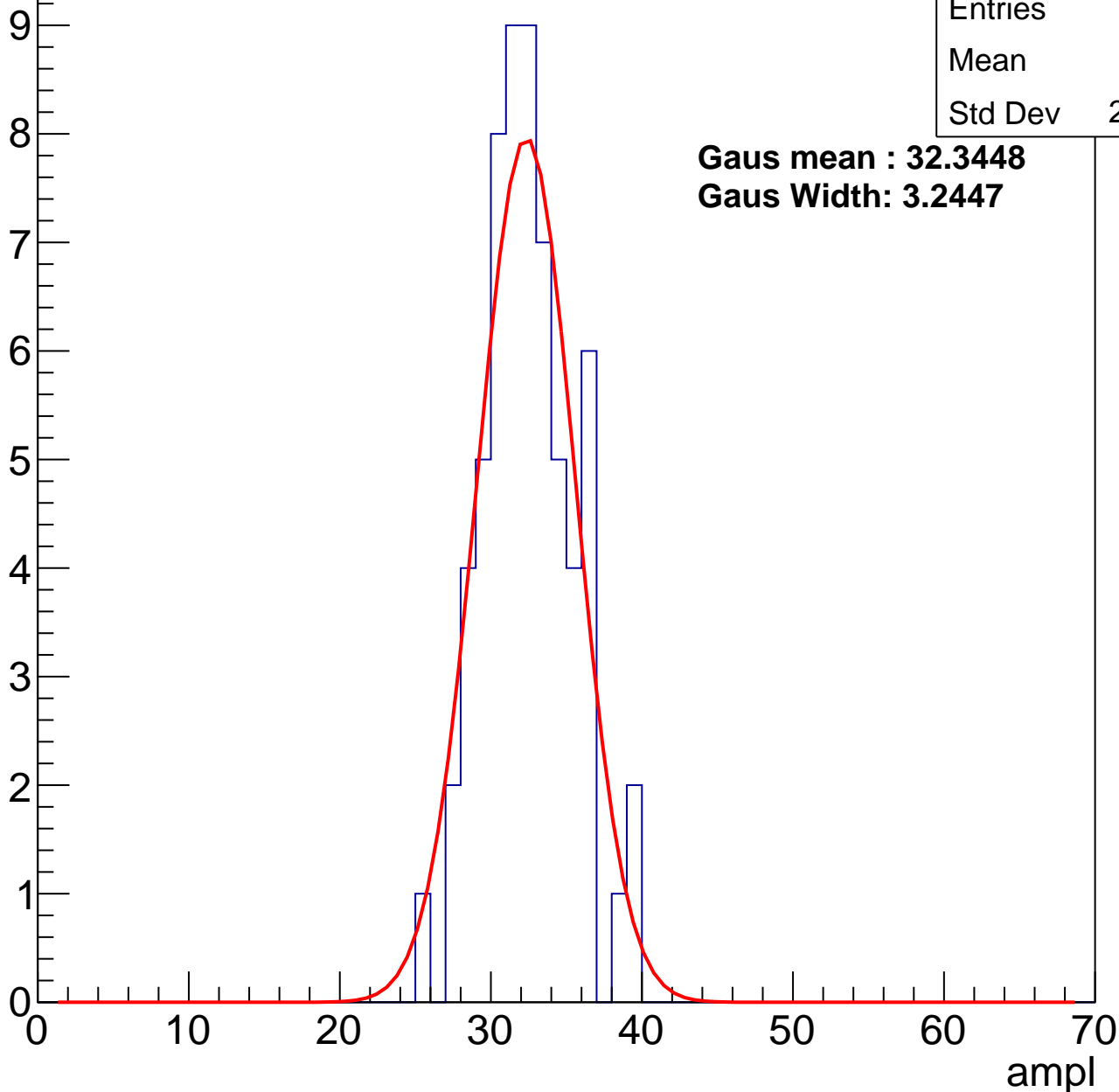
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	32
Std Dev	2.933

Gaus mean : 32.3448

Gaus Width: 3.2447



B1L100S, U13-ch26, adc1

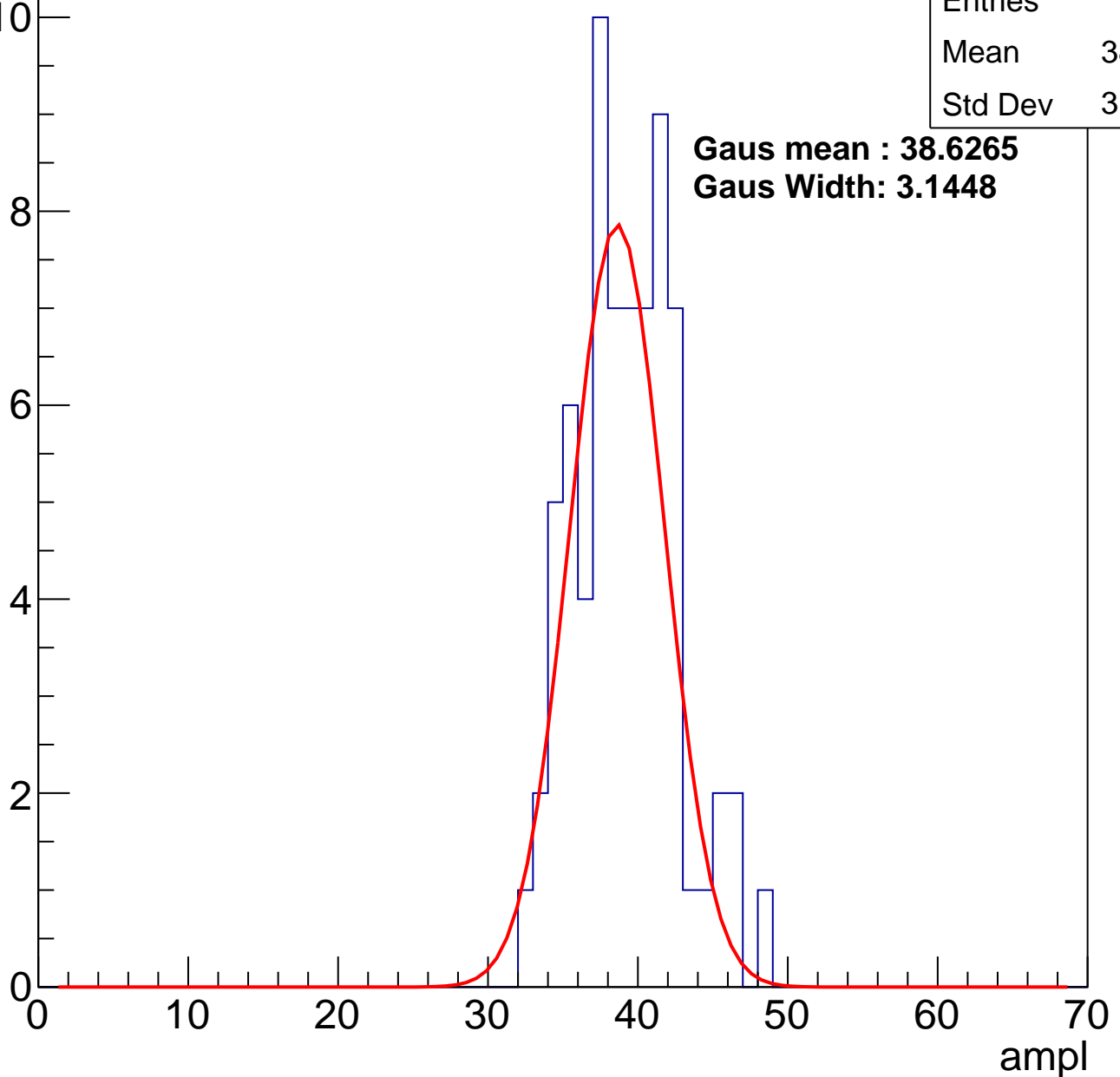
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.76
Std Dev	3.373

Gaus mean : 38.6265

Gaus Width: 3.1448



B1L100S, U13-ch26, adc2

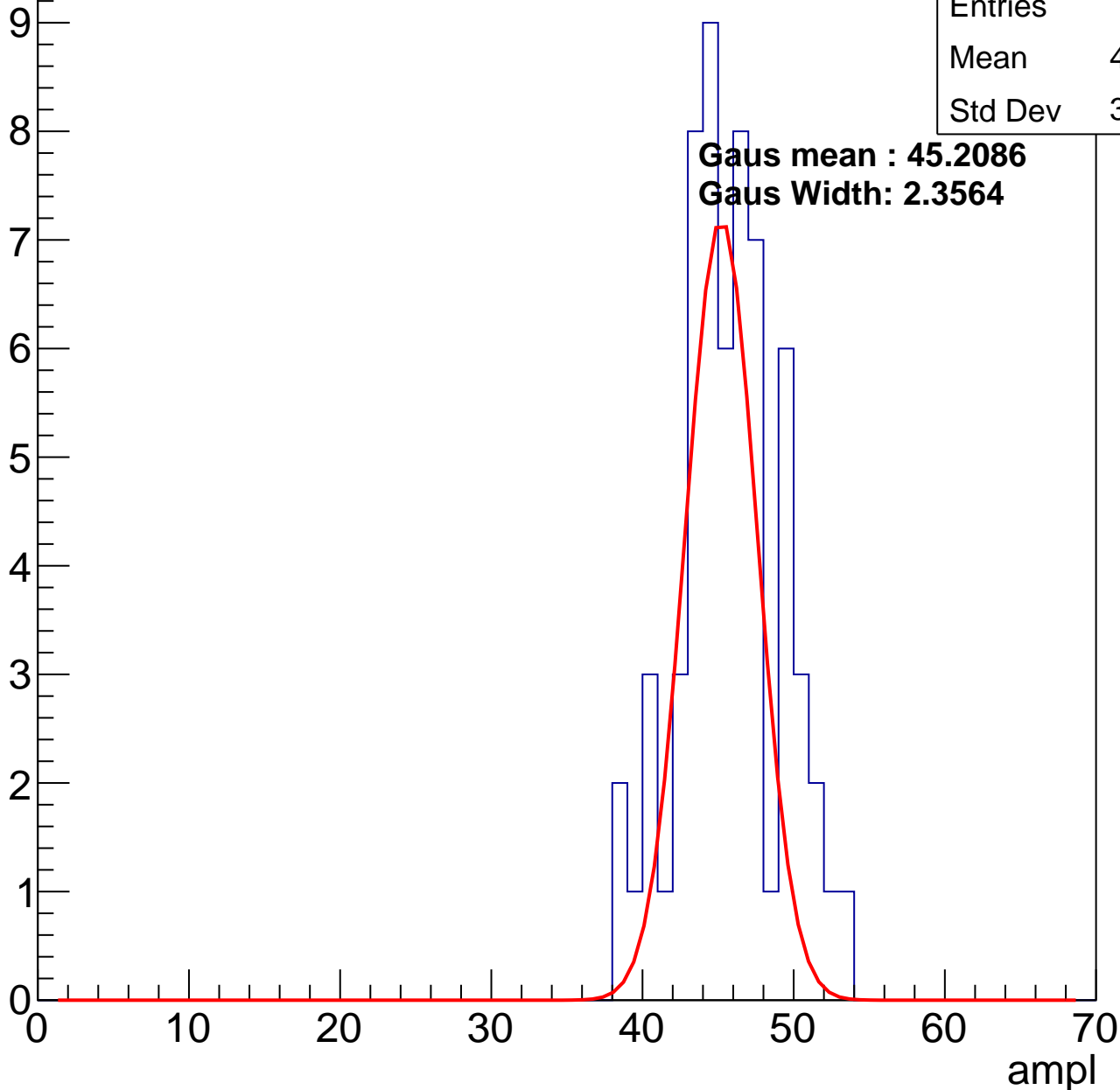
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	45.29
Std Dev	3.333

Gaus mean : 45.2086

Gaus Width: 2.3564

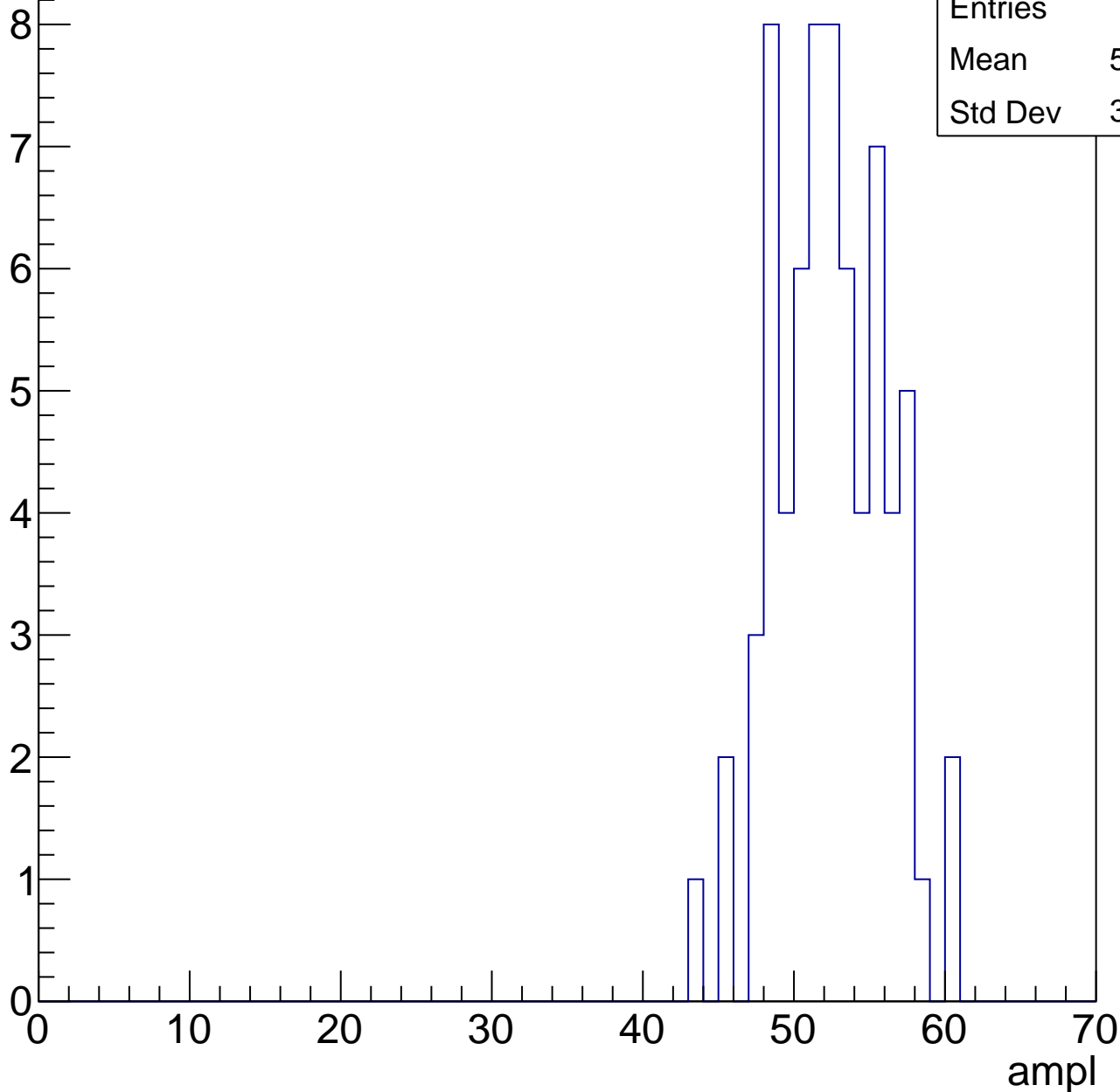


B1L100S, U13-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	51.94
Std Dev	3.587

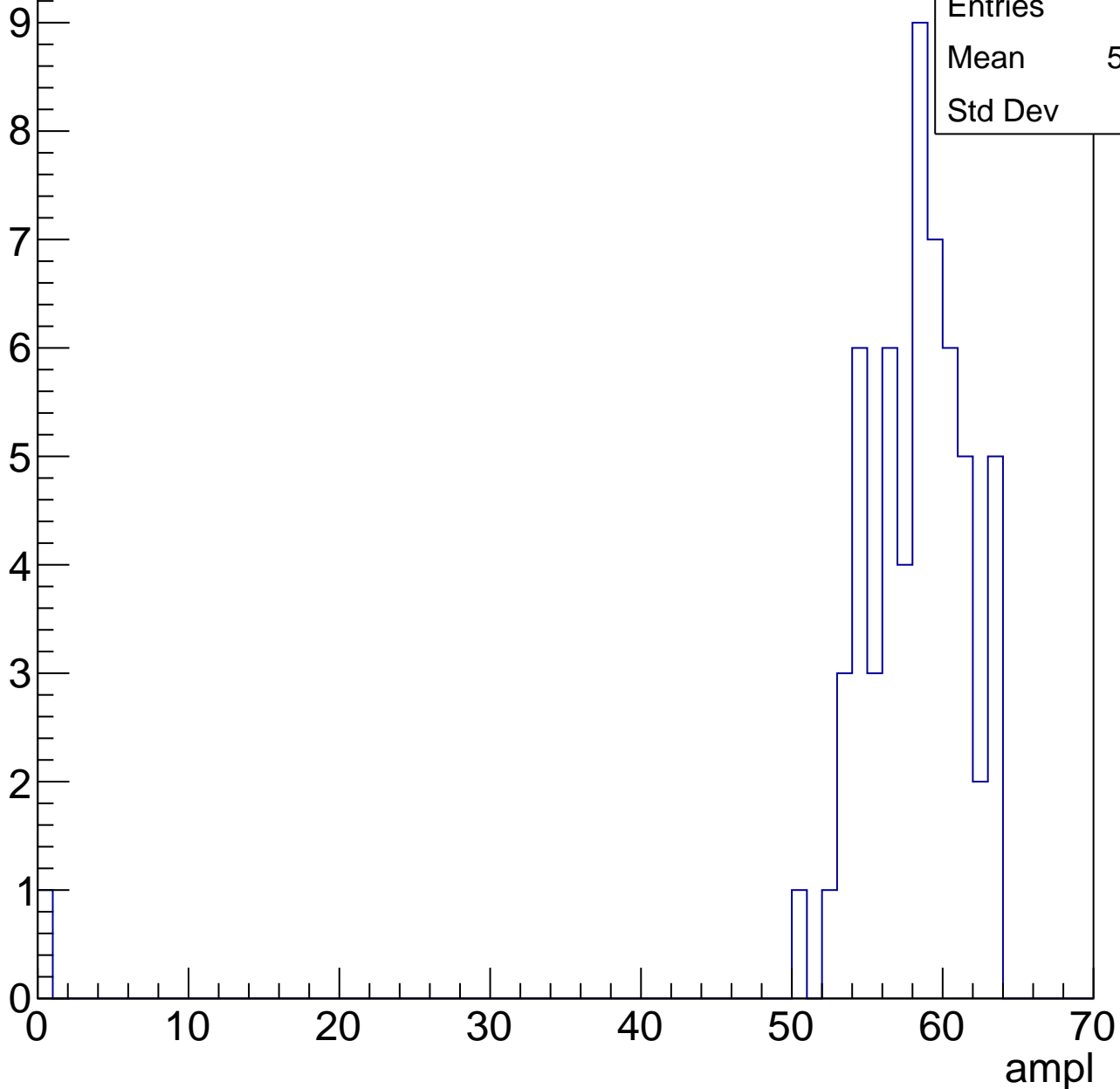


B1L100S, U13-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.83
Std Dev	8.07

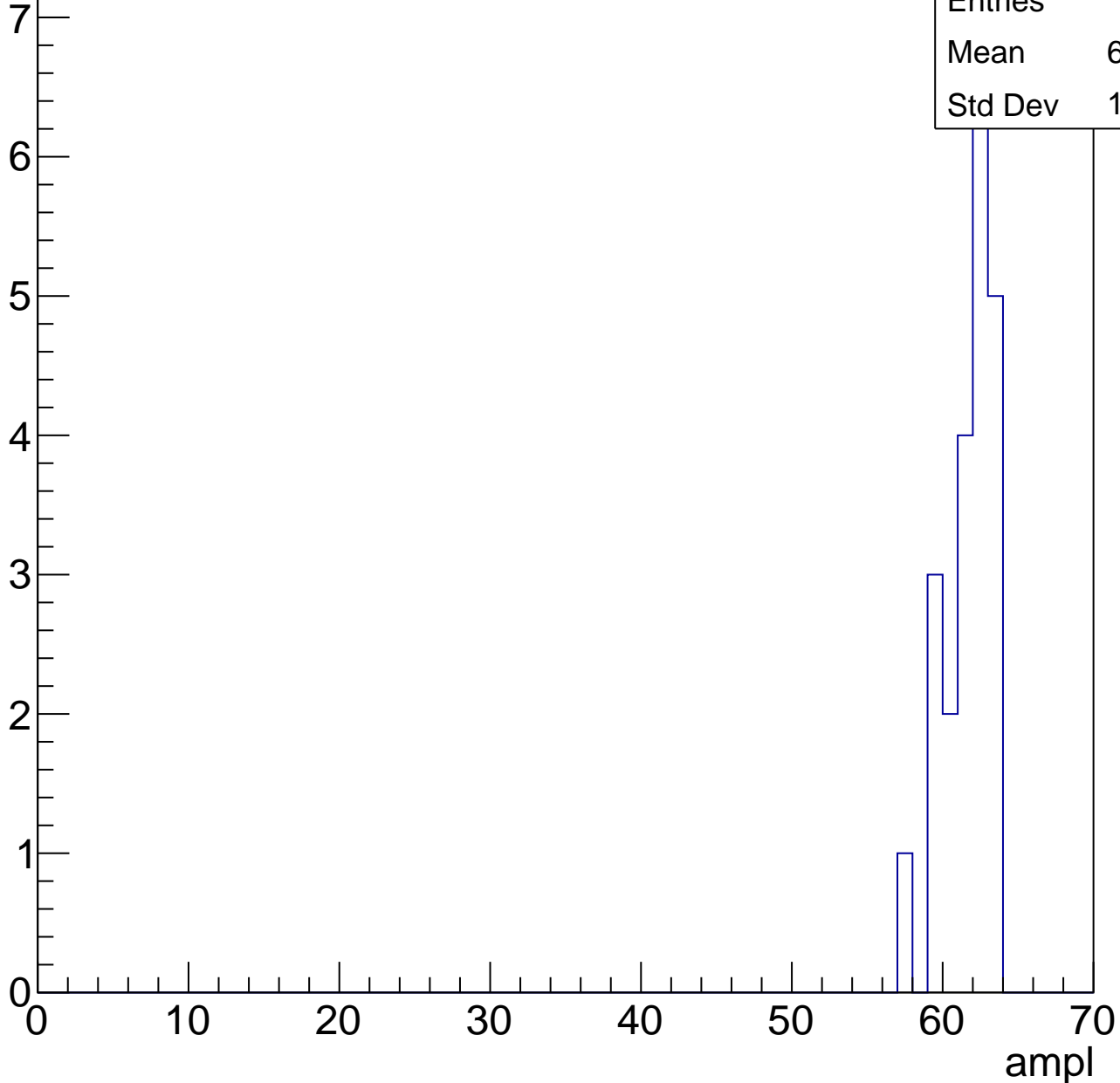


B1L100S, U13-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	61.23
Std Dev	1.594



B1L100S, U13-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L100S, U13-ch27, adc0

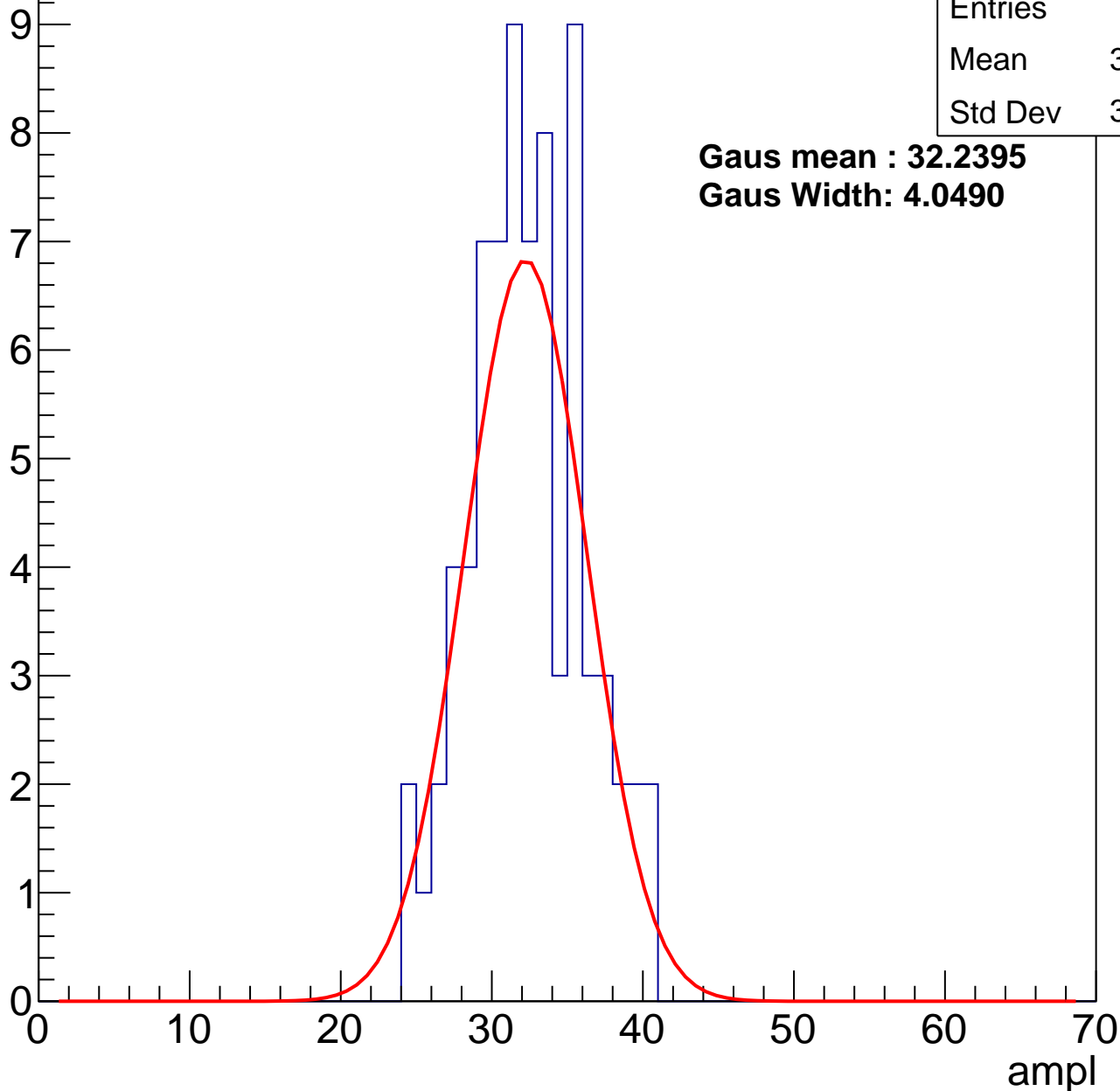
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	31.93
Std Dev	3.714

Gaus mean : 32.2395

Gaus Width: 4.0490



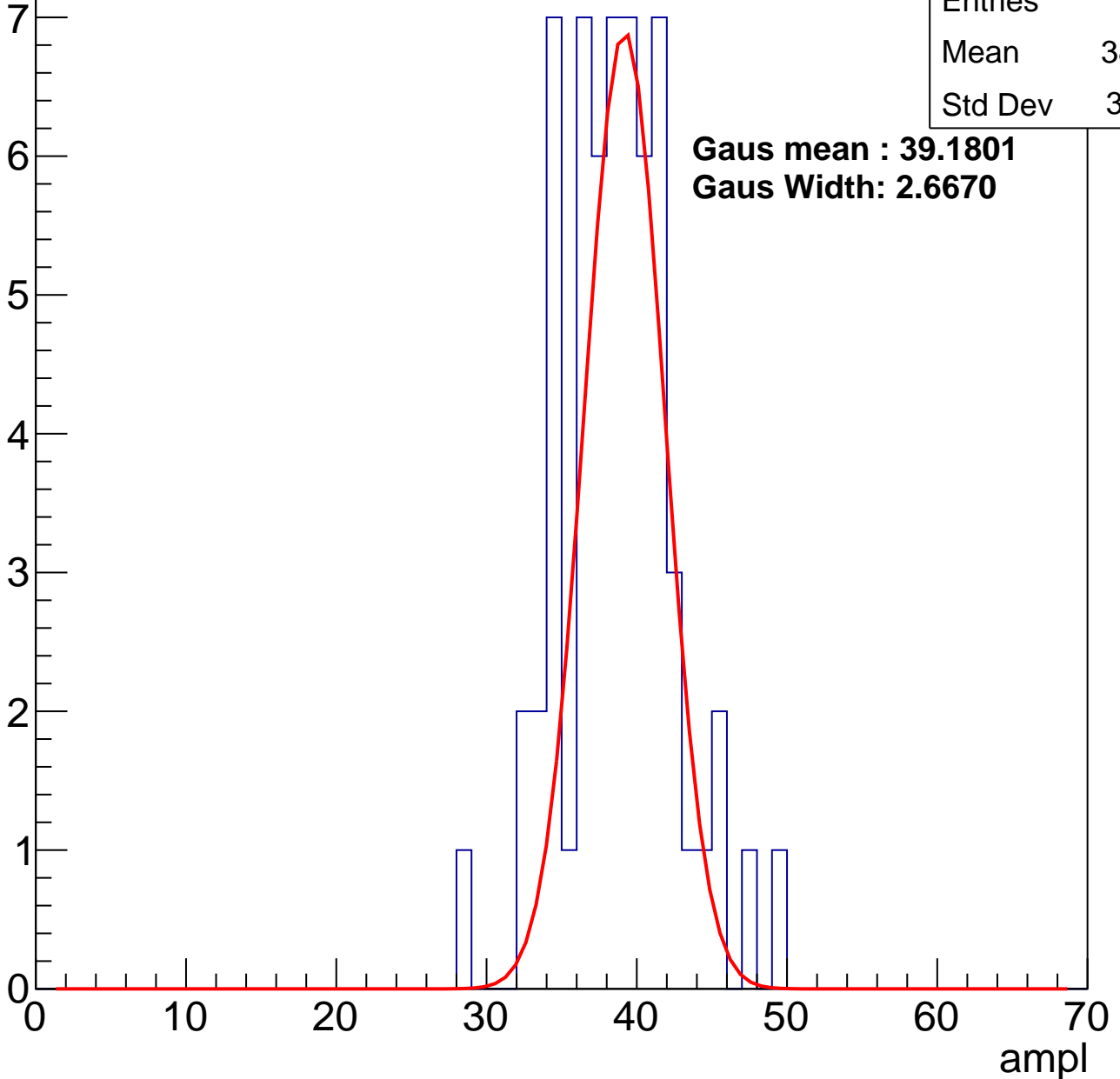
B1L100S, U13-ch27, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	38.23
Std Dev	3.761

Gaus mean : 39.1801
Gaus Width: 2.6670



B1L100S, U13-ch27, adc2

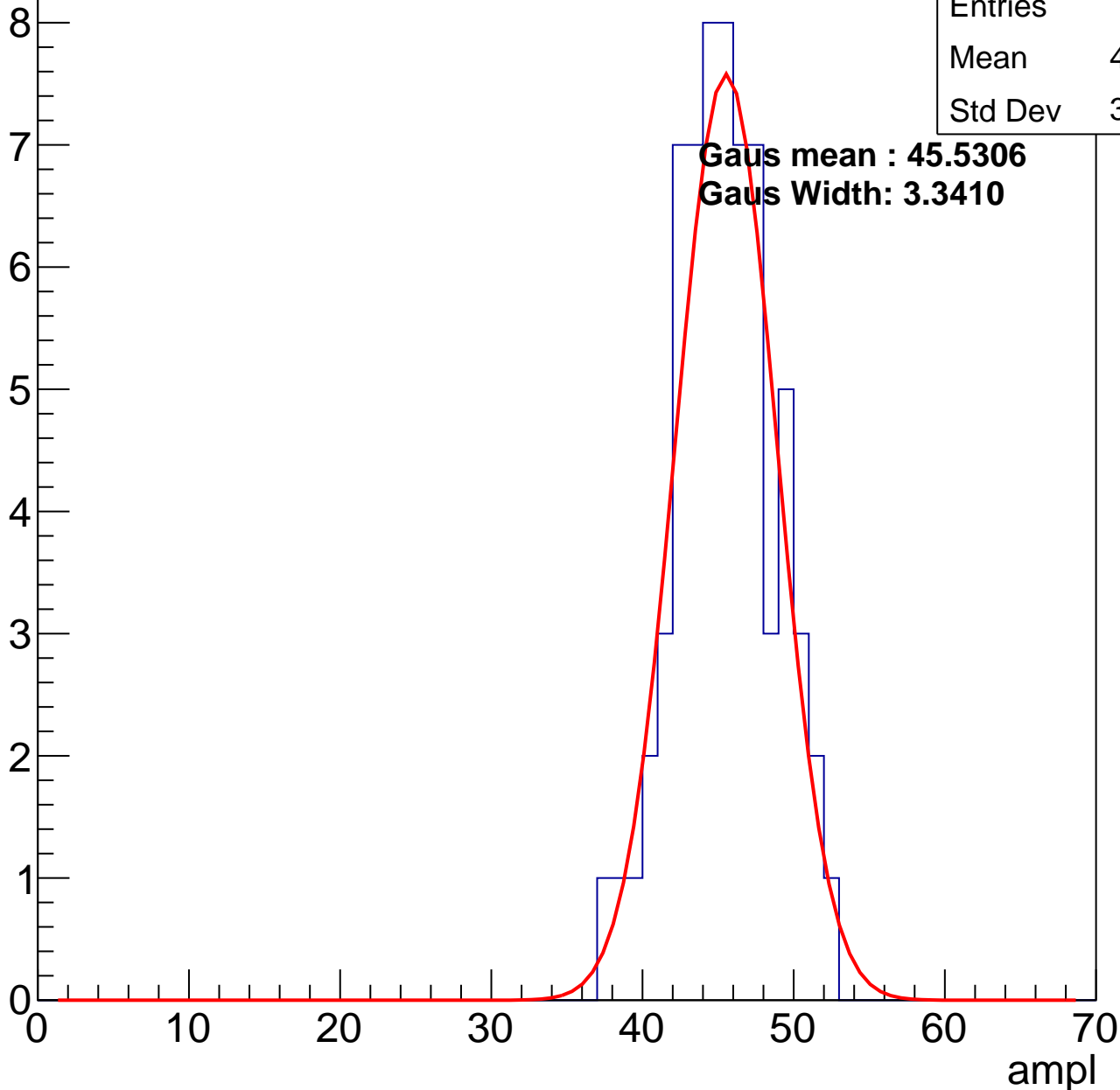
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.97
Std Dev	3.219

Gaus mean : 45.5306

Gaus Width: 3.3410

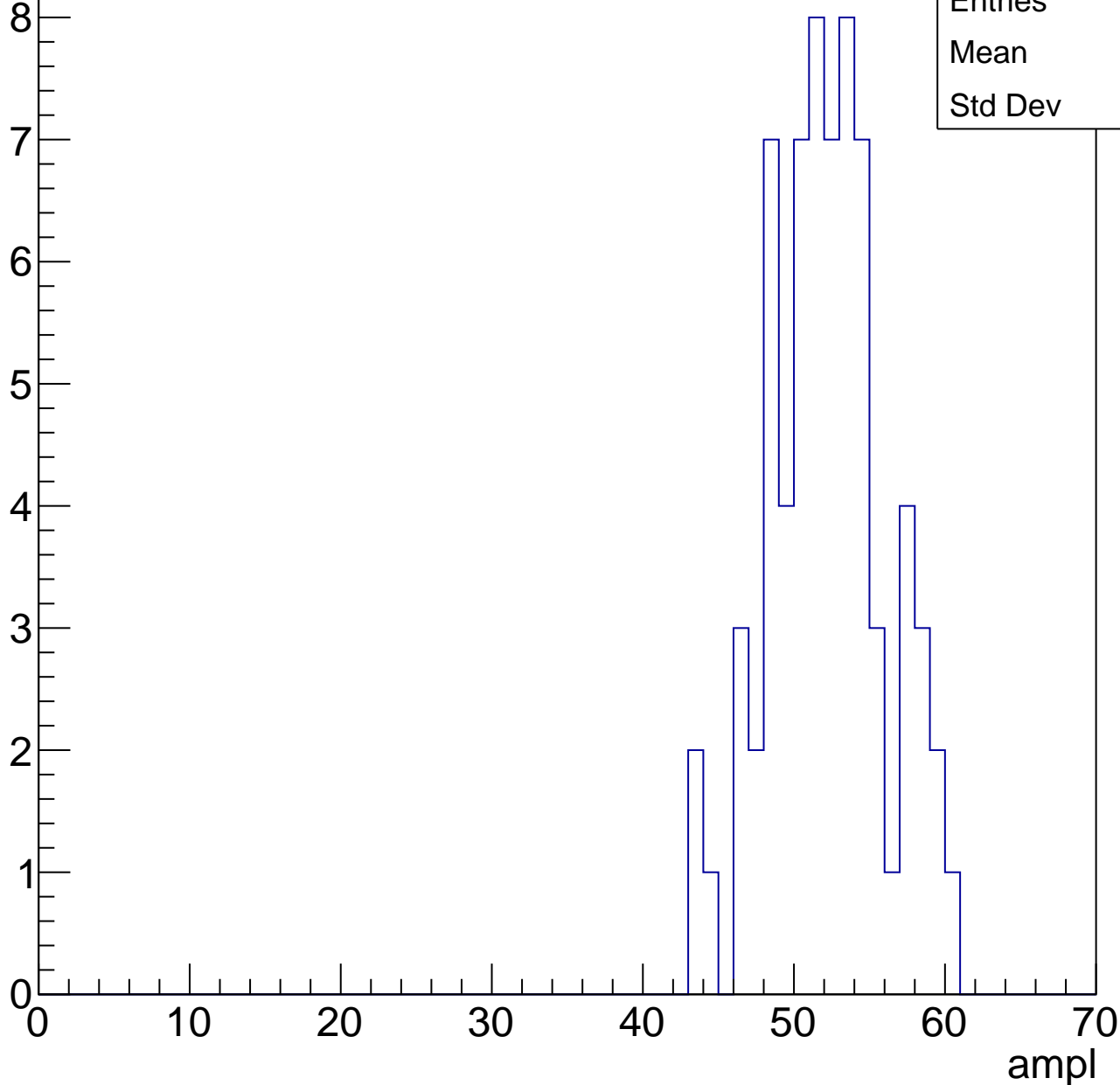


B1L100S, U13-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

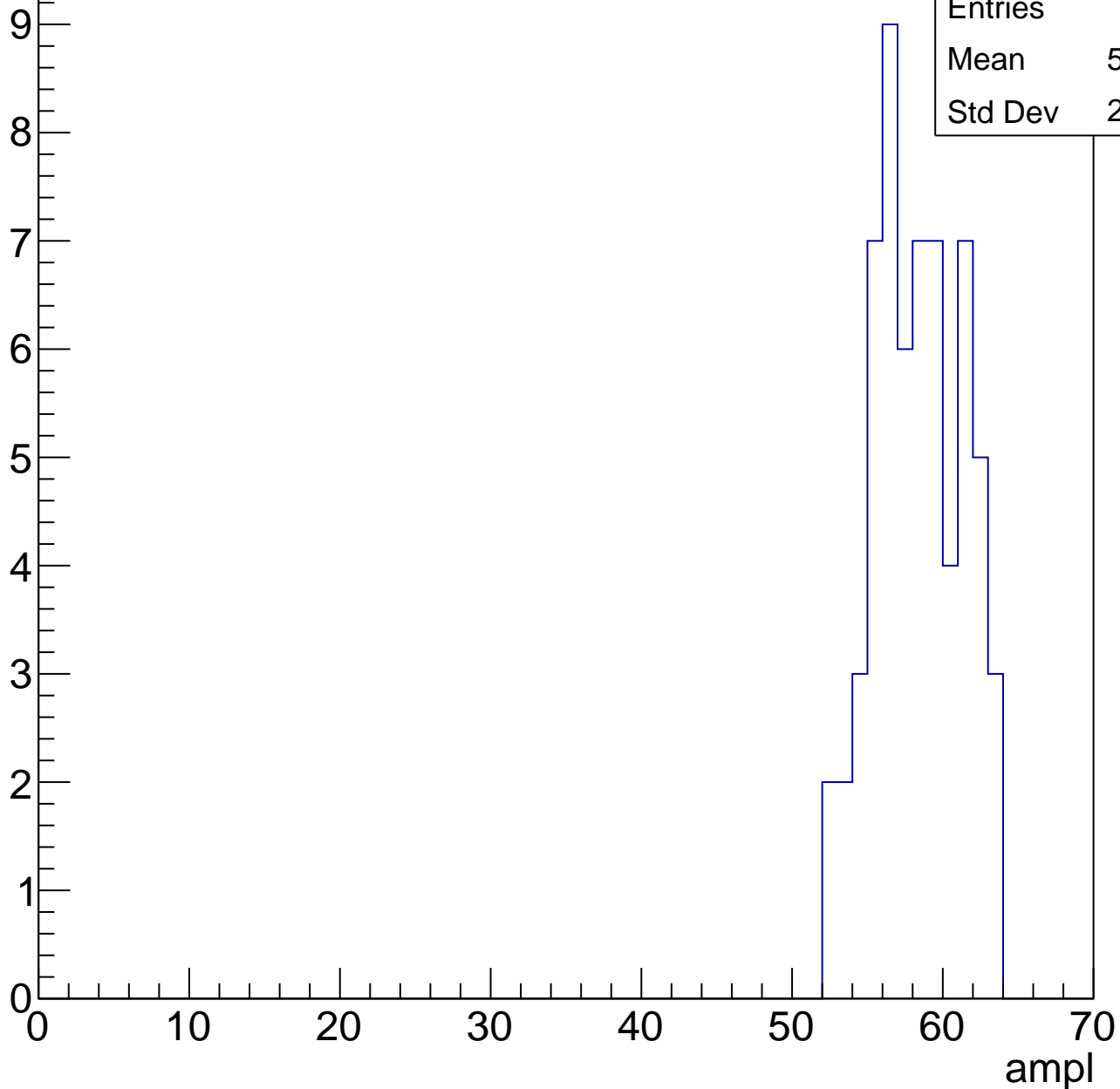
Entries	70
Mean	51.7
Std Dev	3.8



B1L100S, U13-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



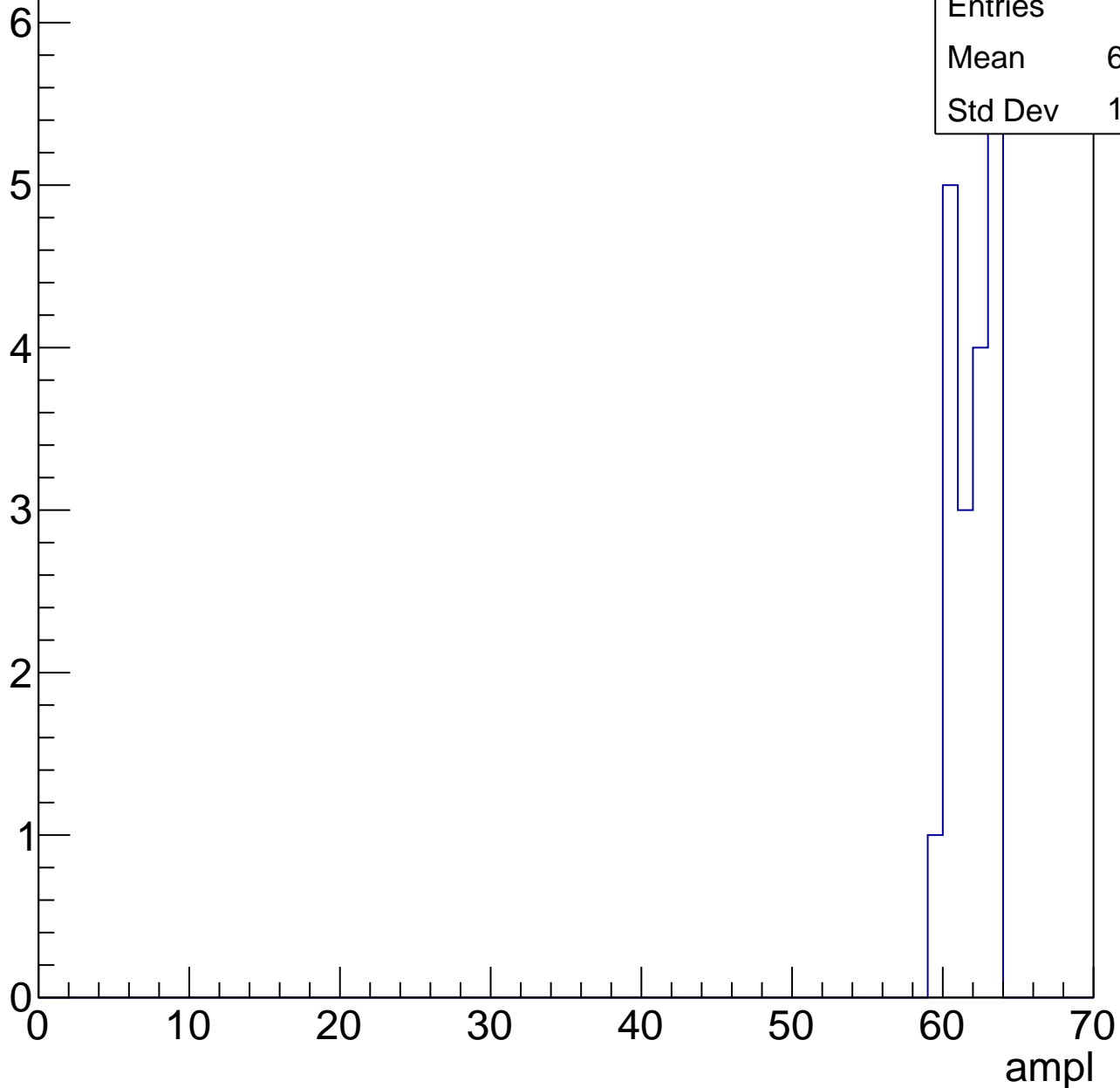
Entries	62
Mean	57.87
Std Dev	2.882

B1L100S, U13-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

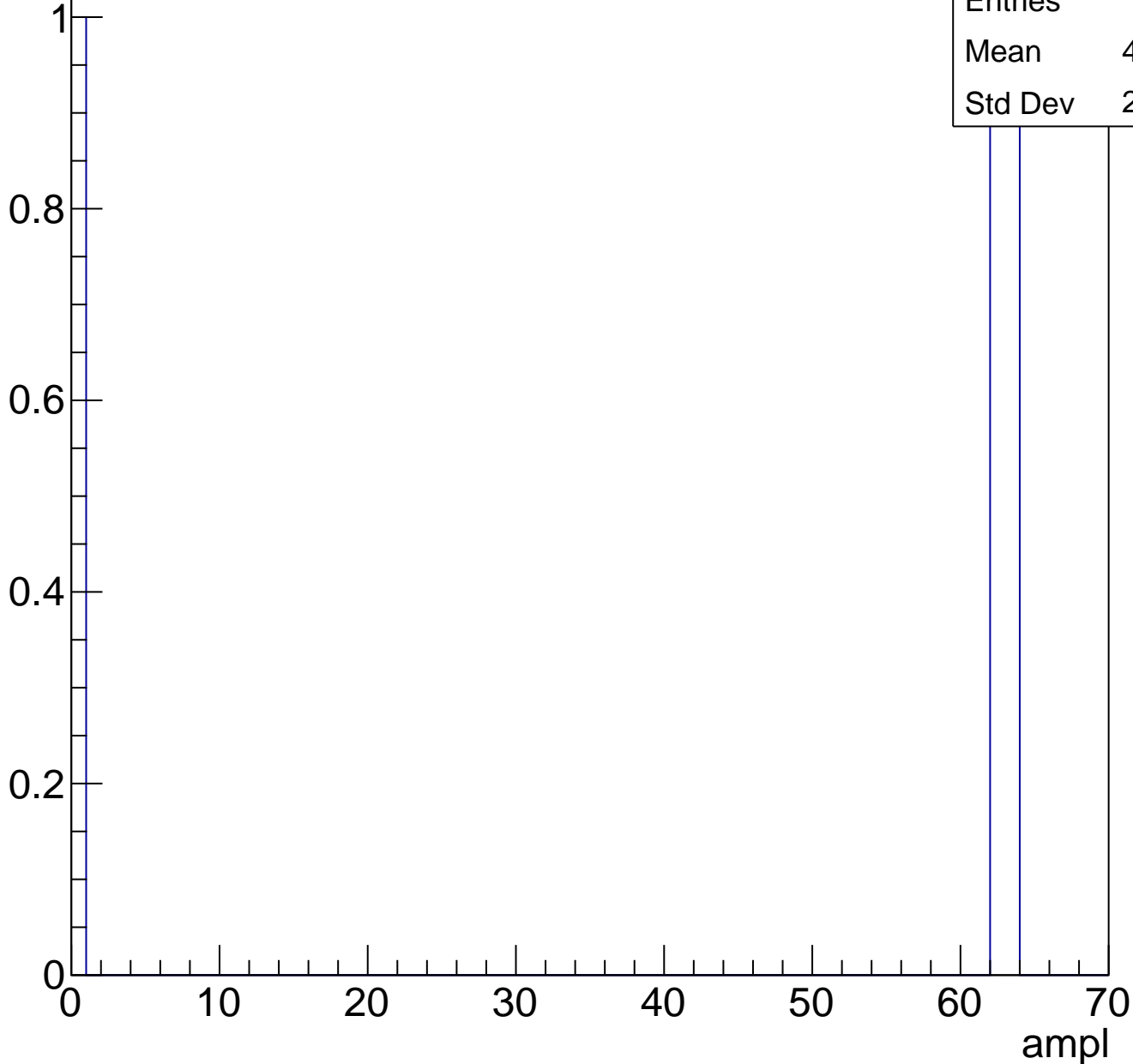
Entries	19
Mean	61.47
Std Dev	1.313



B1L100S, U13-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch28, adc0

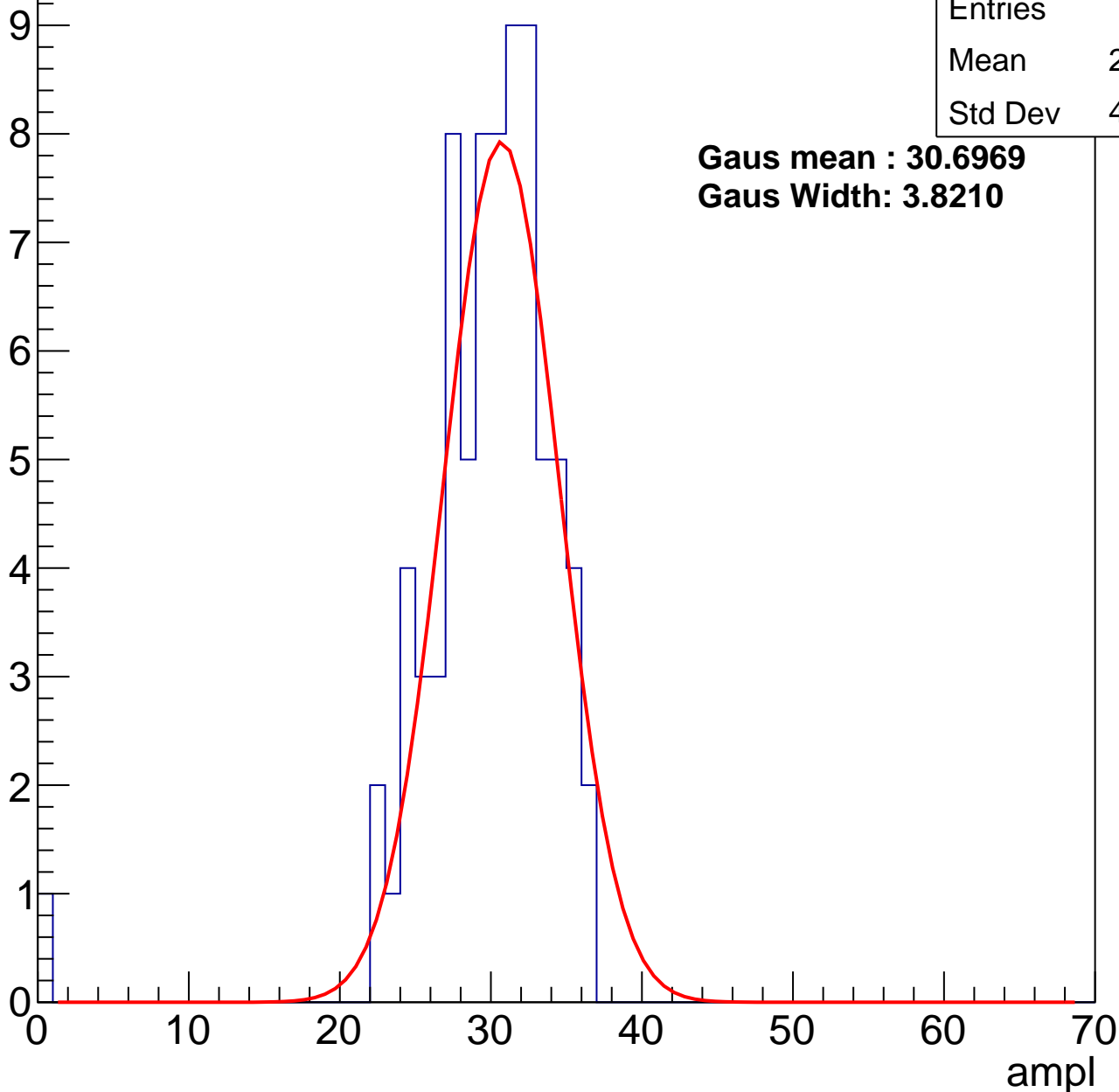
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.32
Std Dev	4.769

Gaus mean : 30.6969

Gaus Width: 3.8210



B1L100S, U13-ch28, adc1

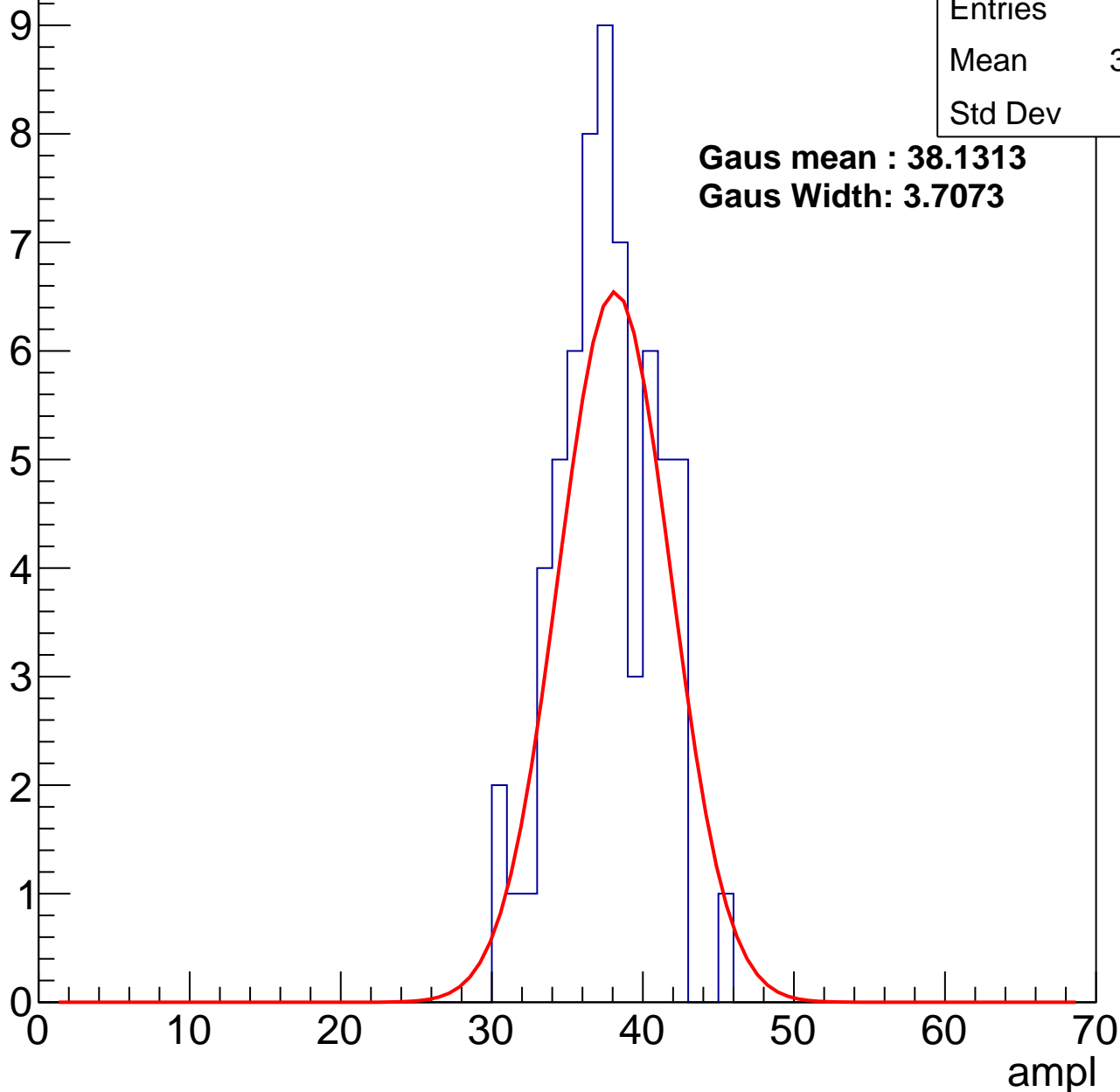
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.13
Std Dev	3.19

Gaus mean : 38.1313

Gaus Width: 3.7073



B1L100S, U13-ch28, adc2

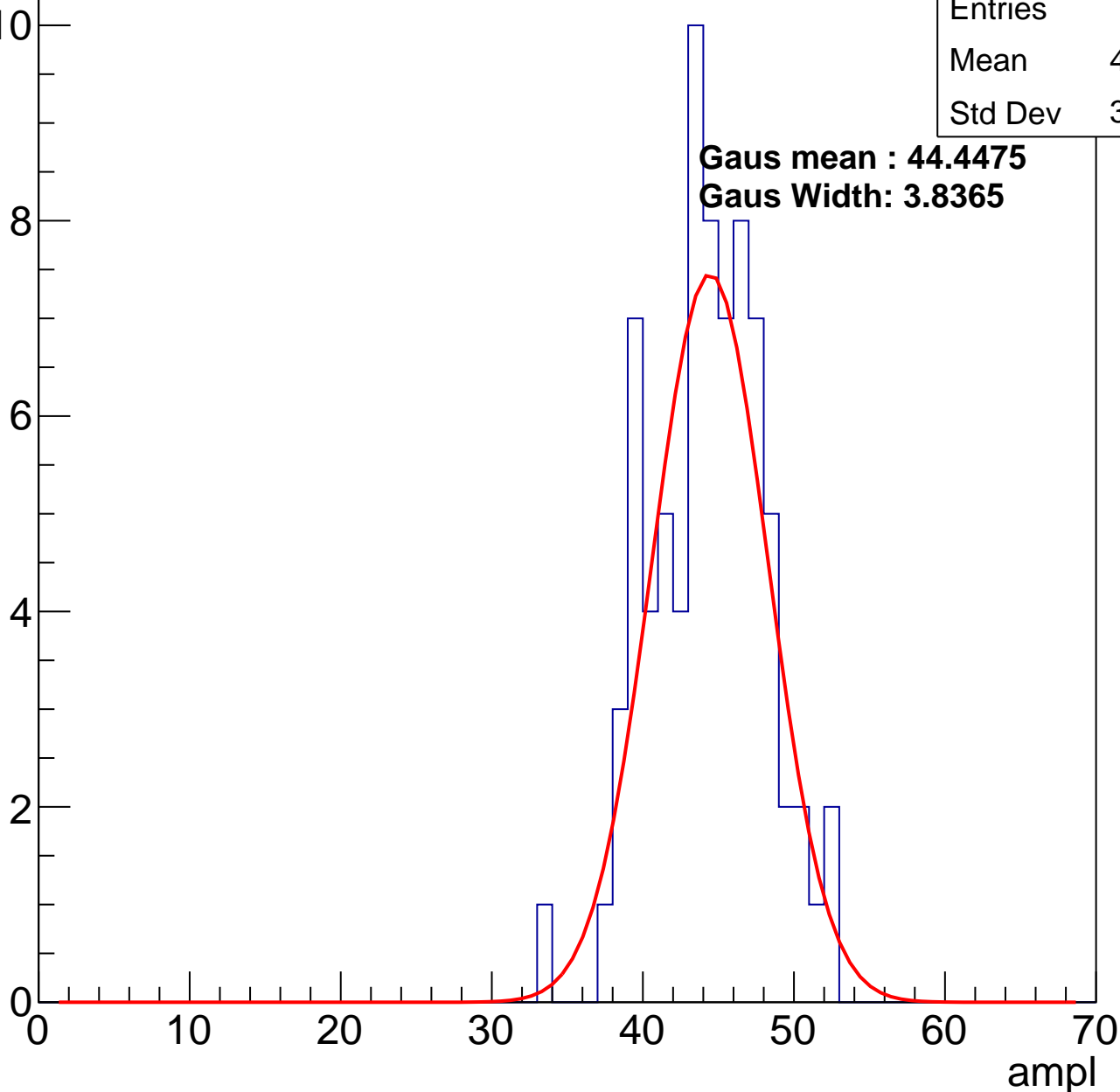
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	43.86
Std Dev	3.723

Gaus mean : 44.4475

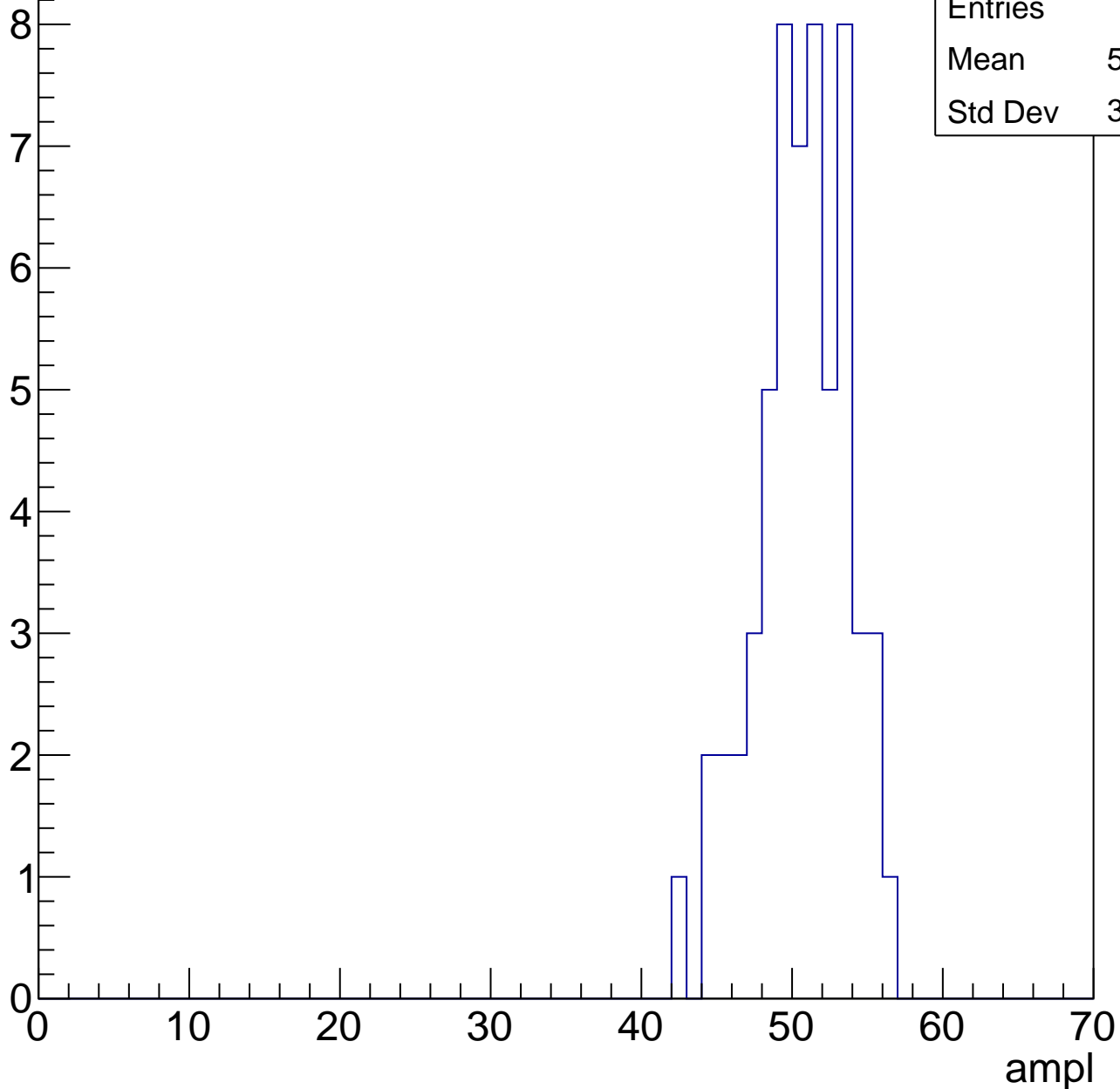
Gaus Width: 3.8365



B1L100S, U13-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

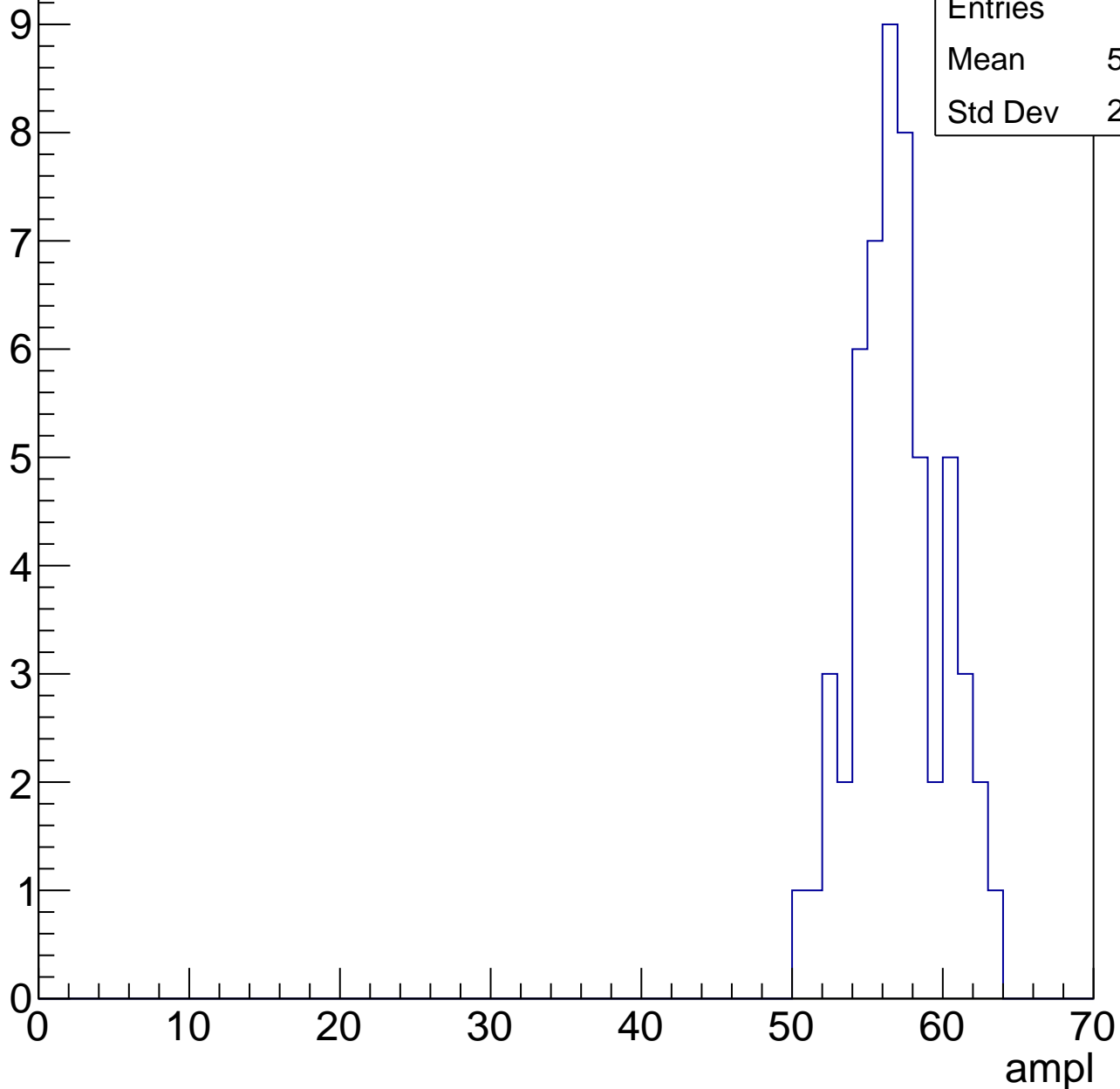


Entries	58
Mean	50.17
Std Dev	3.024

B1L100S, U13-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch28, adc5

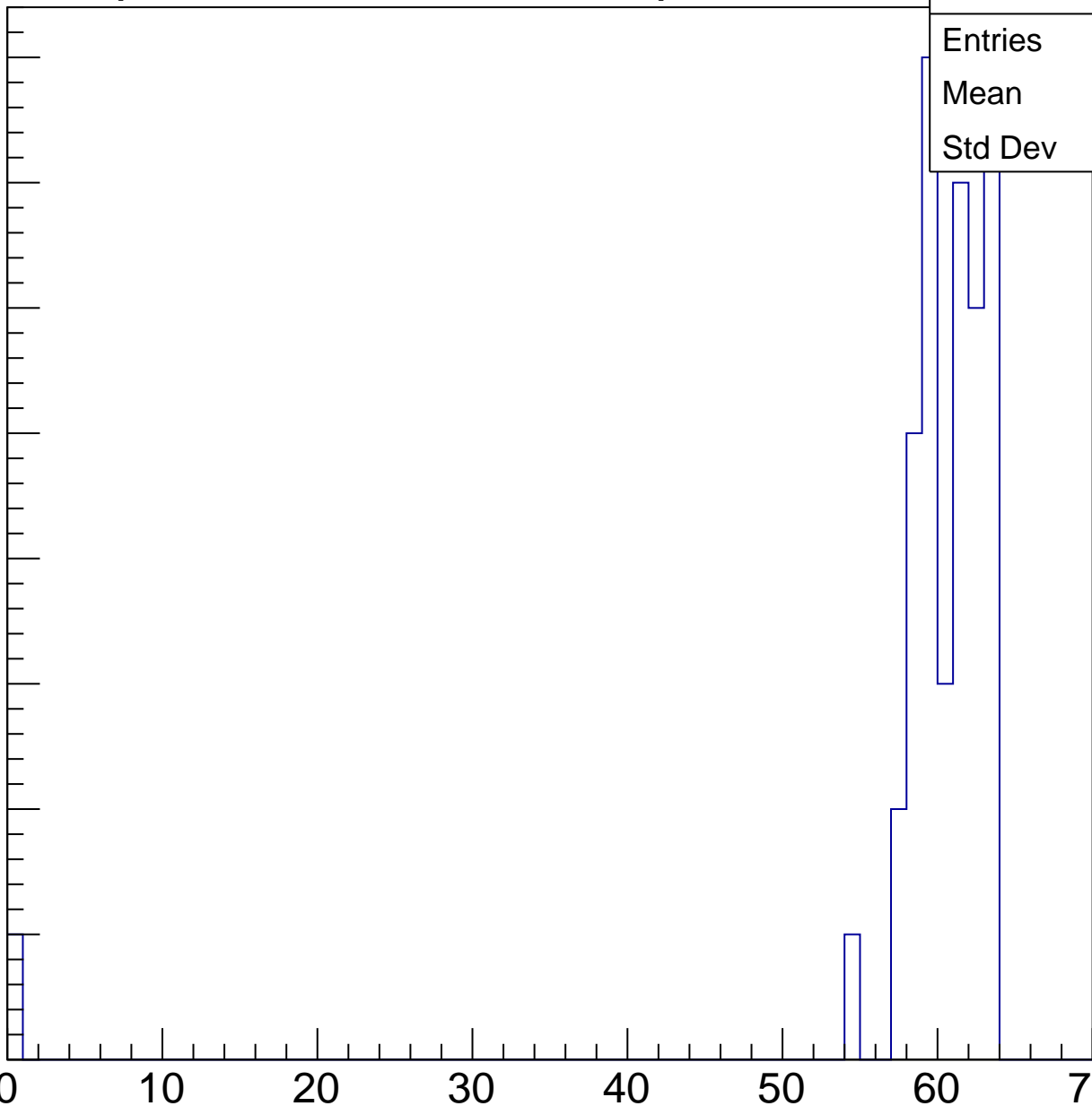
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.85
Std Dev	9.54

ampl



B1L100S, U13-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.2

ampl

B1L100S, U13-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch29, adc0

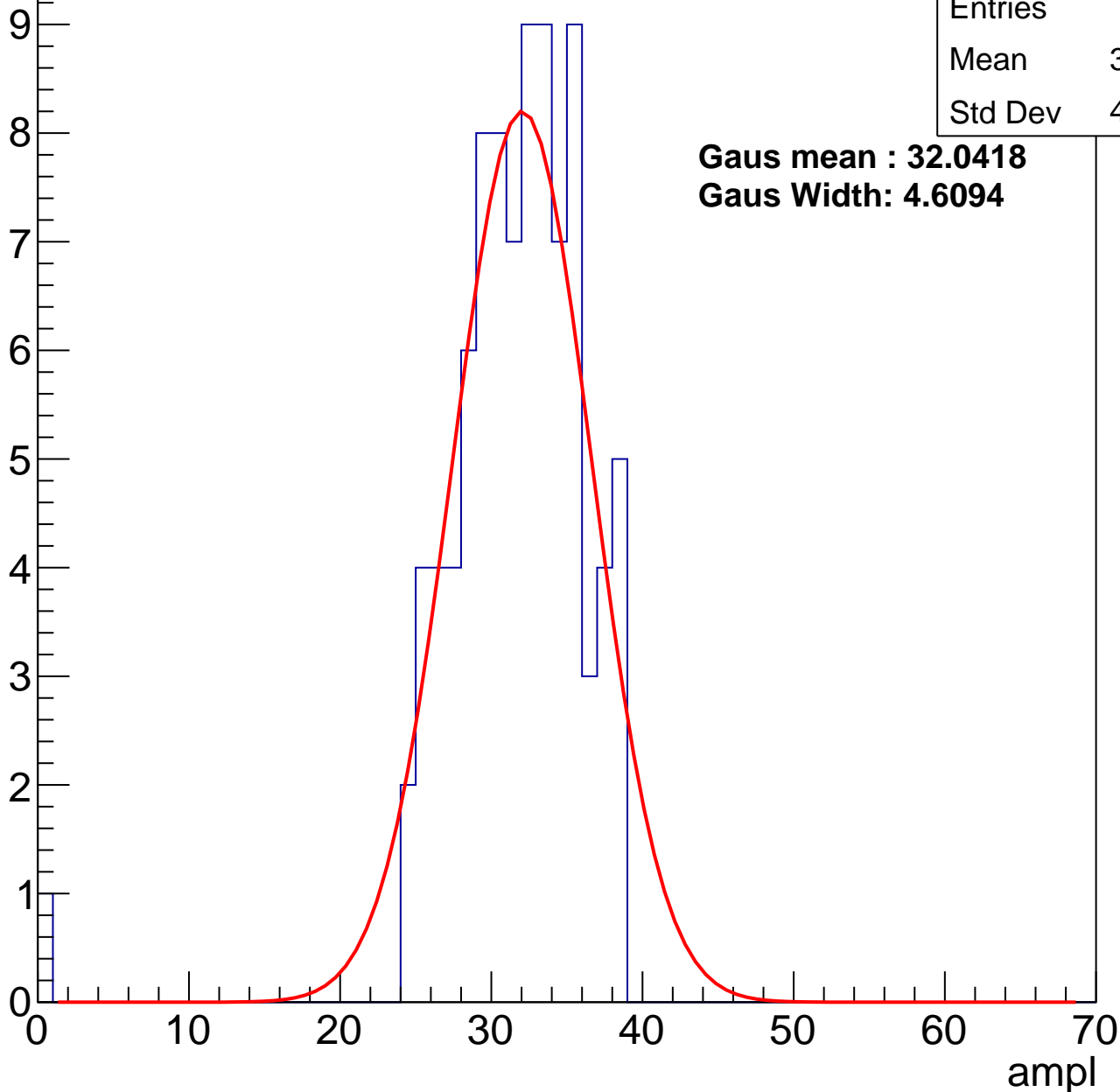
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	31.12
Std Dev	4.917

Gaus mean : 32.0418

Gaus Width: 4.6094



B1L100S, U13-ch29, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	39.37
Std Dev	3.619

Gaus mean : 39.6484

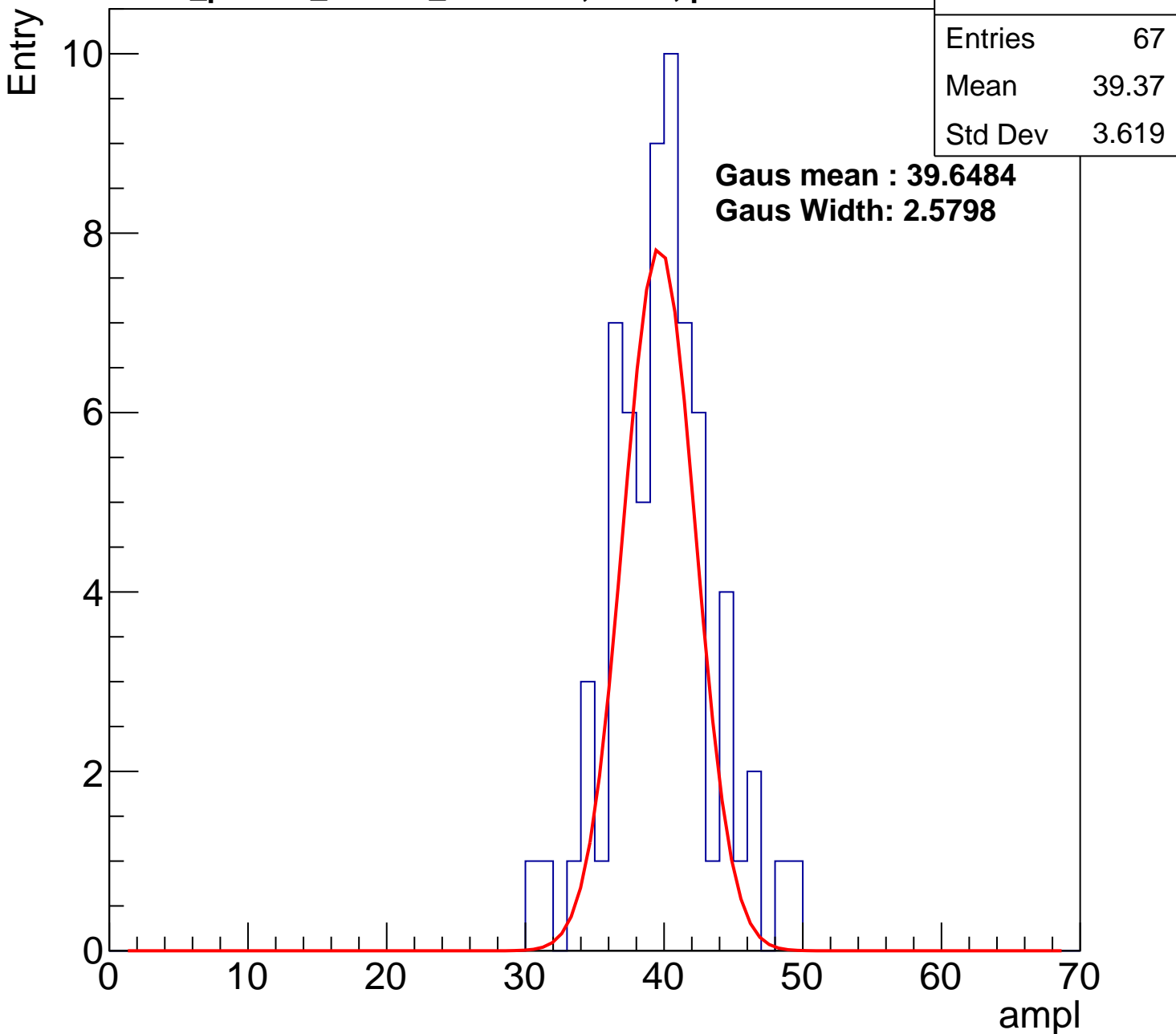
Gaus Width: 2.5798

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch29, adc2

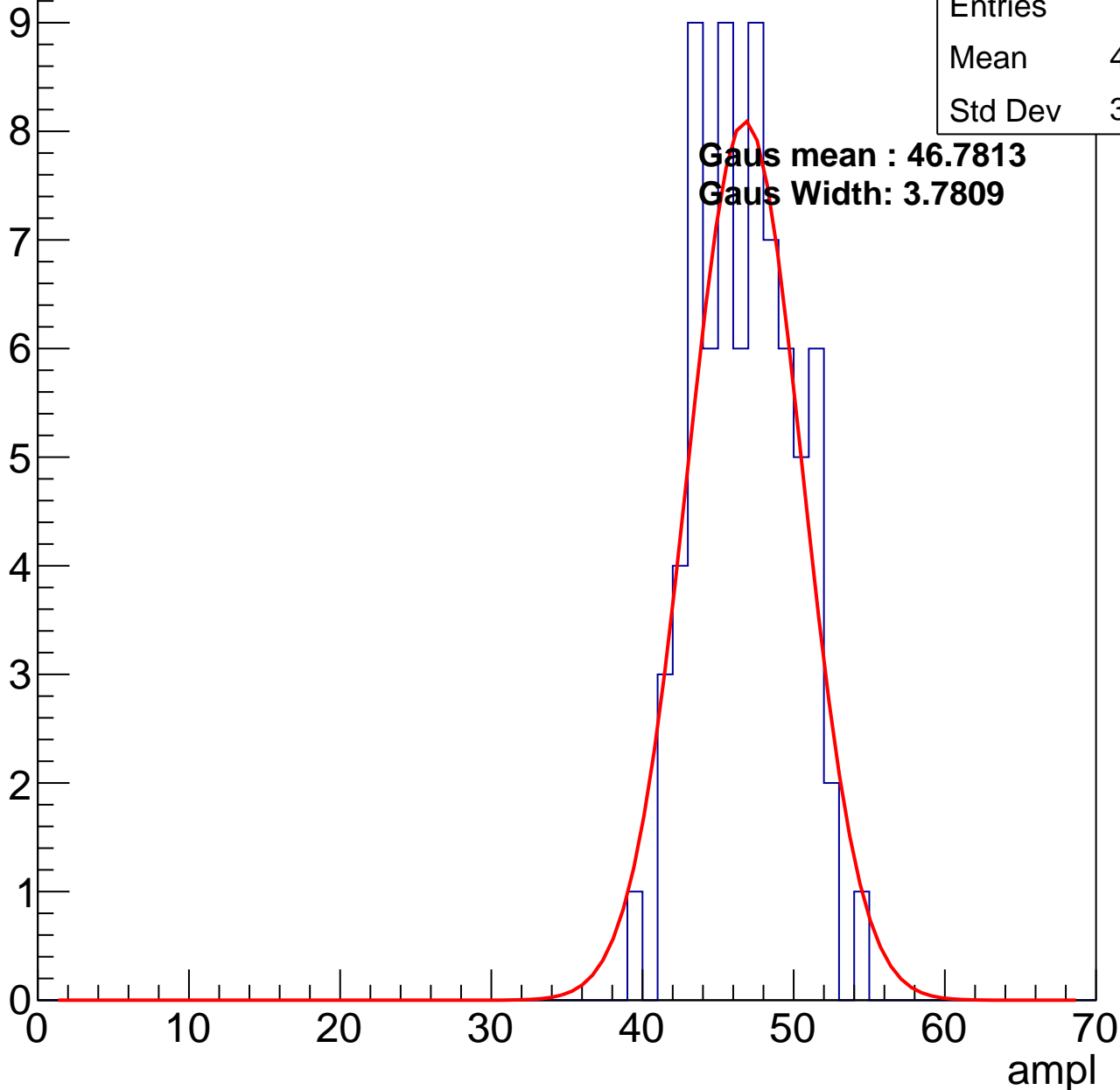
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	46.34
Std Dev	3.185

Gaus mean : 46.7813

Gaus Width: 3.7809

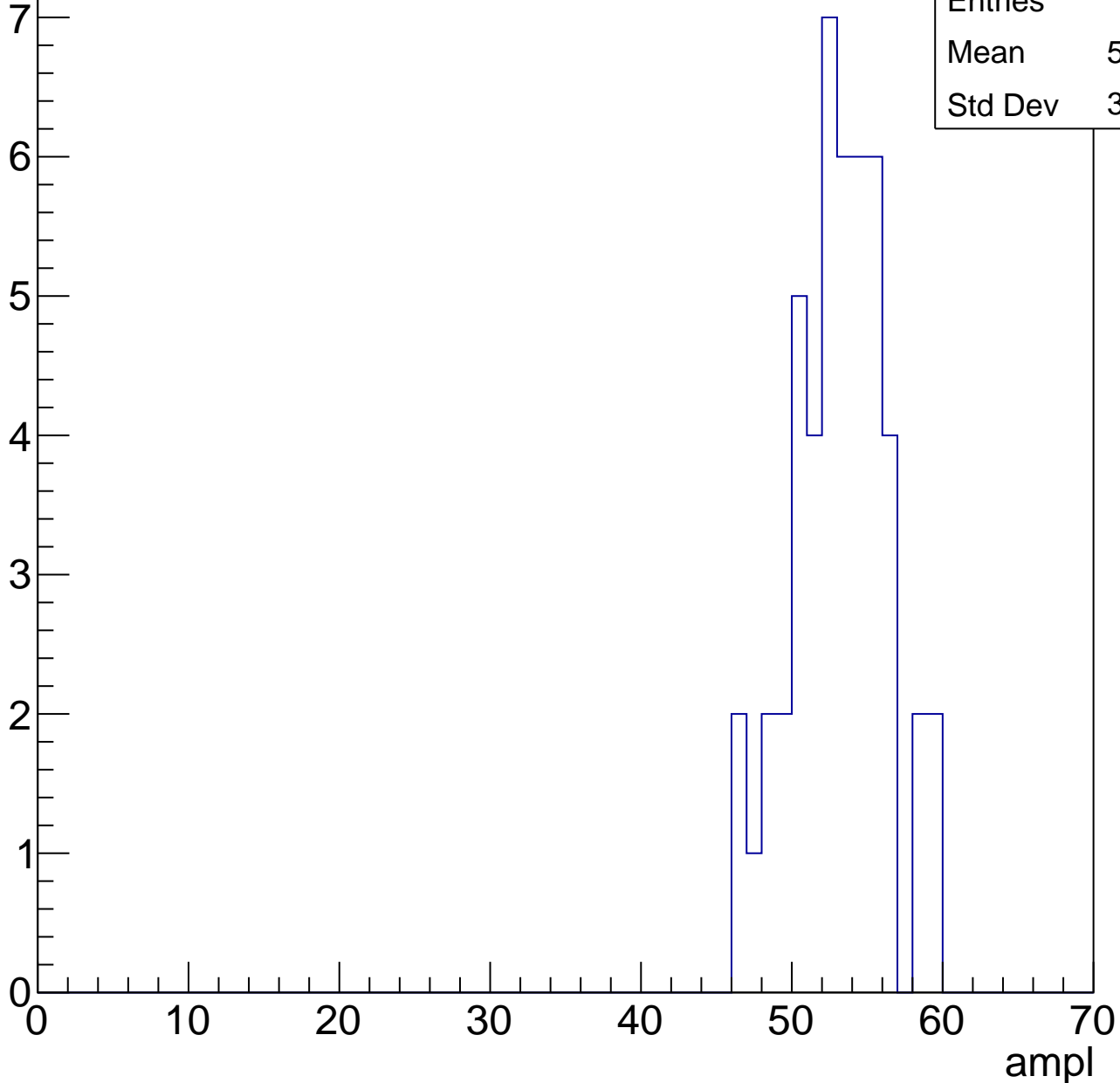


B1L100S, U13-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

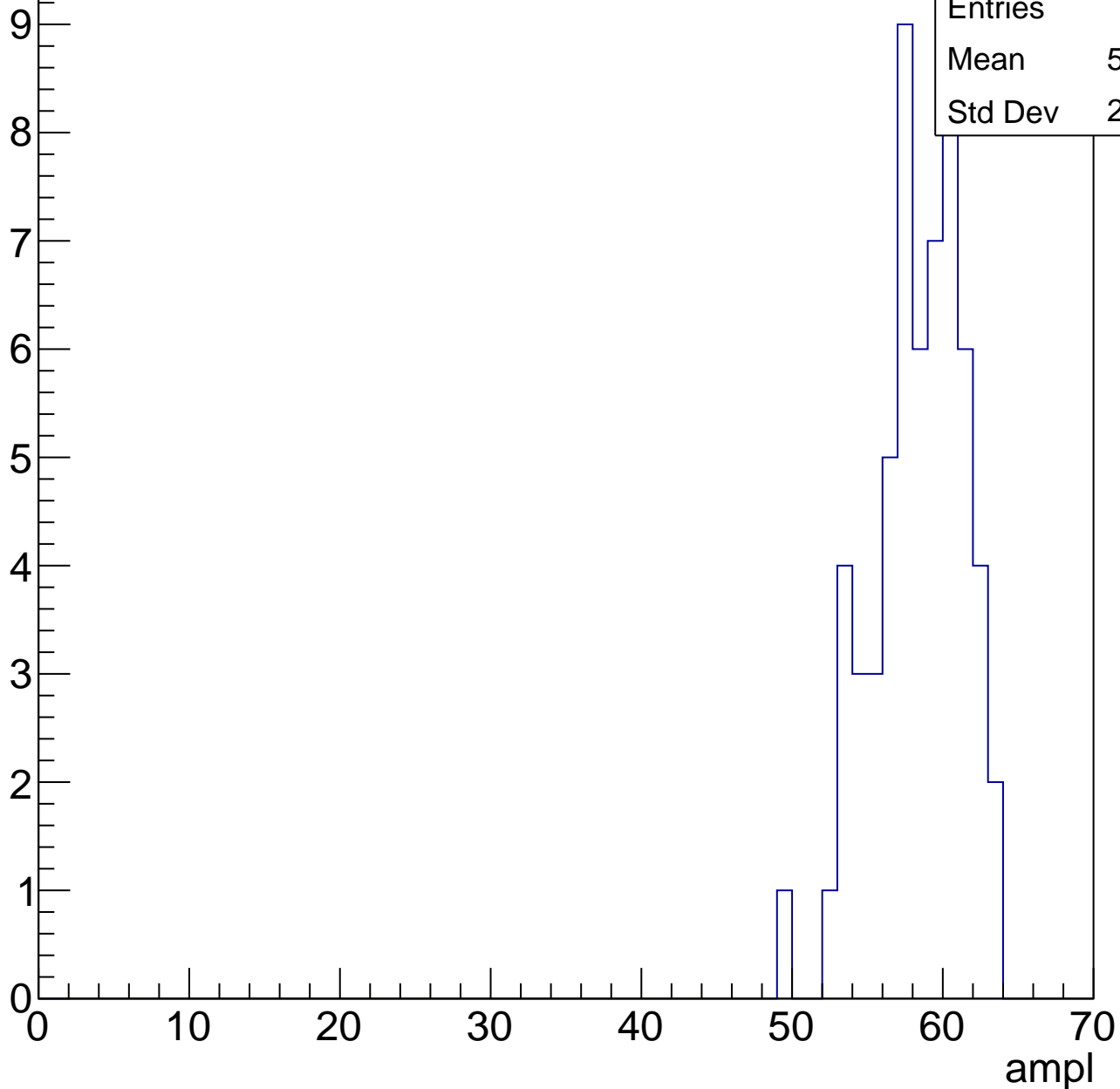
Entries	49
Mean	52.67
Std Dev	3.086



B1L100S, U13-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

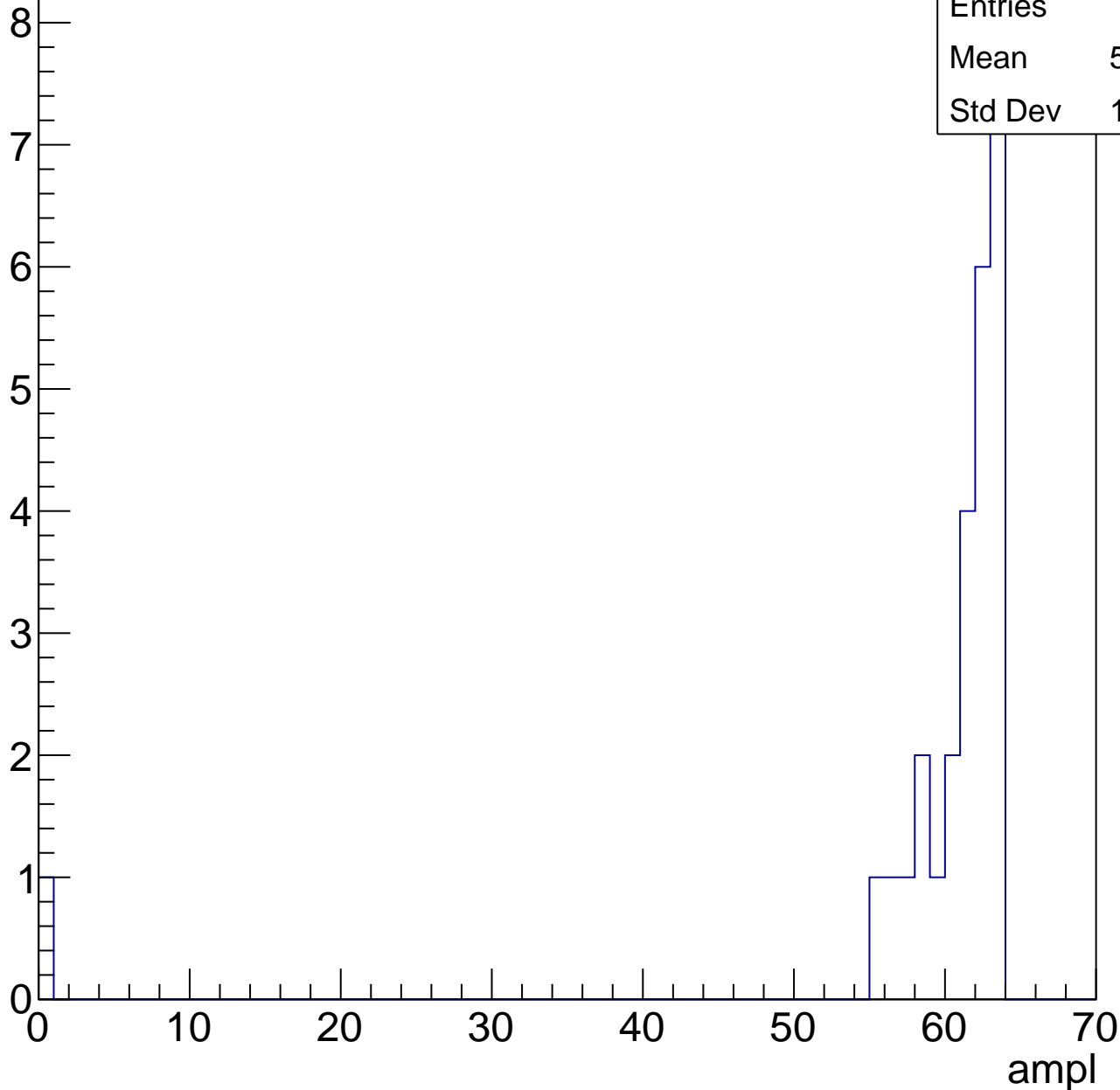


B1L100S, U13-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.63
Std Dev	11.72



B1L100S, U13-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U13-ch30, adc0

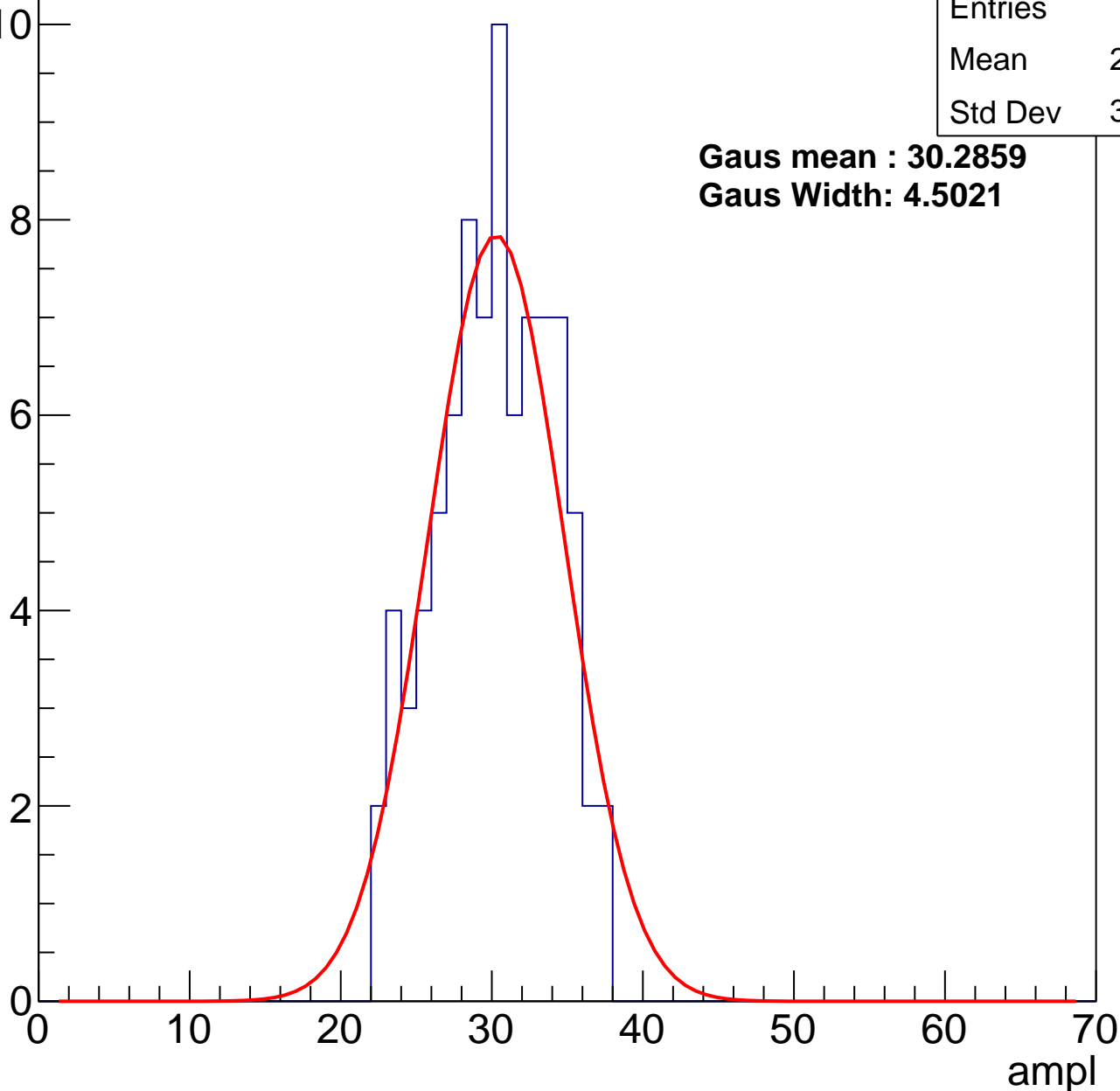
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	29.73
Std Dev	3.762

Gaus mean : 30.2859

Gaus Width: 4.5021



B1L100S, U13-ch30, adc1

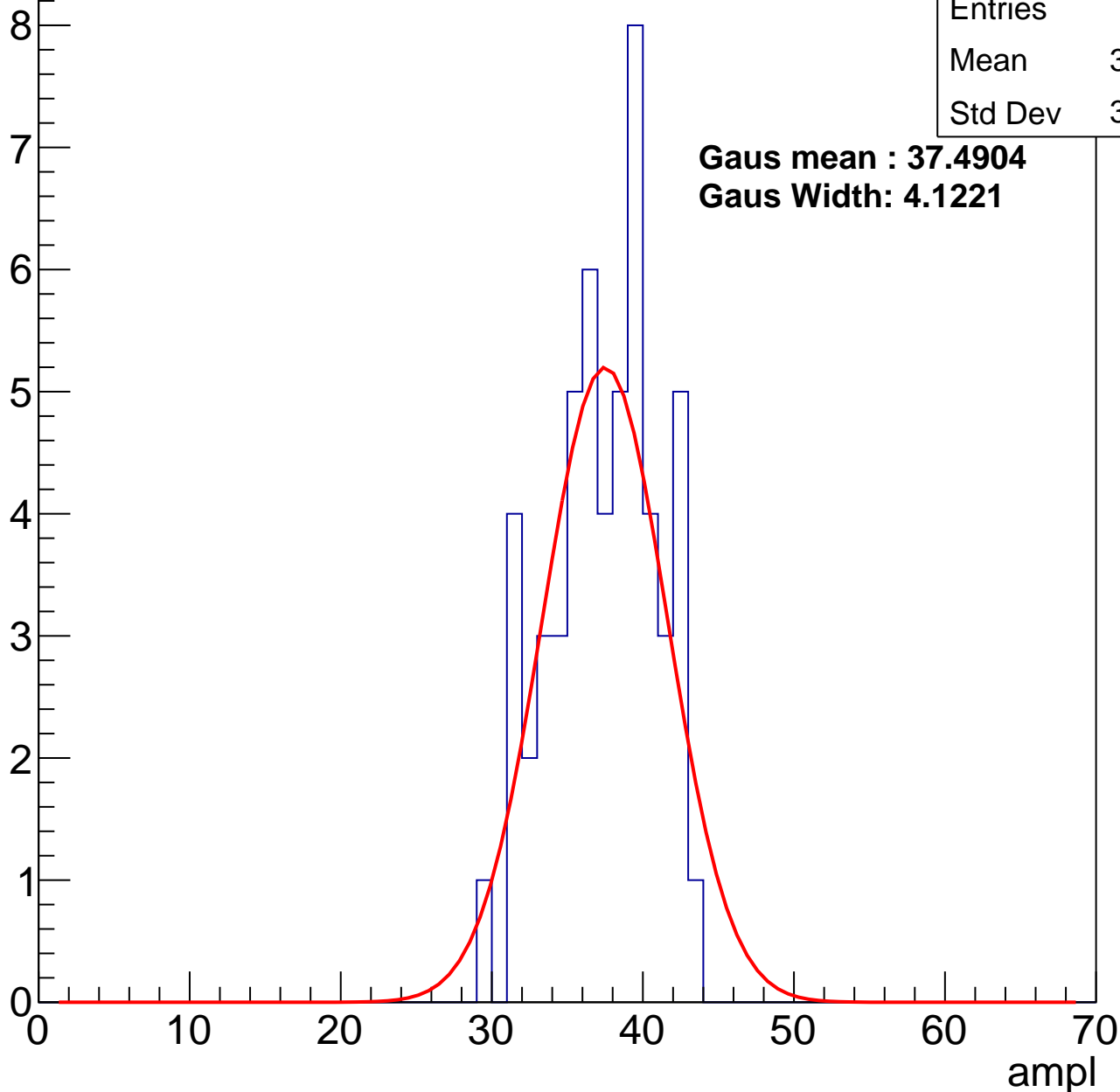
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.94
Std Dev	3.445

Gaus mean : 37.4904

Gaus Width: 4.1221



B1L100S, U13-ch30, adc2

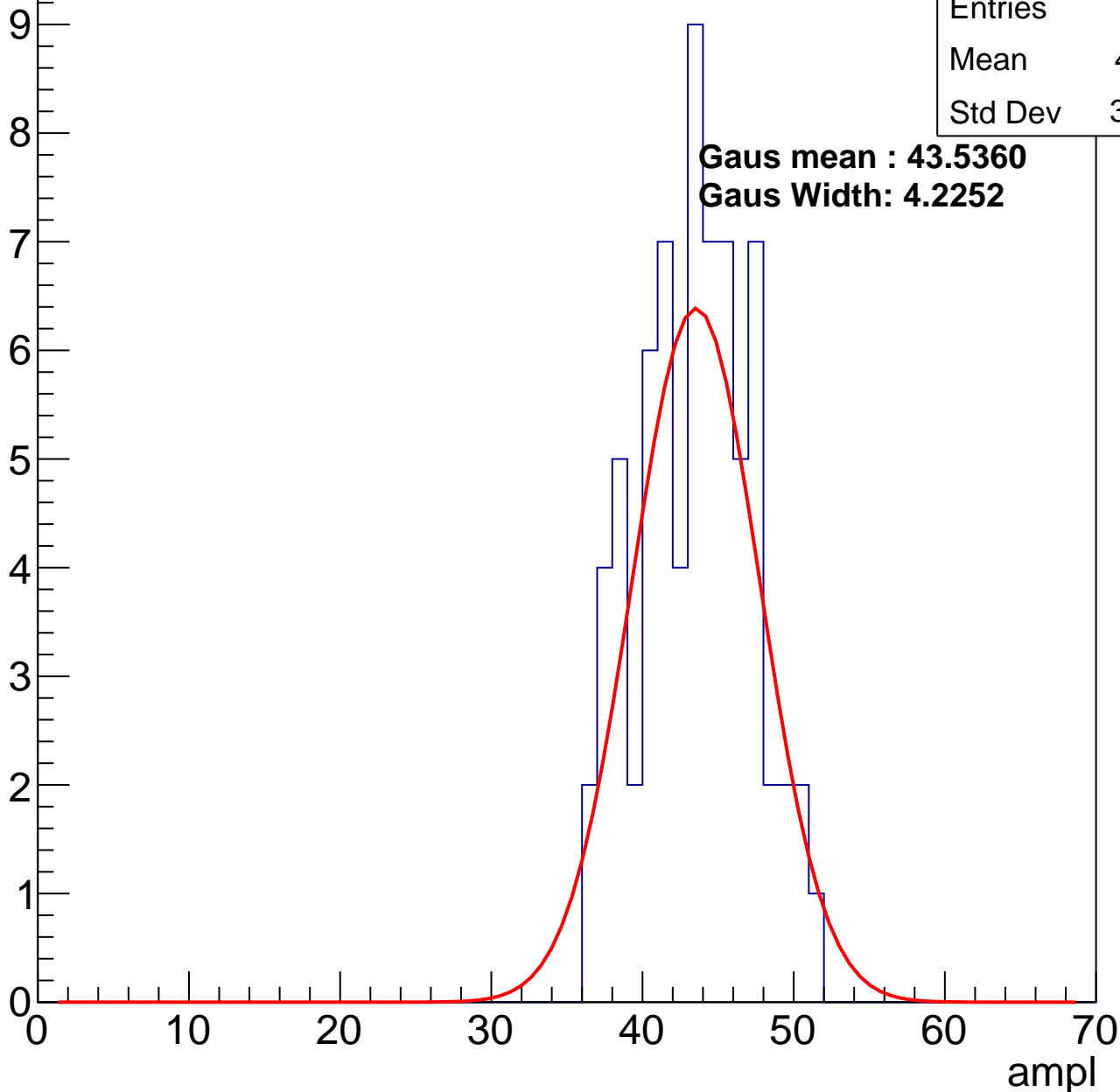
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.01
Std Dev	3.653

Gaus mean : 43.5360

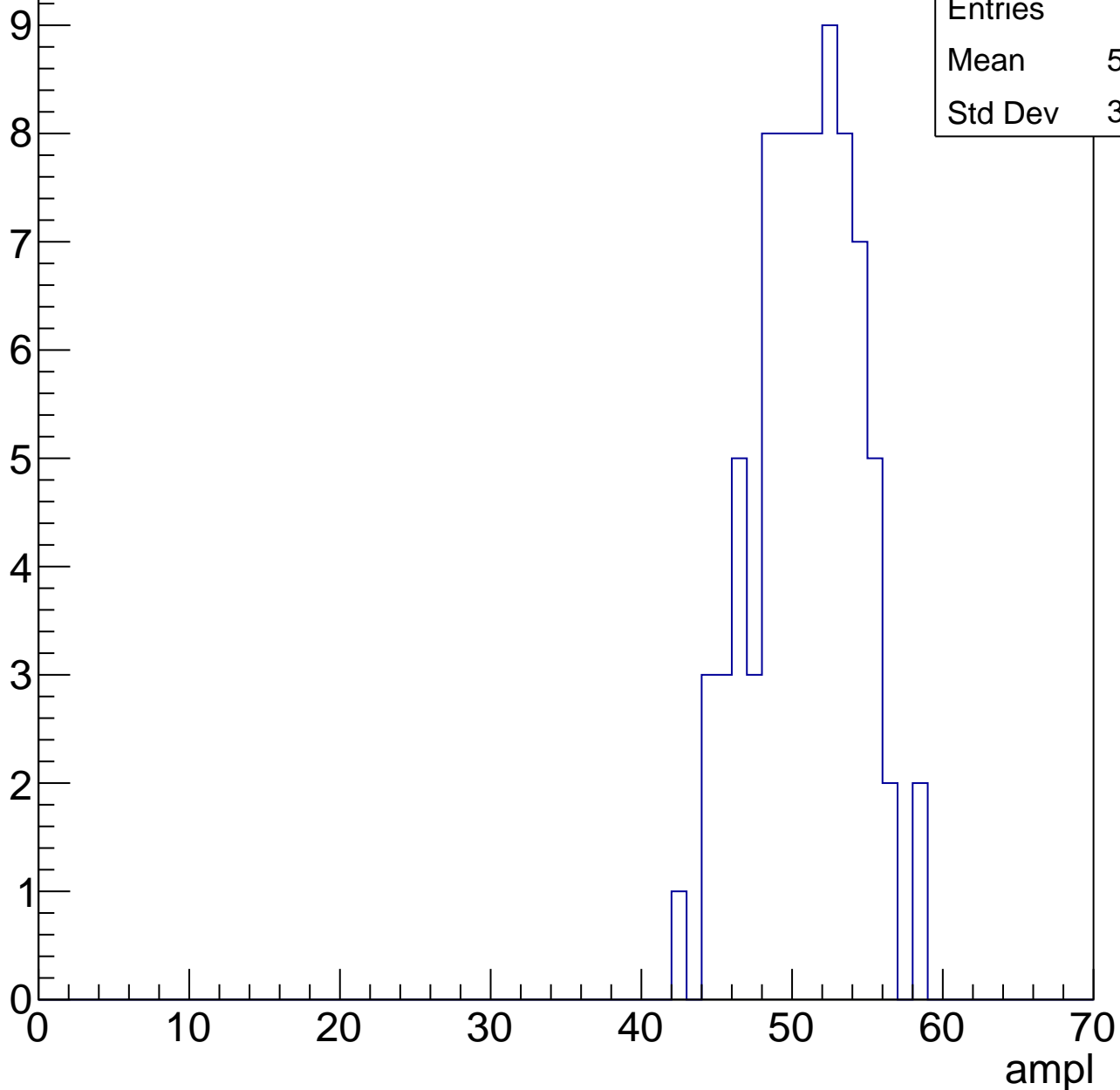
Gaus Width: 4.2252



B1L100S, U13-ch30, adc3

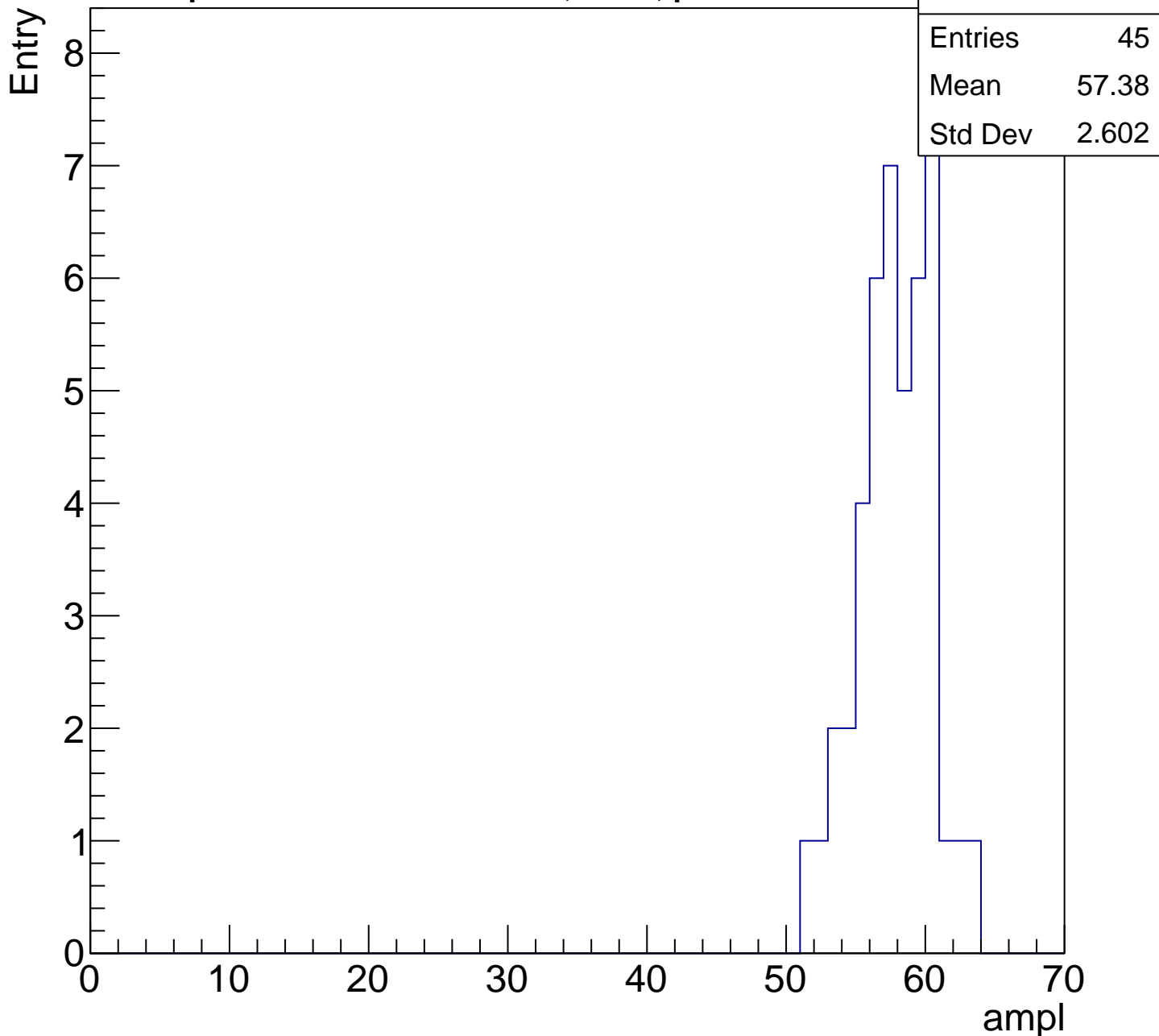
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

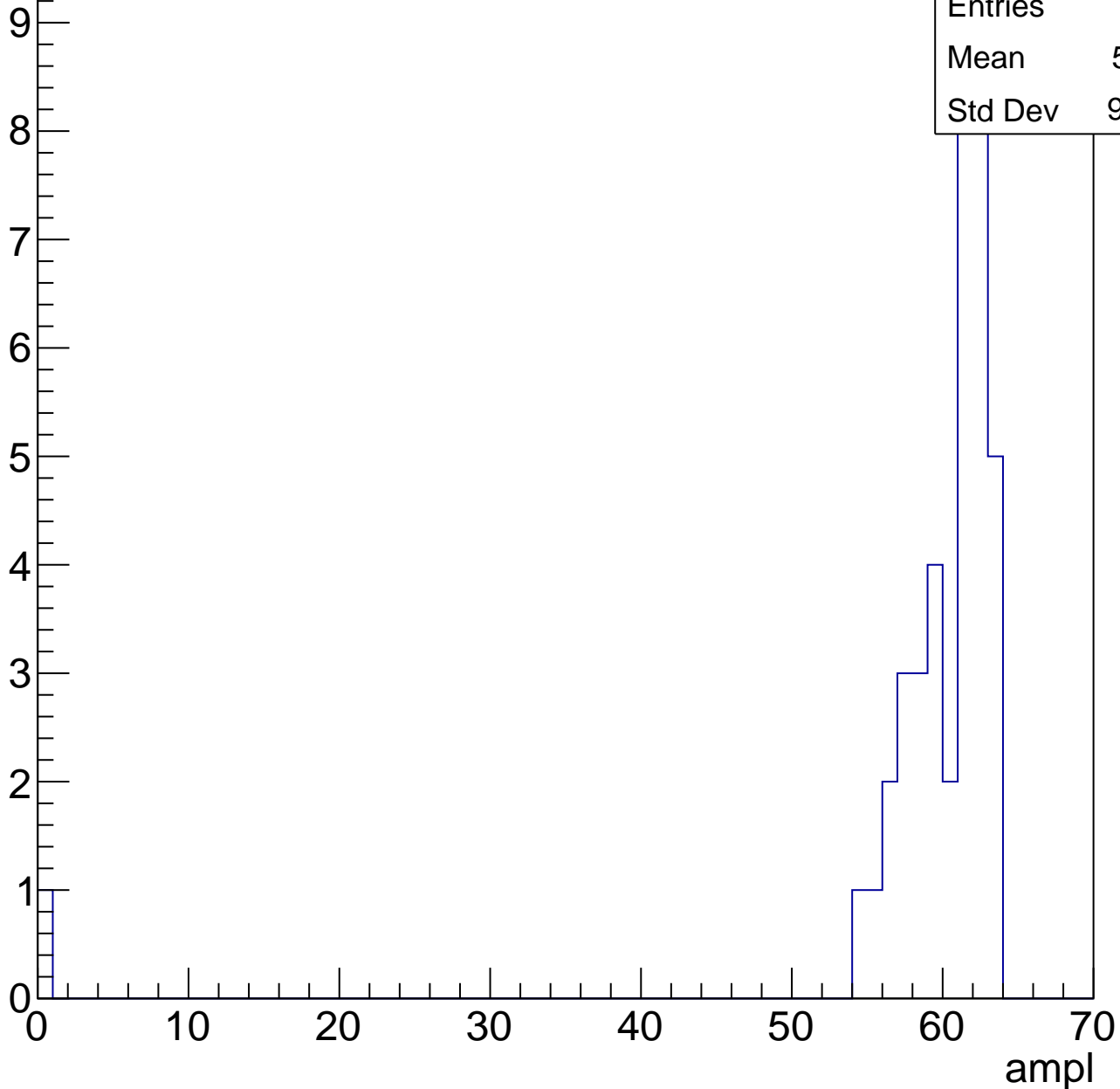


B1L100S, U13-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

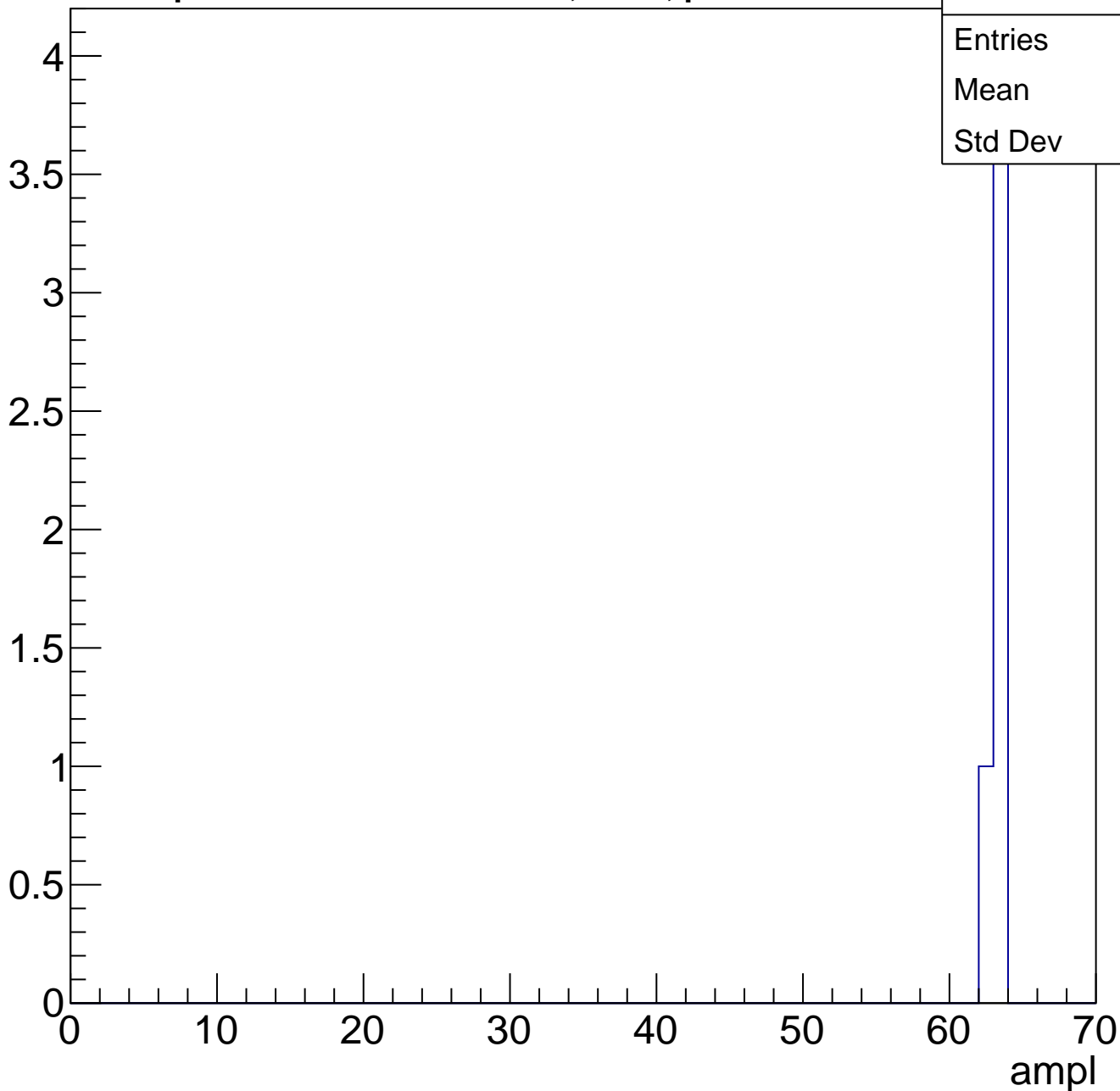
Entries	39
Mean	58.51
Std Dev	9.787



B1L100S, U13-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch31, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	30.41
Std Dev	3.422

Gaus mean : 30.6139

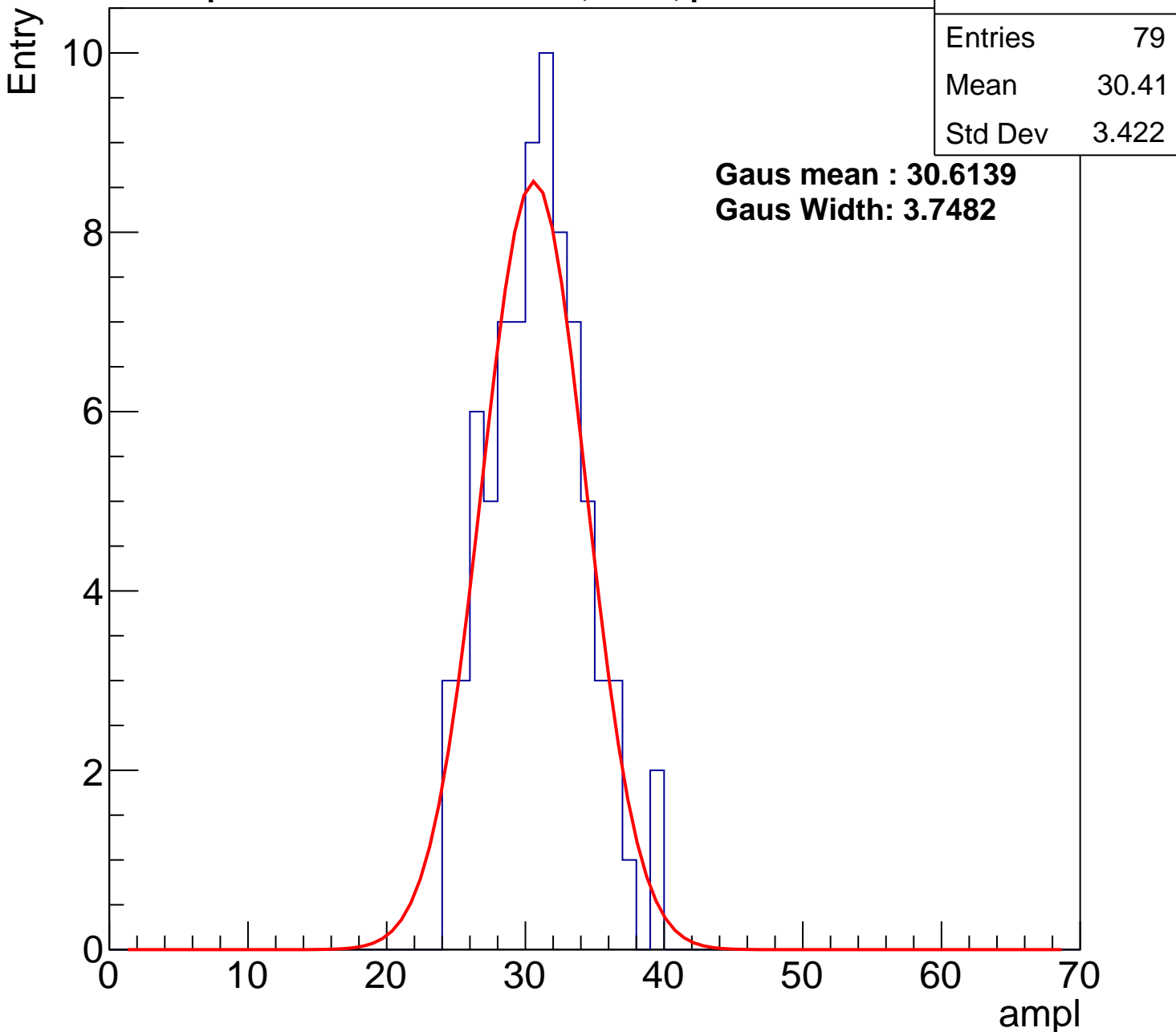
Gaus Width: 3.7482

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch31, adc1

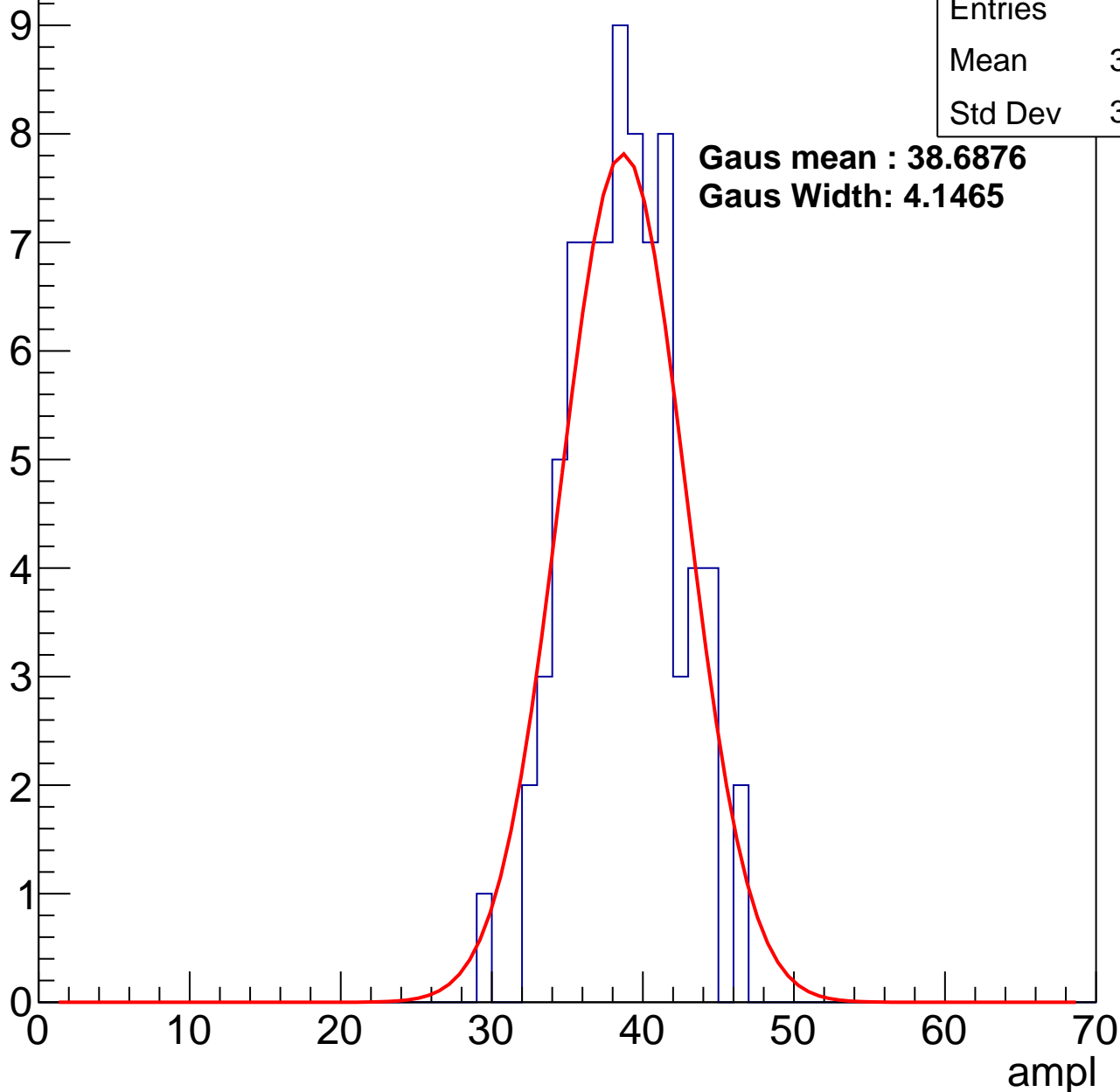
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.26
Std Dev	3.473

Gaus mean : 38.6876

Gaus Width: 4.1465



B1L100S, U13-ch31, adc2

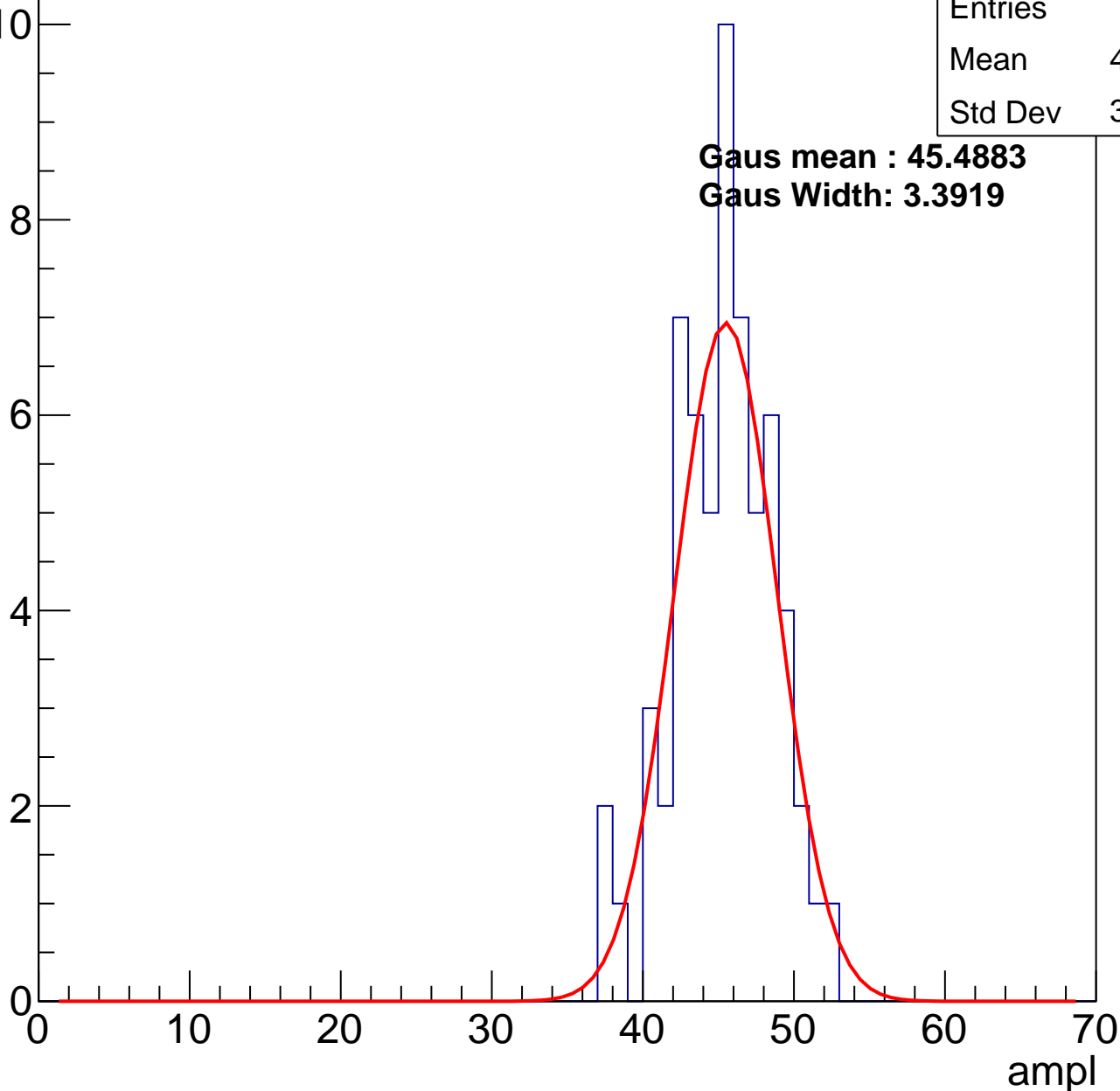
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.84
Std Dev	3.249

Gaus mean : 45.4883

Gaus Width: 3.3919

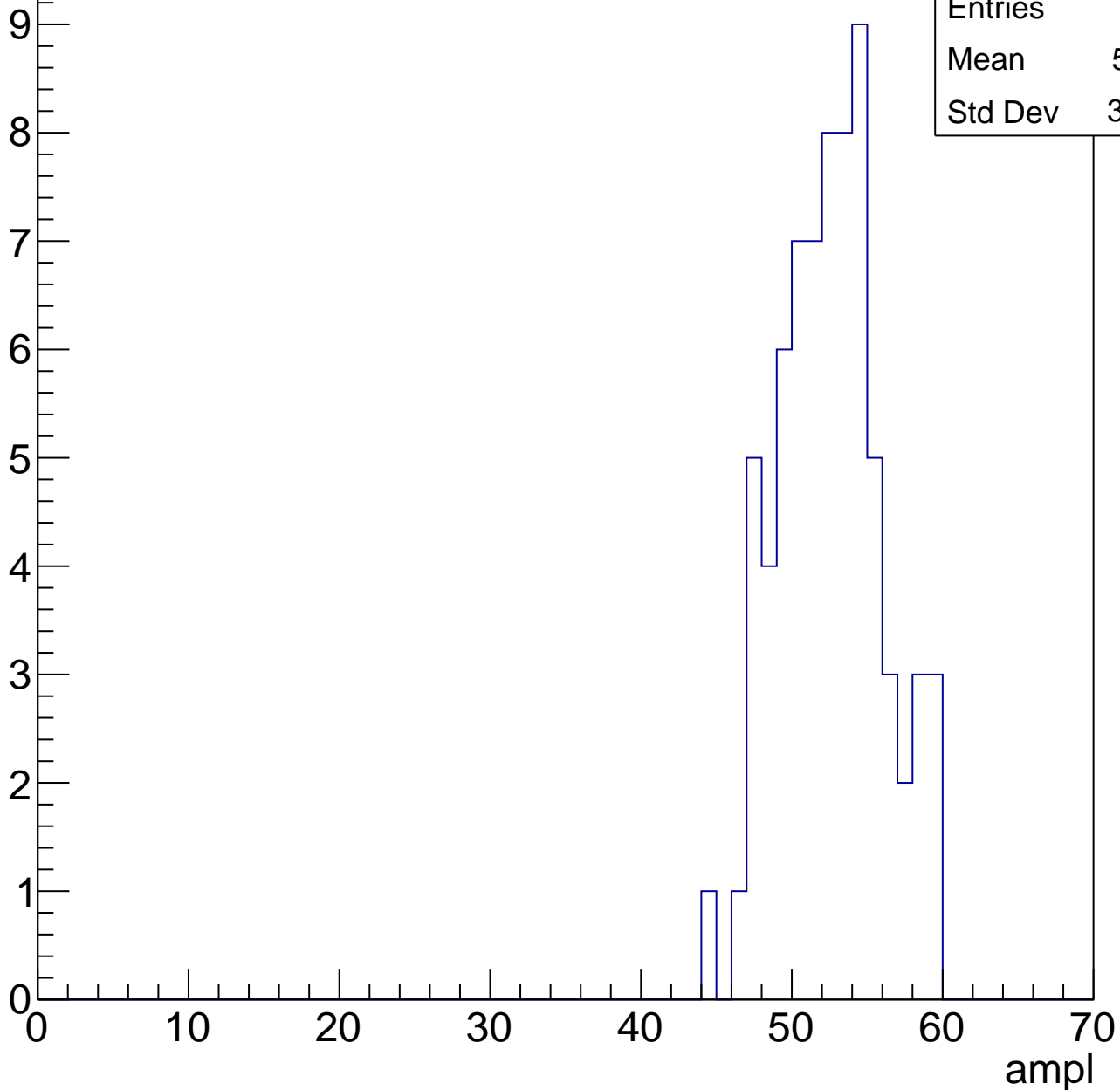


B1L100S, U13-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

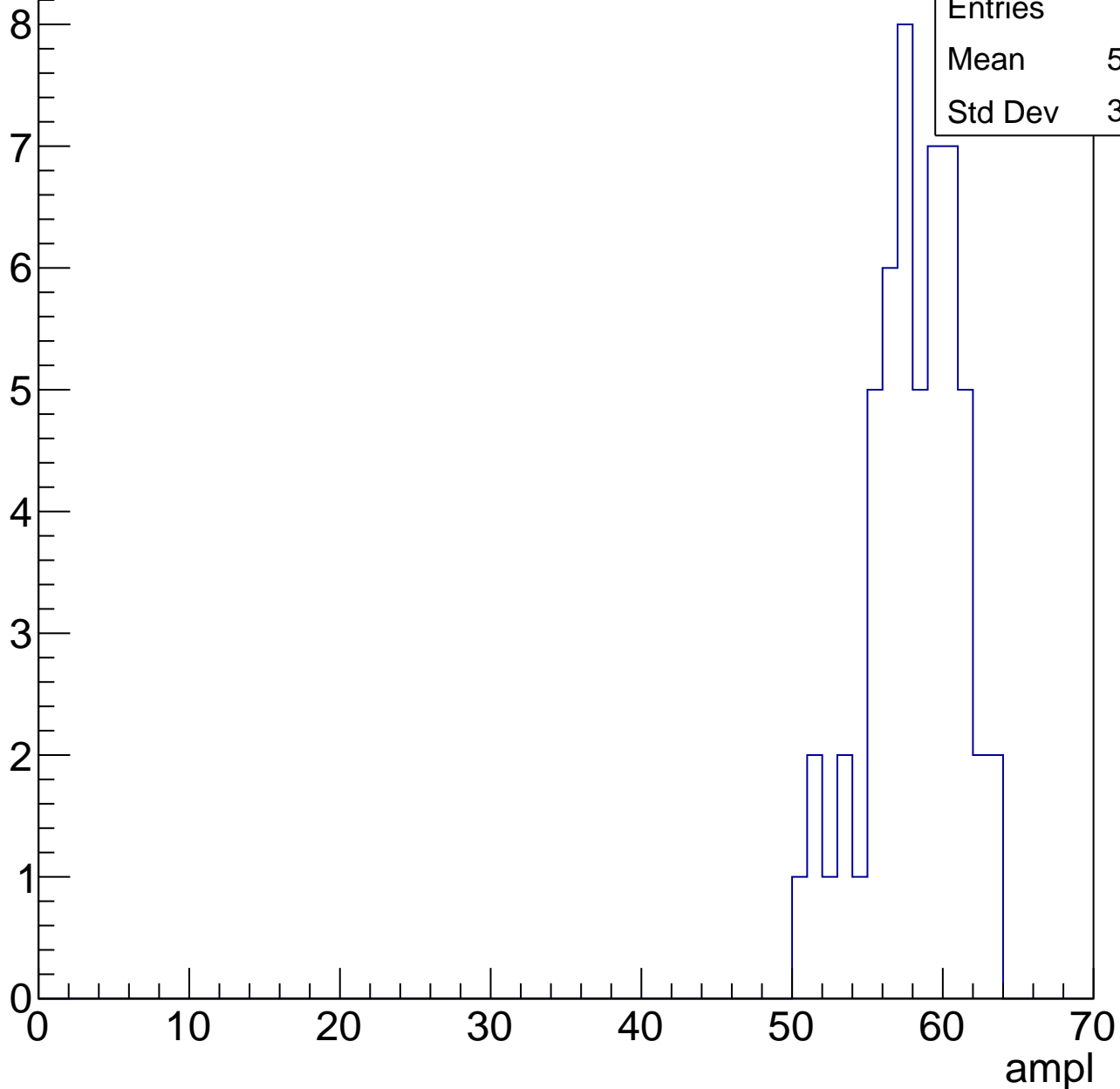
Entries	72
Mean	52.11
Std Dev	3.373



B1L100S, U13-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

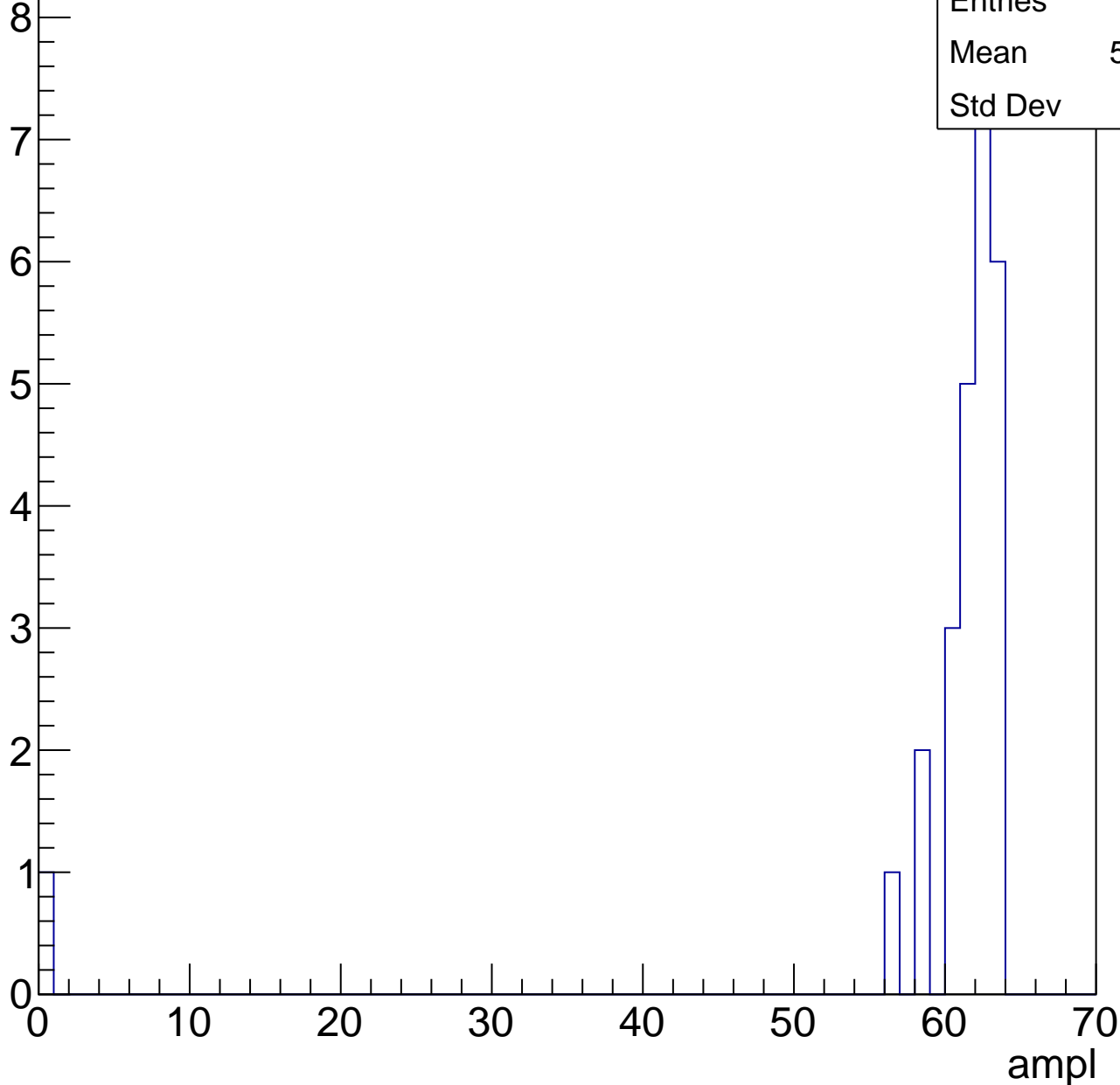


B1L100S, U13-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.88
Std Dev	11.9



B1L100S, U13-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U13-ch32, adc0

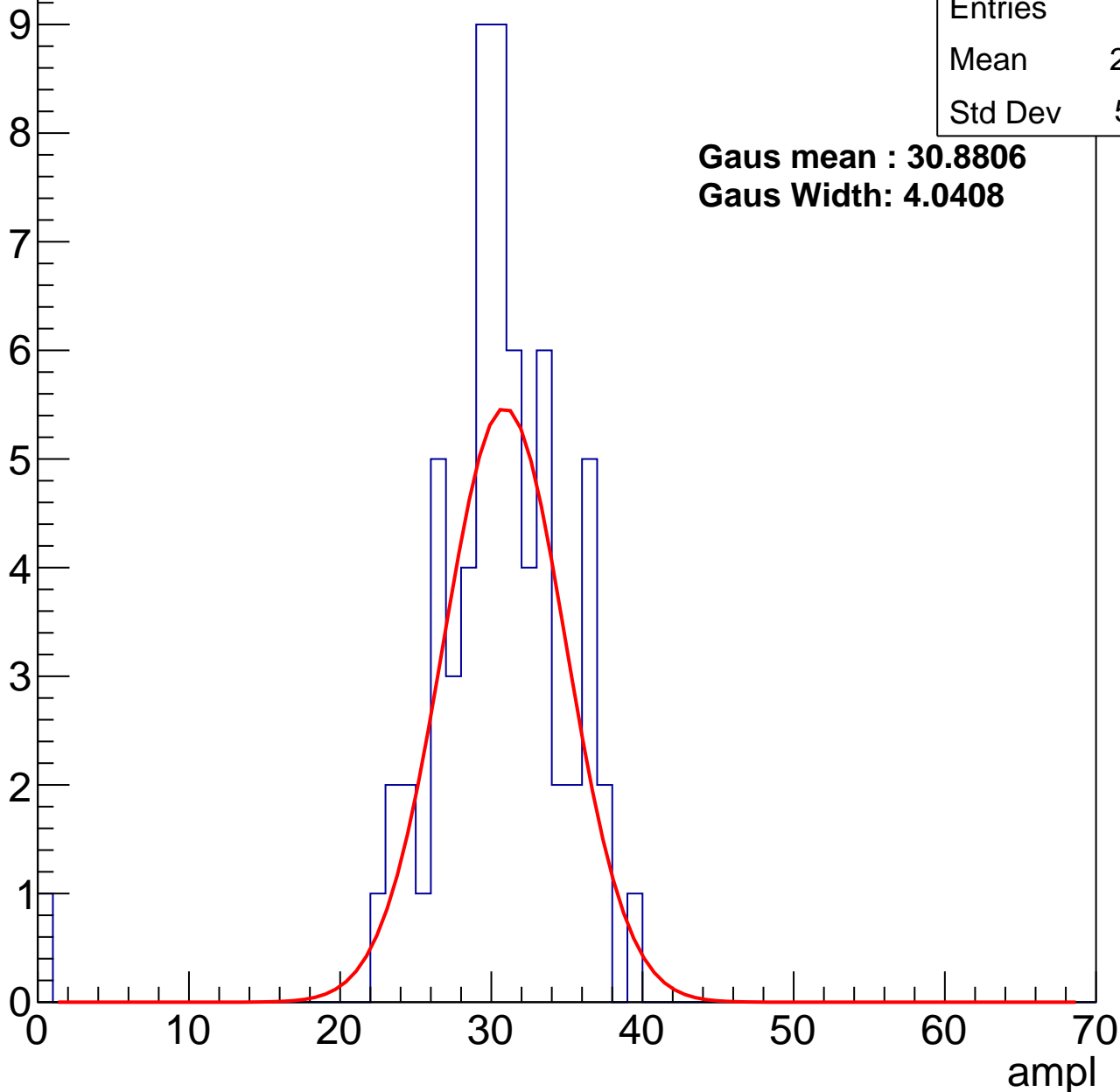
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.82
Std Dev	5.271

Gaus mean : 30.8806

Gaus Width: 4.0408



B1L100S, U13-ch32, adc1

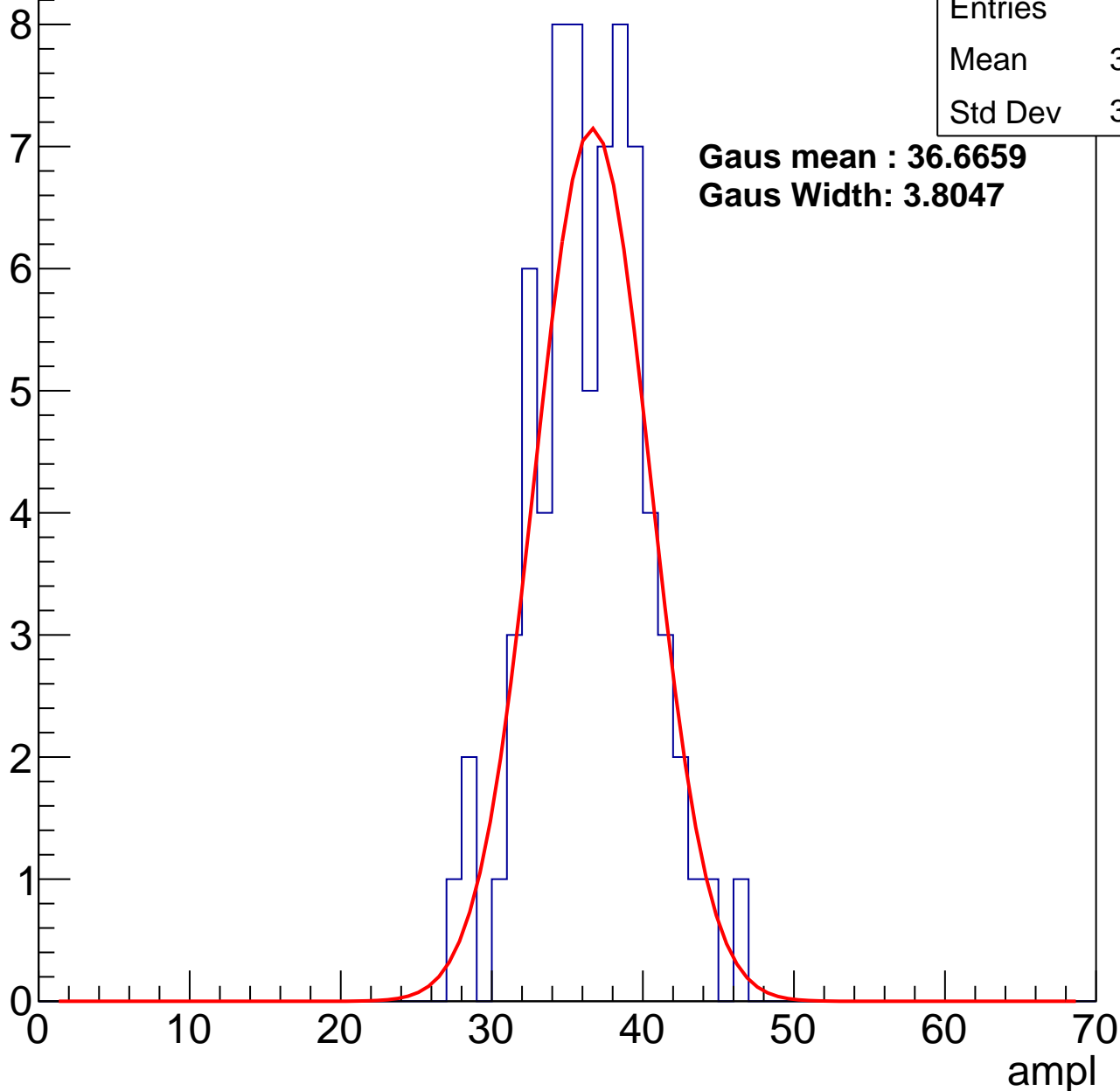
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.08
Std Dev	3.759

Gaus mean : 36.6659

Gaus Width: 3.8047



B1L100S, U13-ch32, adc2

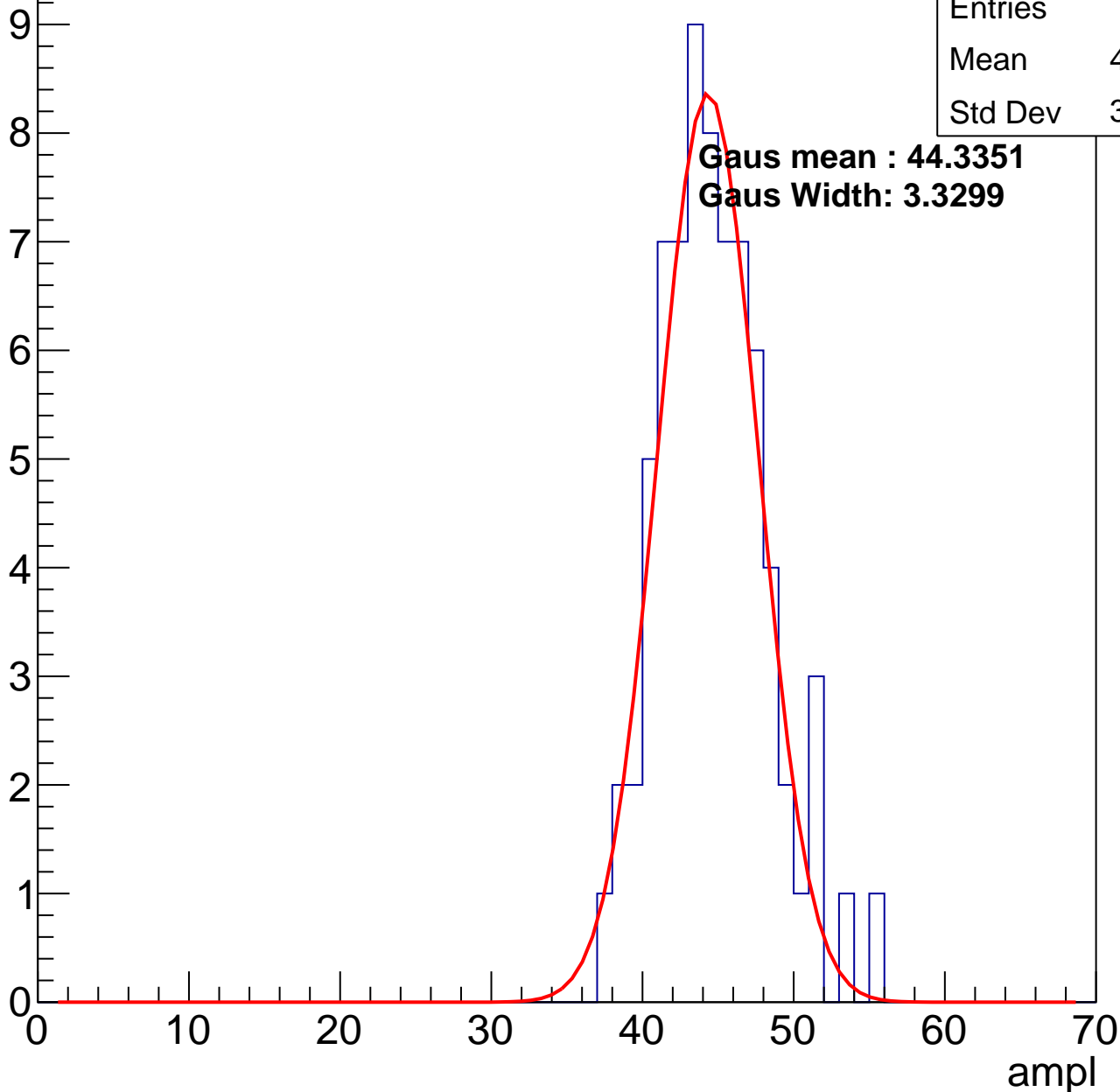
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	44.26
Std Dev	3.577

Gaus mean : 44.3351

Gaus Width: 3.3299

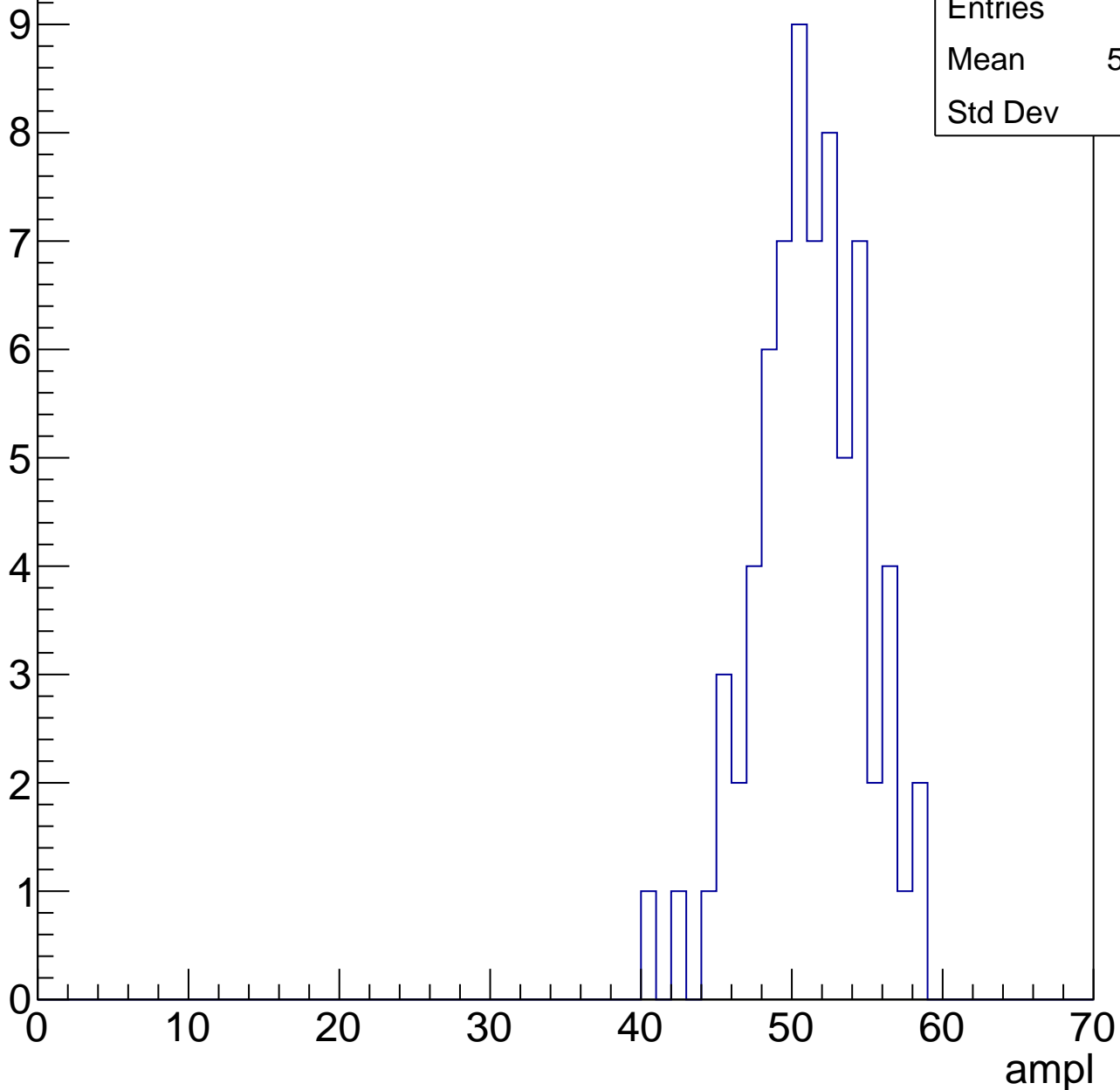


B1L100S, U13-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	50.64
Std Dev	3.61

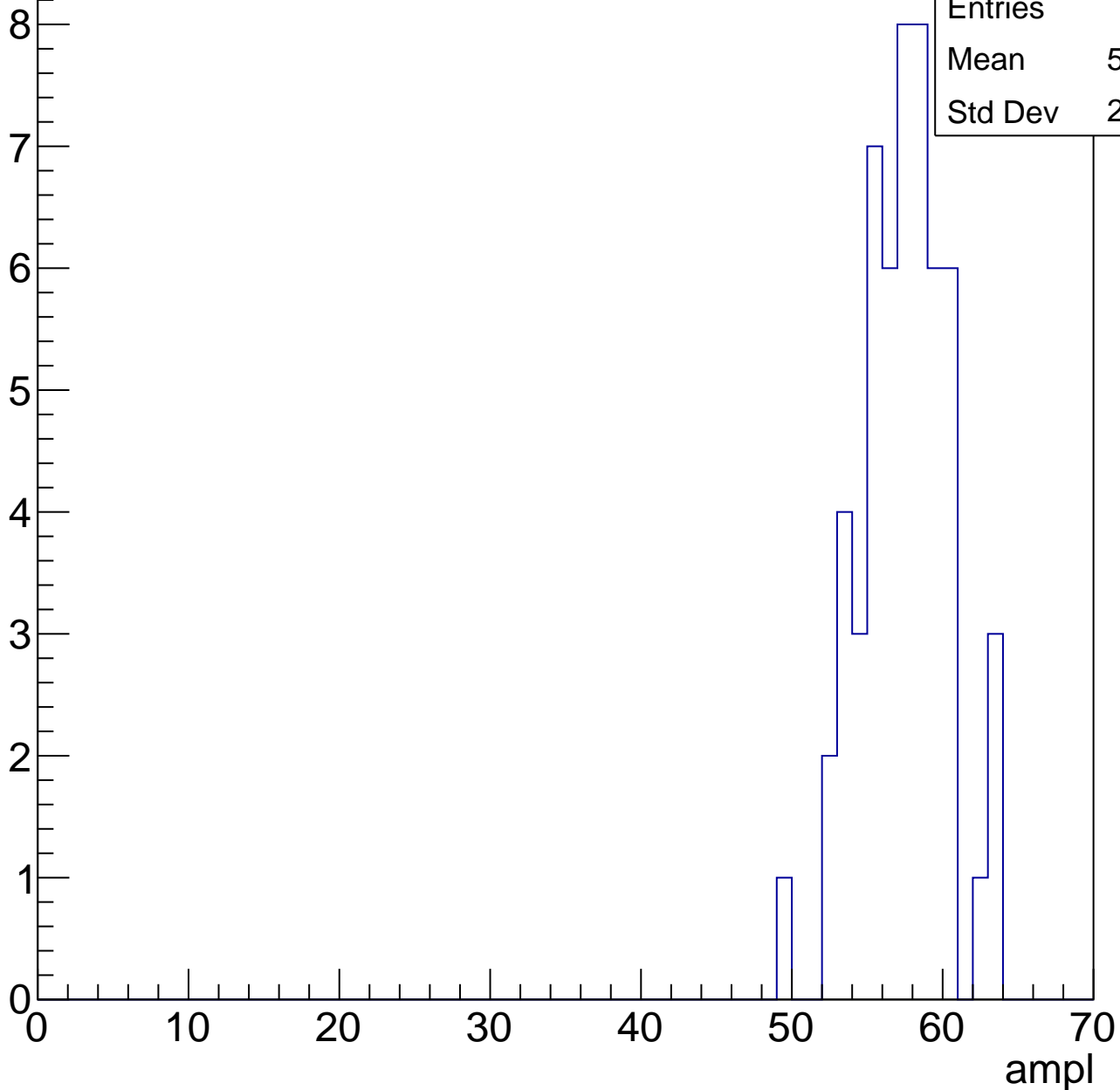


B1L100S, U13-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	56.96
Std Dev	2.885

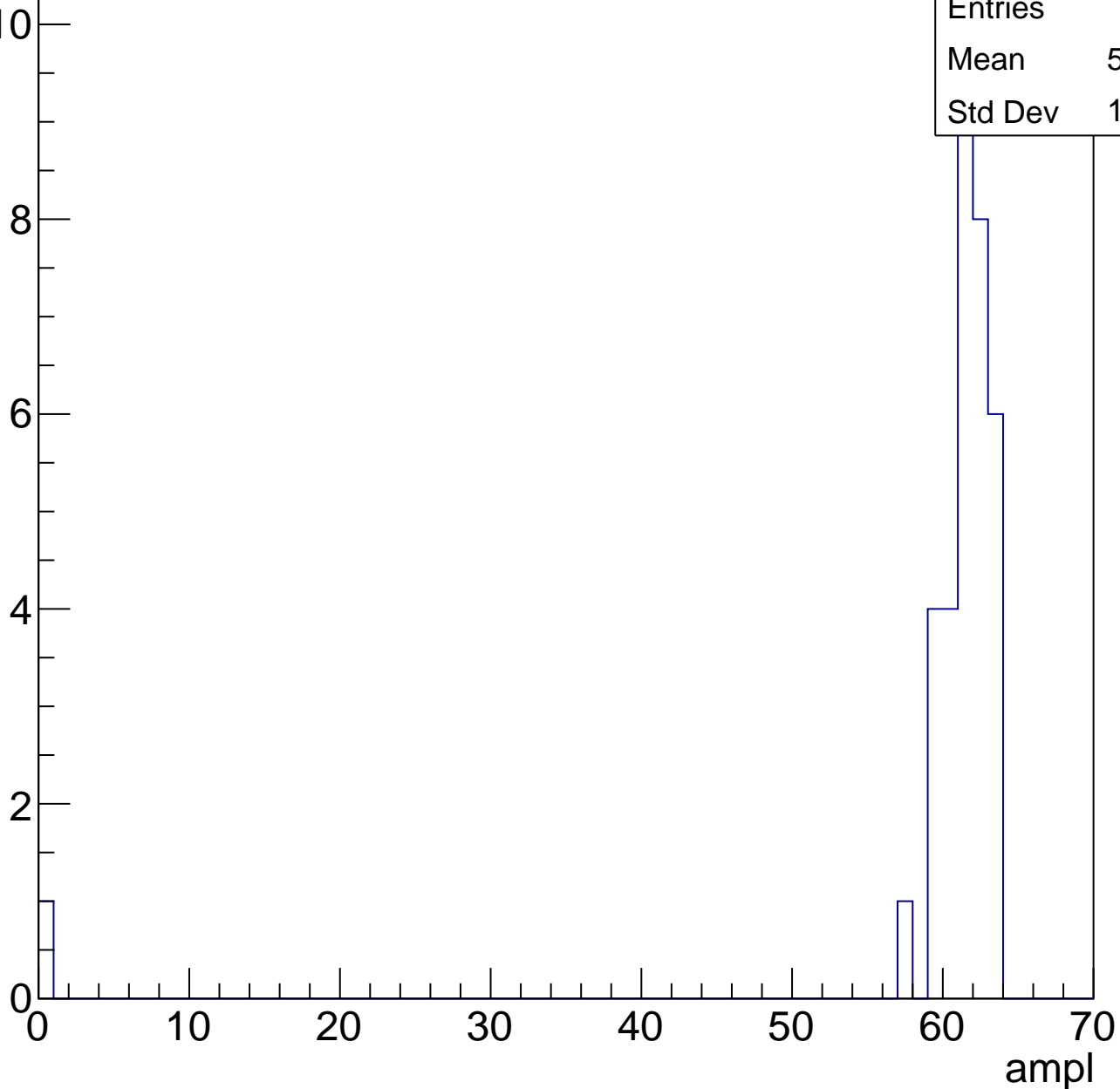


B1L100S, U13-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59.32
Std Dev	10.42



B1L100S, U13-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L100S, U13-ch33, adc0

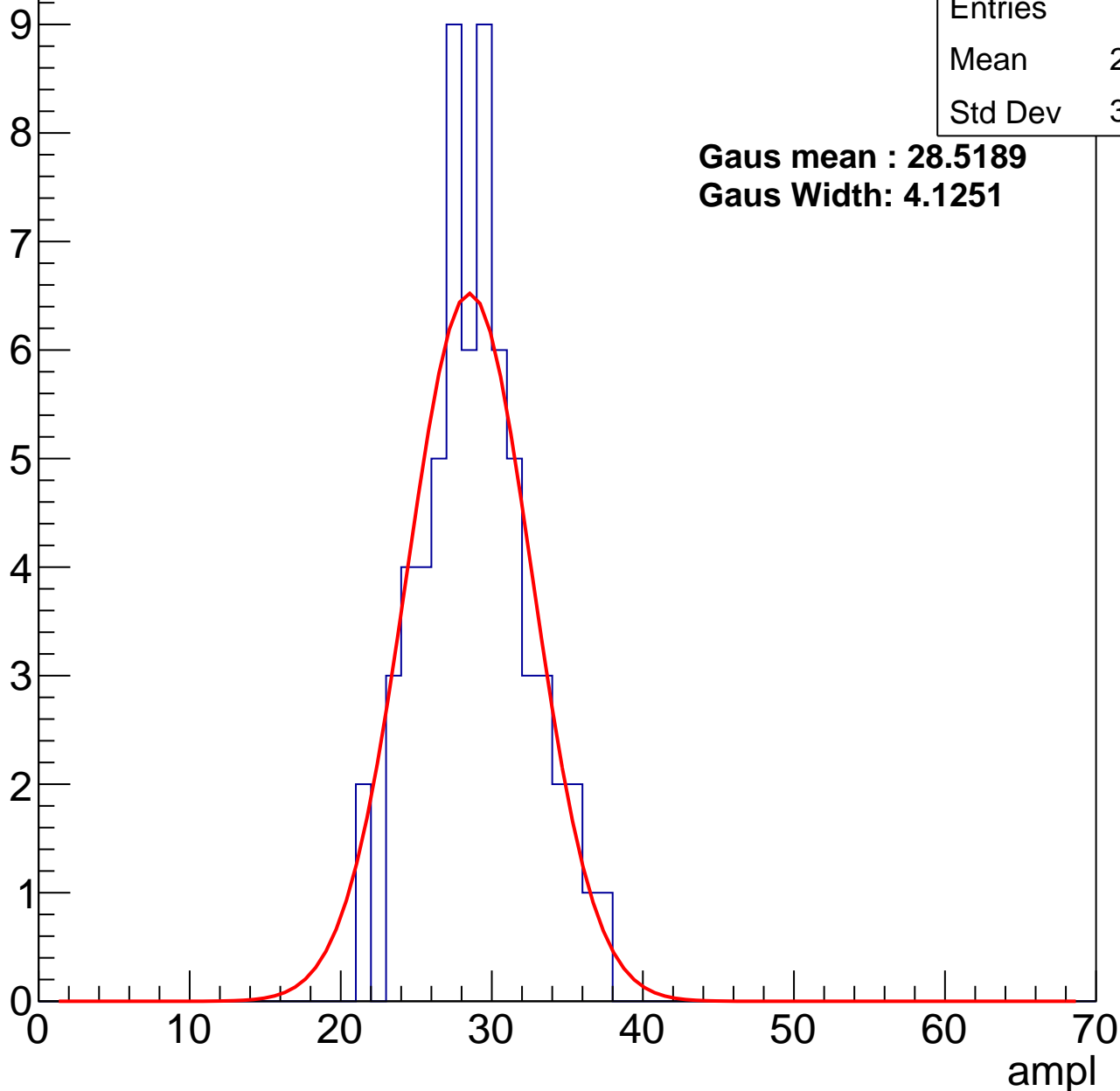
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.46
Std Dev	3.517

Gaus mean : 28.5189

Gaus Width: 4.1251



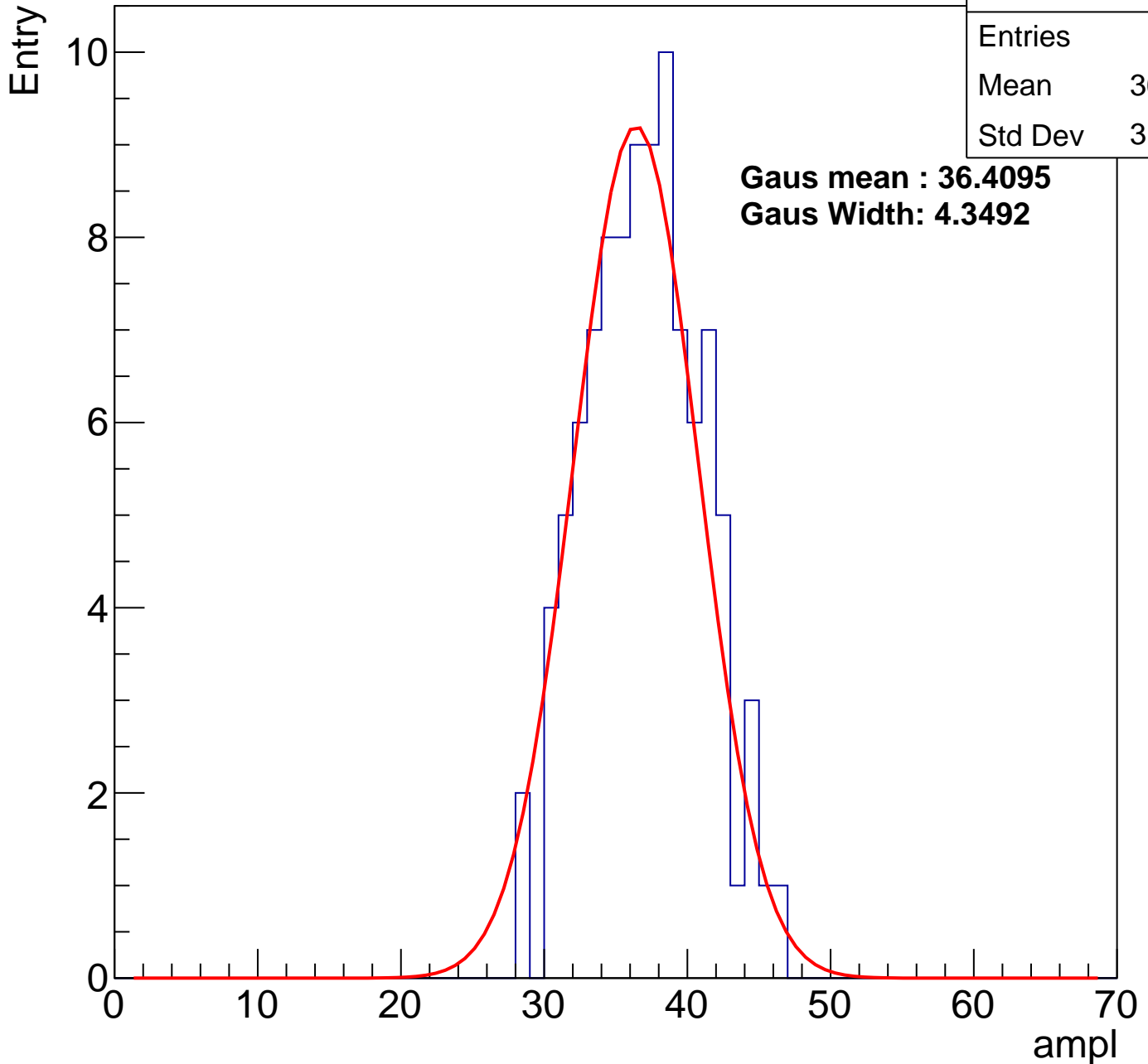
B1L100S, U13-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	99
Mean	36.56
Std Dev	3.957

Gaus mean : 36.4095

Gaus Width: 4.3492



B1L100S, U13-ch33, adc2

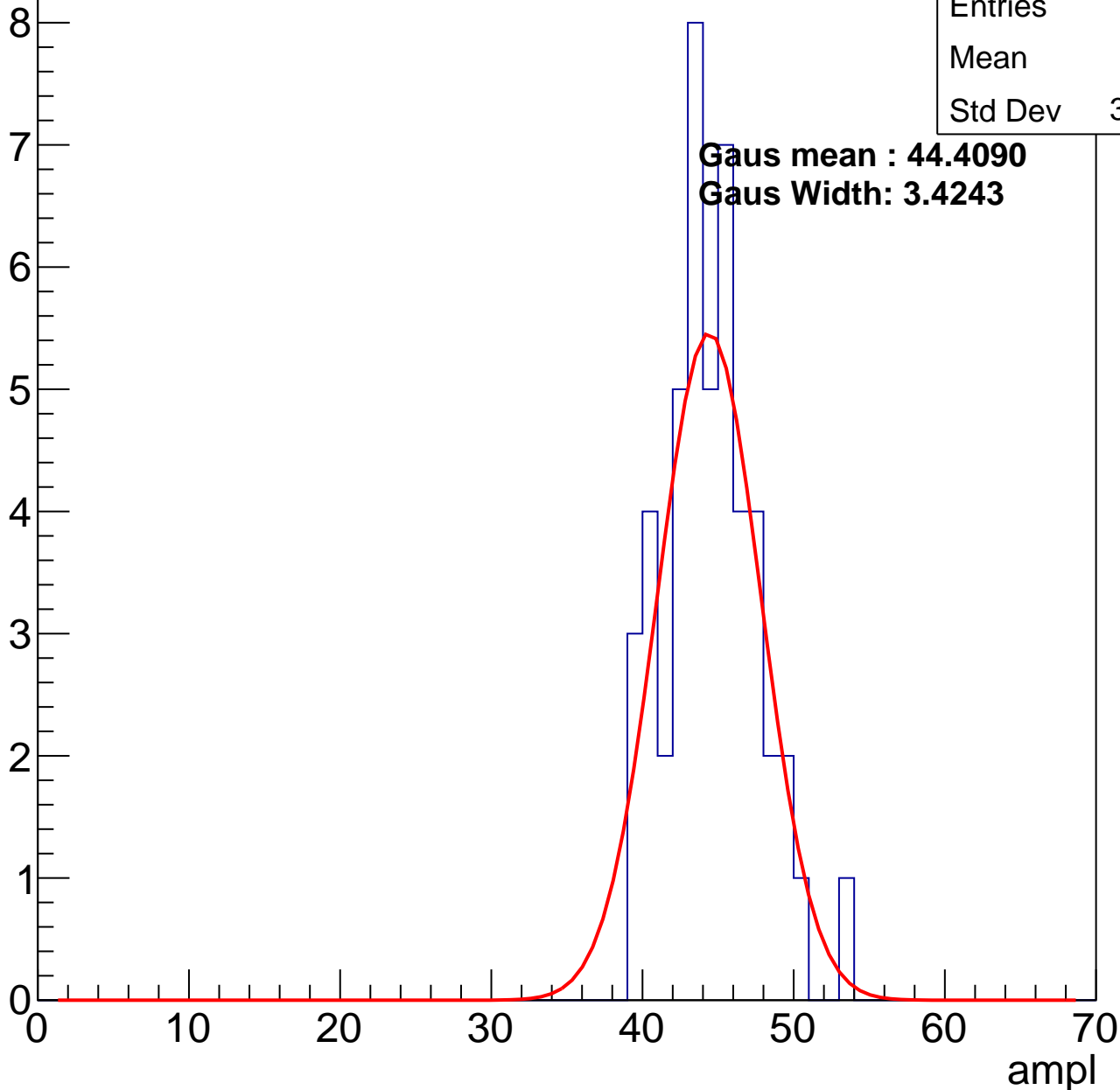
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	44.1
Std Dev	3.036

Gaus mean : 44.4090

Gaus Width: 3.4243



B1L100S, U13-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	50.89
Std Dev	3.752

Entry

10

8

6

4

2

0

0

10

20

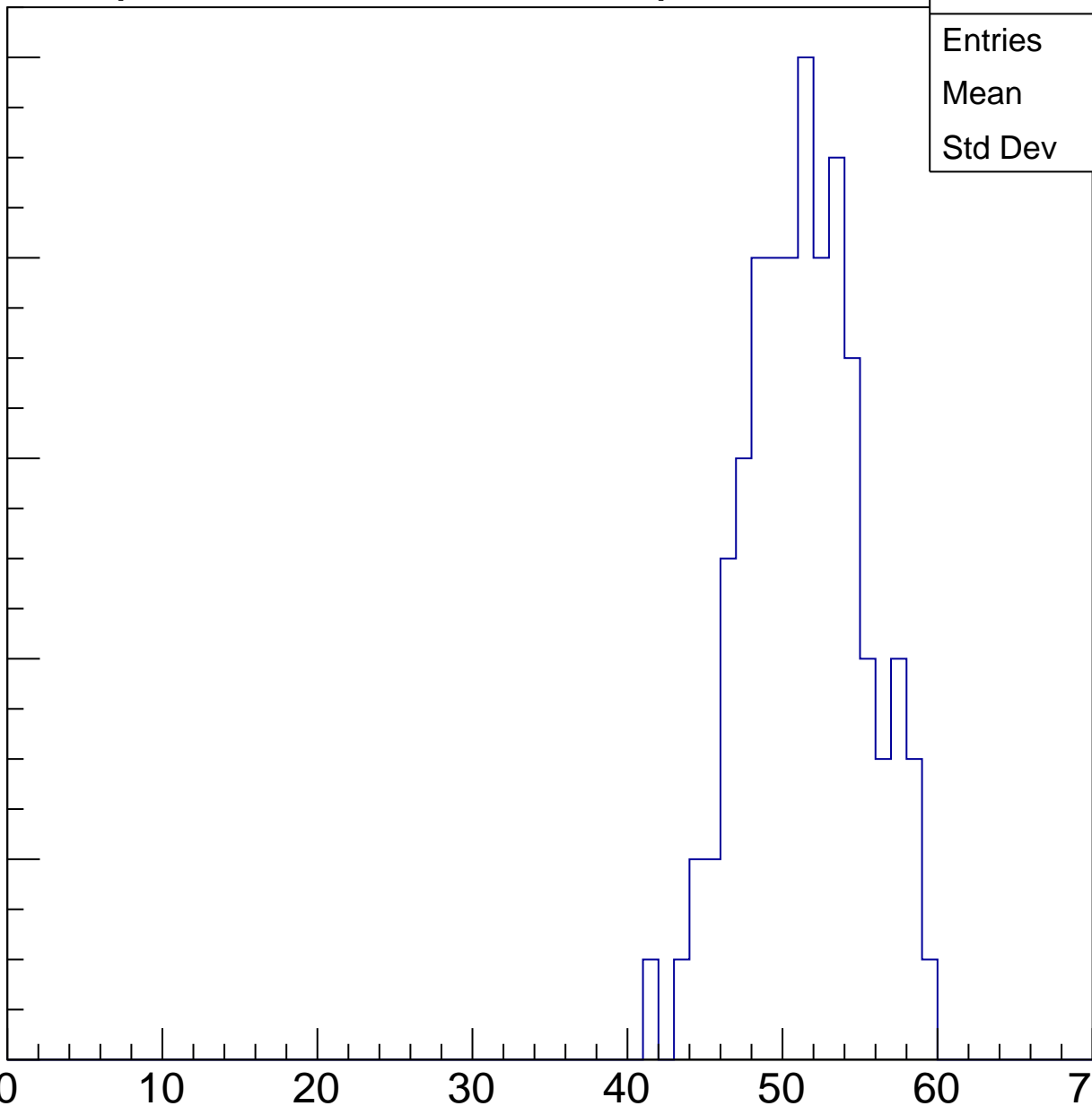
30

40

50

60

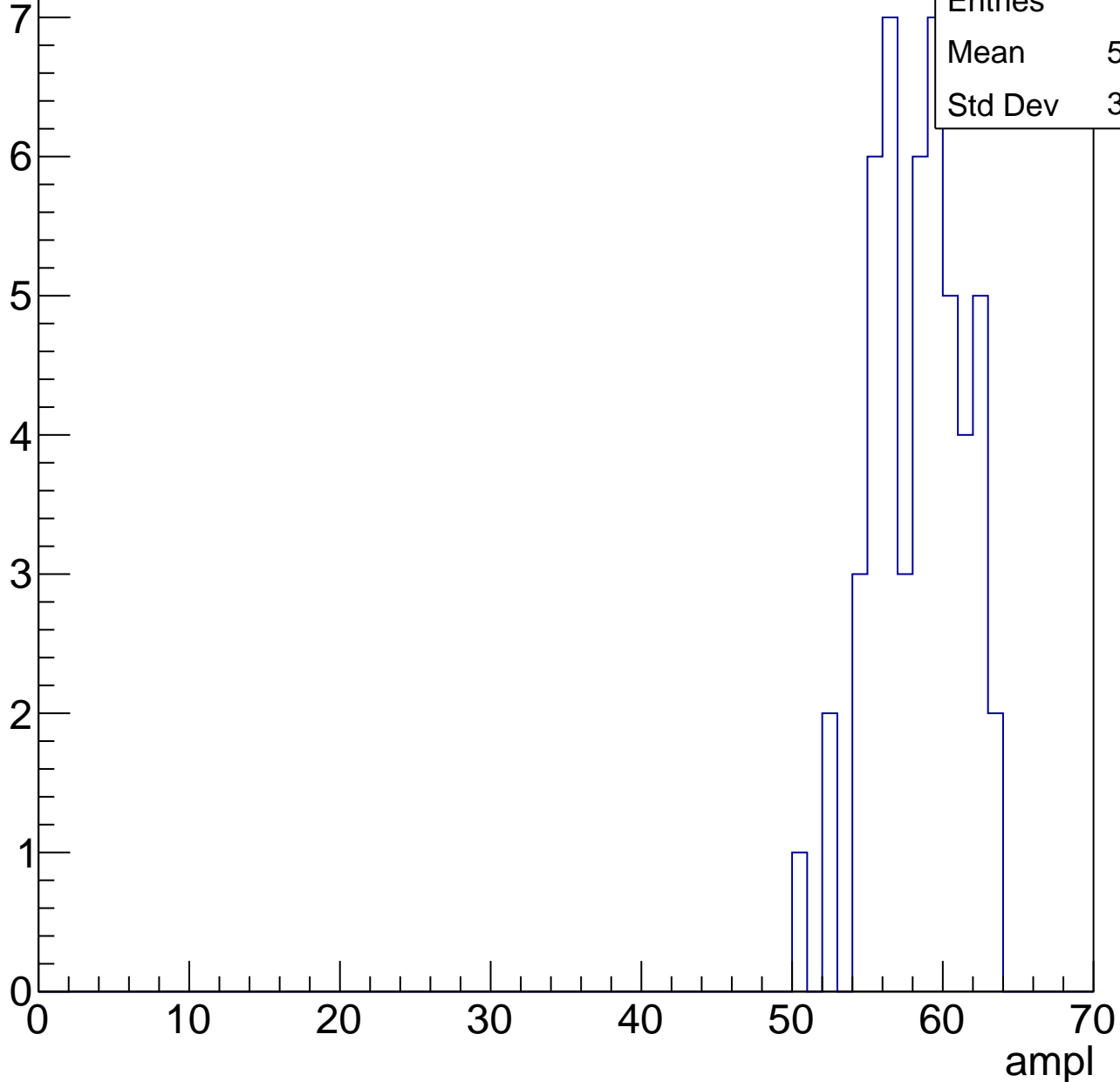
ampl



B1L100S, U13-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

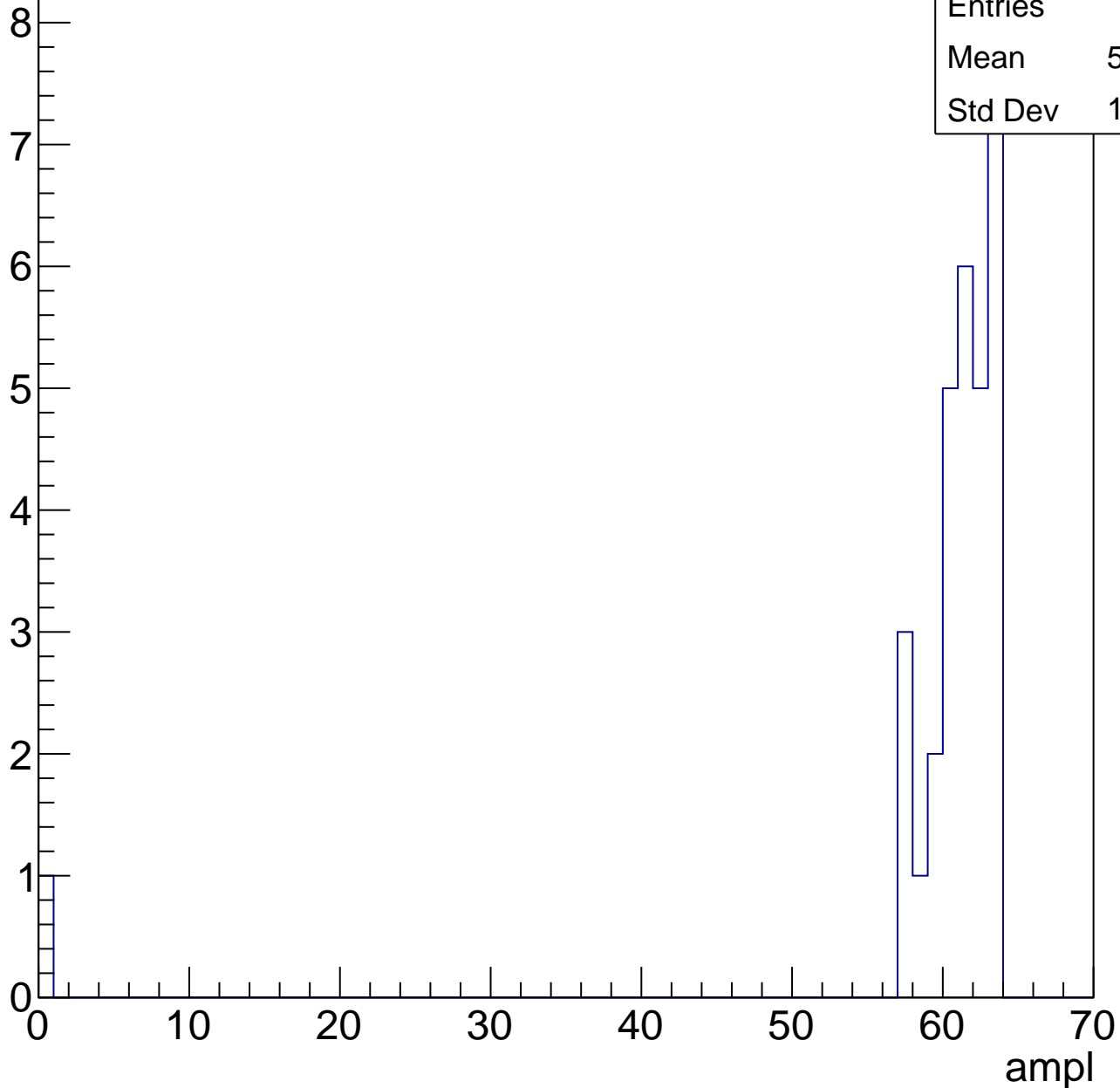


B1L100S, U13-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.94
Std Dev	10.92



B1L100S, U13-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch34, adc0

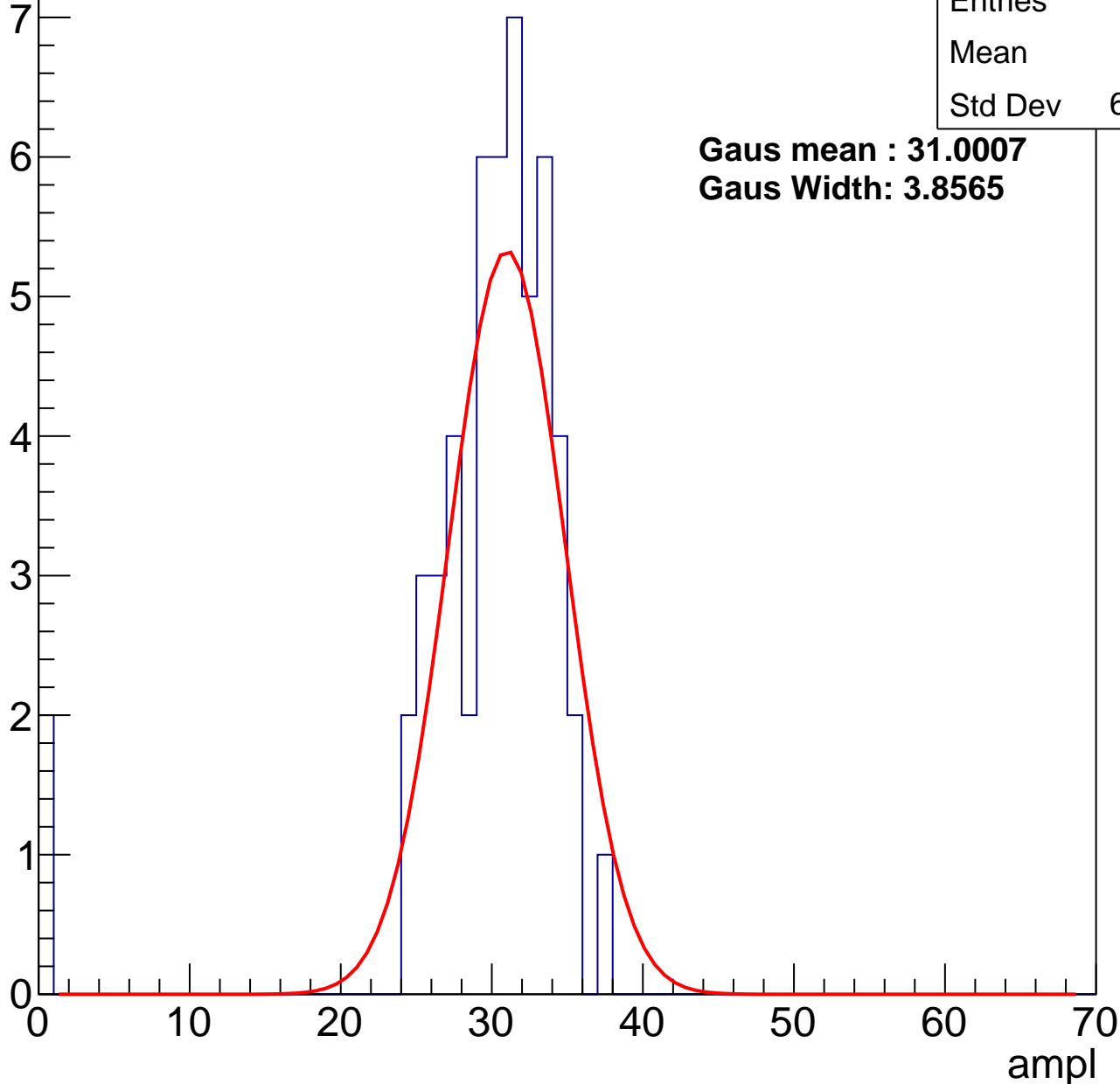
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	29
Std Dev	6.495

Gaus mean : 31.0007

Gaus Width: 3.8565



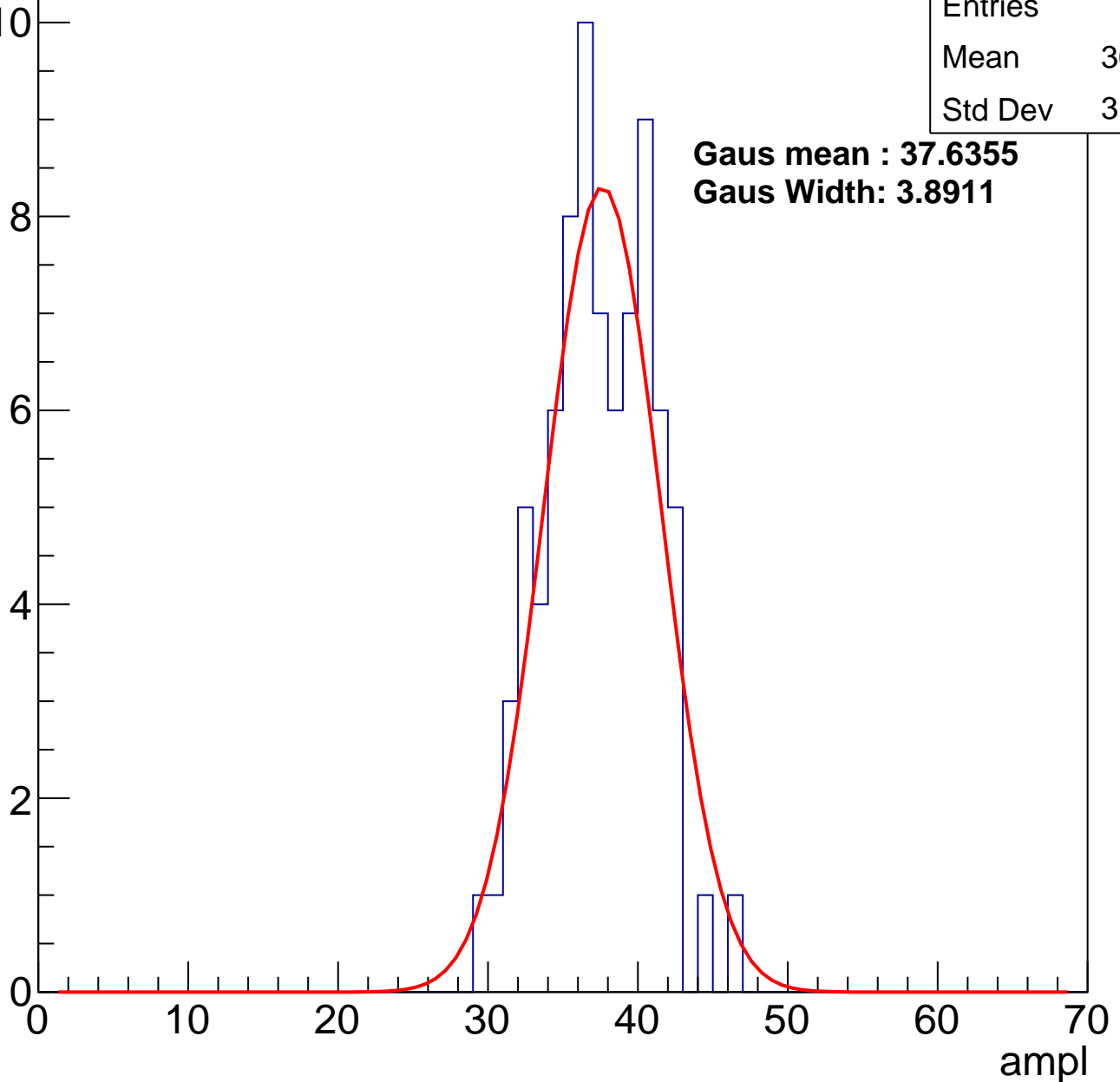
B1L100S, U13-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	36.92
Std Dev	3.485

Gaus mean : 37.6355
Gaus Width: 3.8911



B1L100S, U13-ch34, adc2

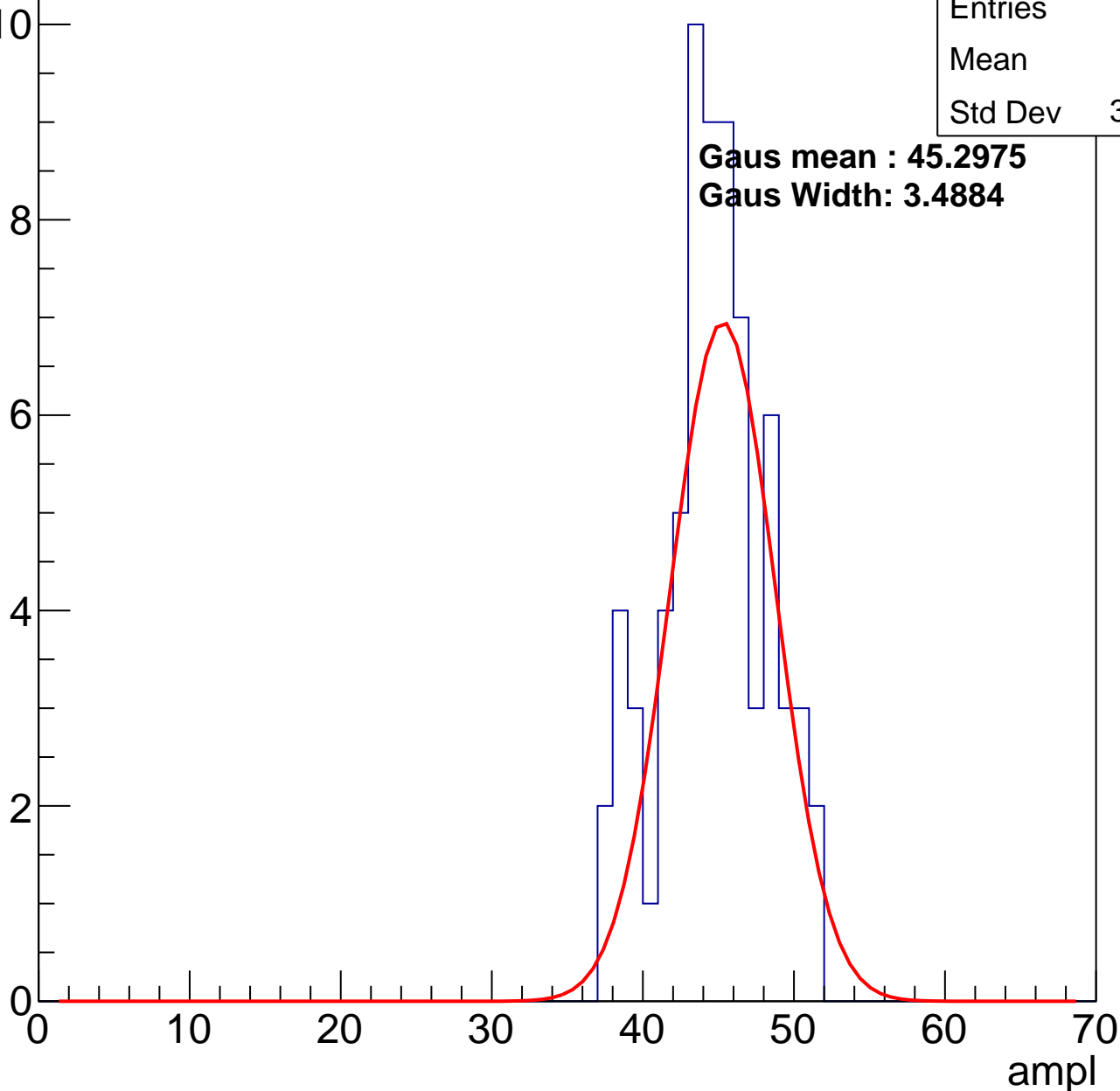
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.2
Std Dev	3.434

Gaus mean : 45.2975

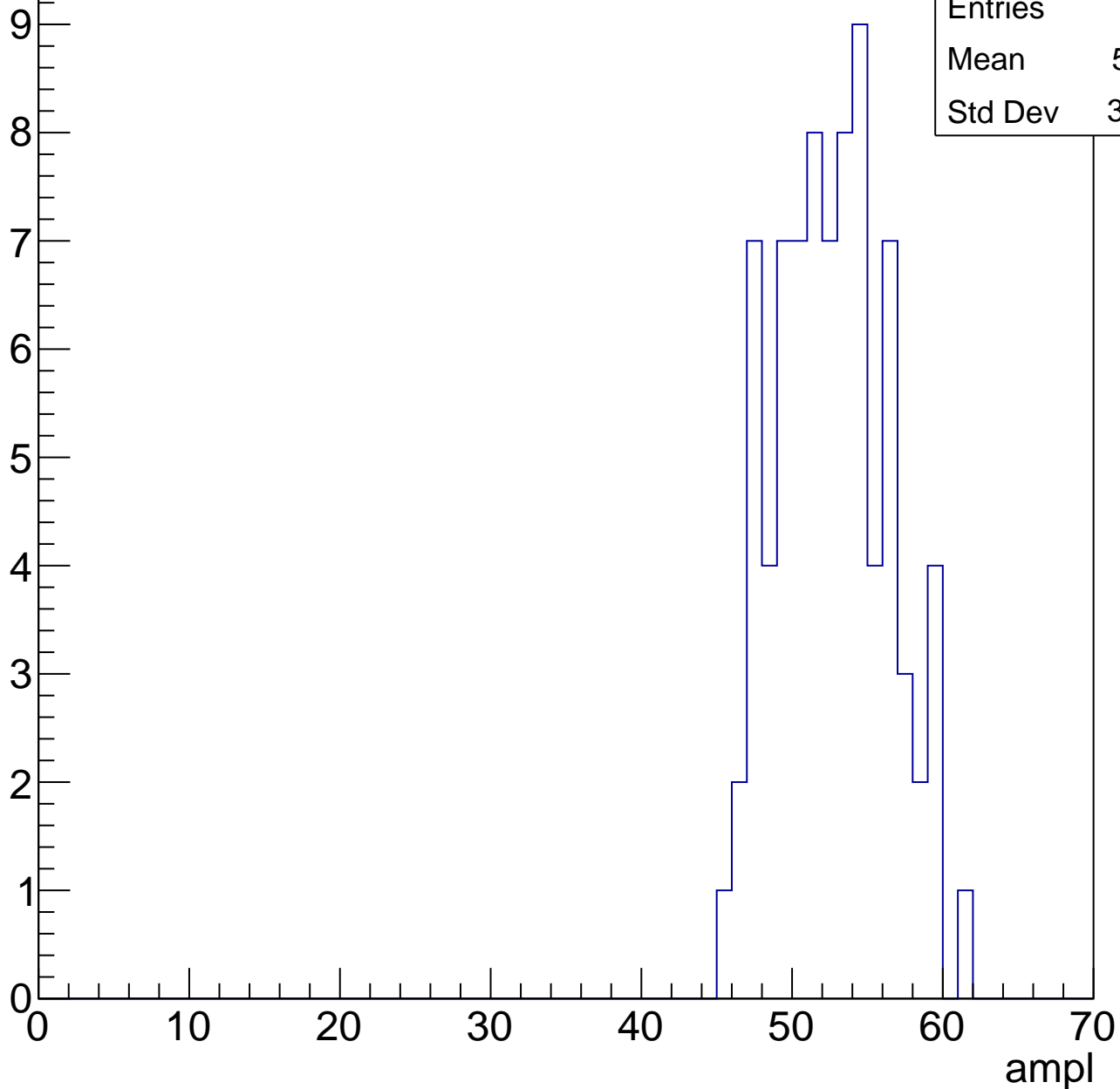
Gaus Width: 3.4884



B1L100S, U13-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



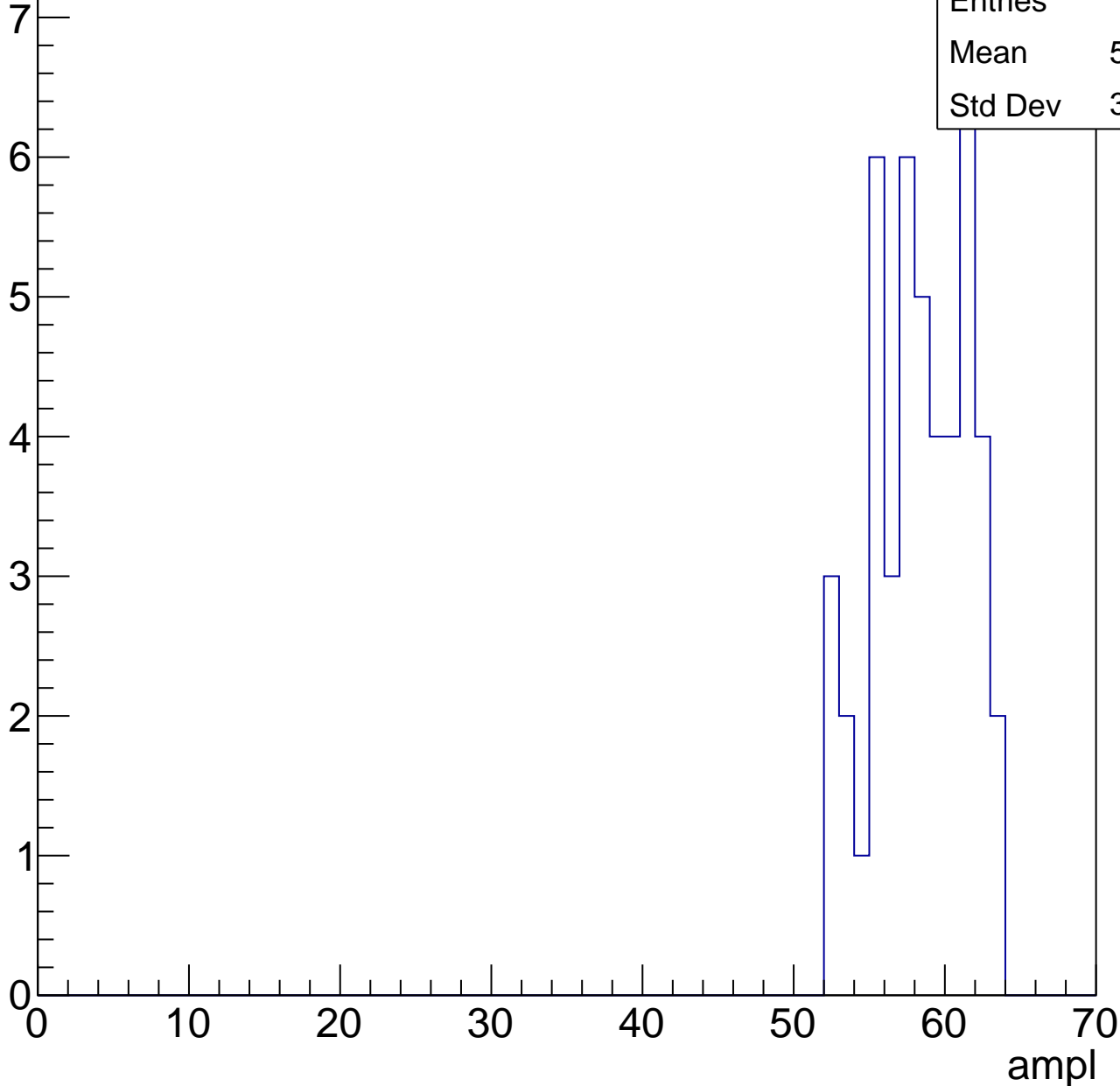
Entries	81
Mean	52.21
Std Dev	3.637

B1L100S, U13-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

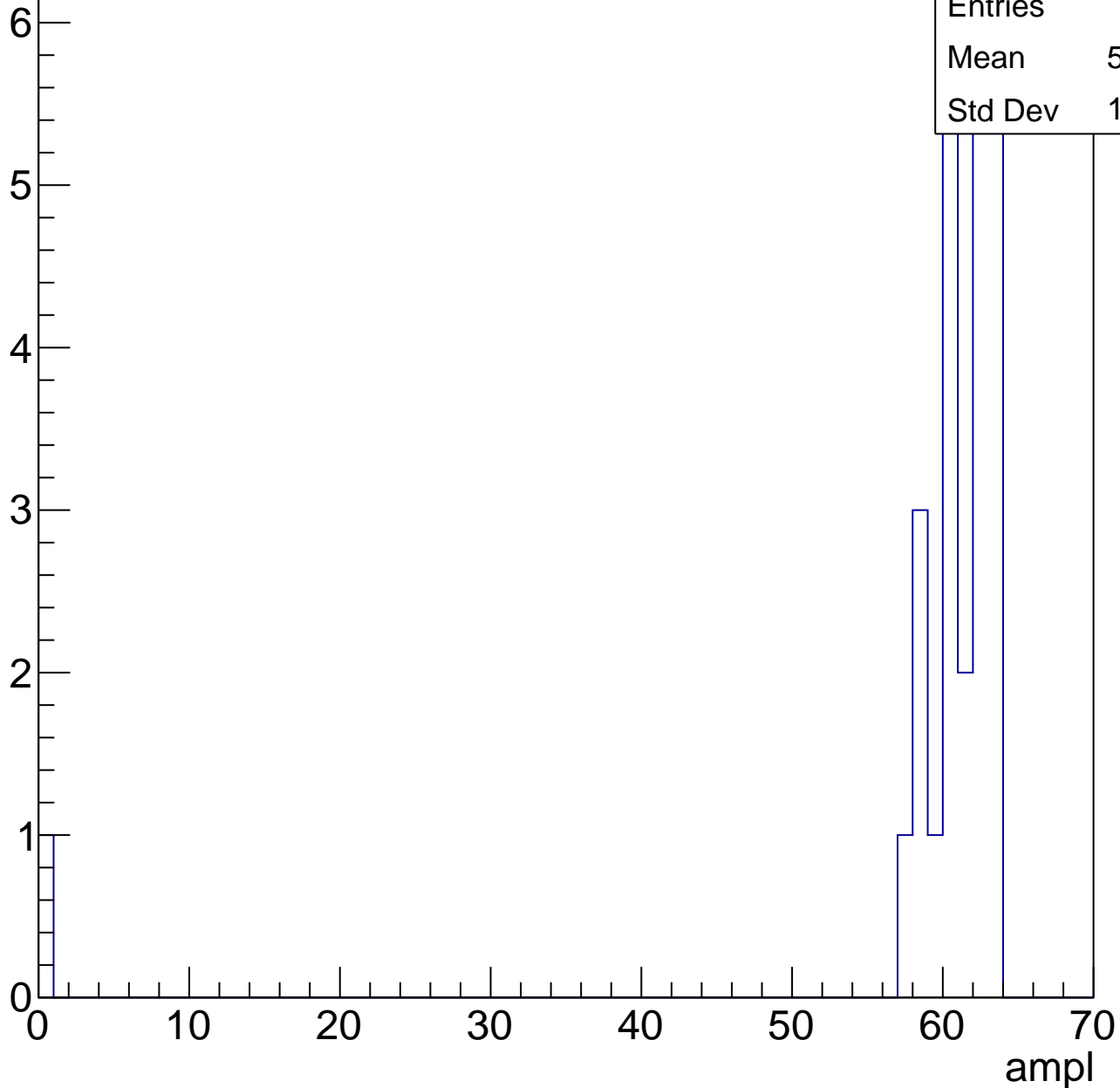
Entries	47
Mean	57.94
Std Dev	3.069



B1L100S, U13-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch35, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	32.44
Std Dev	3.107

Gaus mean : 32.7876

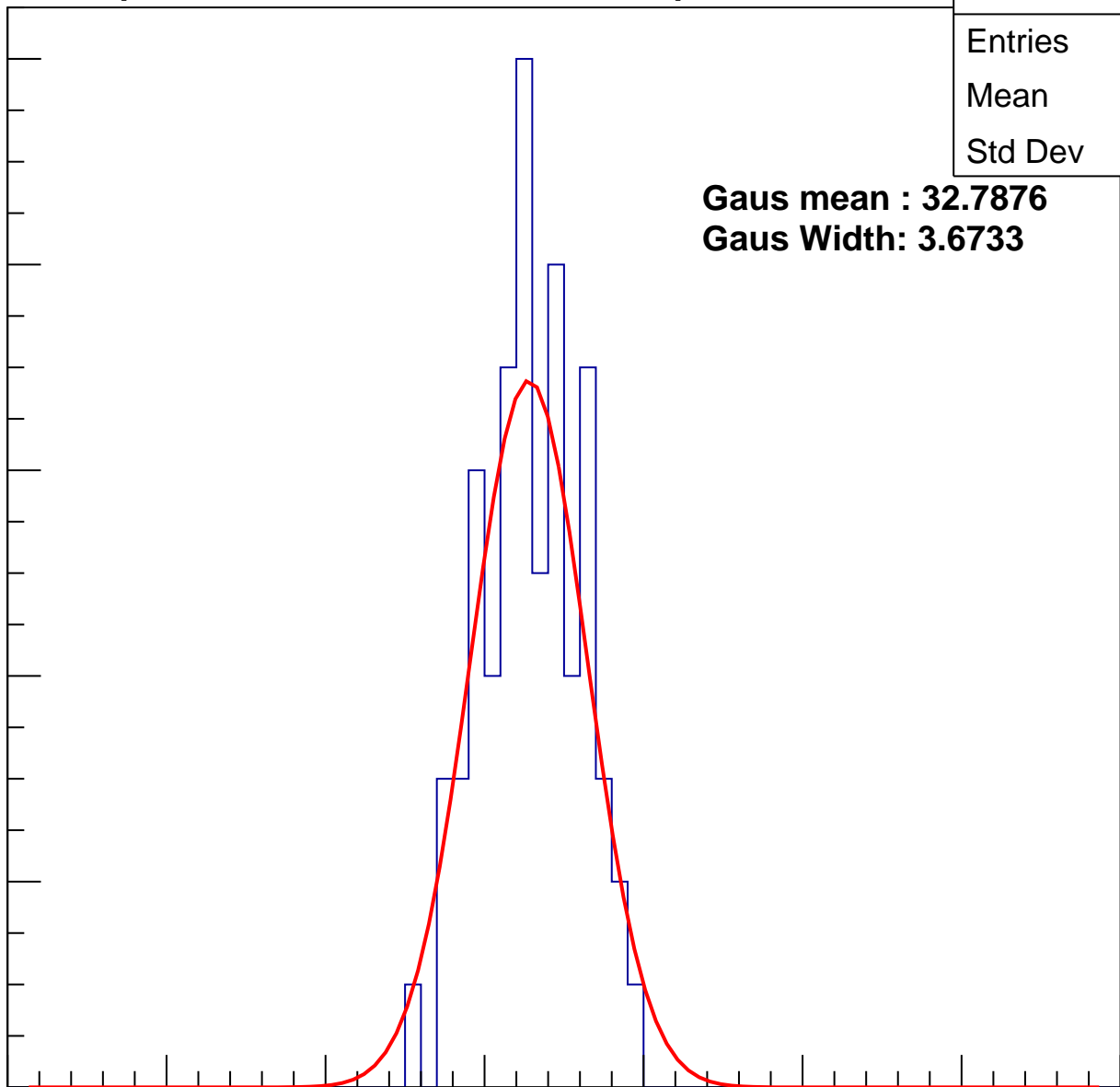
Gaus Width: 3.6733

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch35, adc1

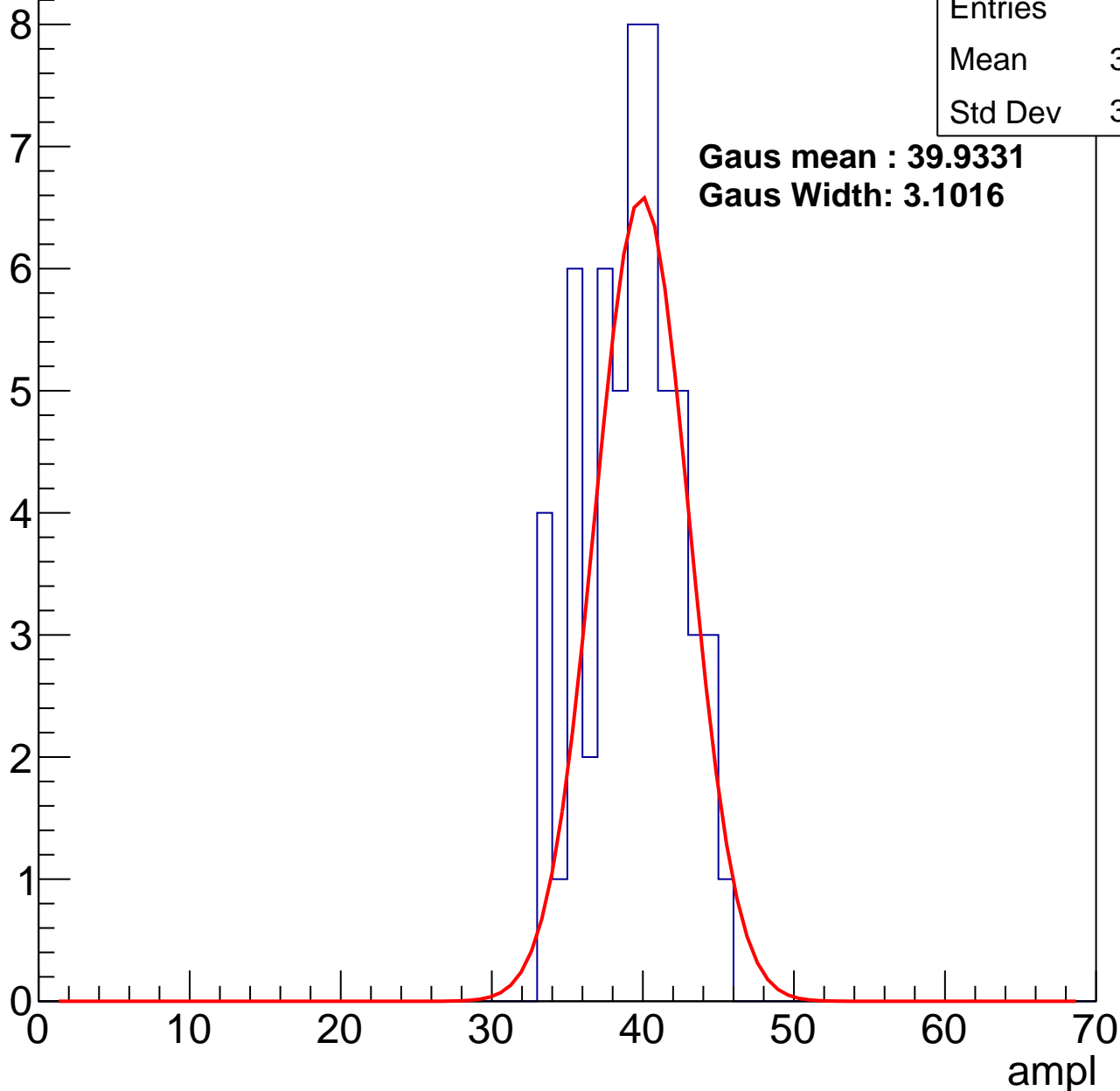
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	38.82
Std Dev	3.084

Gaus mean : 39.9331

Gaus Width: 3.1016



B1L100S, U13-ch35, adc2

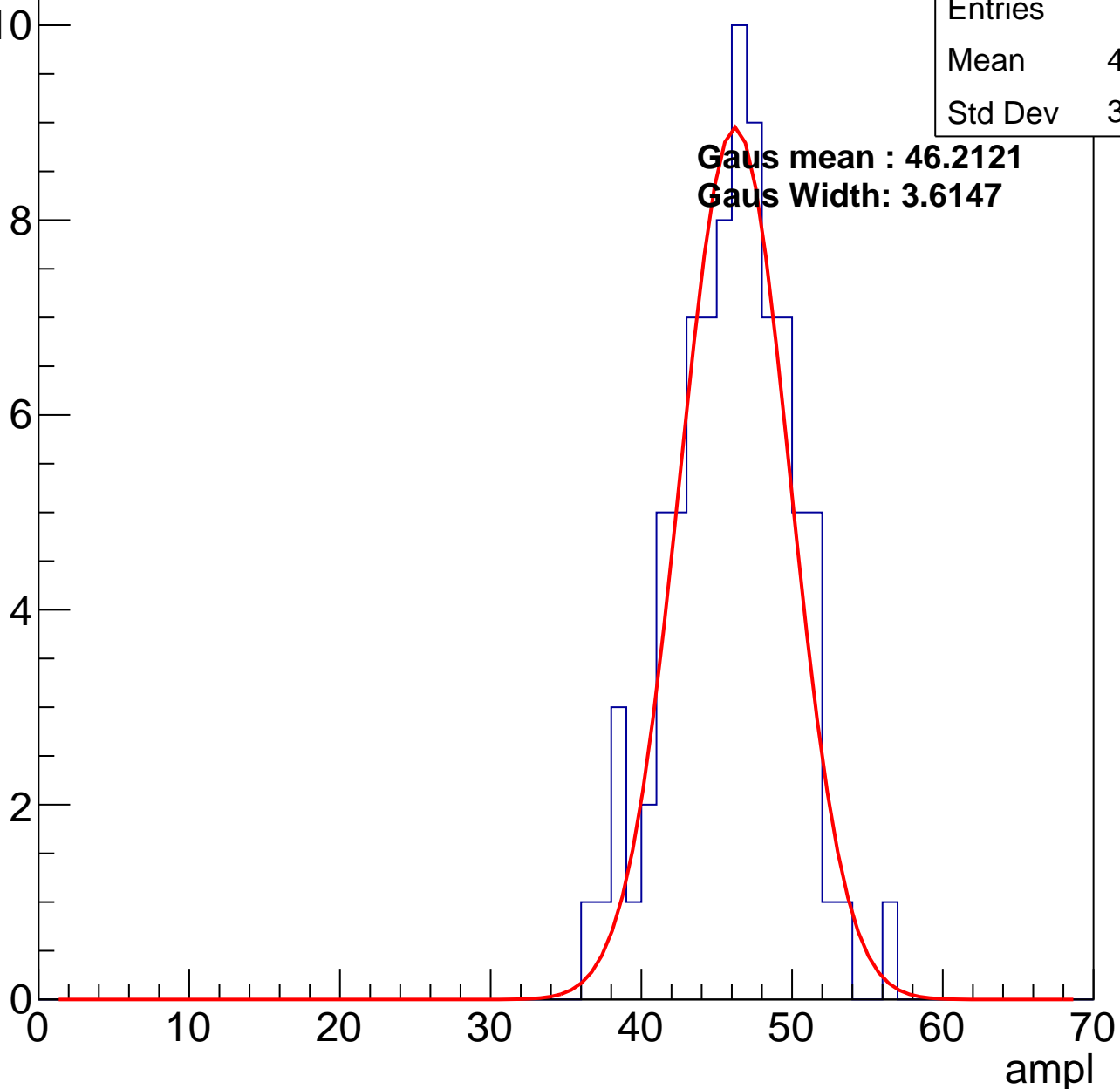
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	45.56
Std Dev	3.845

Gaus mean : 46.2121

Gaus Width: 3.6147

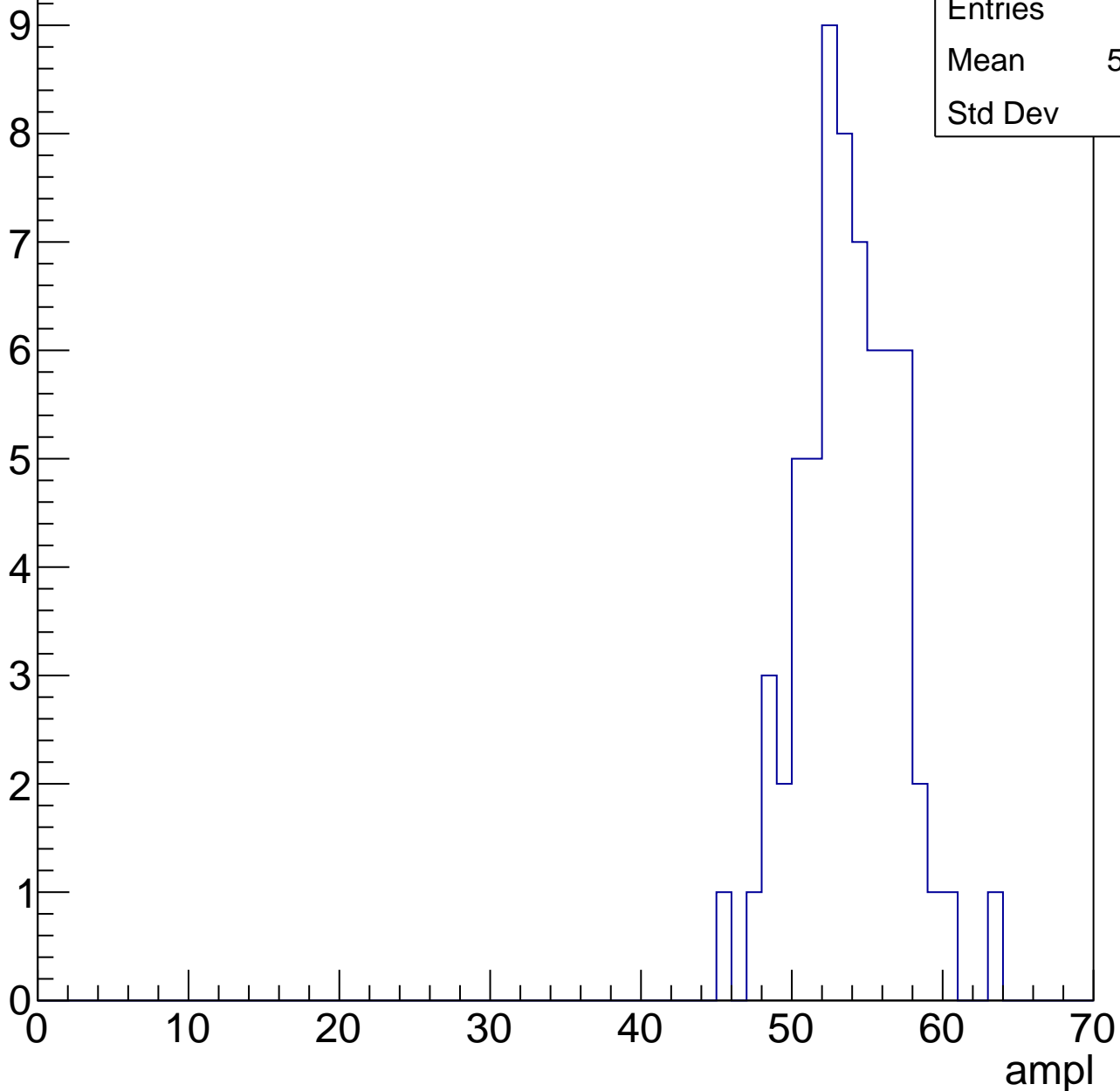


B1L100S, U13-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

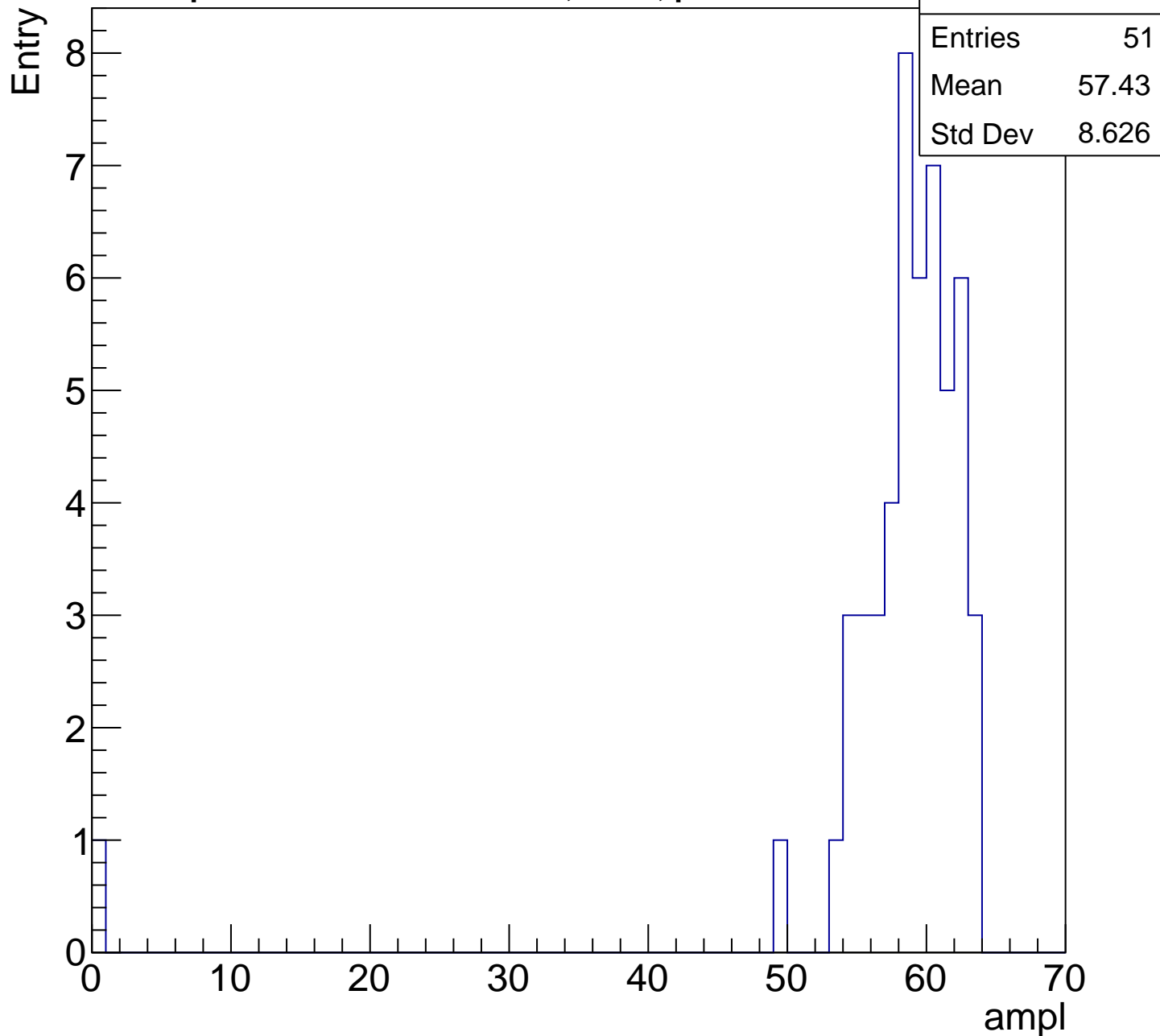
Entry

Entries	64
Mean	53.36
Std Dev	3.28



B1L100S, U13-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

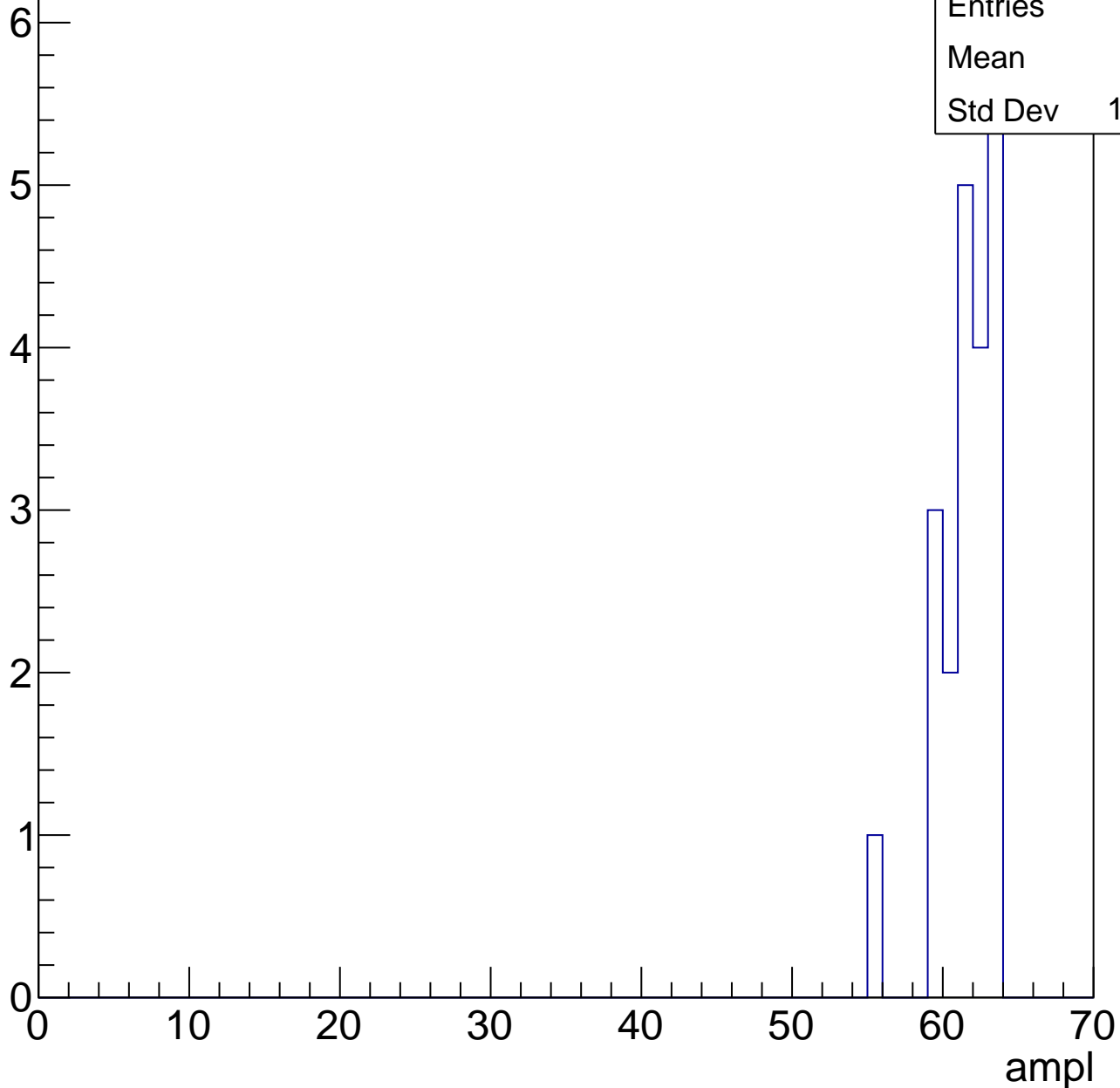


B1L100S, U13-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	61.1
Std Dev	1.925



B1L100S, U13-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

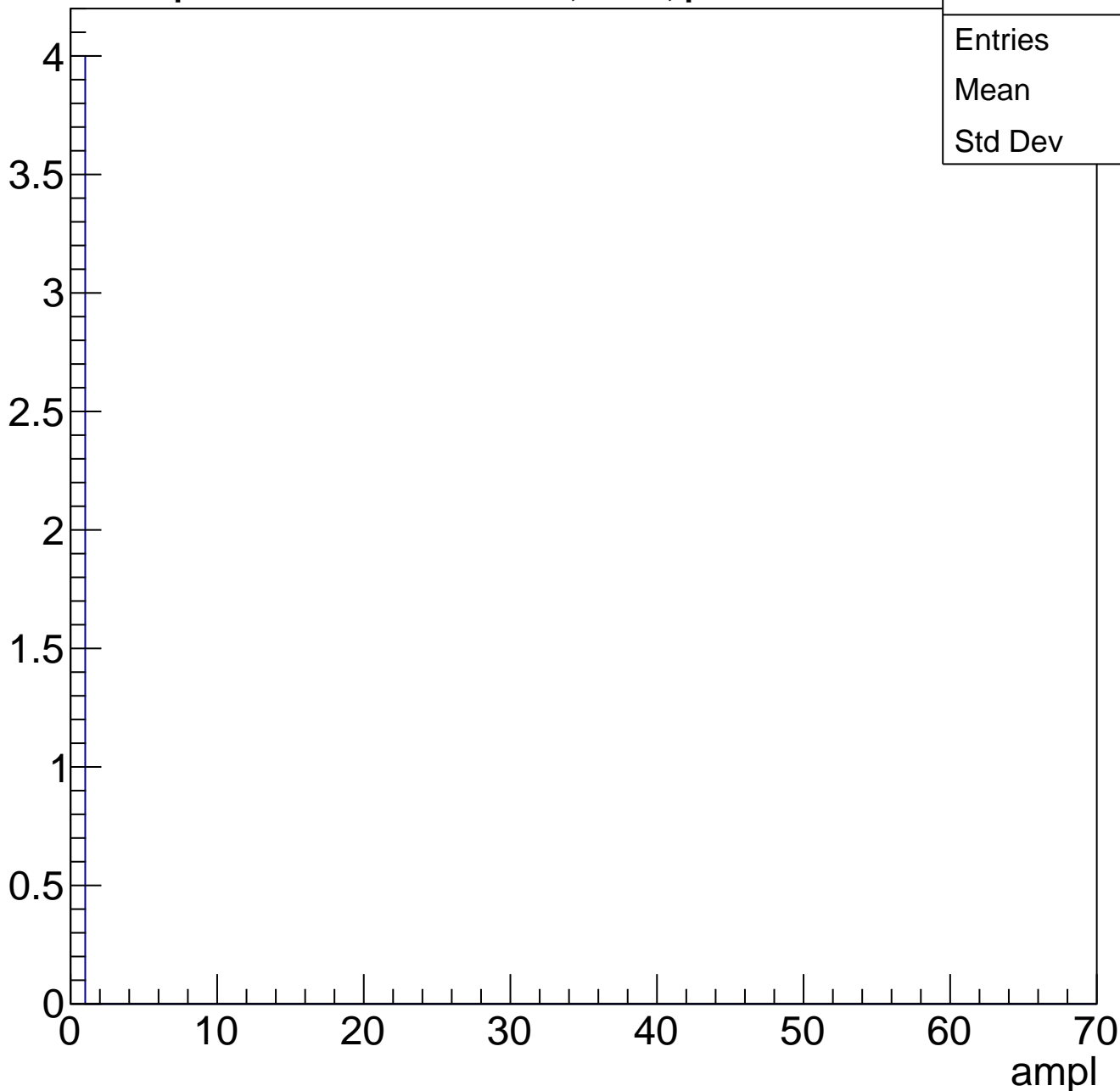
Entry



B1L100S, U13-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L100S, U13-ch36, adc0

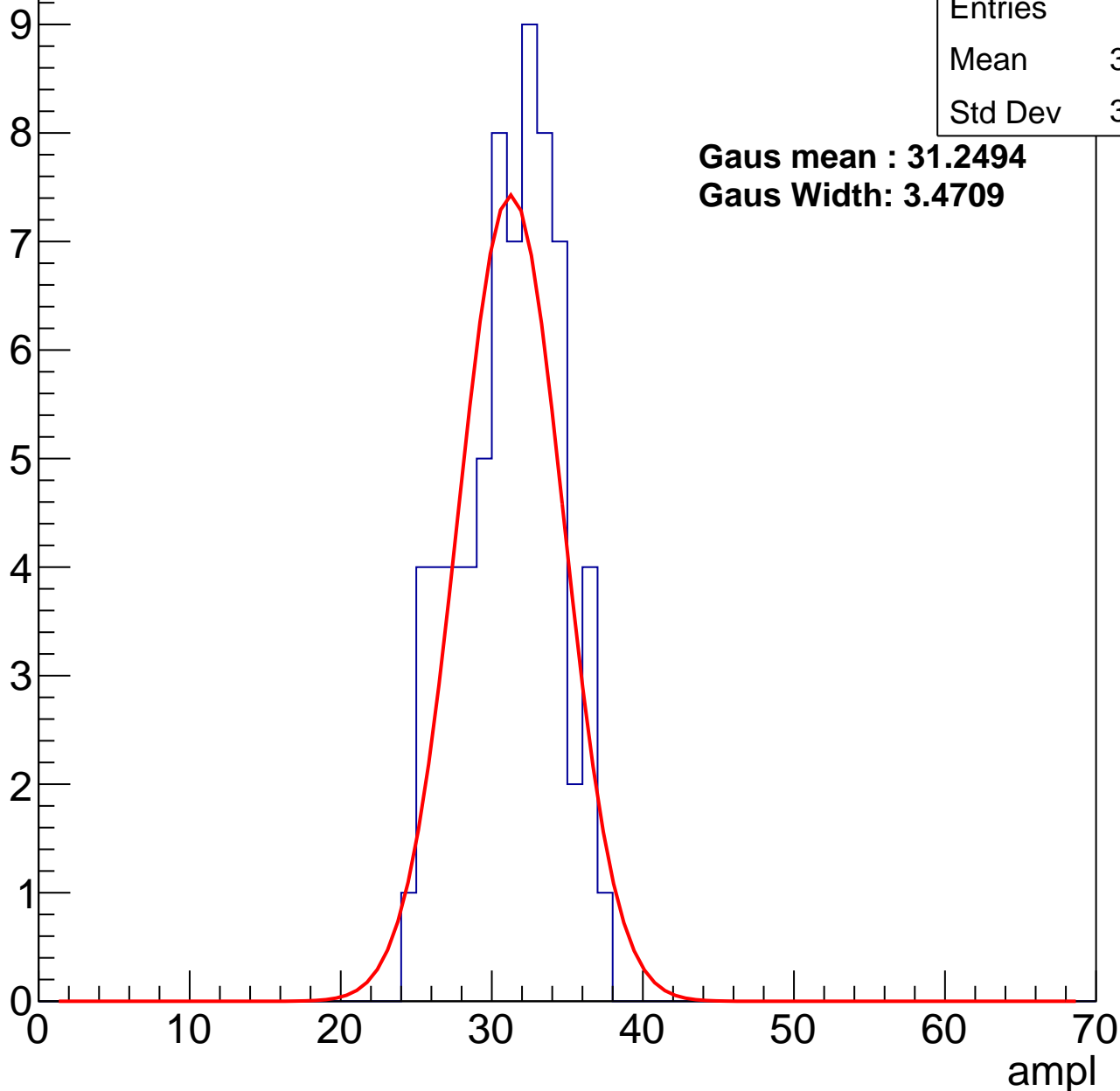
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.75
Std Dev	3.183

Gaus mean : 31.2494

Gaus Width: 3.4709



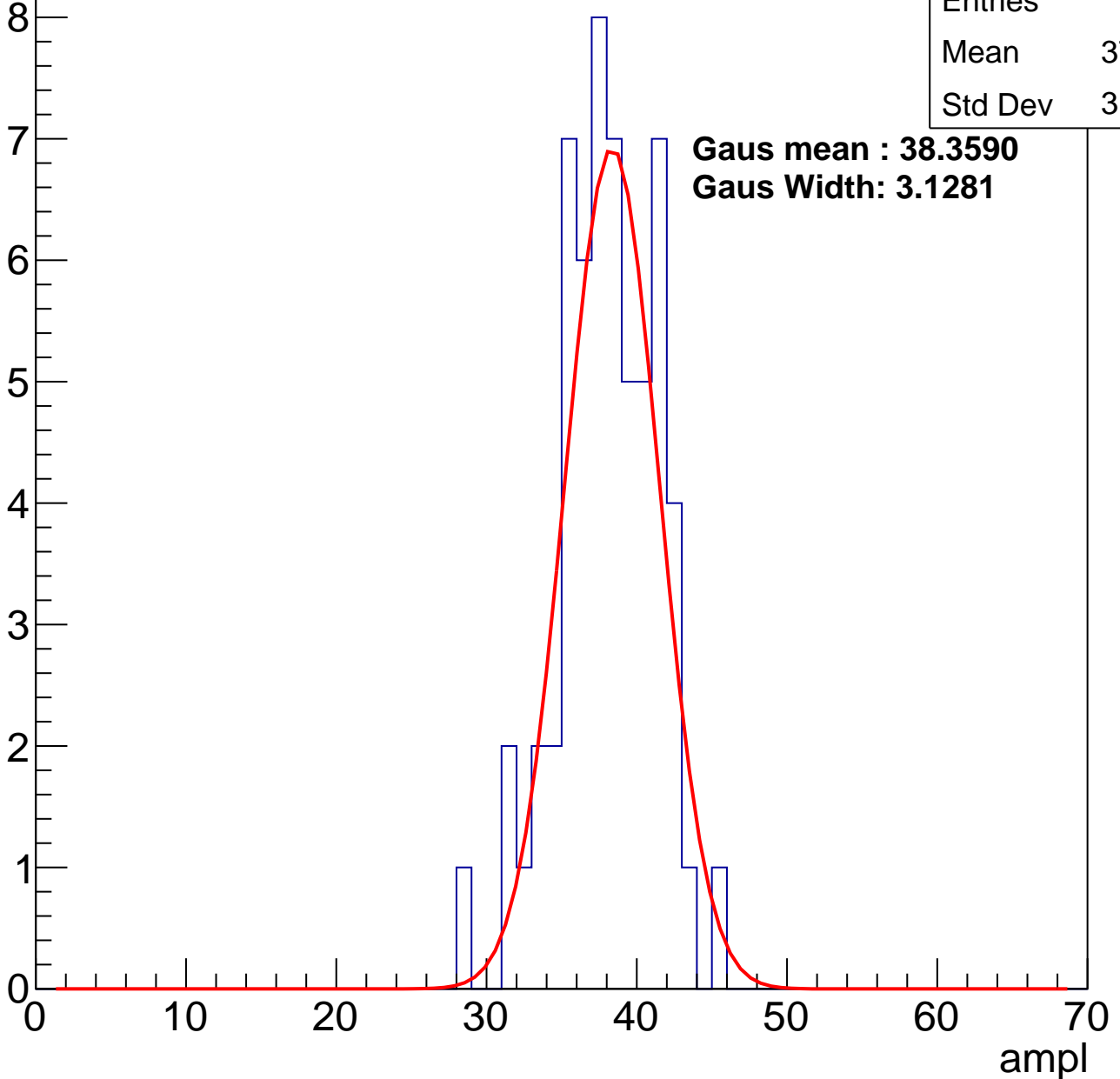
B1L100S, U13-ch36, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.58
Std Dev	3.258

Gaus mean : 38.3590
Gaus Width: 3.1281



B1L100S, U13-ch36, adc2

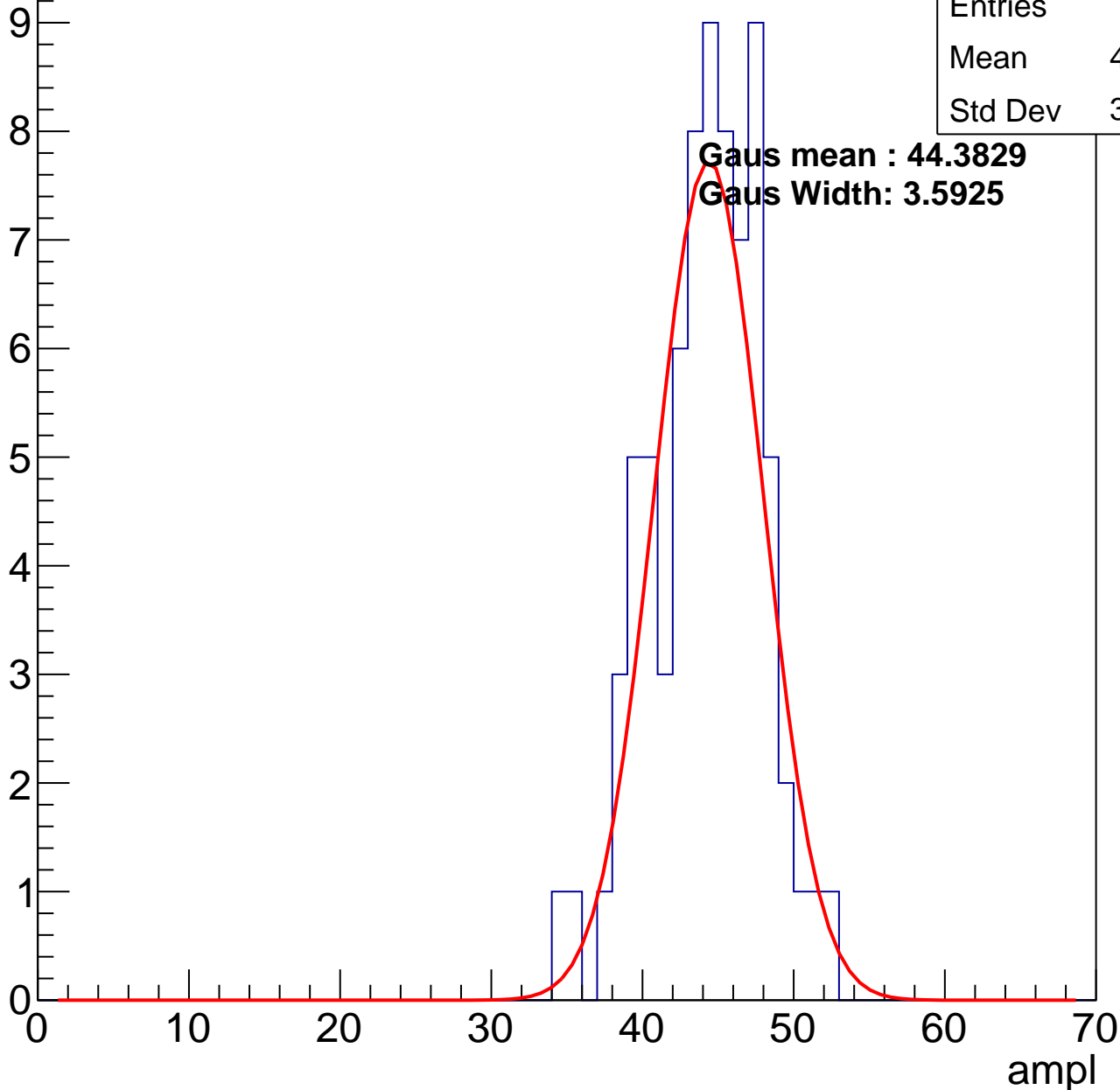
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	43.76
Std Dev	3.616

Gaus mean : 44.3829

Gaus Width: 3.5925

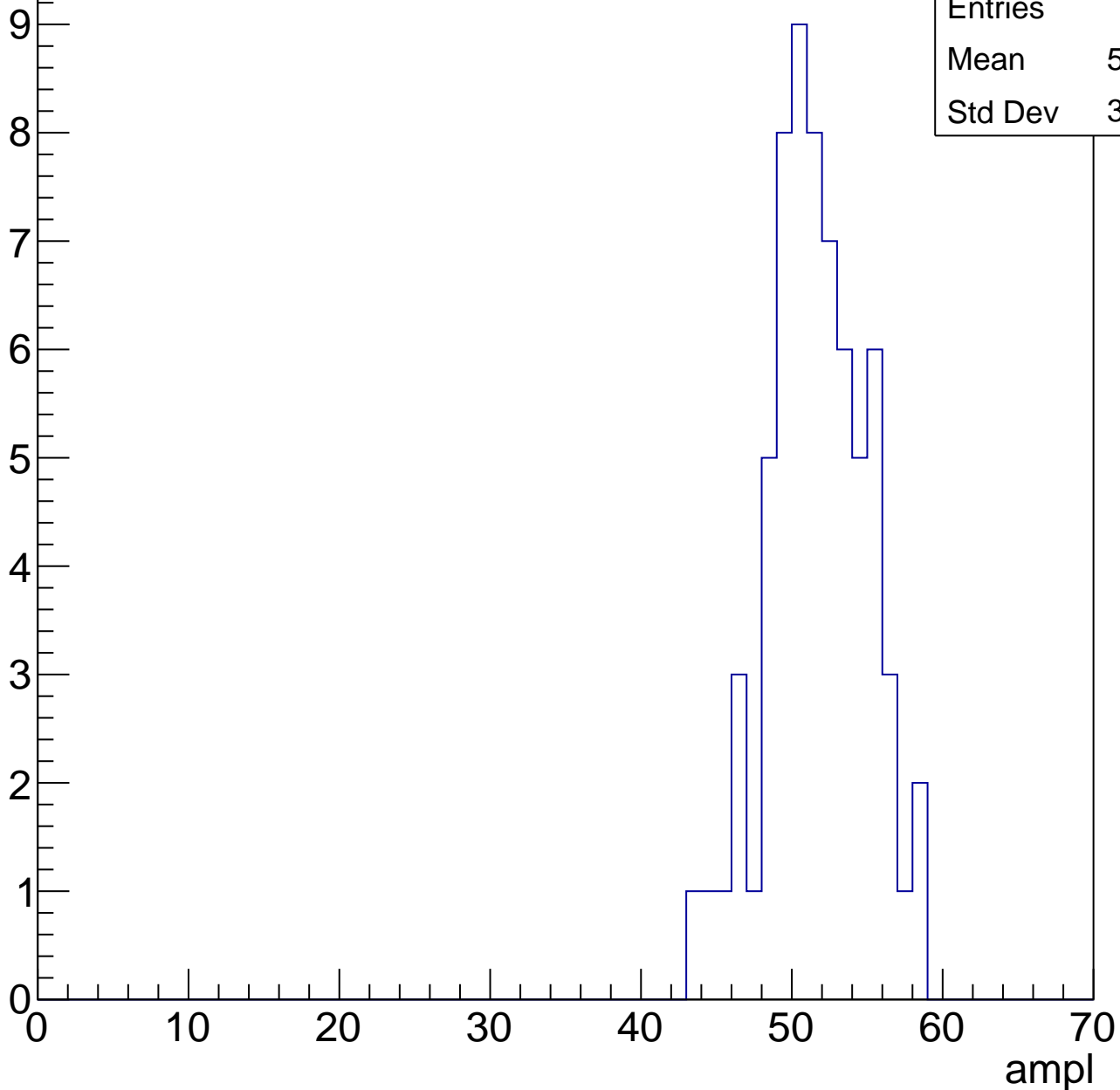


B1L100S, U13-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	51.19
Std Dev	3.243

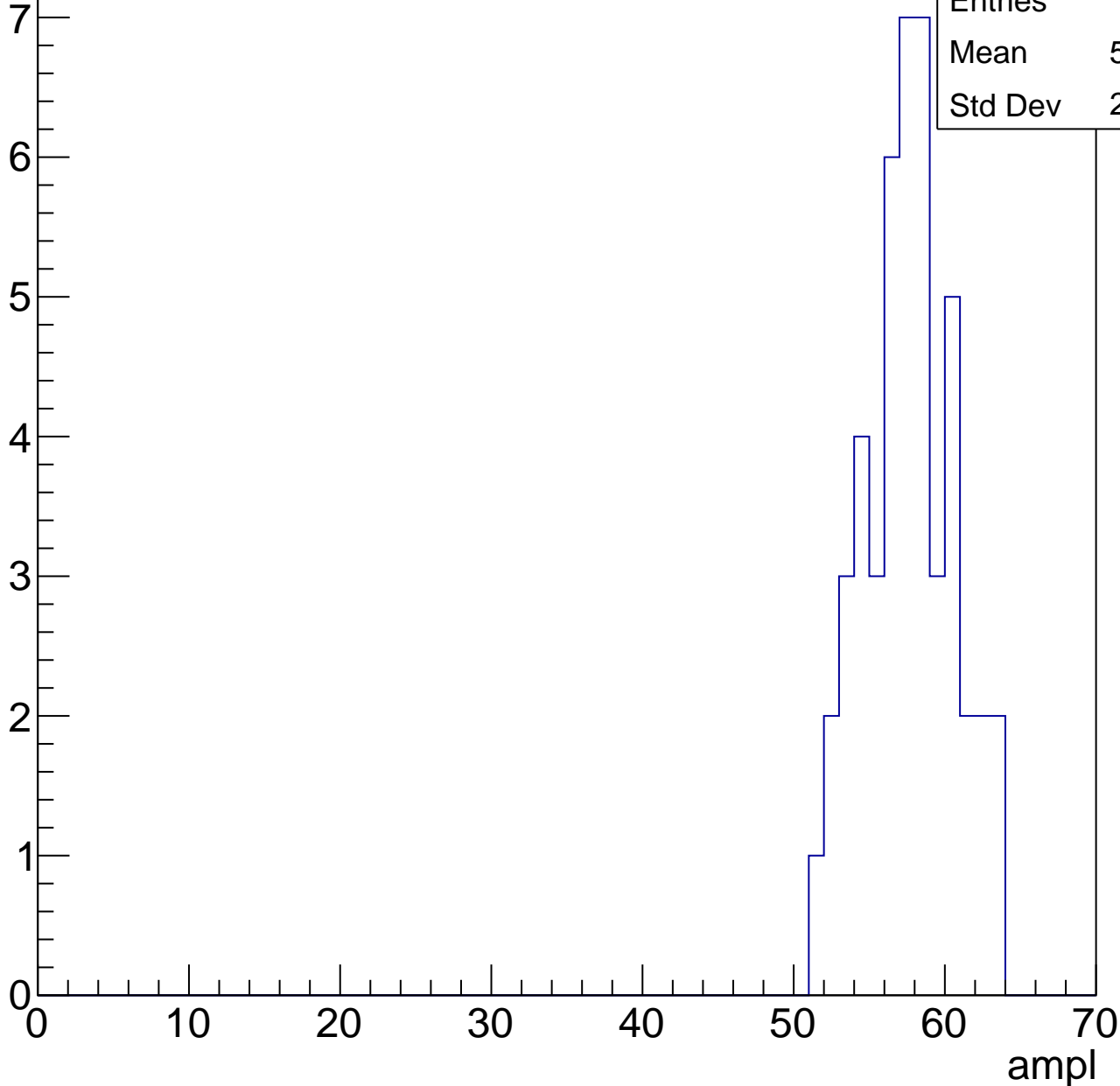


B1L100S, U13-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

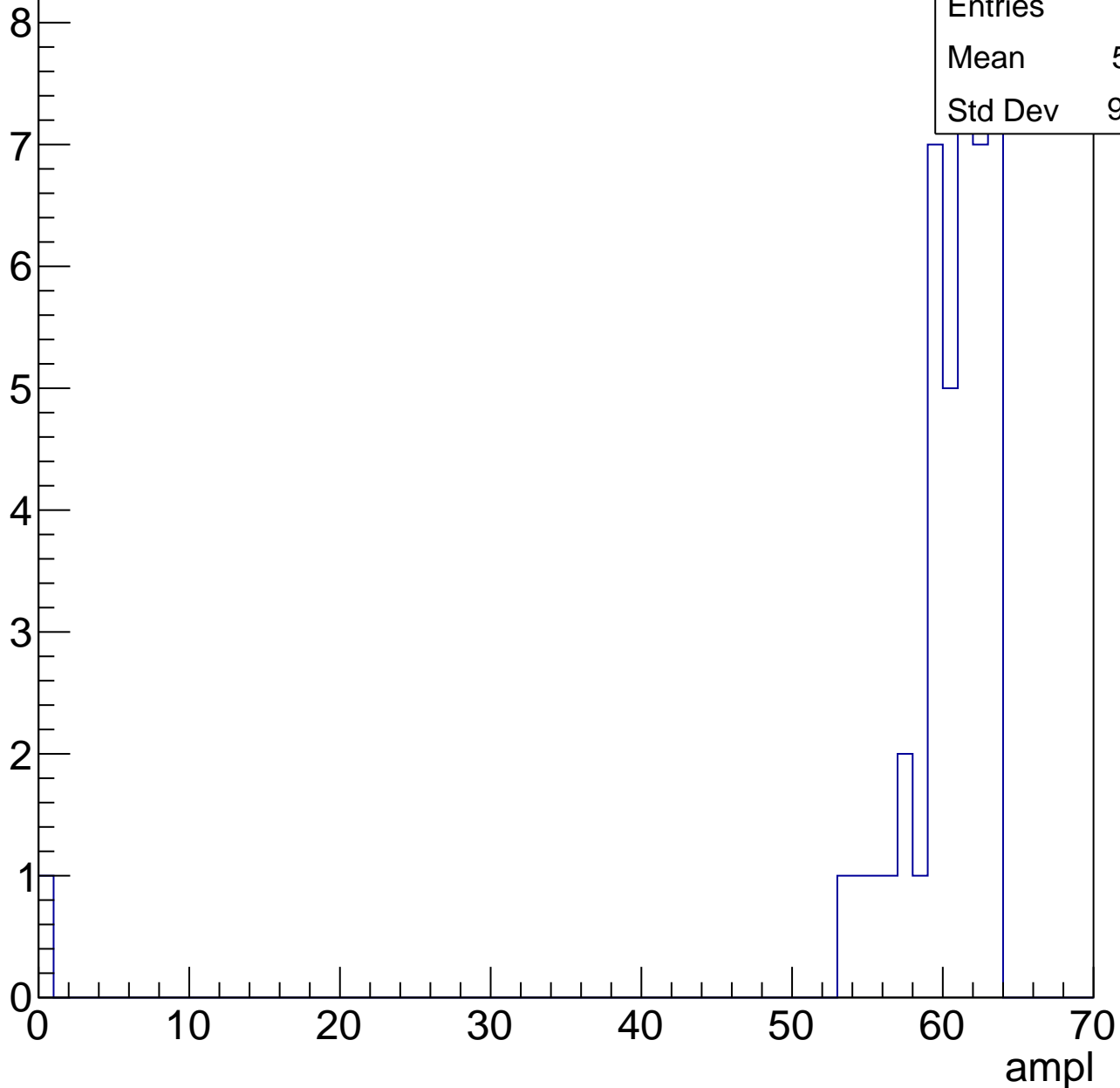
Entries	47
Mean	57.13
Std Dev	2.936



B1L100S, U13-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch37, adc0

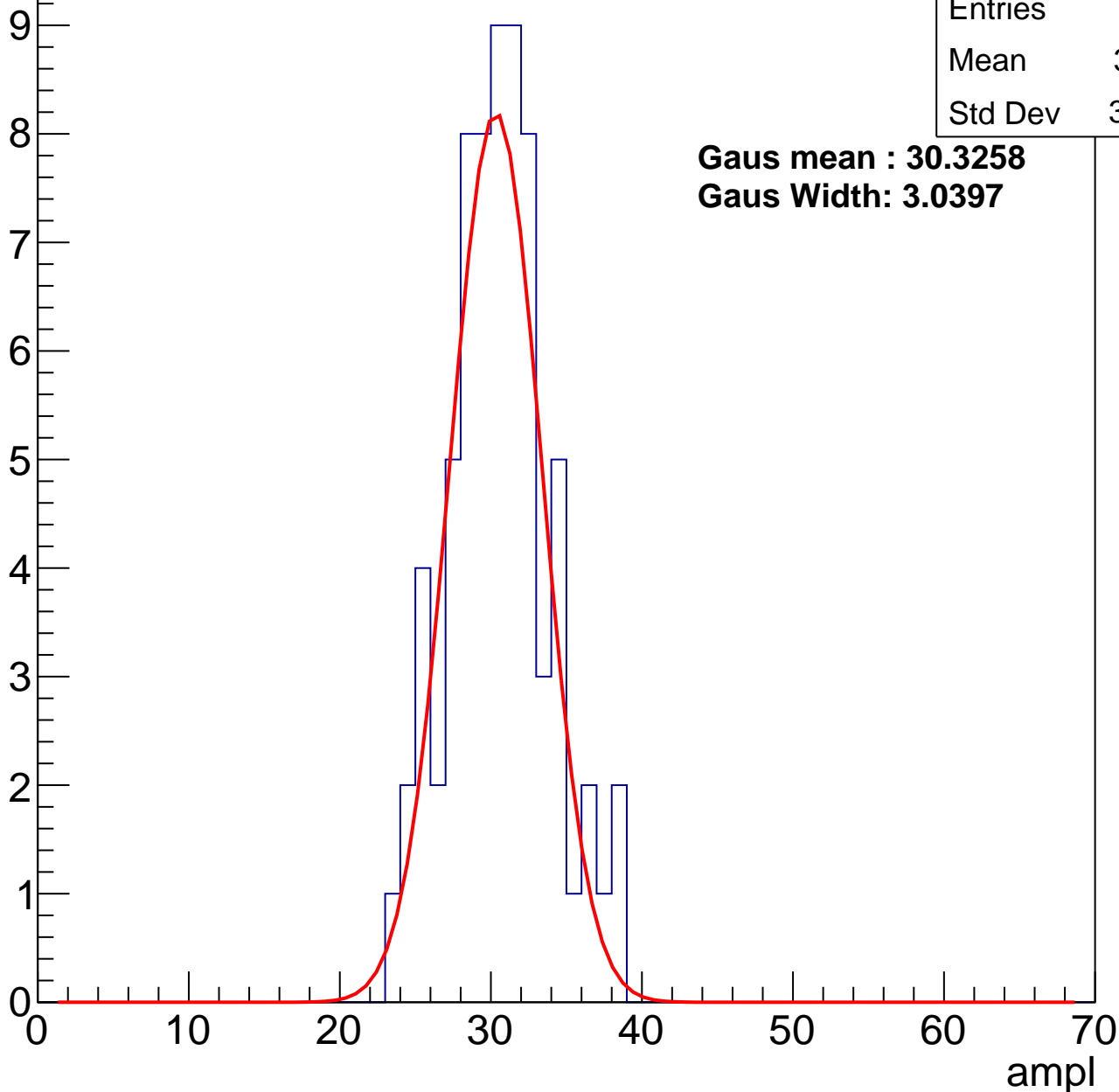
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.11
Std Dev	3.293

Gaus mean : 30.3258

Gaus Width: 3.0397



B1L100S, U13-ch37, adc1

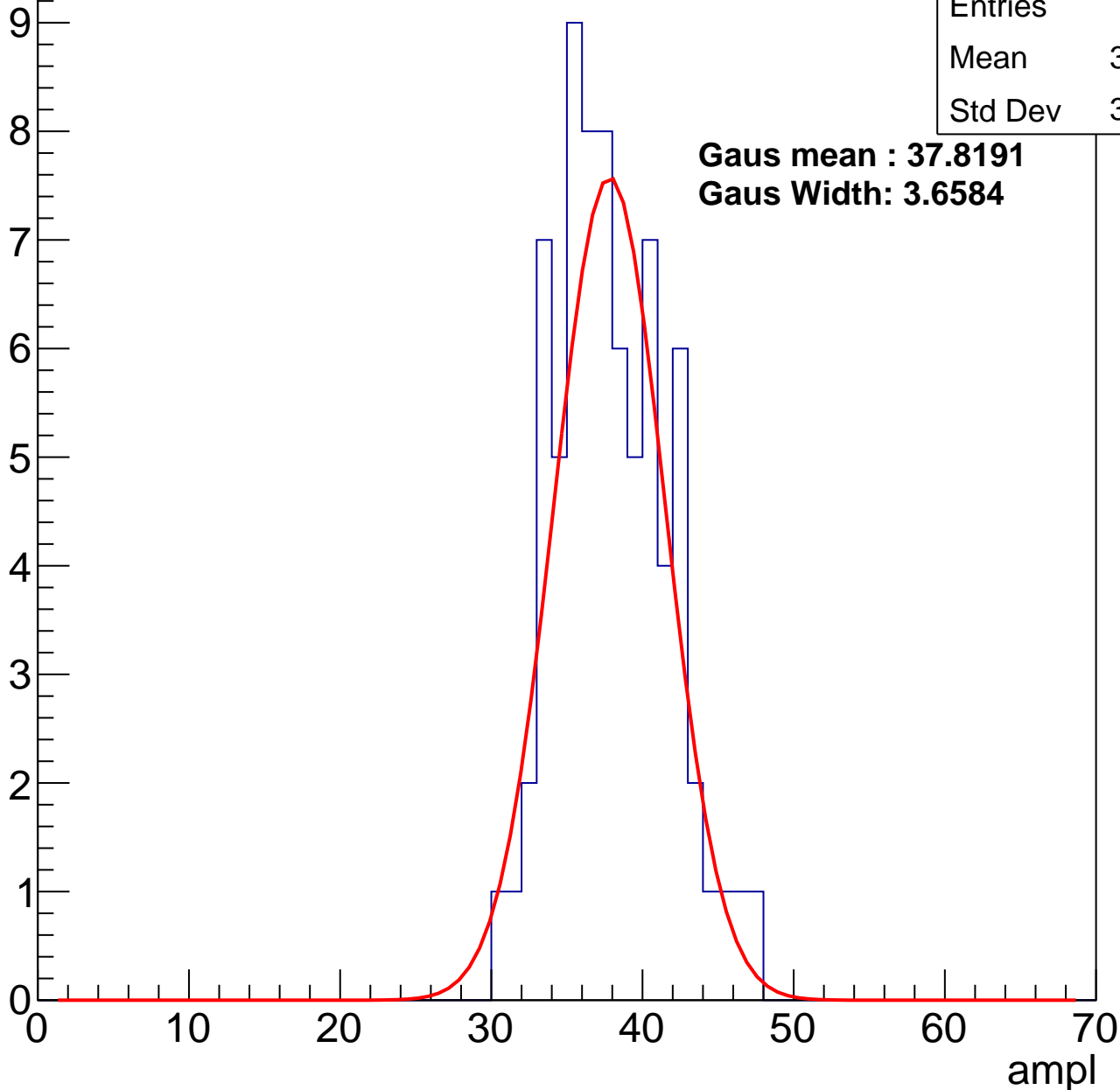
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.49
Std Dev	3.627

Gaus mean : 37.8191

Gaus Width: 3.6584



B1L100S, U13-ch37, adc2

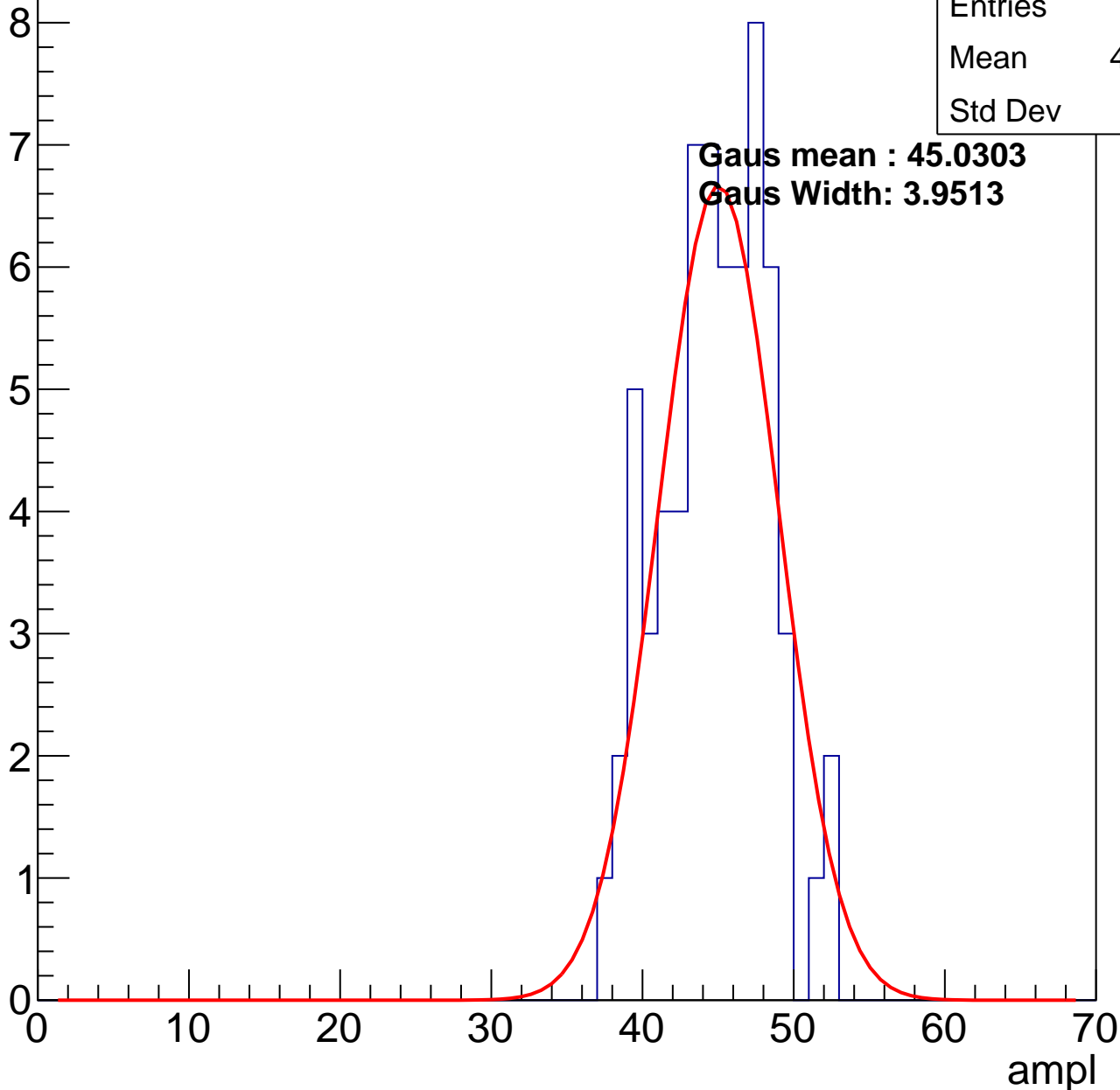
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.32
Std Dev	3.5

Gaus mean : 45.0303

Gaus Width: 3.9513



B1L100S, U13-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	51.17
Std Dev	4.07

Entry

10

8

6

4

2

0

0

10

20

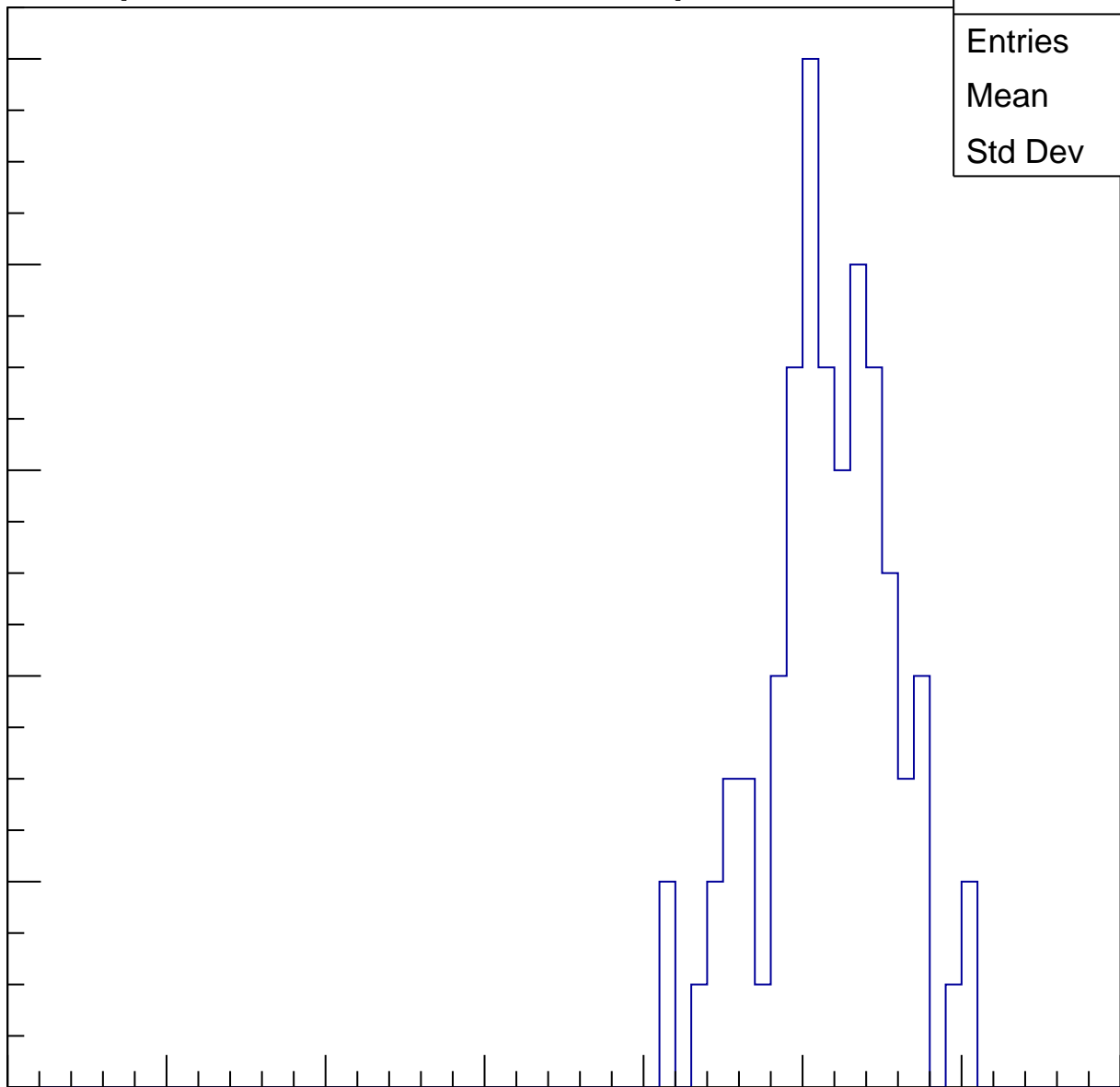
30

40

50

60

ampl



B1L100S, U13-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	57.39
Std Dev	2.932

Entry

10

8

6

4

2

0

0

10

20

30

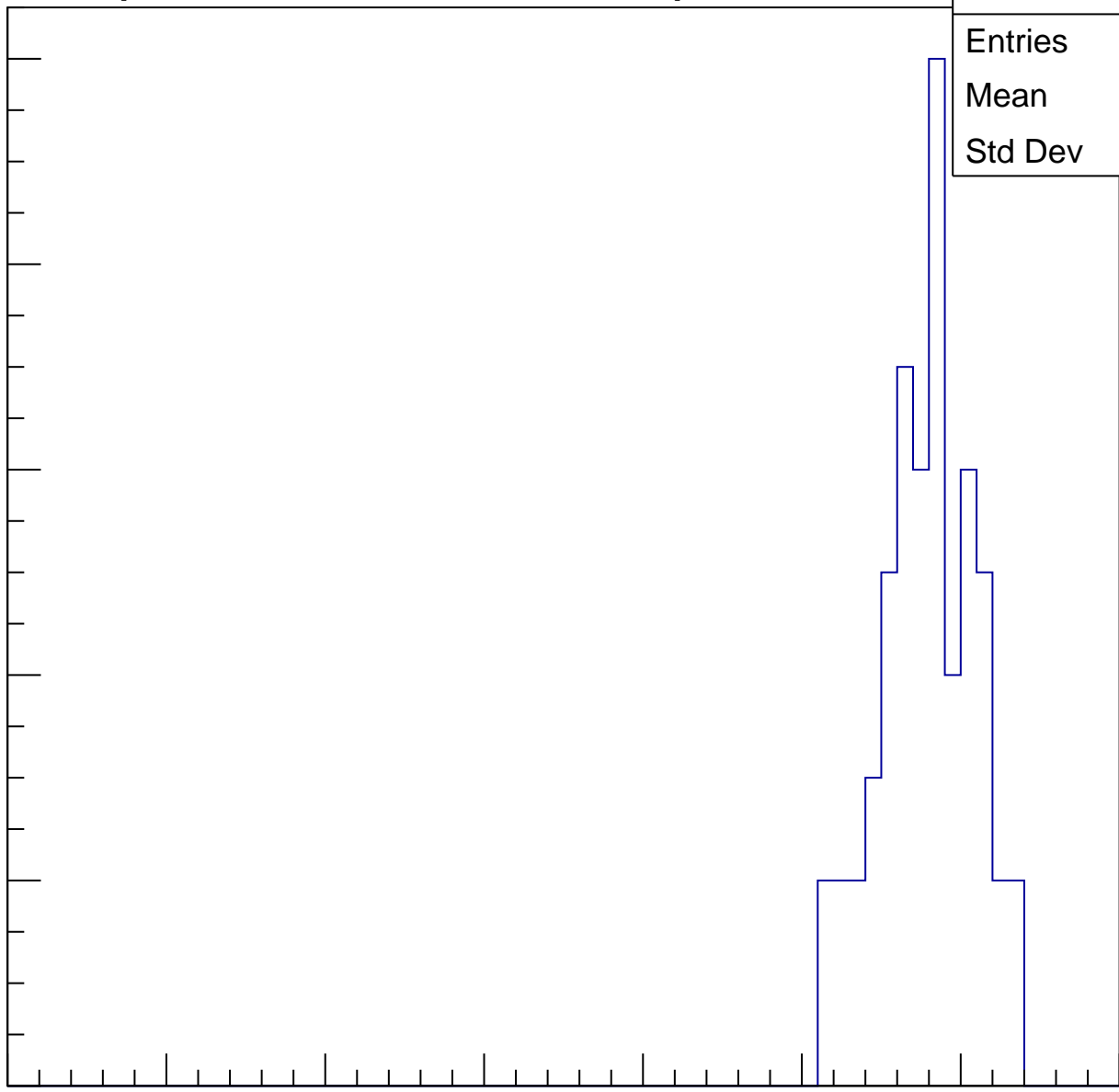
40

50

60

70

ampl

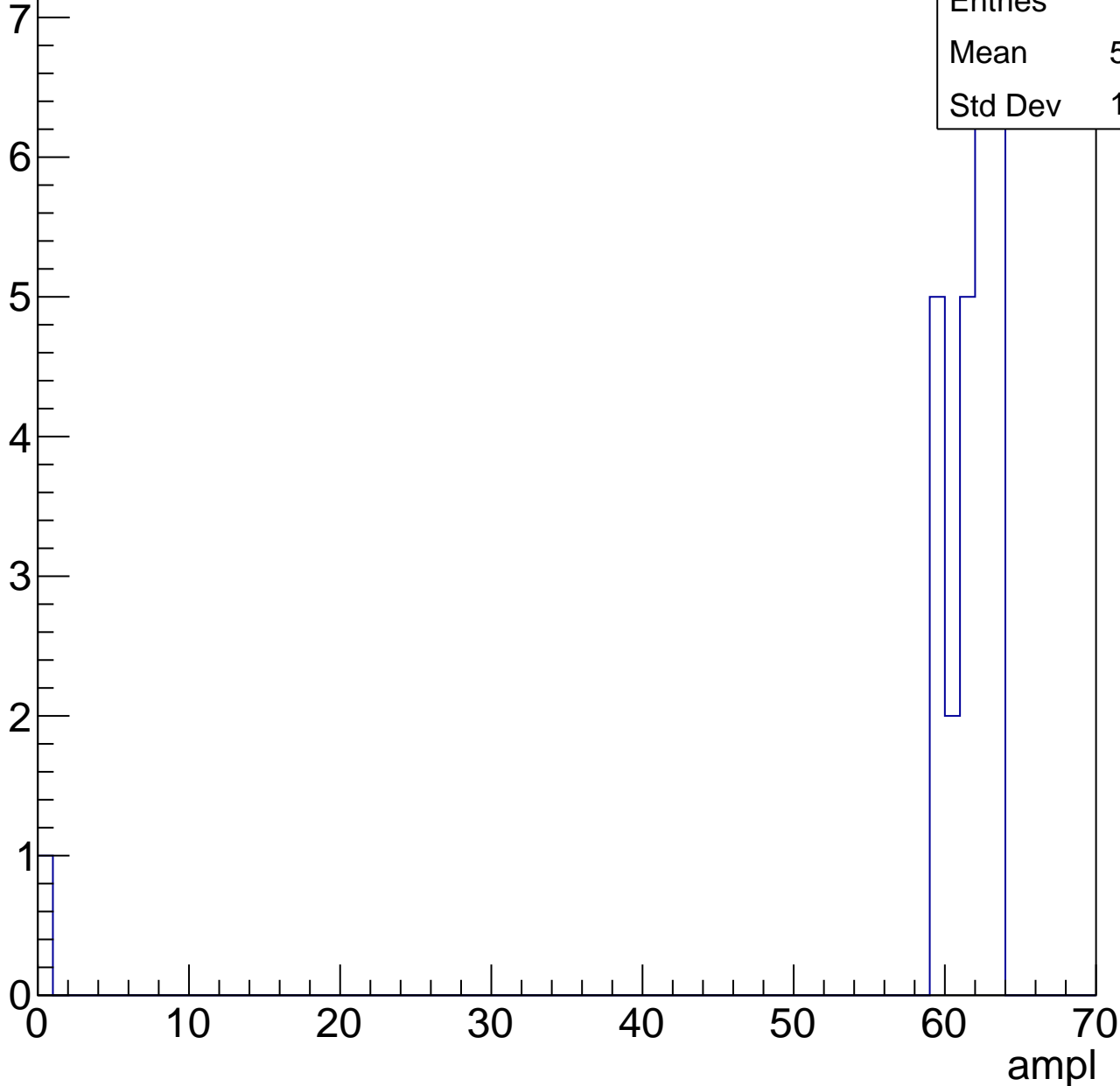


B1L100S, U13-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	59.07
Std Dev	11.67



B1L100S, U13-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L100S, U13-ch38, adc0

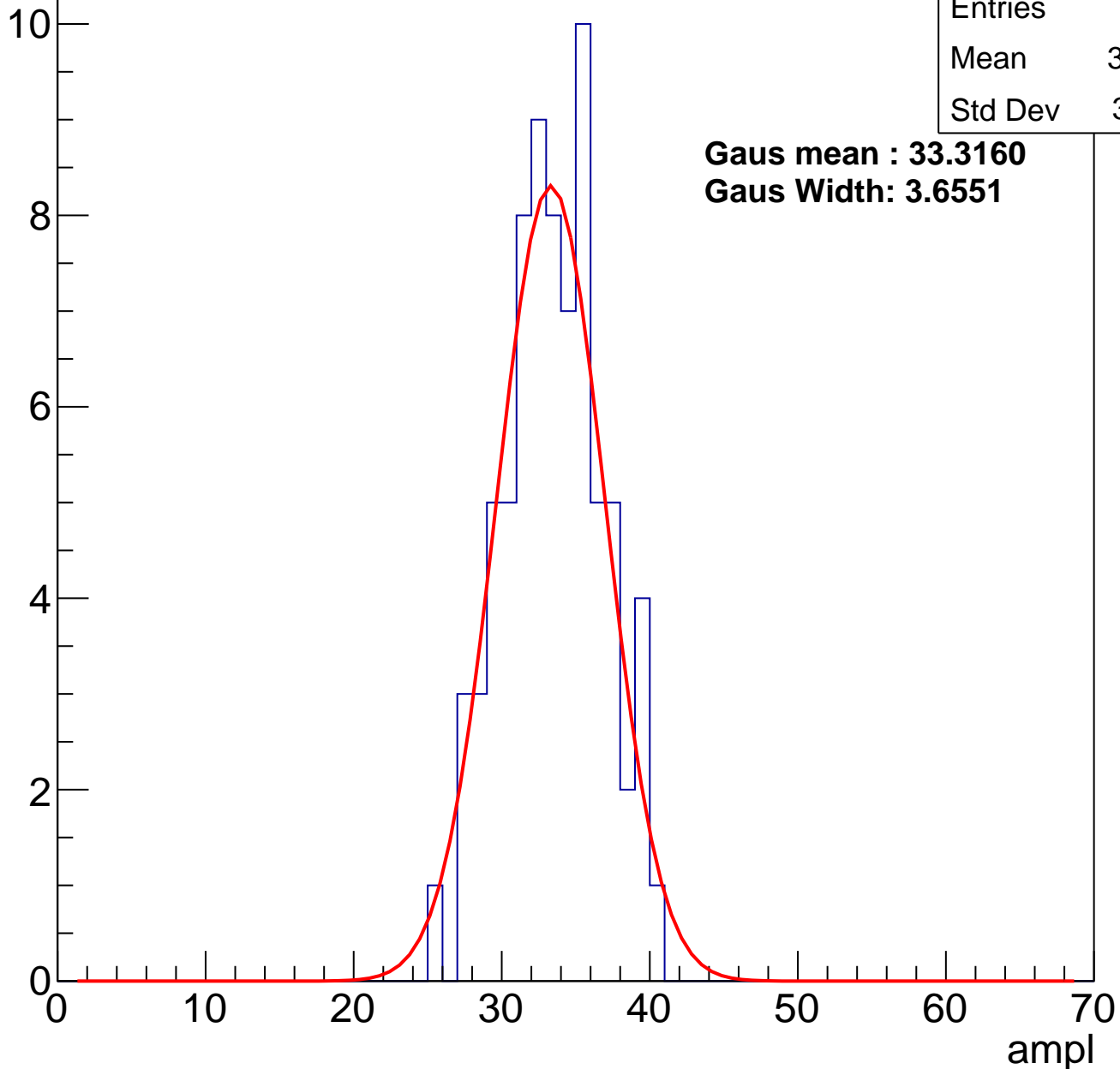
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	33.03
Std Dev	3.301

Gaus mean : 33.3160

Gaus Width: 3.6551

Entry



B1L100S, U13-ch38, adc1

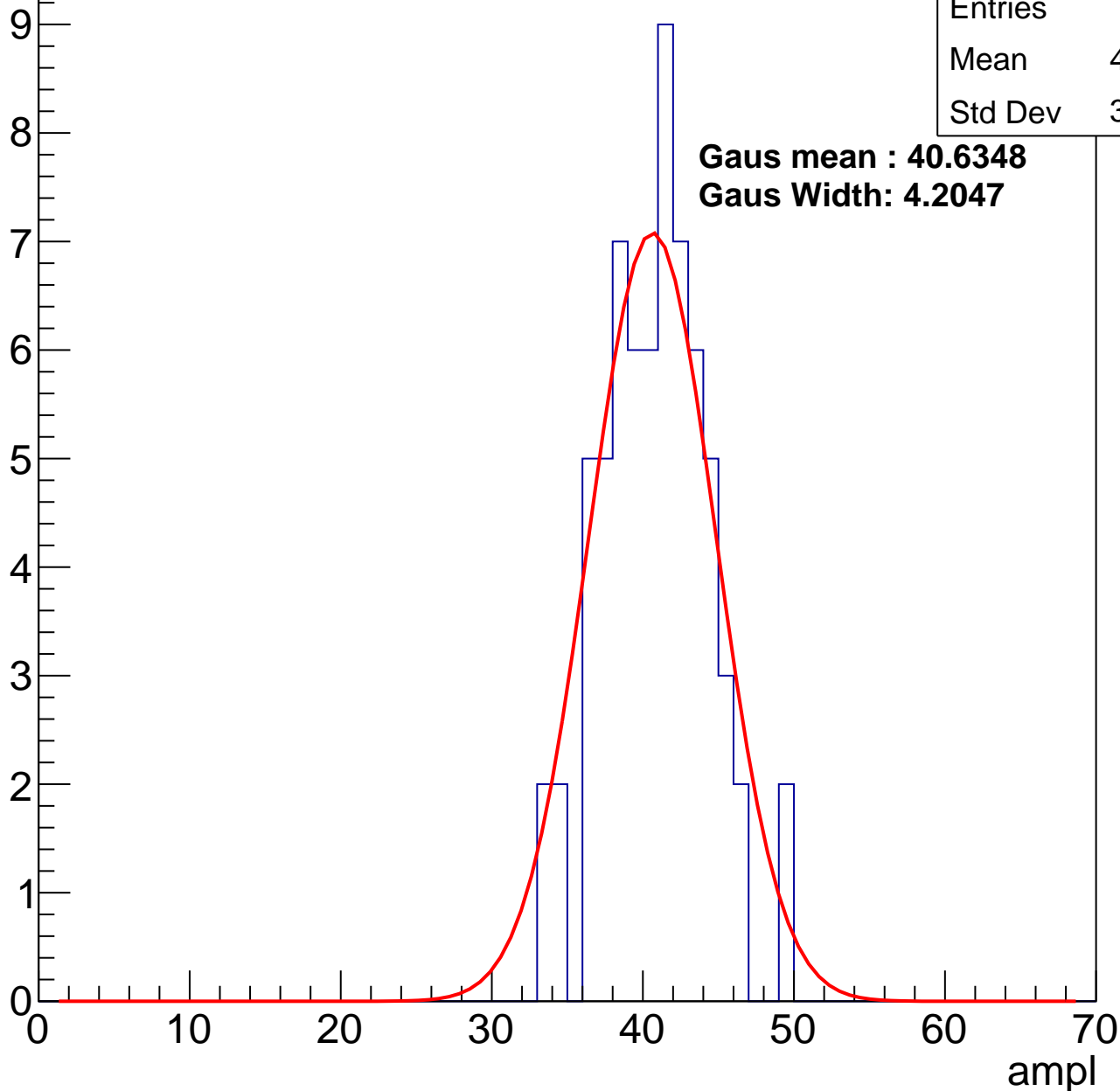
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	40.37
Std Dev	3.455

Gaus mean : 40.6348

Gaus Width: 4.2047



B1L100S, U13-ch38, adc2

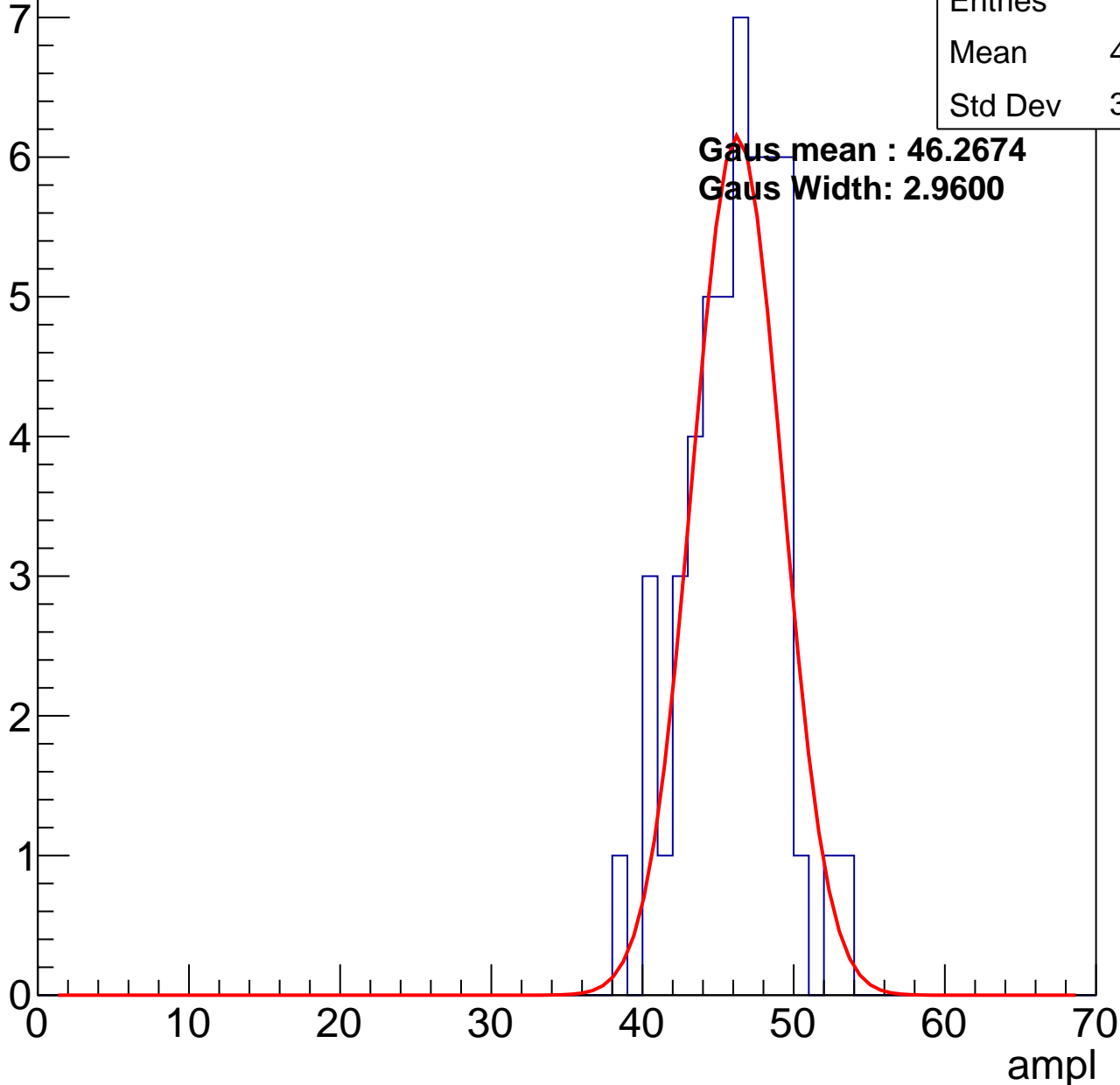
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	45.66
Std Dev	3.115

Gaus mean : 46.2674

Gaus Width: 2.9600

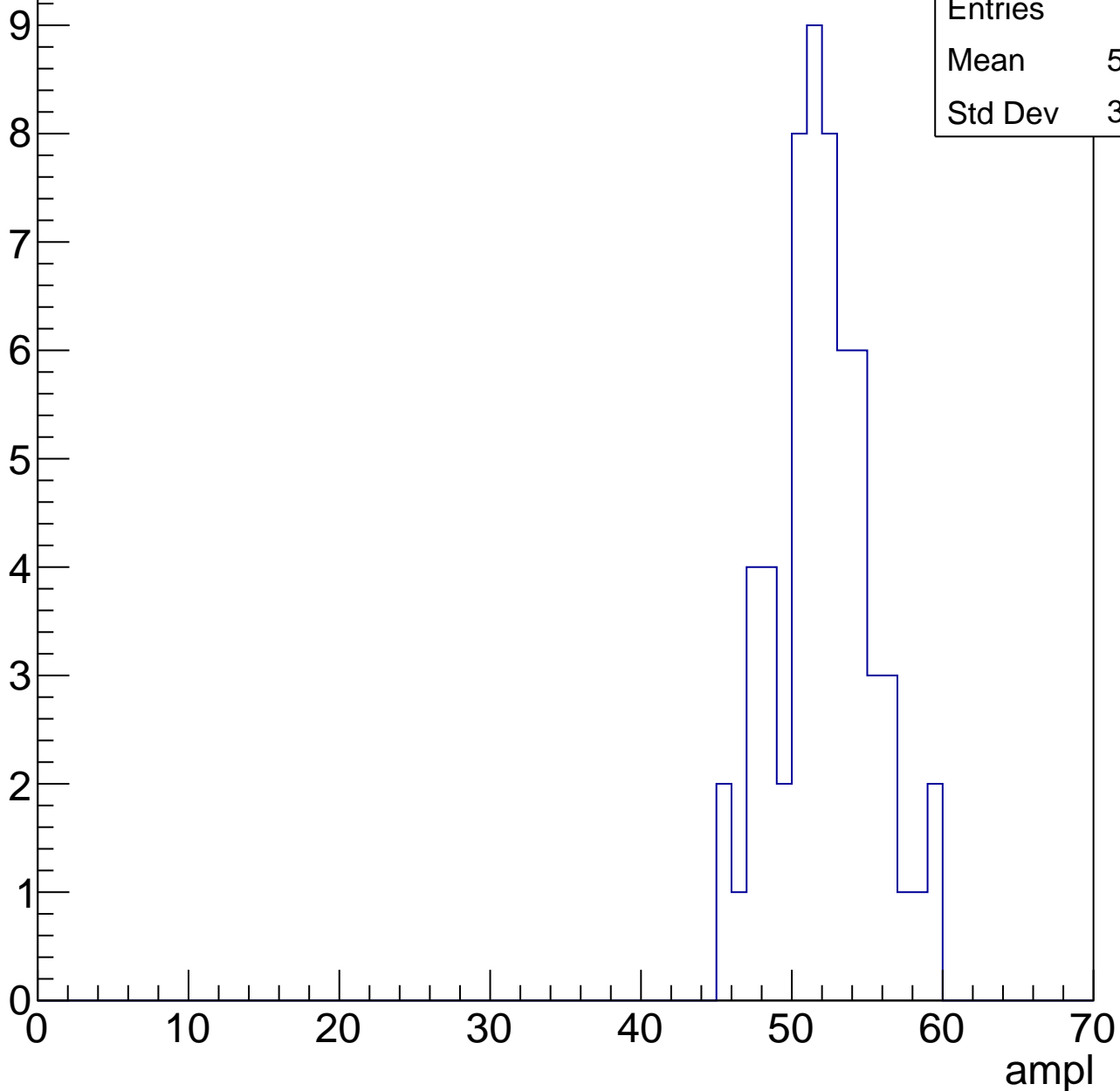


B1L100S, U13-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

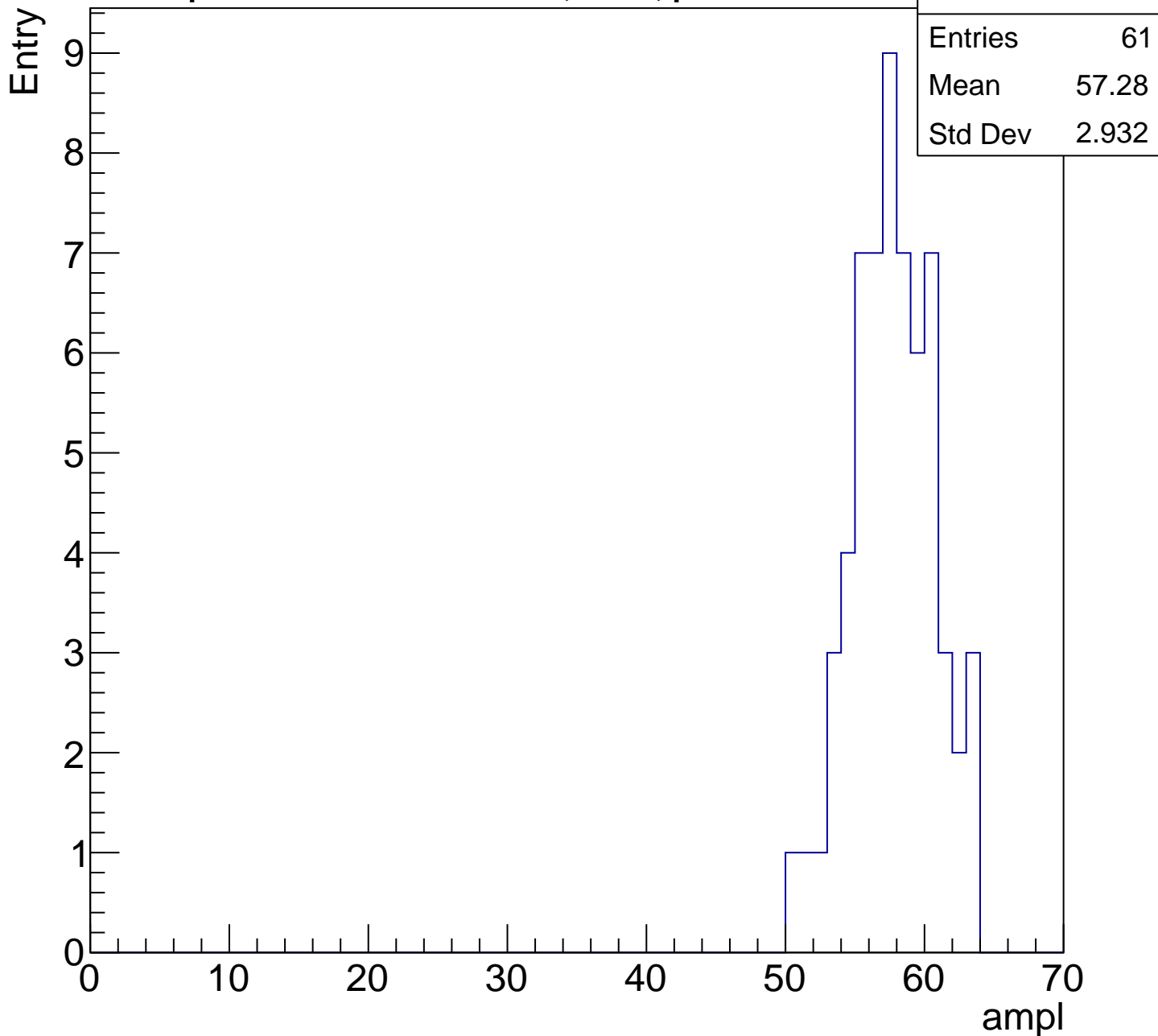
Entry

Entries	60
Mean	51.62
Std Dev	3.194



B1L100S, U13-ch38, adc4

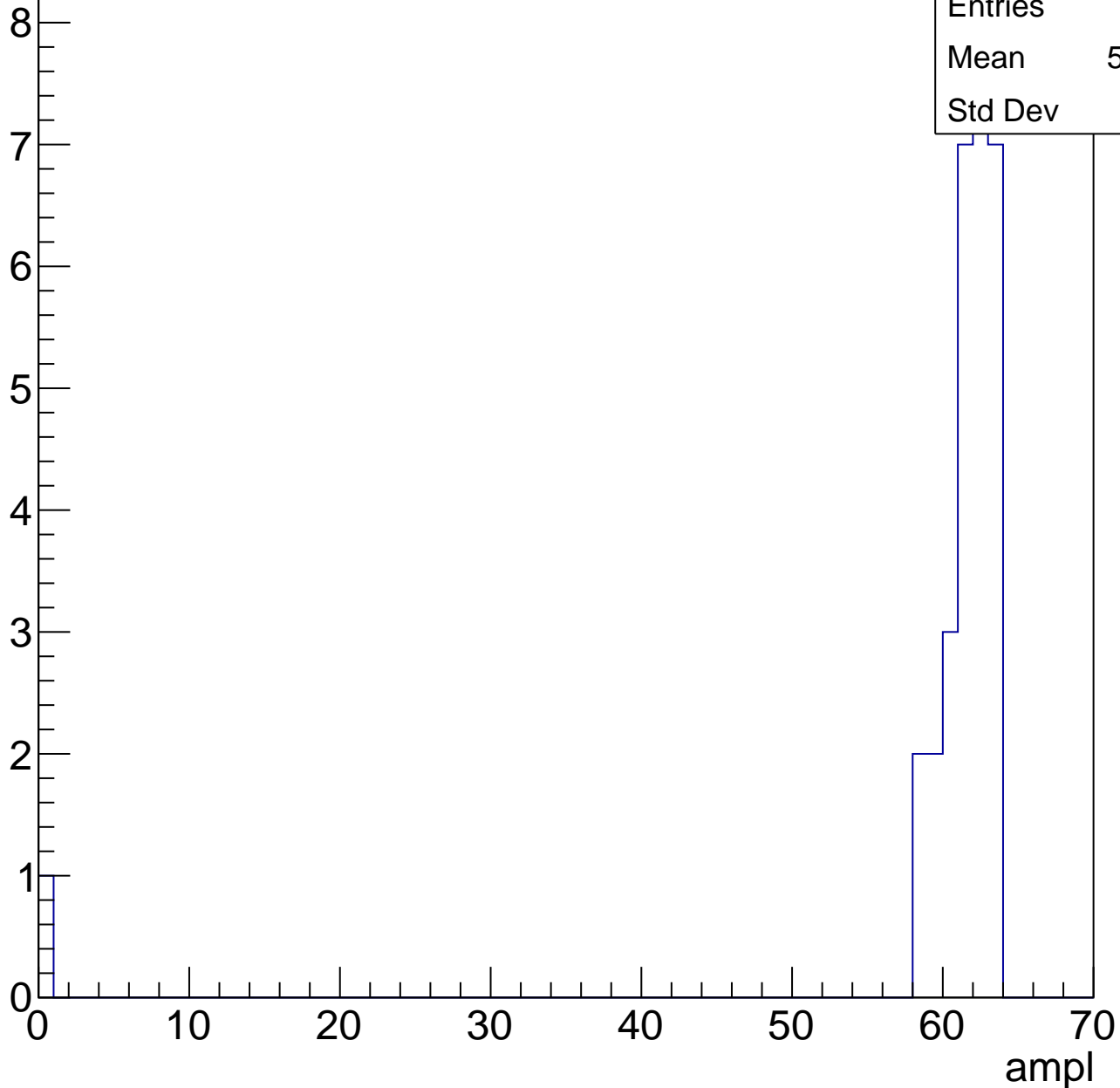
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	30
Mean	59.27
Std Dev	11.1

B1L100S, U13-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L100S, U13-ch39, adc0

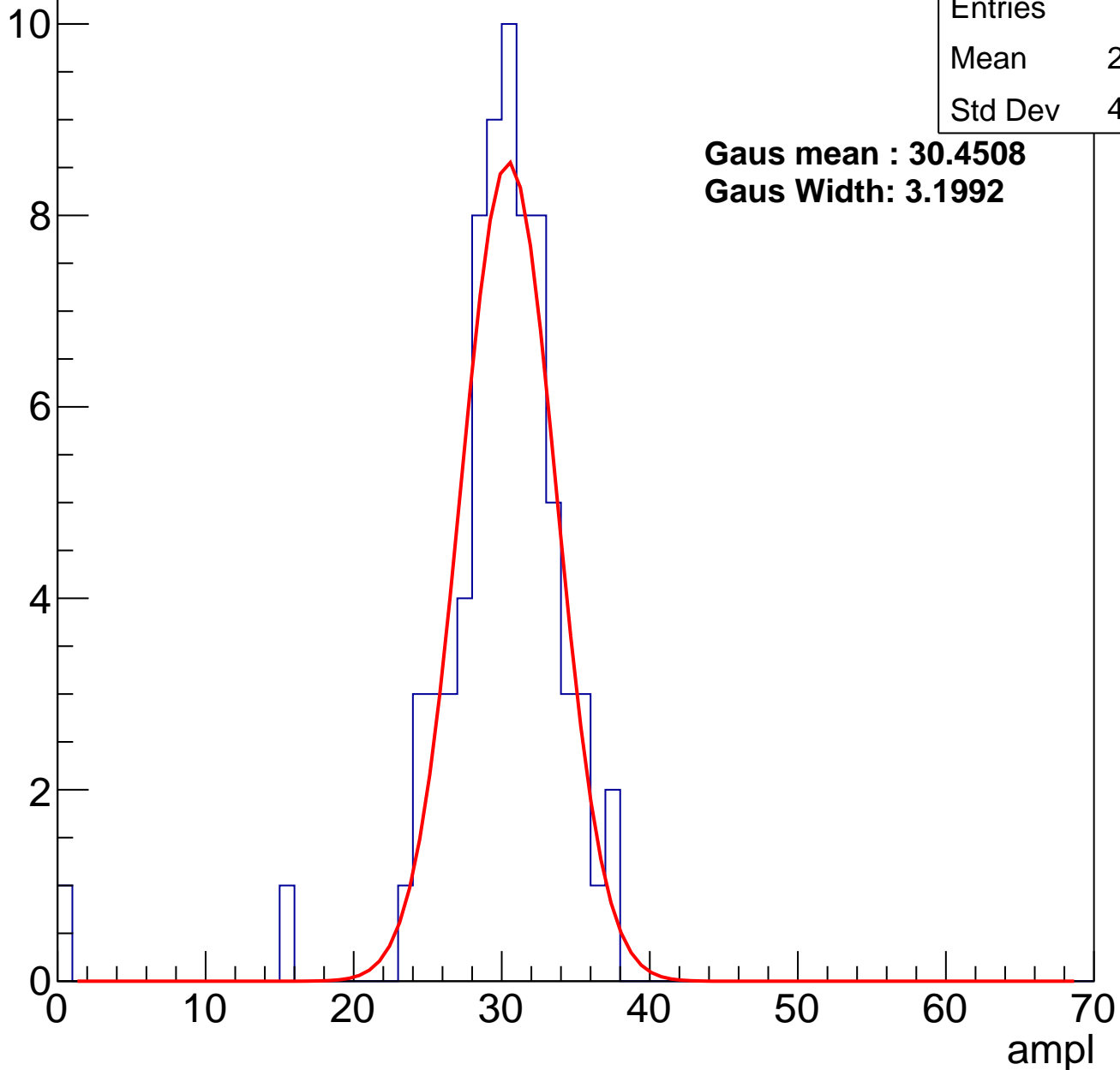
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	29.34
Std Dev	4.955

Gaus mean : 30.4508

Gaus Width: 3.1992

Entry



B1L100S, U13-ch39, adc1

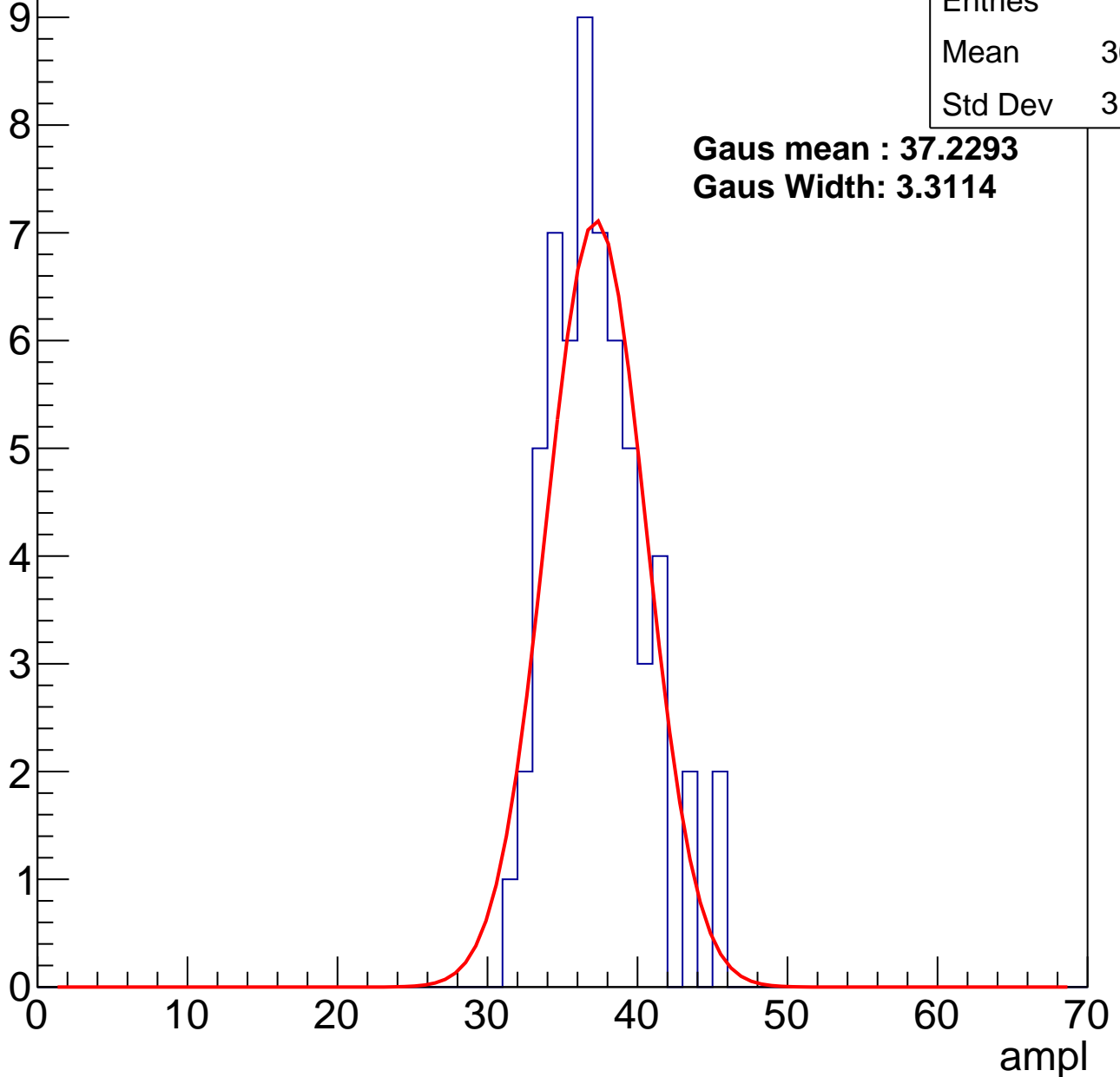
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.85
Std Dev	3.129

Gaus mean : 37.2293

Gaus Width: 3.3114



B1L100S, U13-ch39, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	44.02
Std Dev	3.869

Gaus mean : 44.3008

Gaus Width: 4.0226

Entry

10

8

6

4

2

0

0

10

20

30

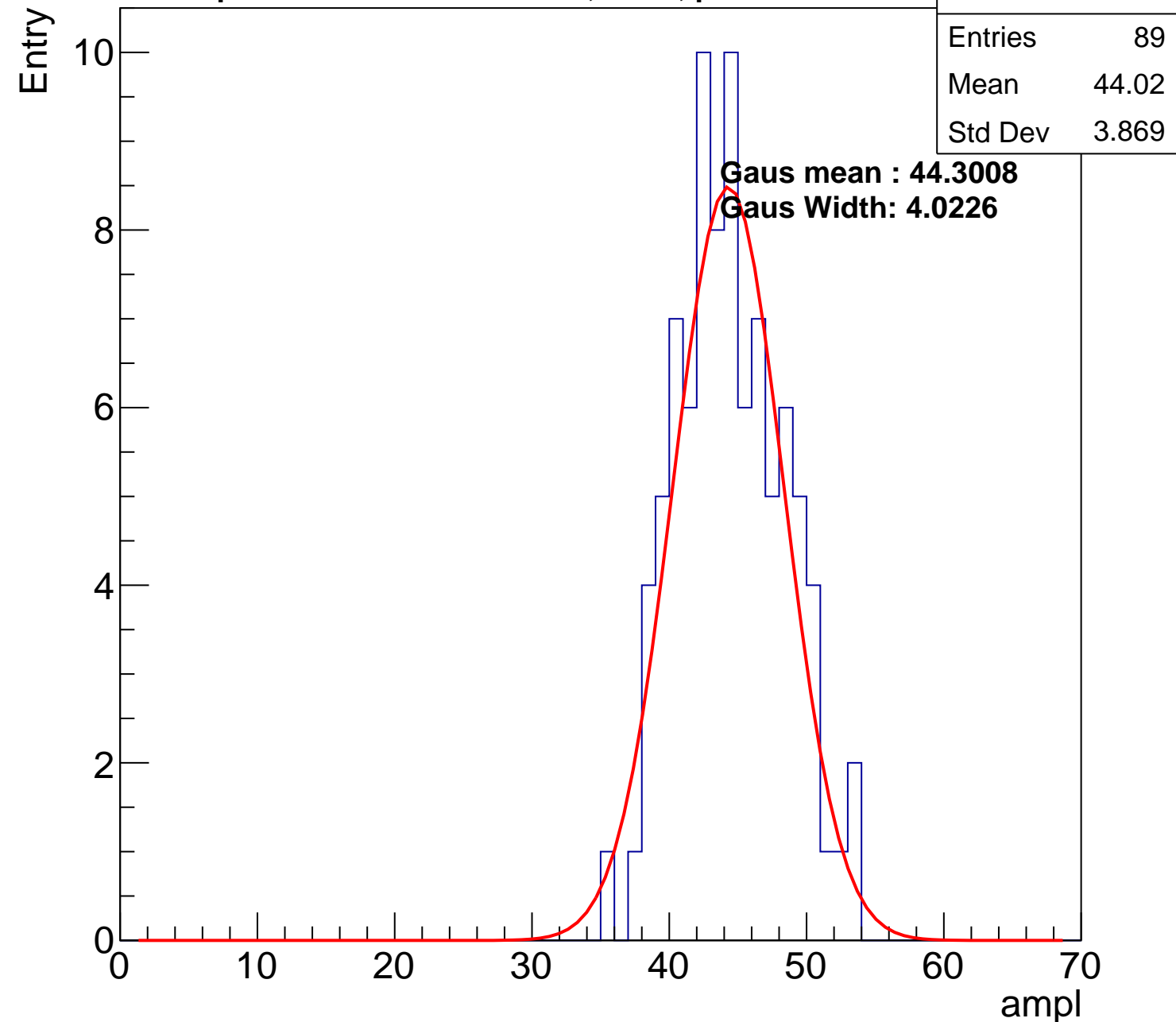
40

50

60

70

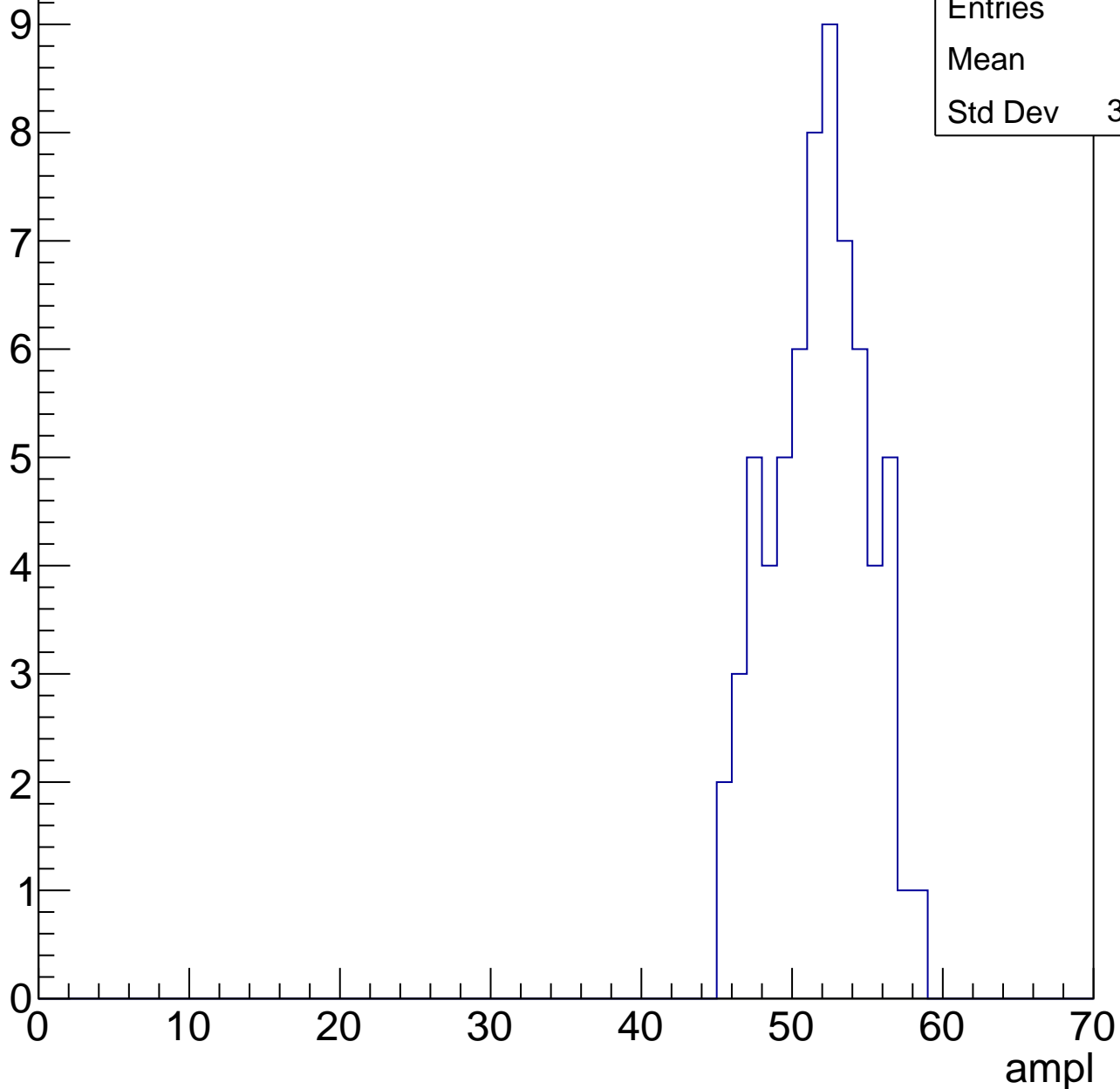
ampl



B1L100S, U13-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

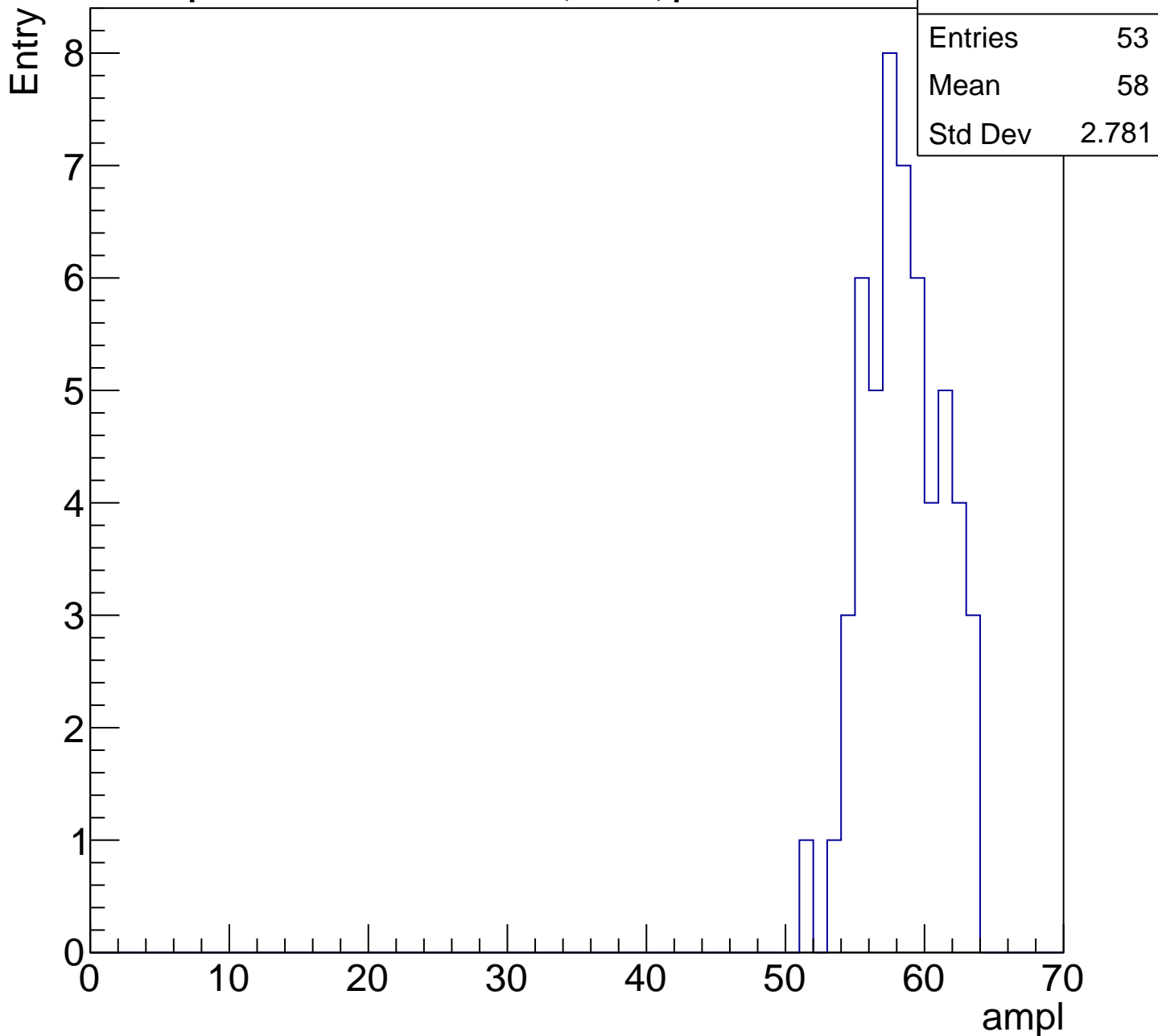
Entry



Entries	66
Mean	51.3
Std Dev	3.133

B1L100S, U13-ch39, adc4

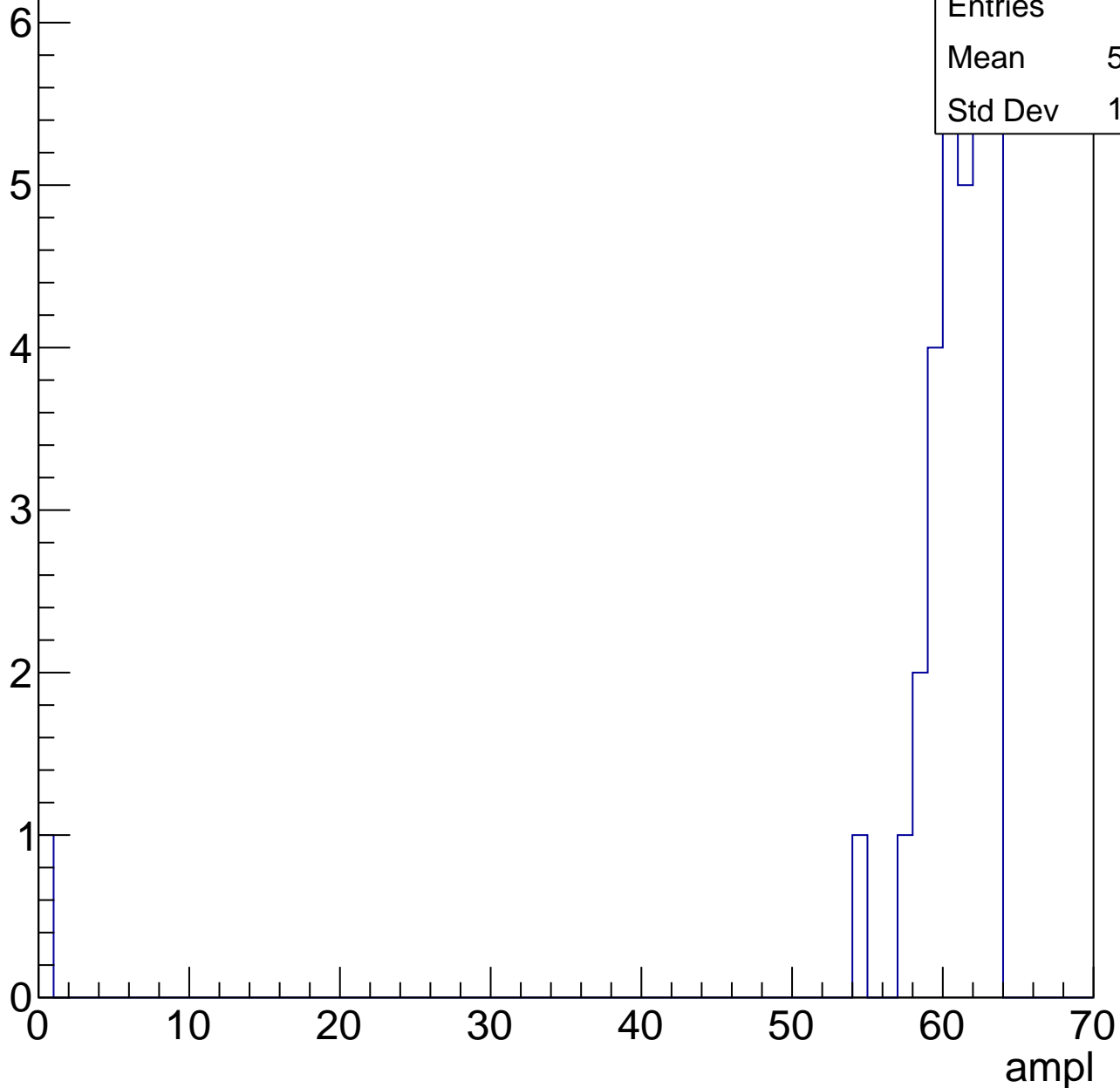
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch40, adc0

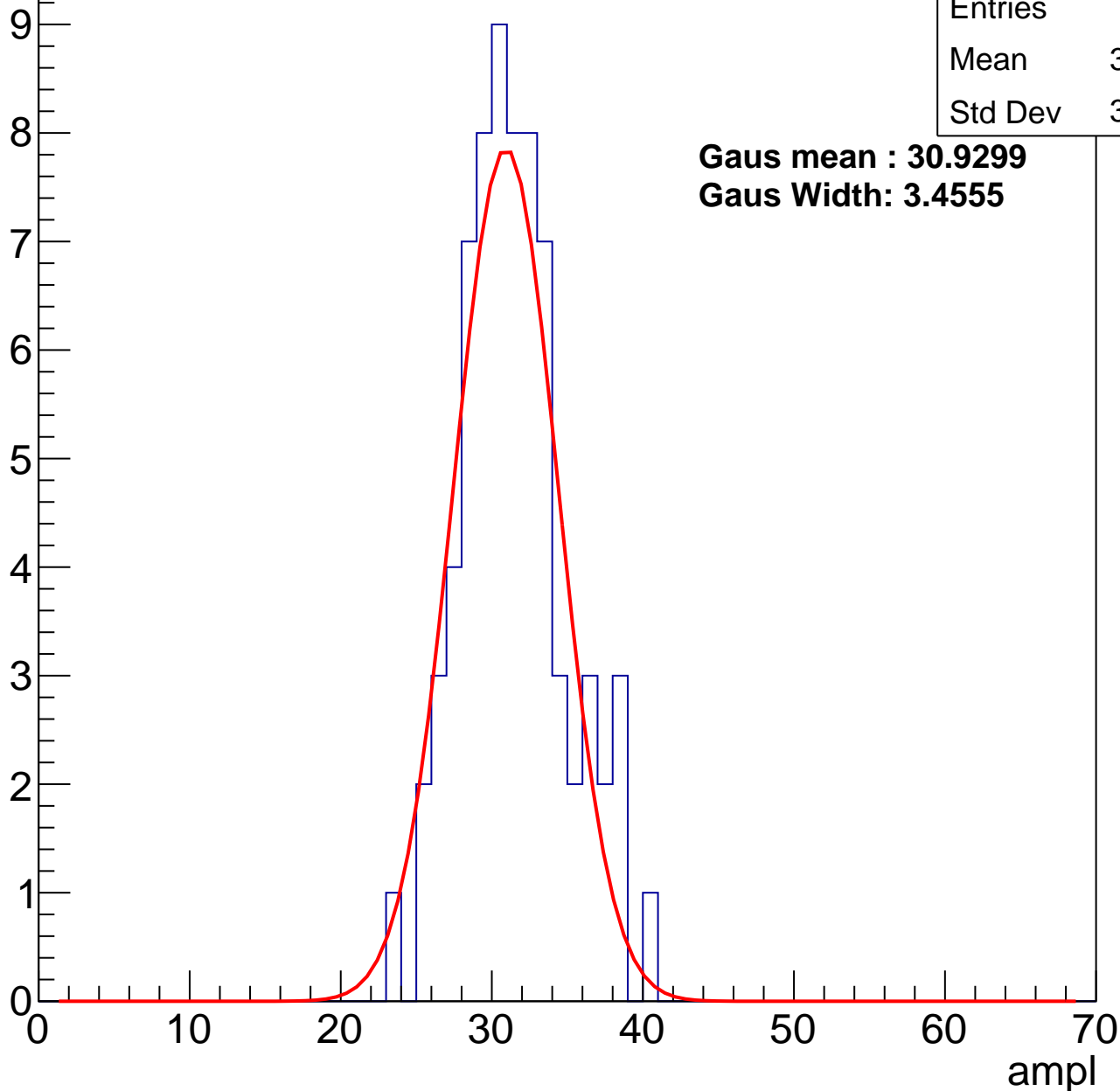
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.99
Std Dev	3.458

Gaus mean : 30.9299

Gaus Width: 3.4555



B1L100S, U13-ch40, adc1

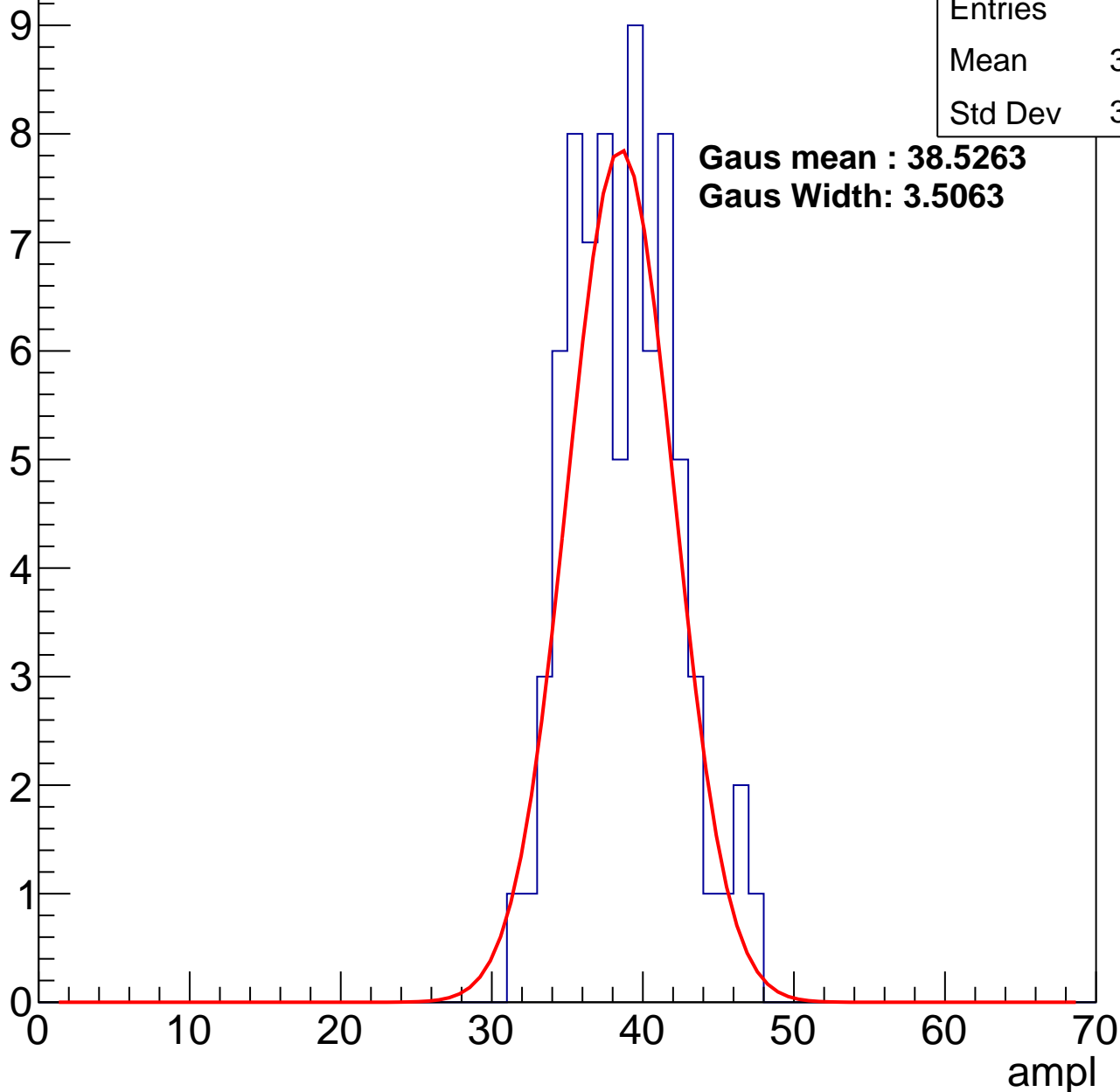
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	38.27
Std Dev	3.488

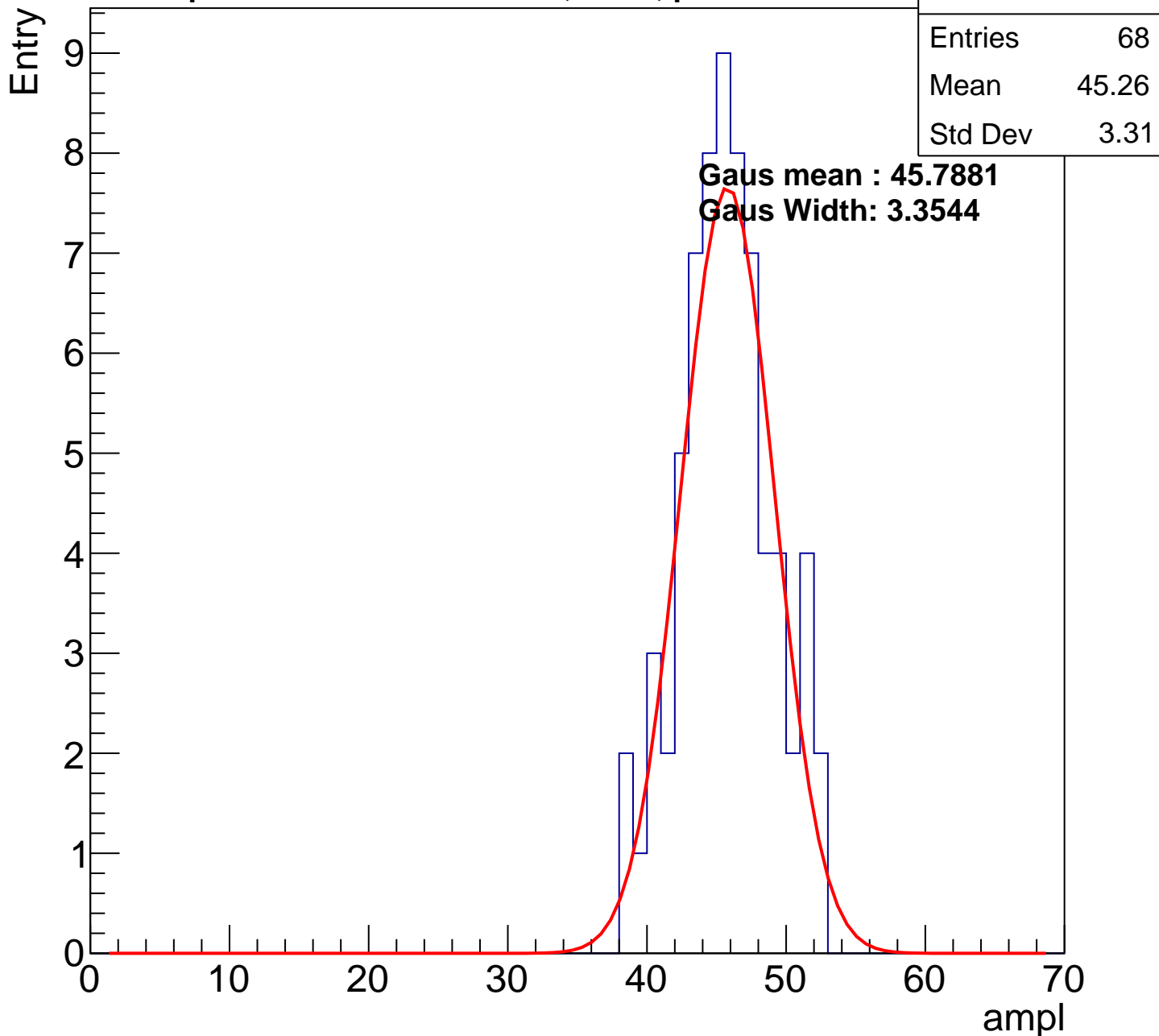
Gaus mean : 38.5263

Gaus Width: 3.5063



B1L100S, U13-ch40, adc2

calib_packv5_042523_0143.root, FC#4, port A2

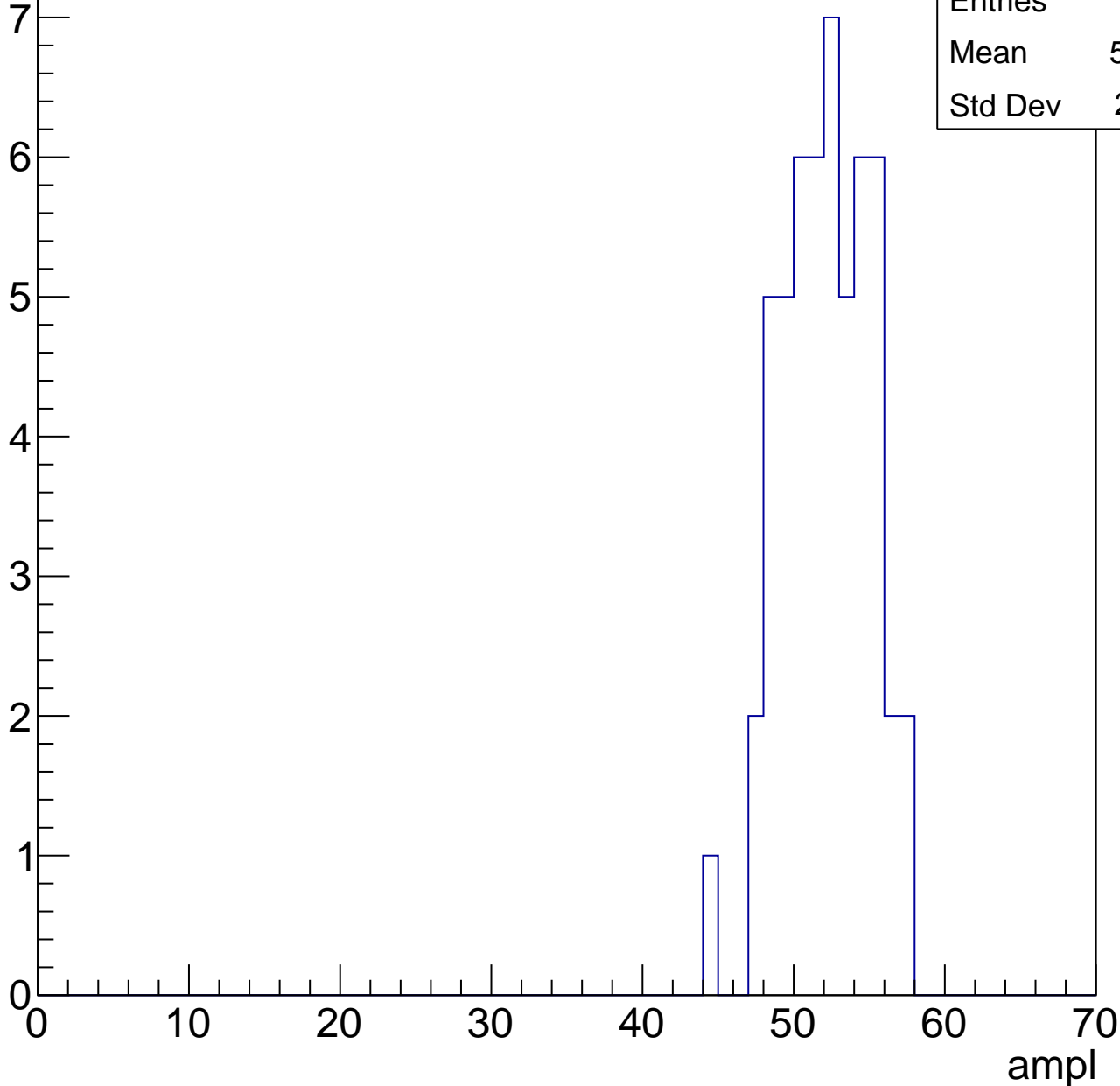


B1L100S, U13-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	51.66
Std Dev	2.841

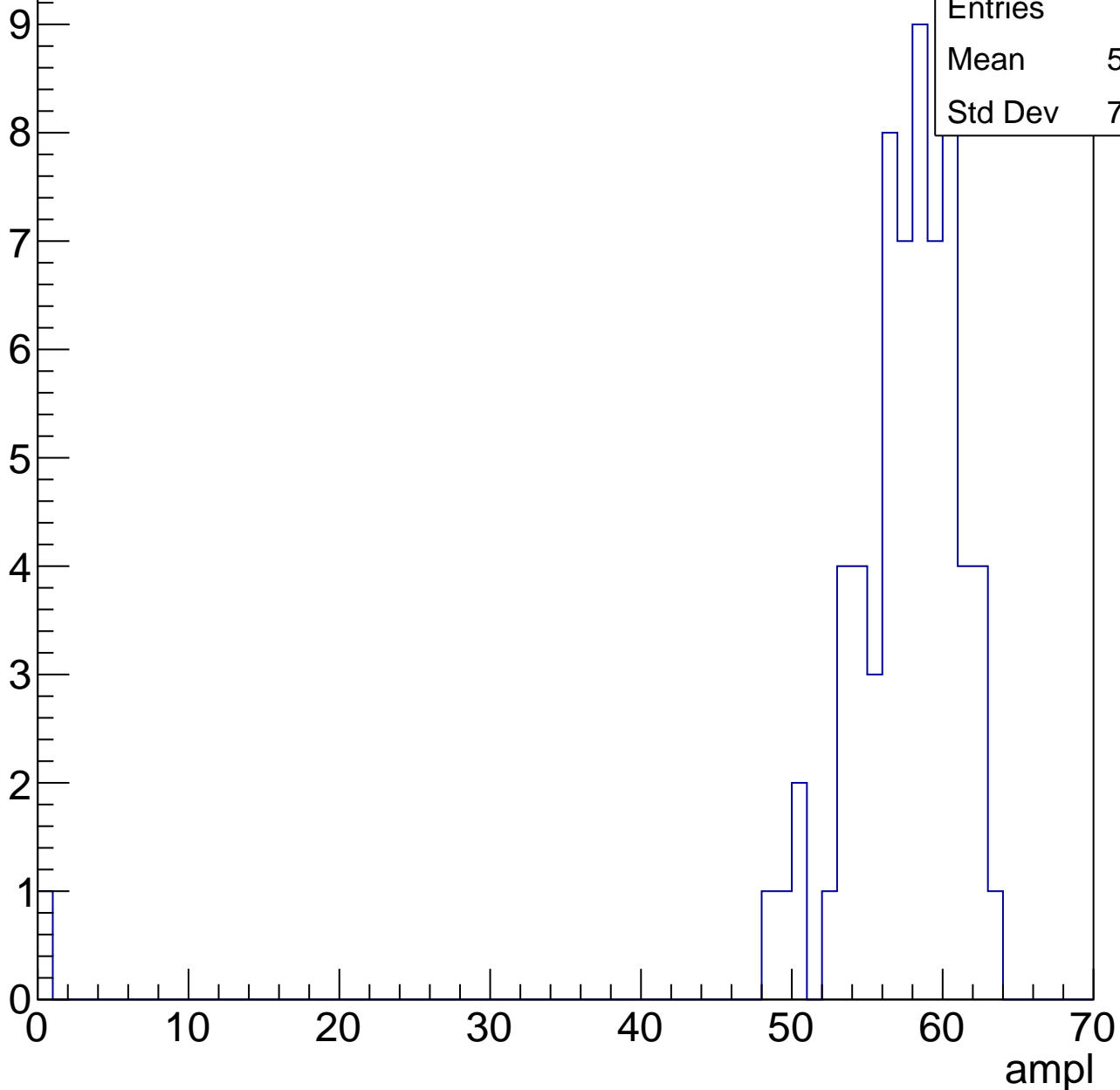


B1L100S, U13-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	56.35
Std Dev	7.714

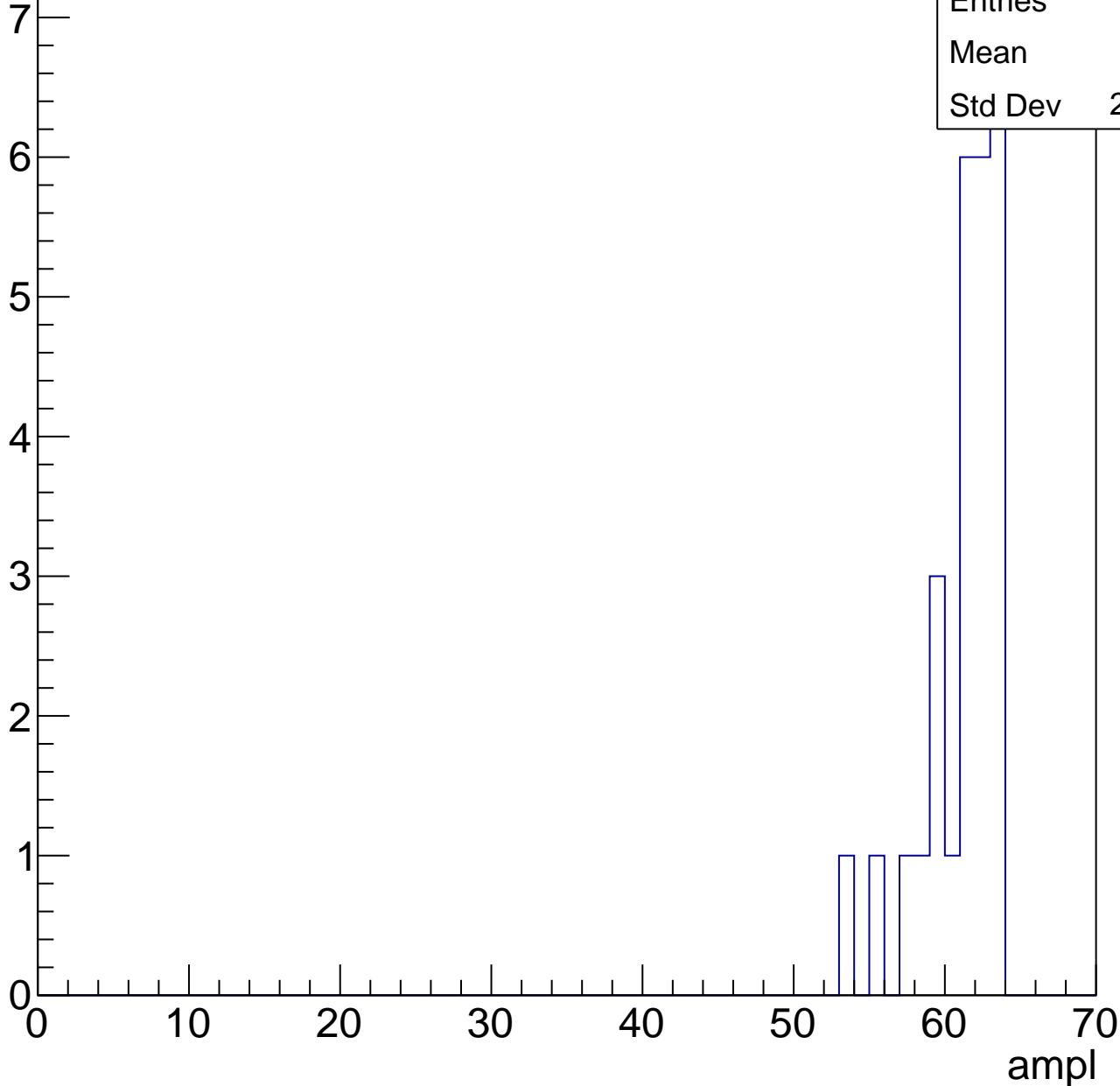


B1L100S, U13-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	60.7
Std Dev	2.507



B1L100S, U13-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U13-ch41, adc0

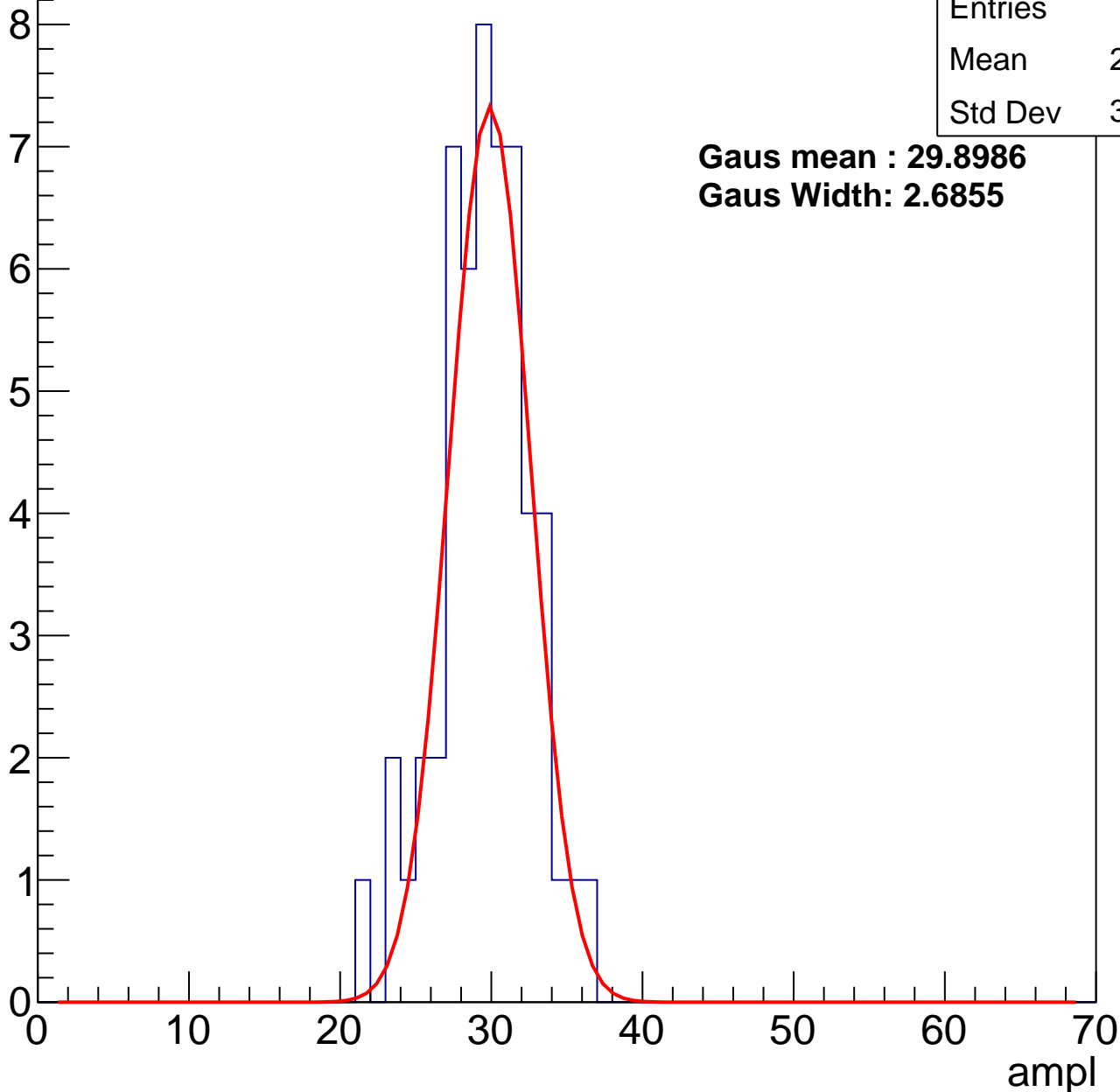
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	29.15
Std Dev	3.009

Gaus mean : 29.8986

Gaus Width: 2.6855

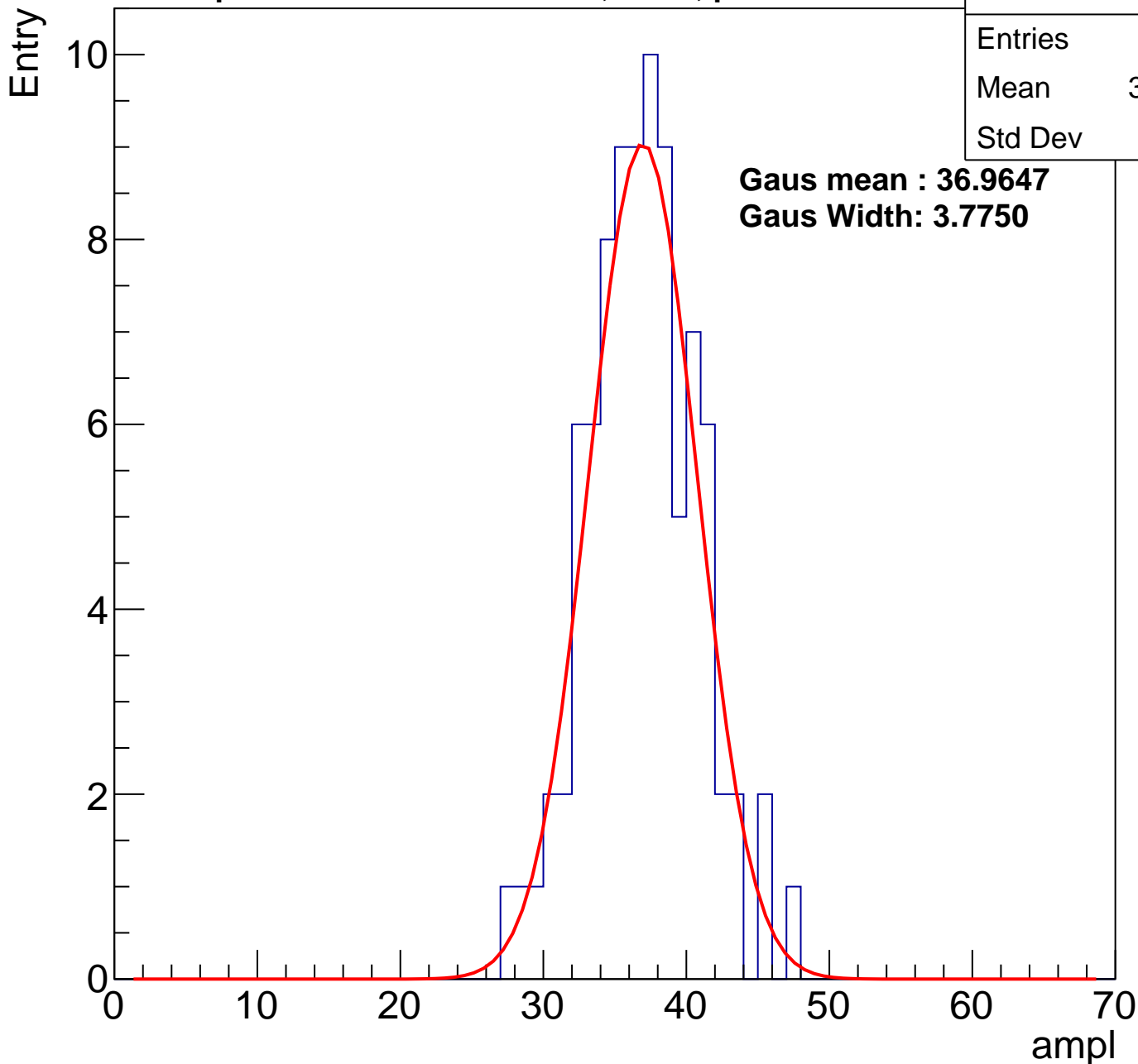


B1L100S, U13-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	36.48
Std Dev	3.81

Gaus mean : 36.9647
Gaus Width: 3.7750



B1L100S, U13-ch41, adc2

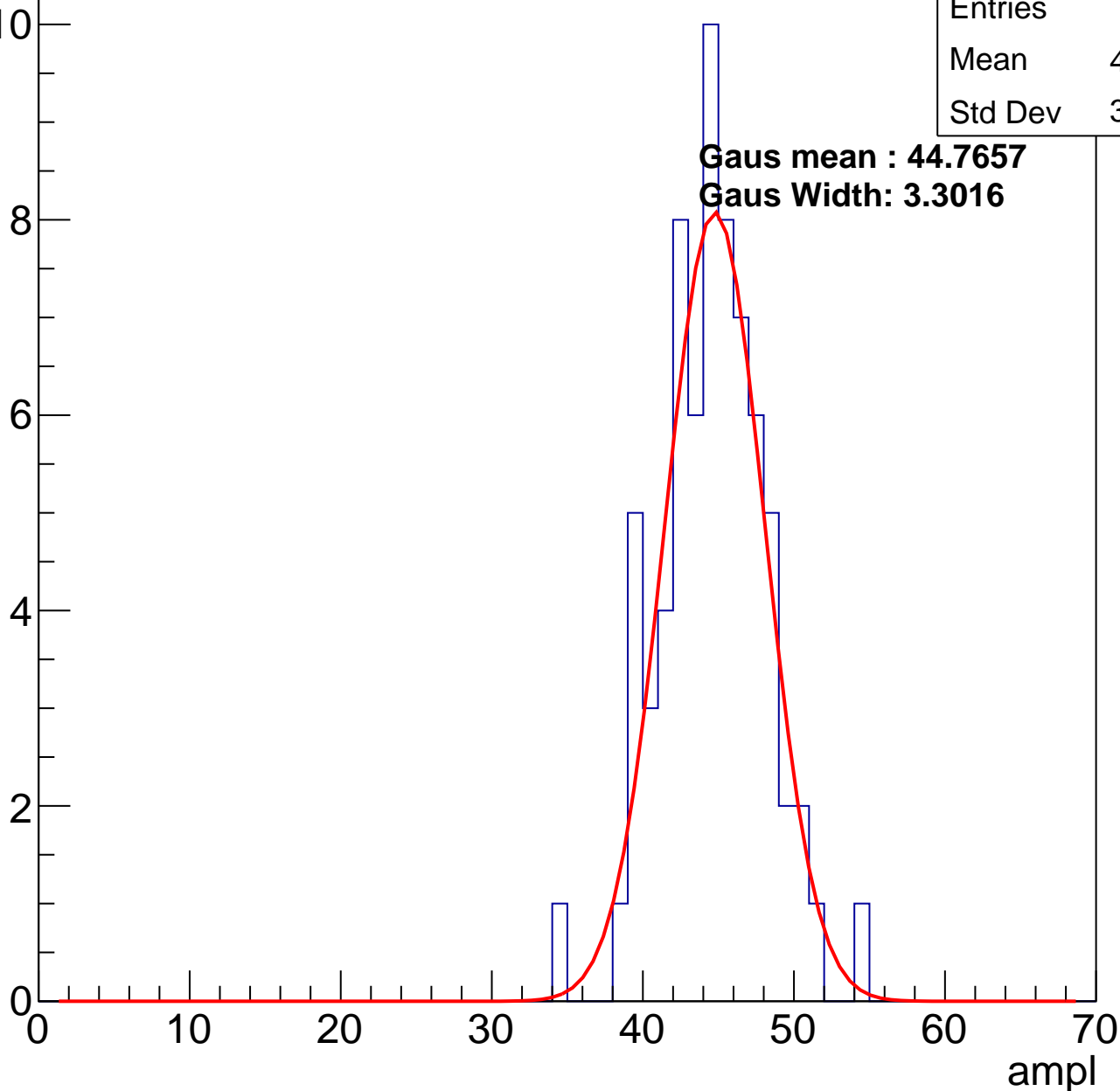
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.17
Std Dev	3.427

Gaus mean : 44.7657

Gaus Width: 3.3016

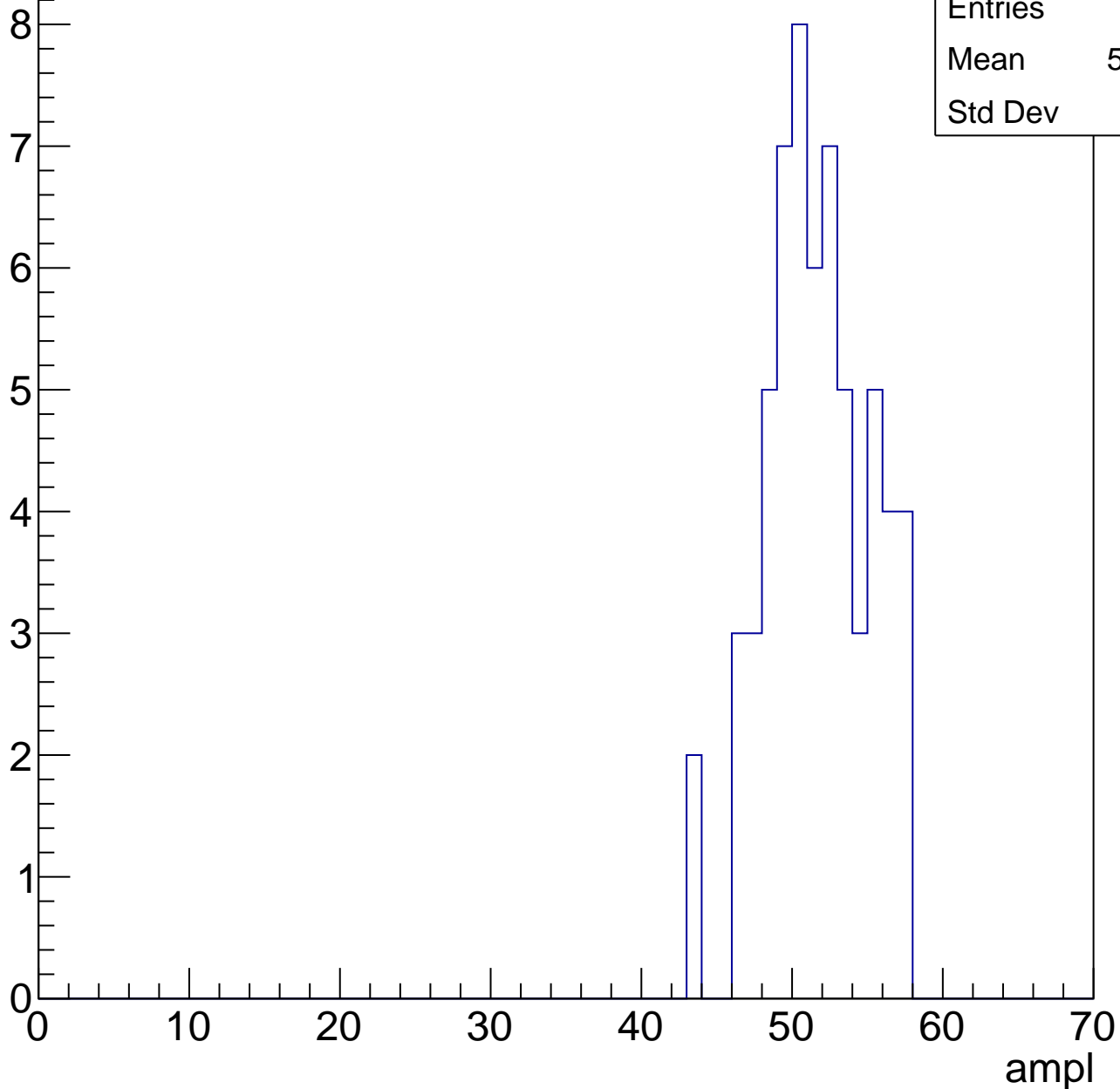


B1L100S, U13-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	51.16
Std Dev	3.38



B1L100S, U13-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 54

Mean 56.31

Std Dev 3.338

ampl

0

10

20

30

40

50

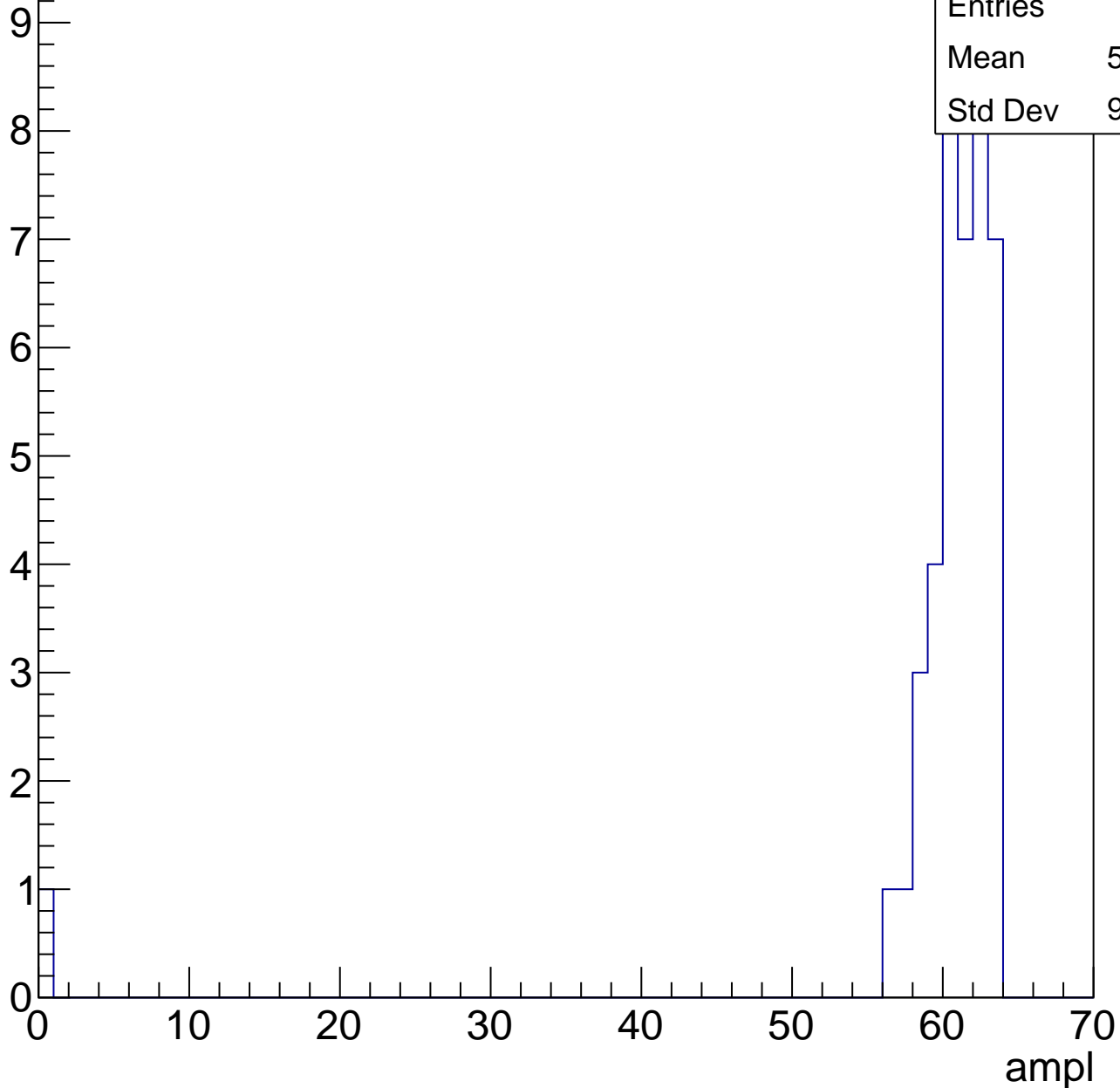
60

70

B1L100S, U13-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch42, adc0

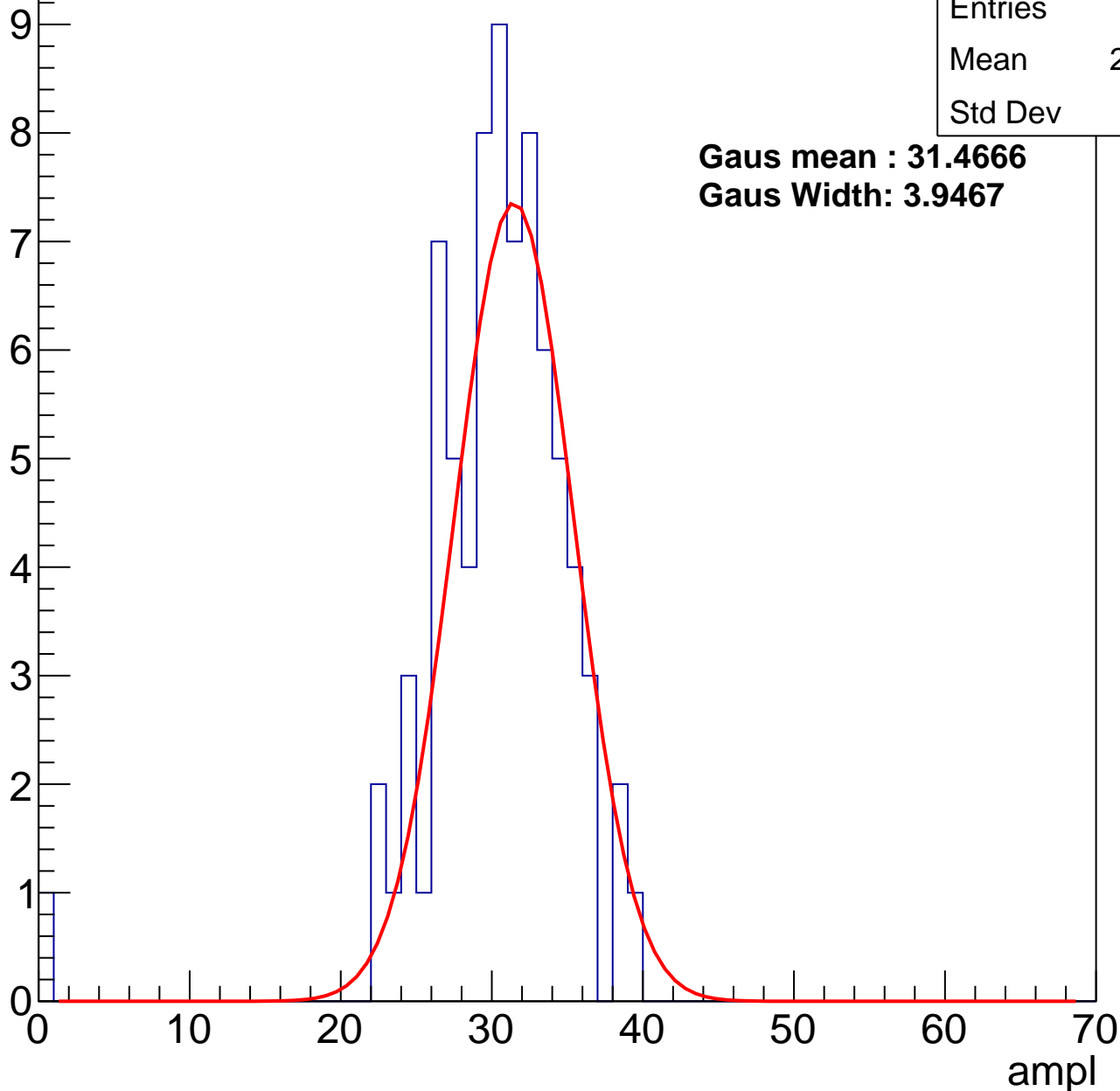
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.86
Std Dev	5.07

Gaus mean : 31.4666

Gaus Width: 3.9467



B1L100S, U13-ch42, adc1

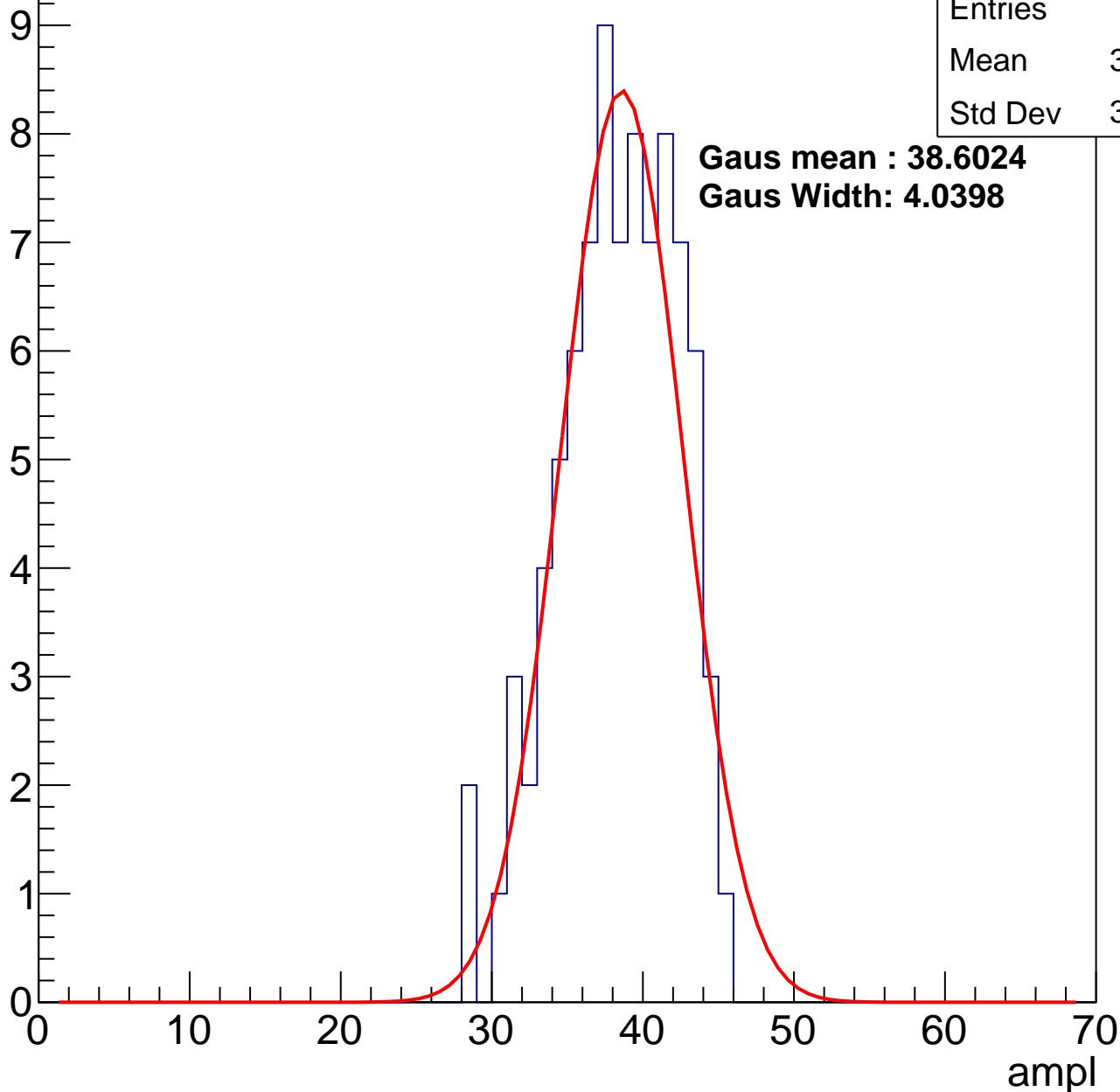
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	37.85
Std Dev	3.856

Gaus mean : 38.6024

Gaus Width: 4.0398



B1L100S, U13-ch42, adc2

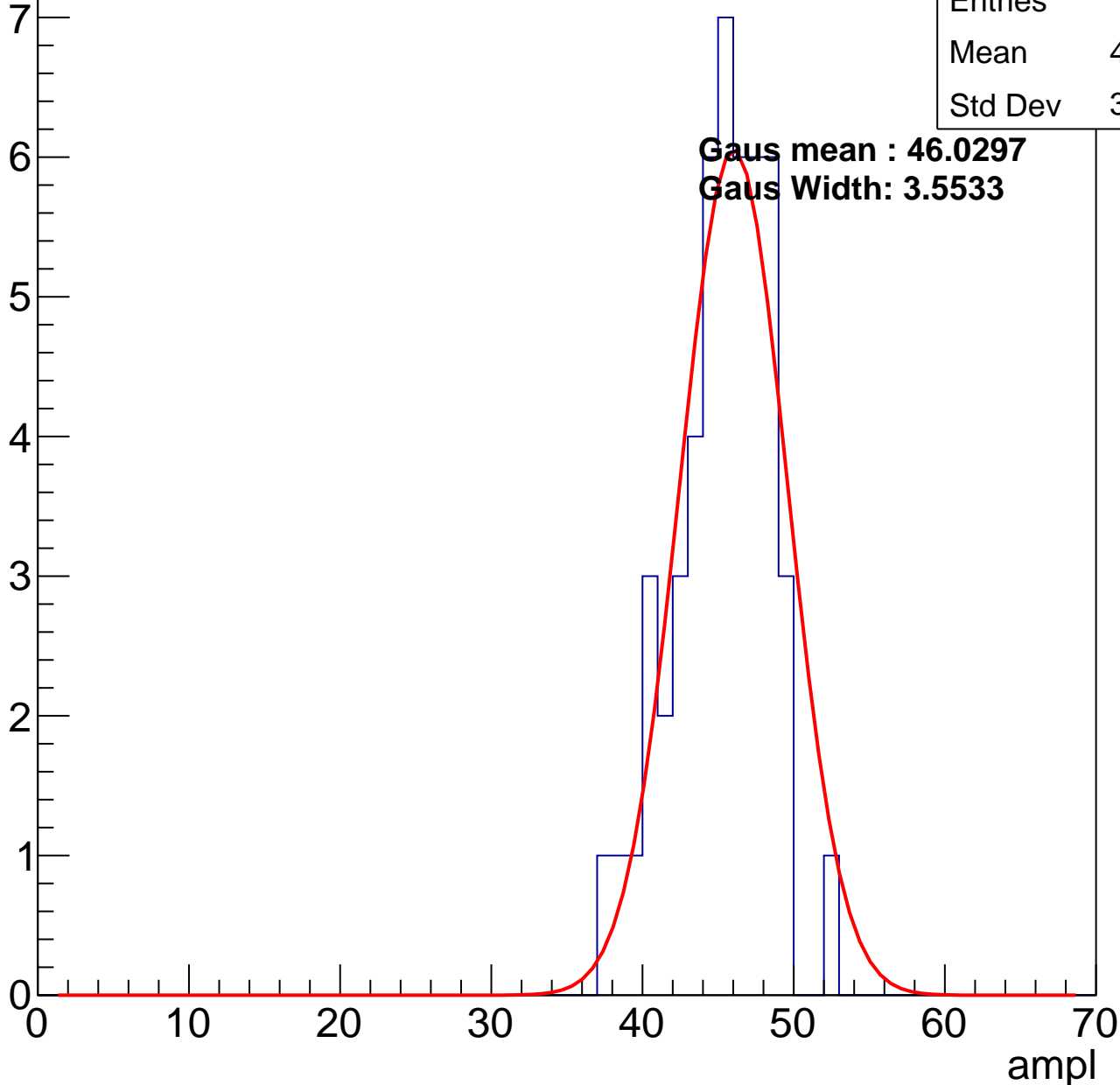
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	44.76
Std Dev	3.108

Gaus mean : 46.0297

Gaus Width: 3.5533

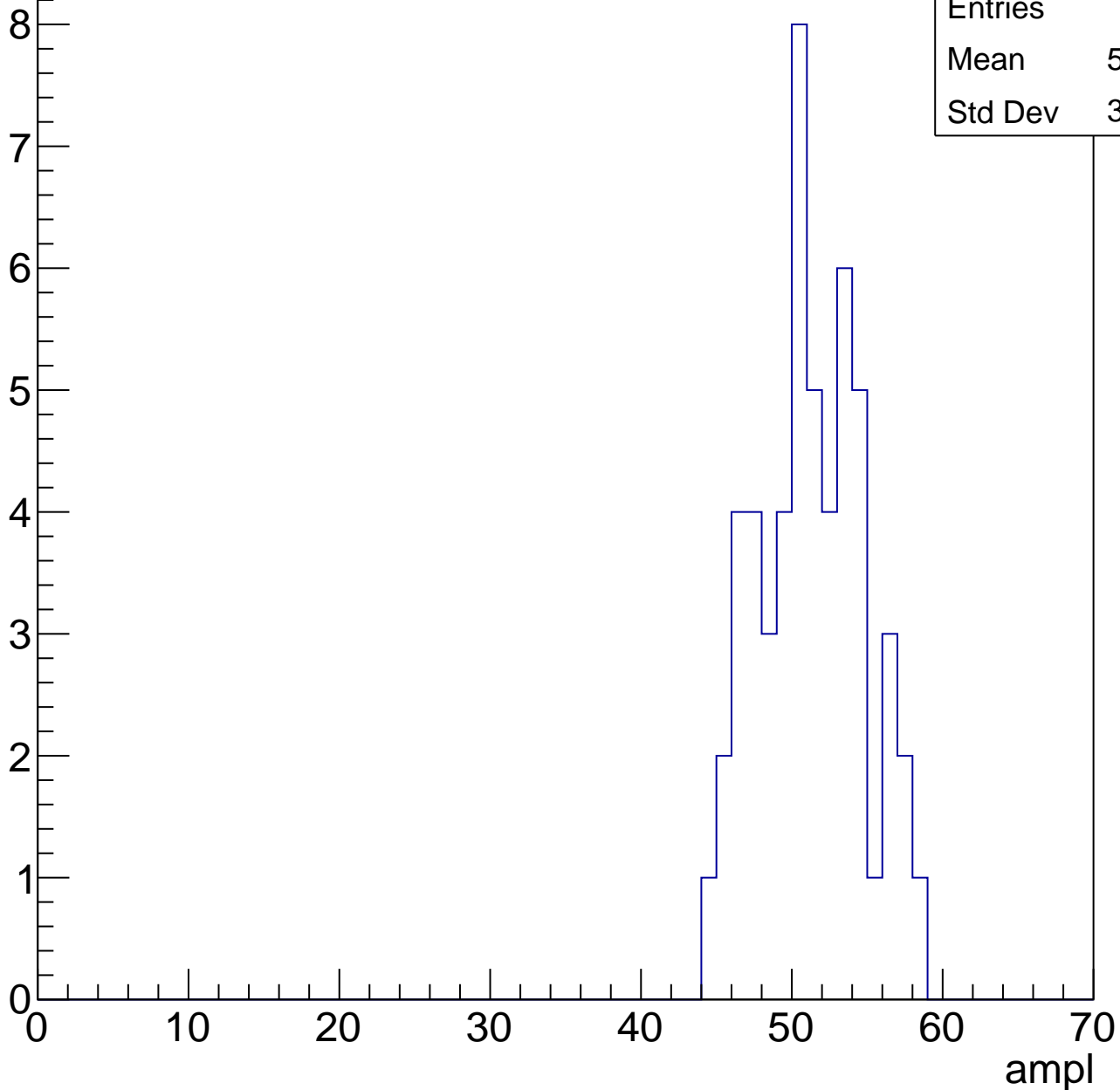


B1L100S, U13-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

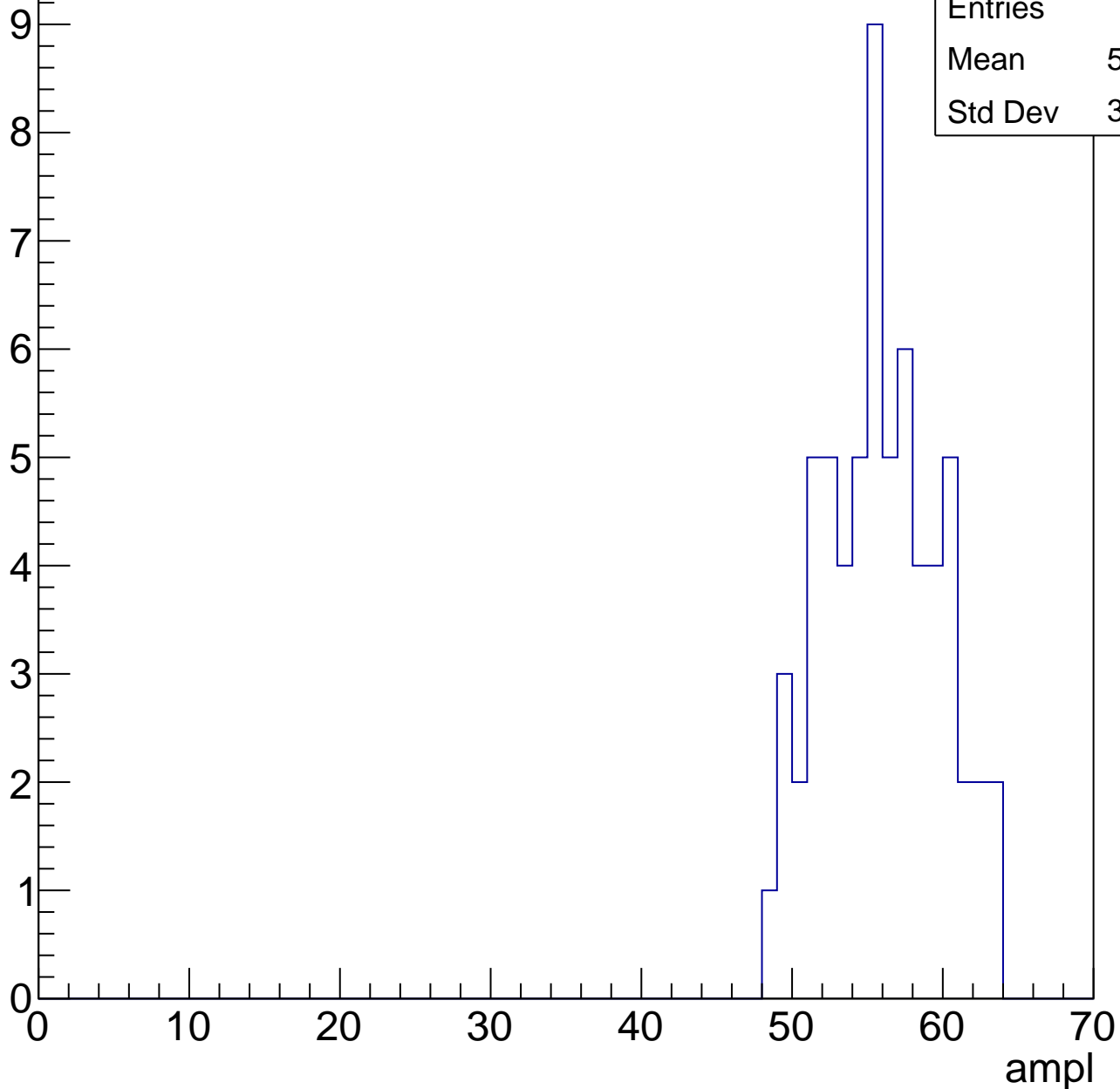
Entries	53
Mean	50.79
Std Dev	3.417



B1L100S, U13-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

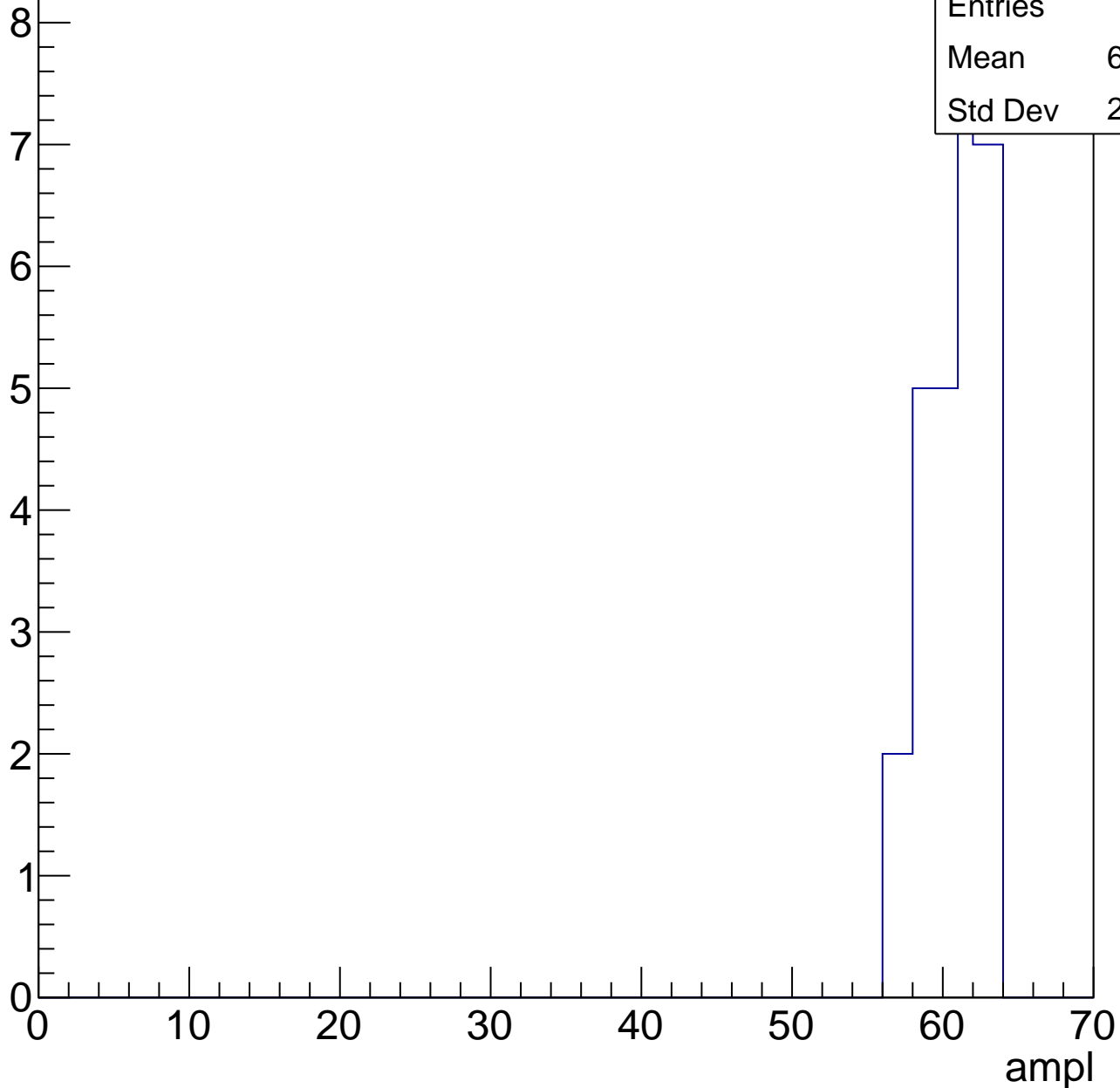


Entries	64
Mean	55.45
Std Dev	3.733

B1L100S, U13-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

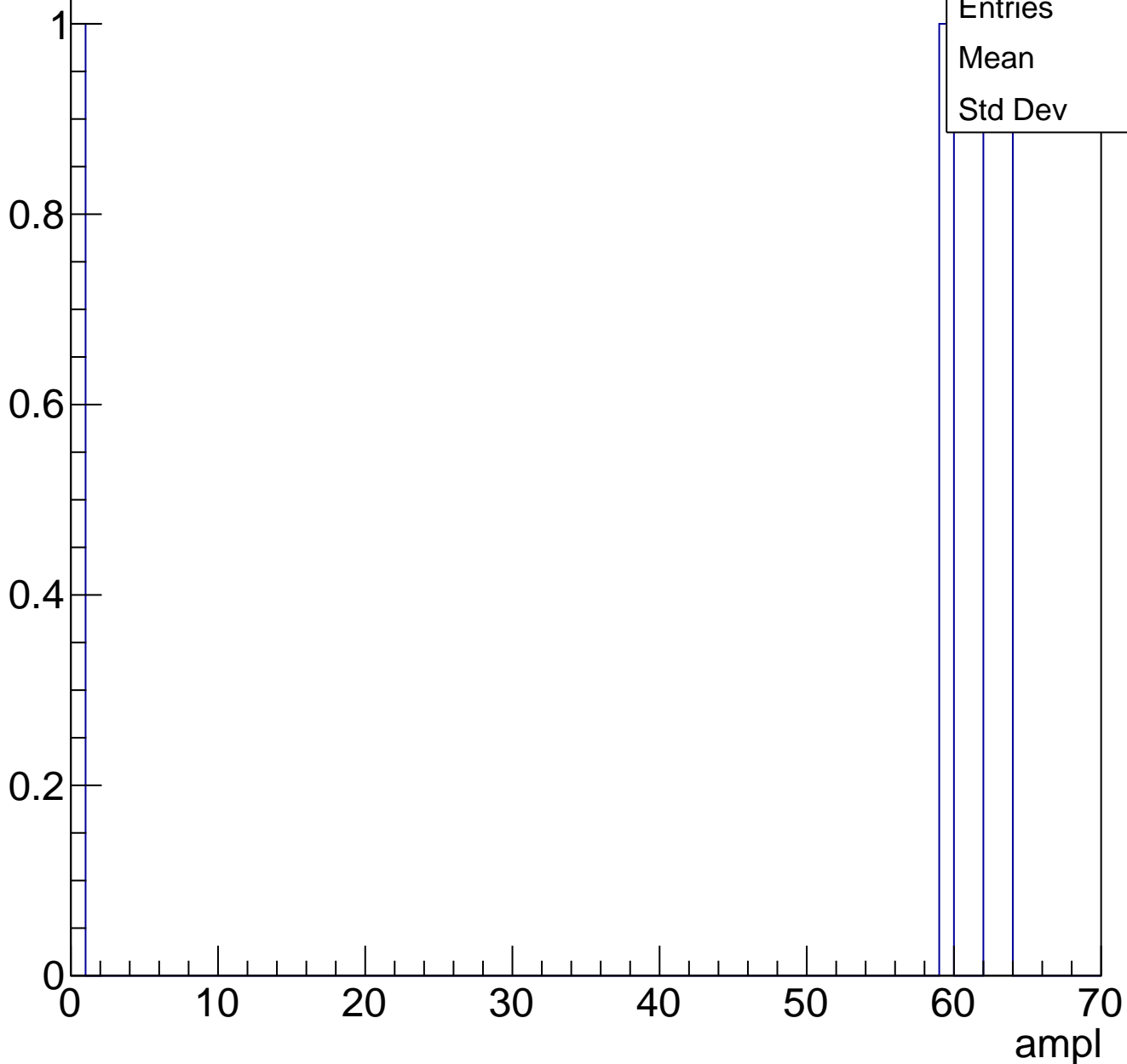
Entry



B1L100S, U13-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch43, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	95
Mean	28.87
Std Dev	4.942

Gaus mean : 29.5434

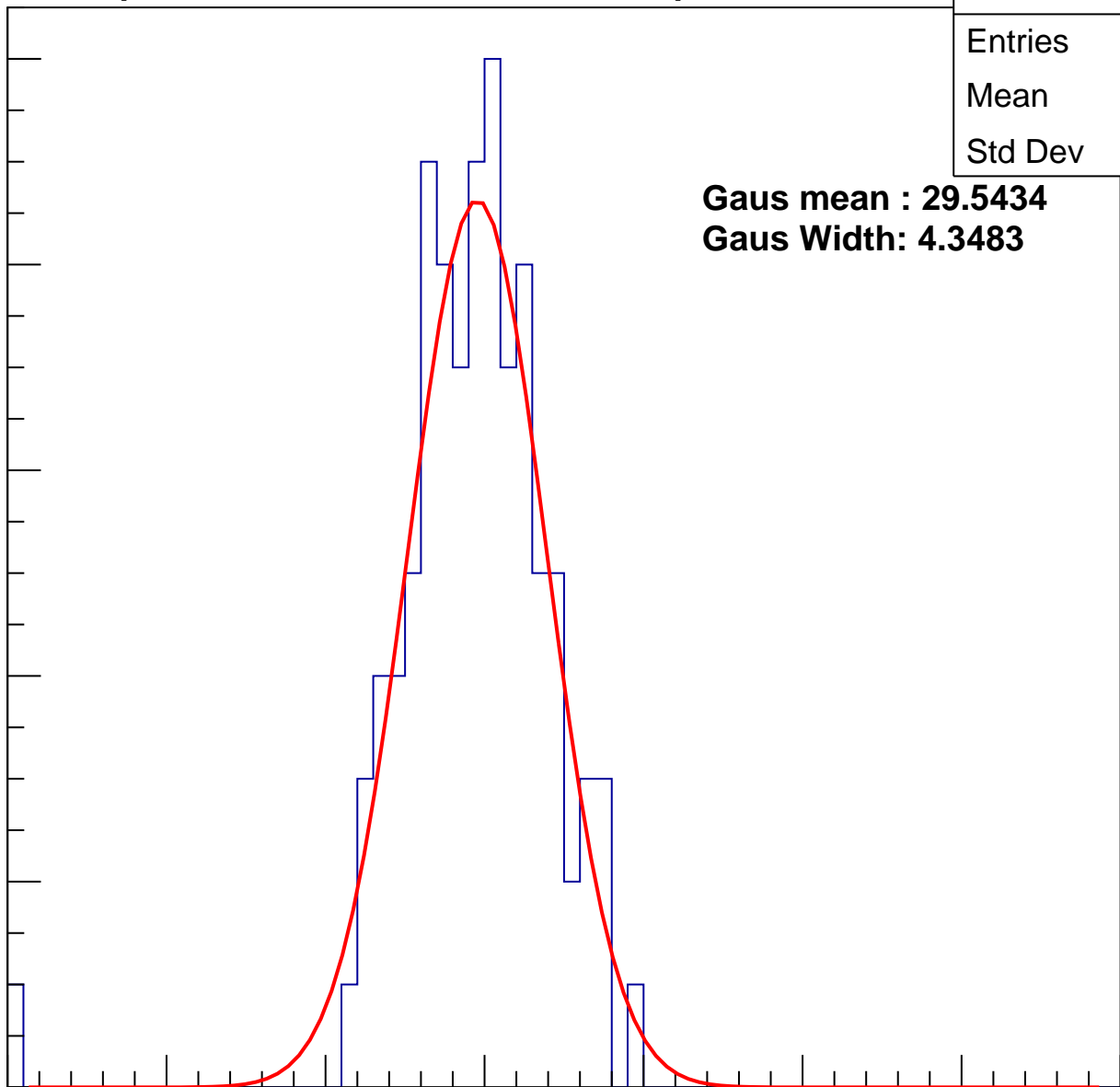
Gaus Width: 4.3483

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch43, adc1

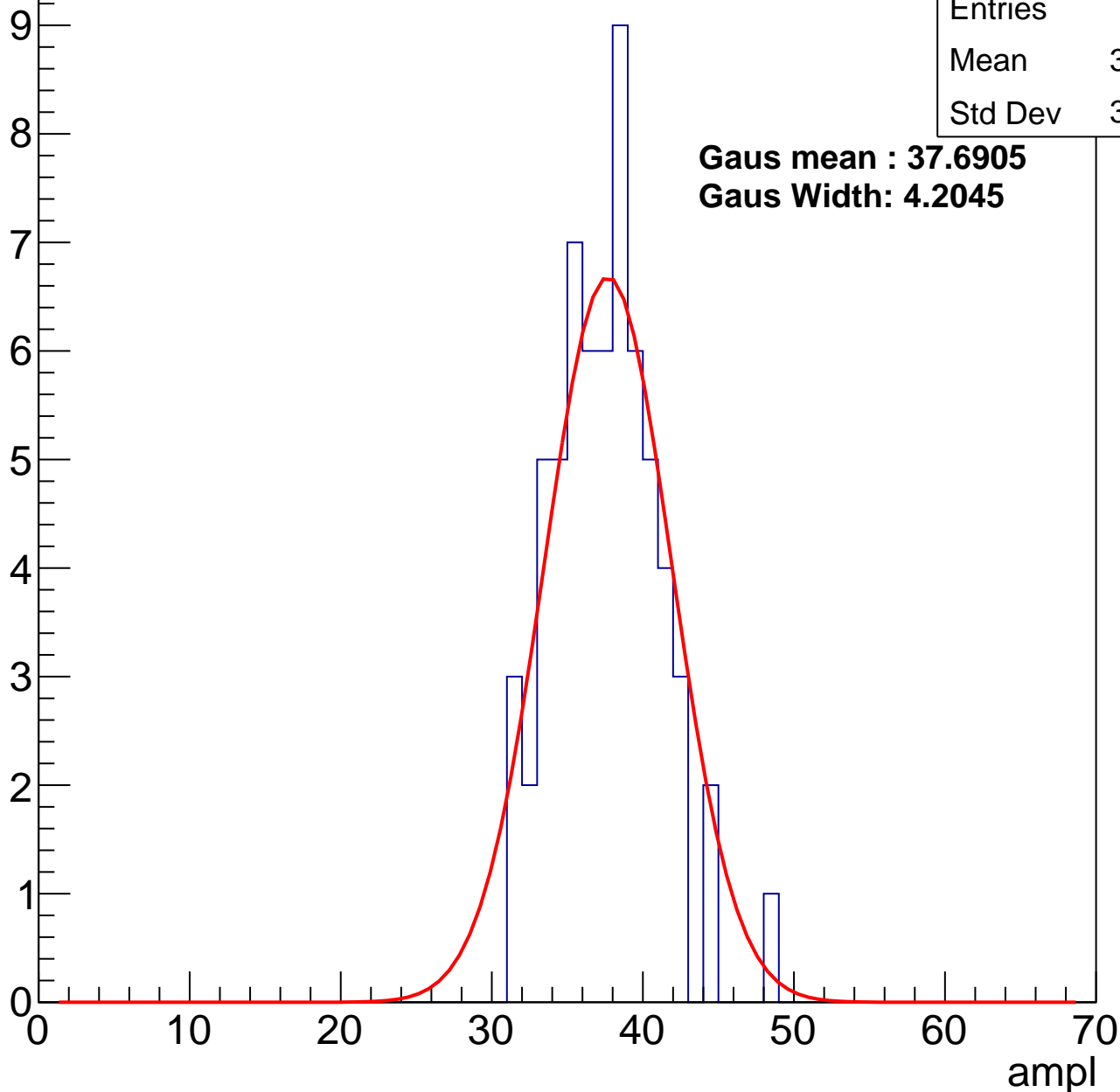
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.14
Std Dev	3.427

Gaus mean : 37.6905

Gaus Width: 4.2045



B1L100S, U13-ch43, adc2

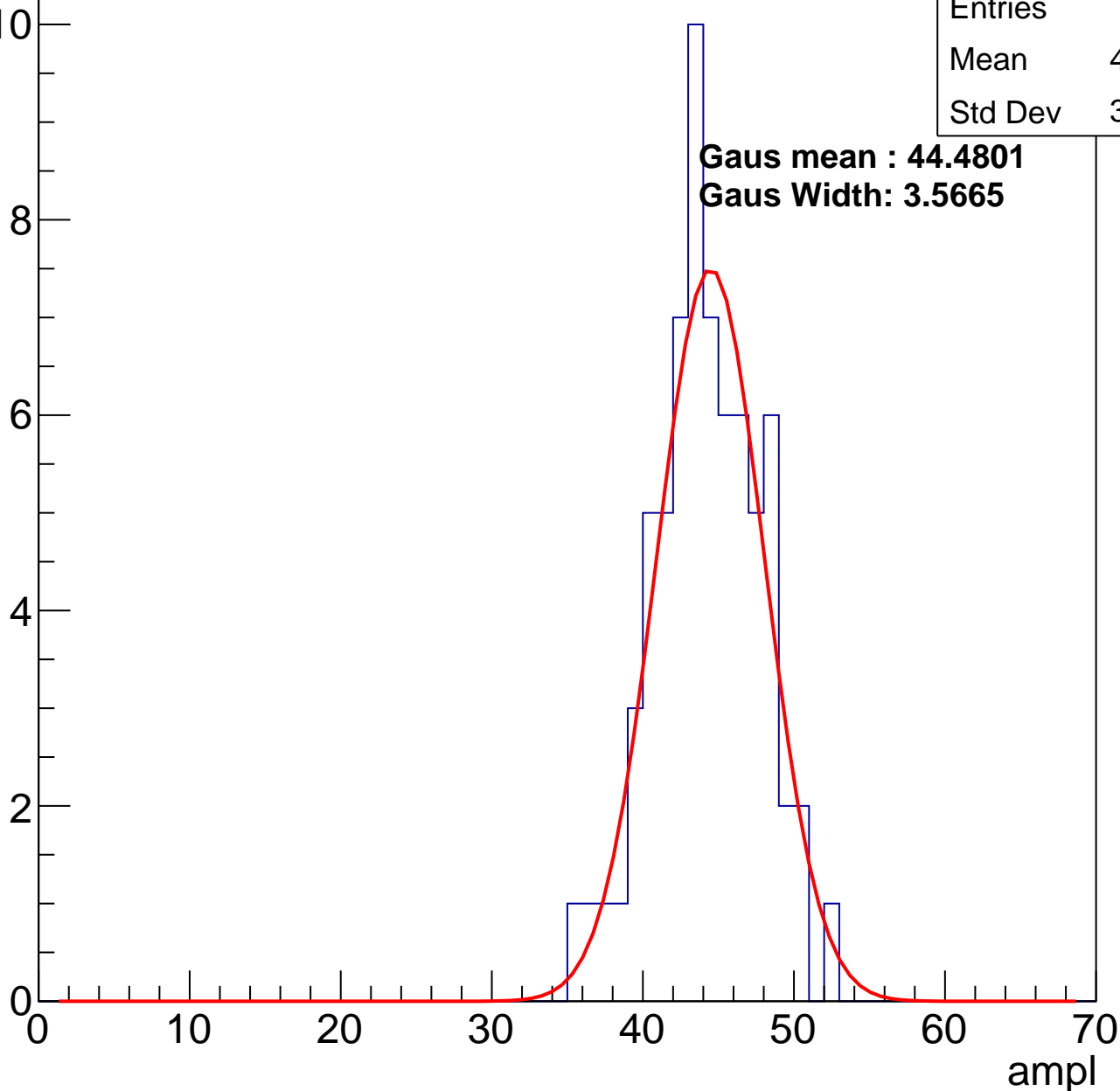
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.75
Std Dev	3.449

Gaus mean : 44.4801

Gaus Width: 3.5665

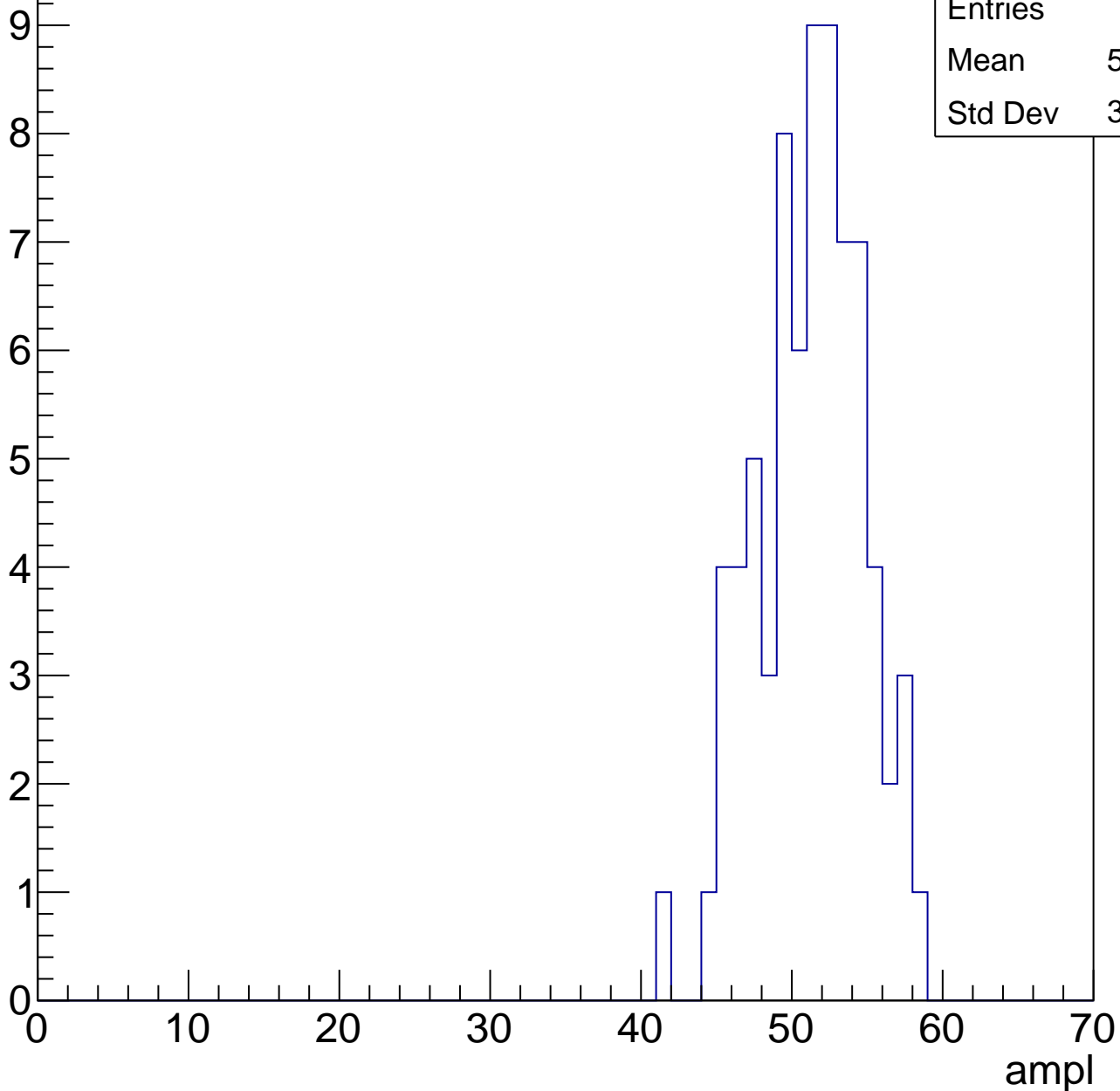


B1L100S, U13-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.77
Std Dev	3.497

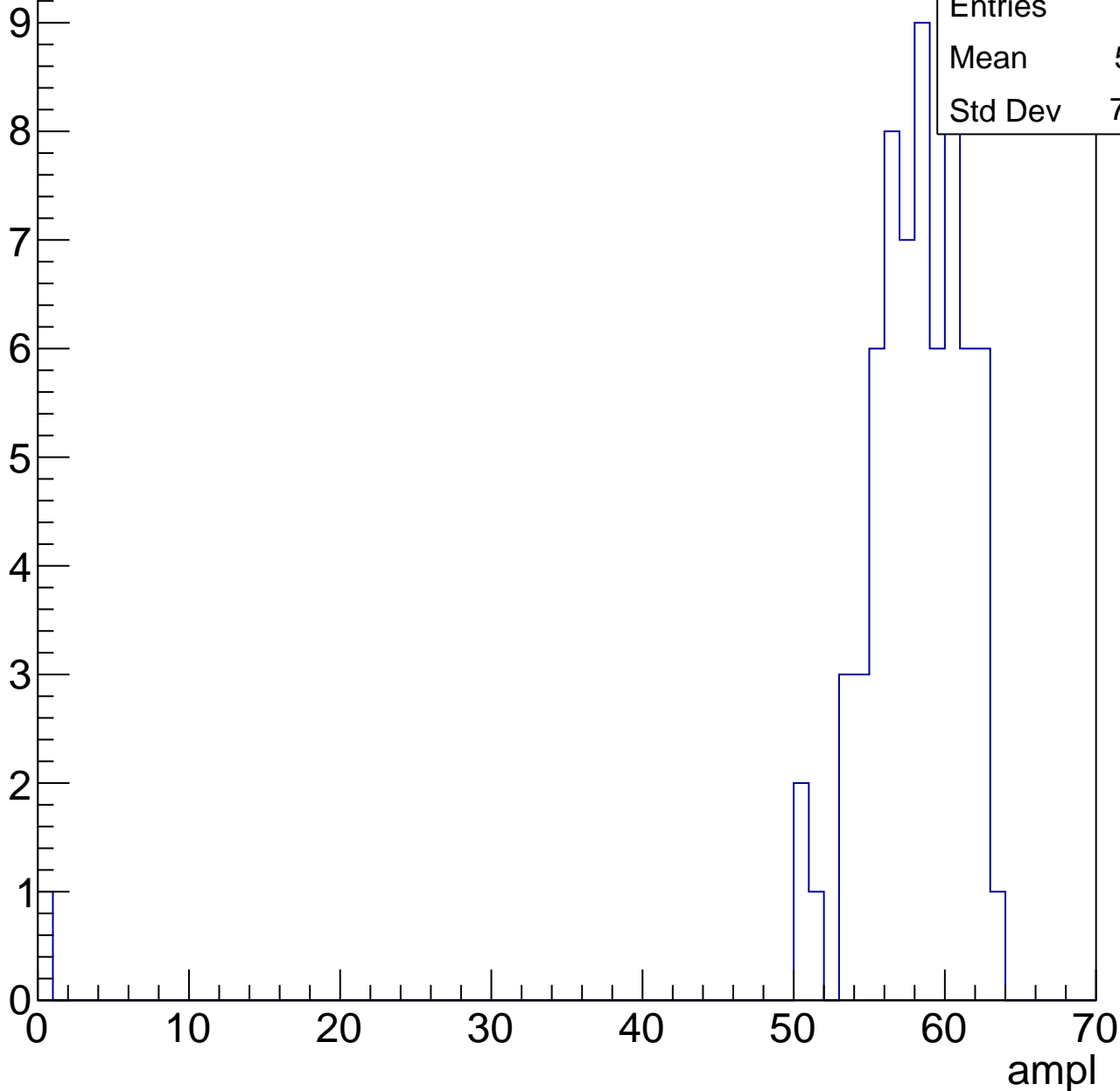


B1L100S, U13-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

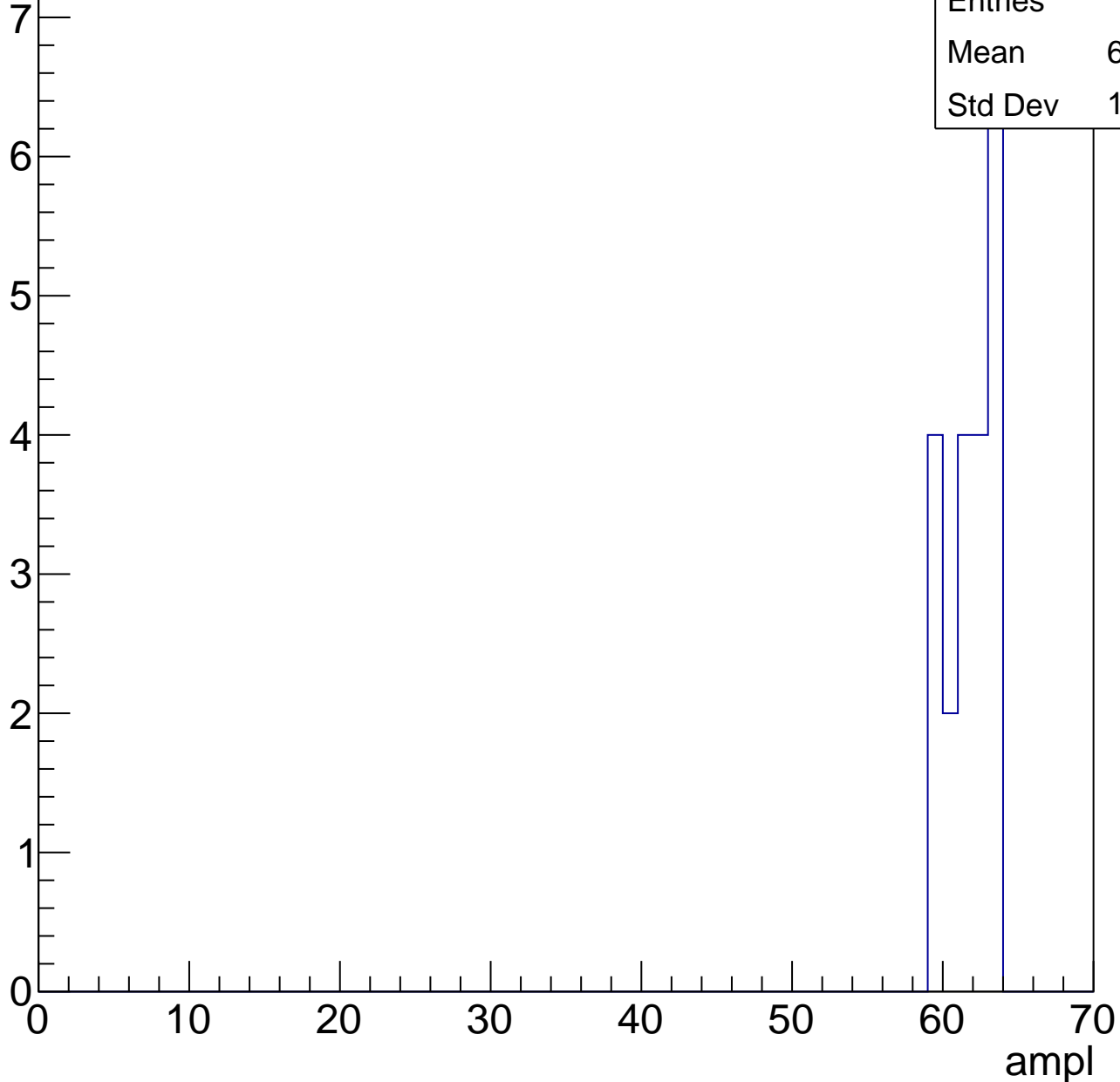
Entries	67
Mean	56.81
Std Dev	7.604



B1L100S, U13-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	21
Mean	61.38
Std Dev	1.495

B1L100S, U13-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch44, adc0

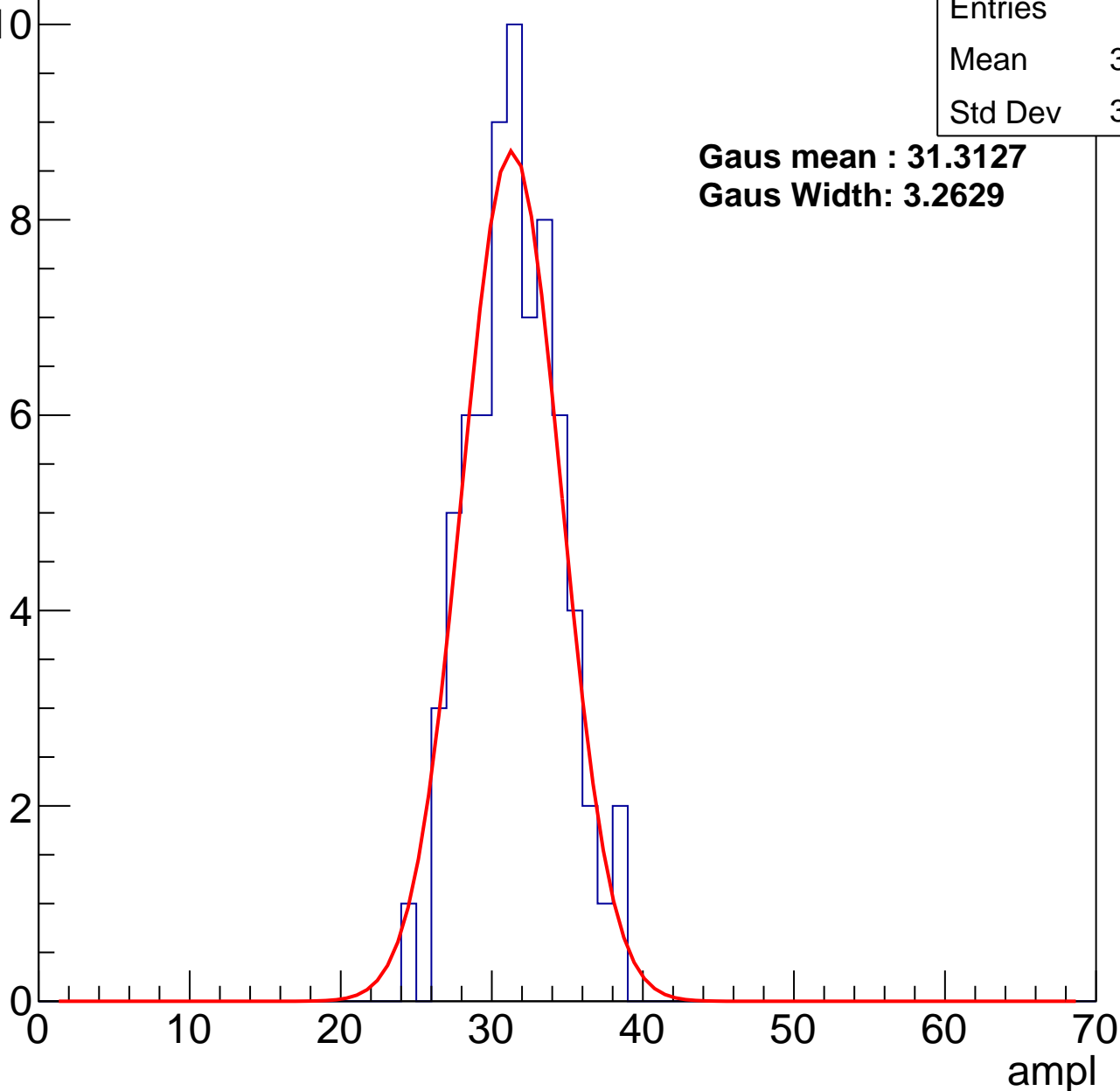
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	31.09
Std Dev	3.004

Gaus mean : 31.3127

Gaus Width: 3.2629



B1L100S, U13-ch44, adc1

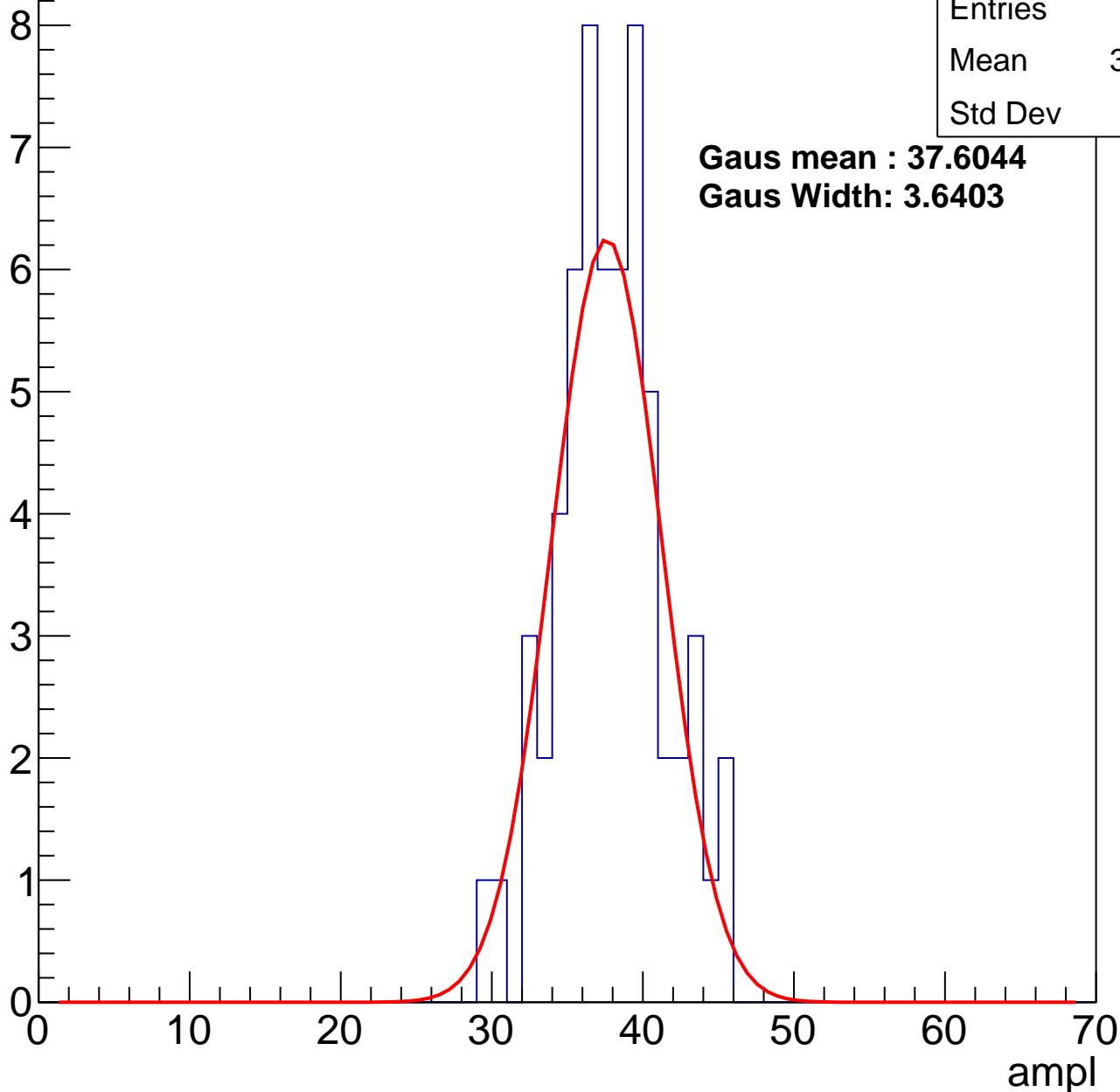
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	37.43
Std Dev	3.48

Gaus mean : 37.6044

Gaus Width: 3.6403



B1L100S, U13-ch44, adc2

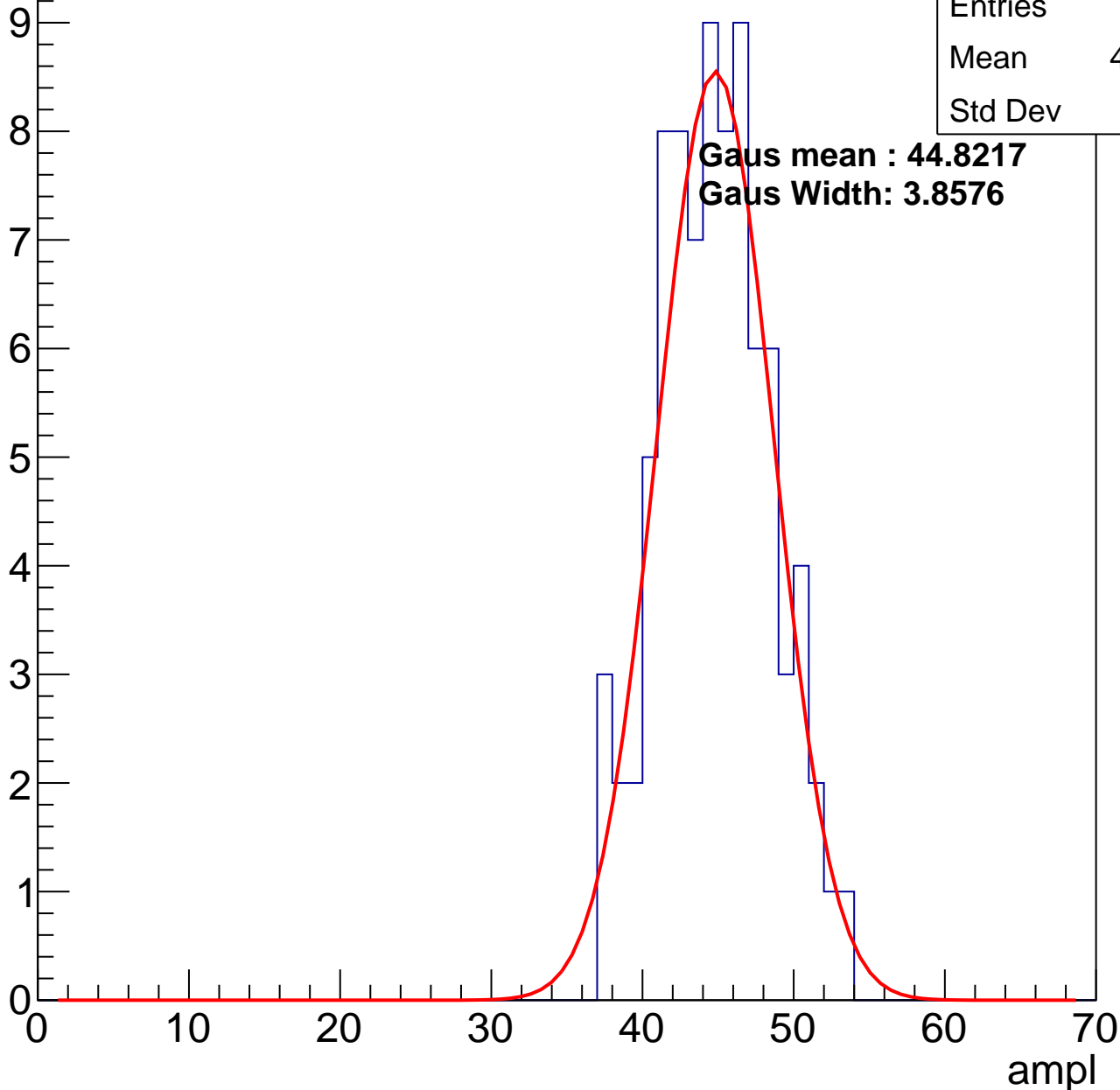
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	44.33
Std Dev	3.63

Gaus mean : 44.8217

Gaus Width: 3.8576

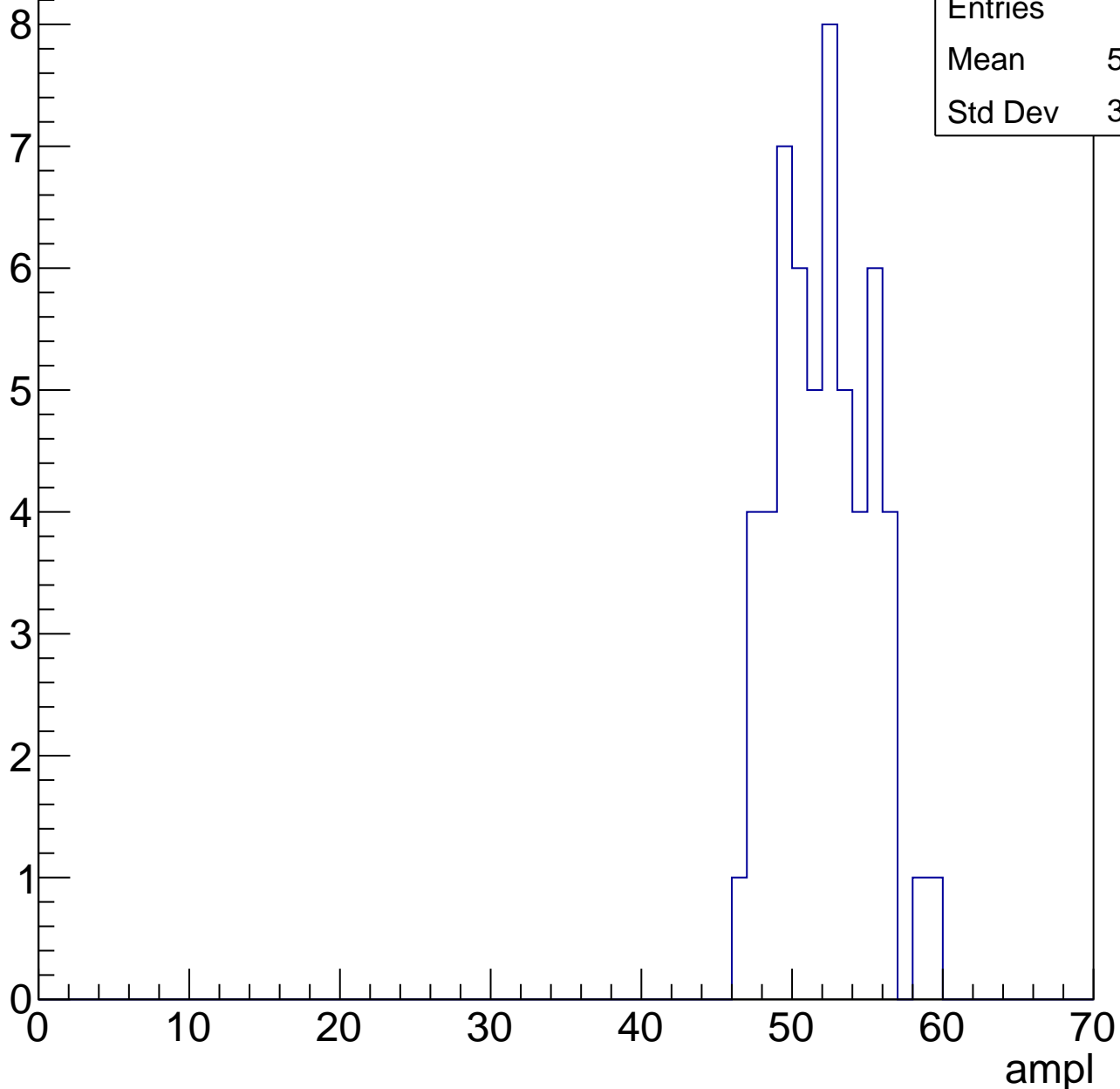


B1L100S, U13-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

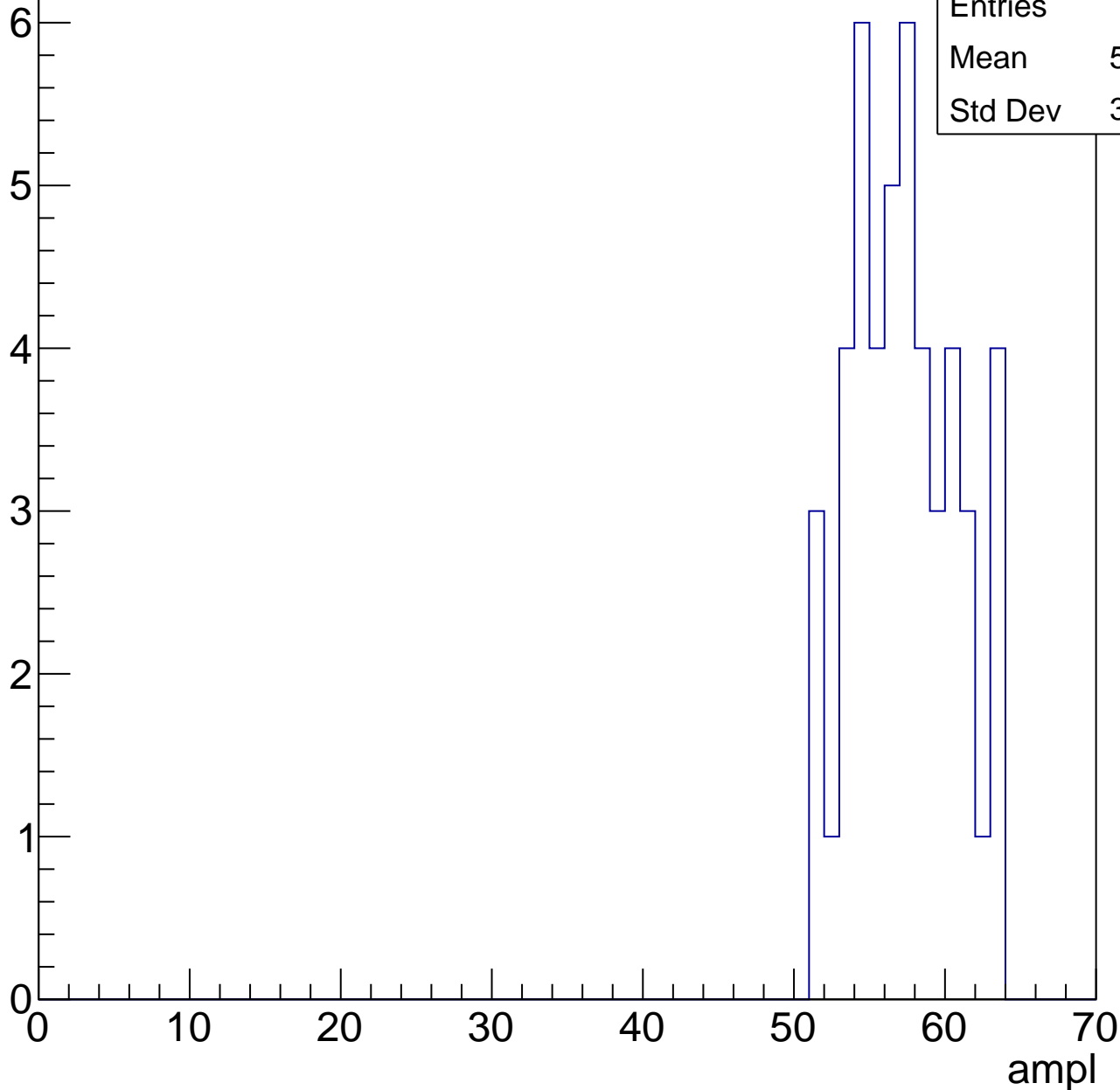
Entries	56
Mean	51.64
Std Dev	3.014



B1L100S, U13-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



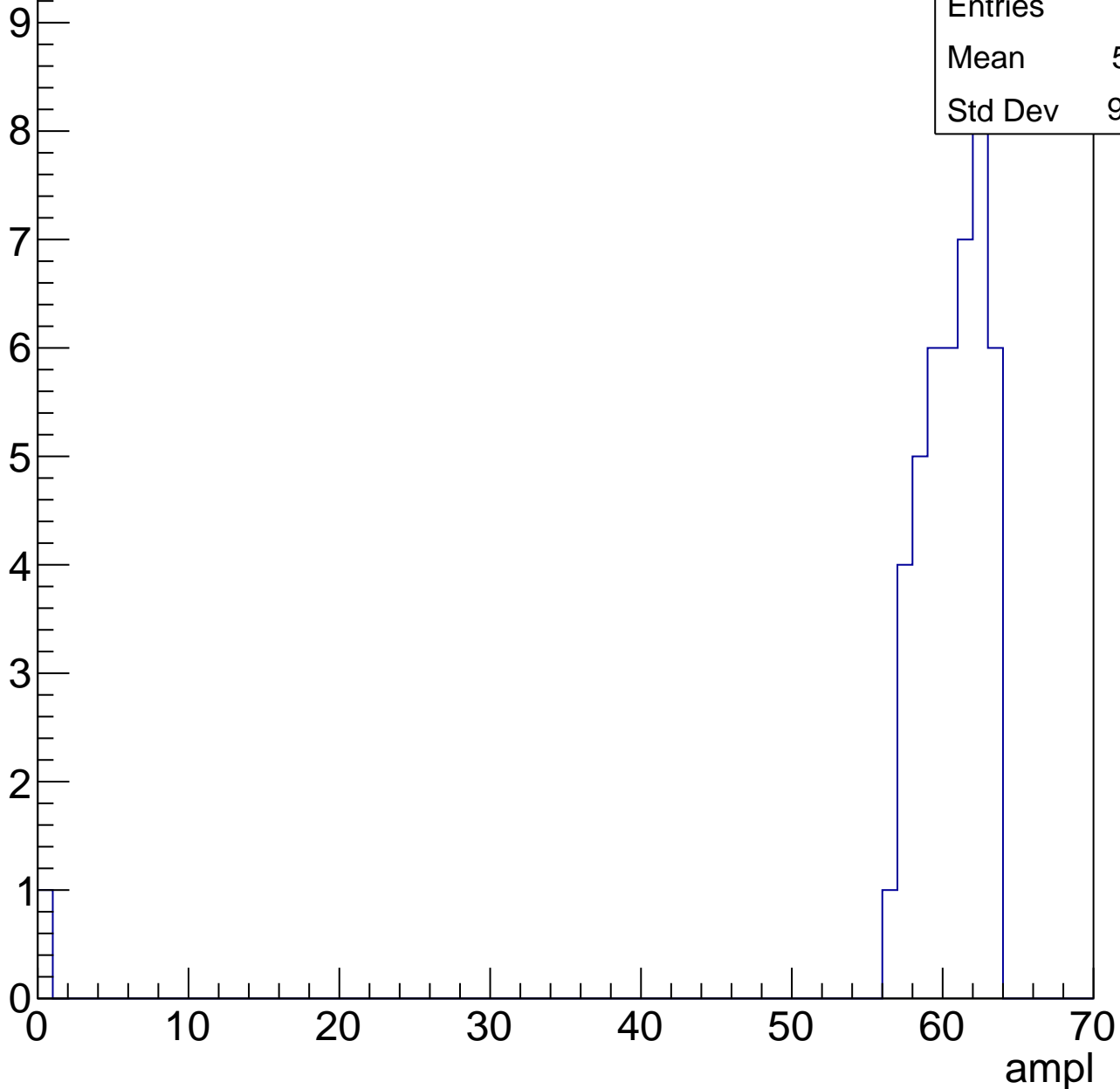
Entries	48
Mean	56.85
Std Dev	3.354

B1L100S, U13-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	58.91
Std Dev	9.094



B1L100S, U13-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch45, adc0

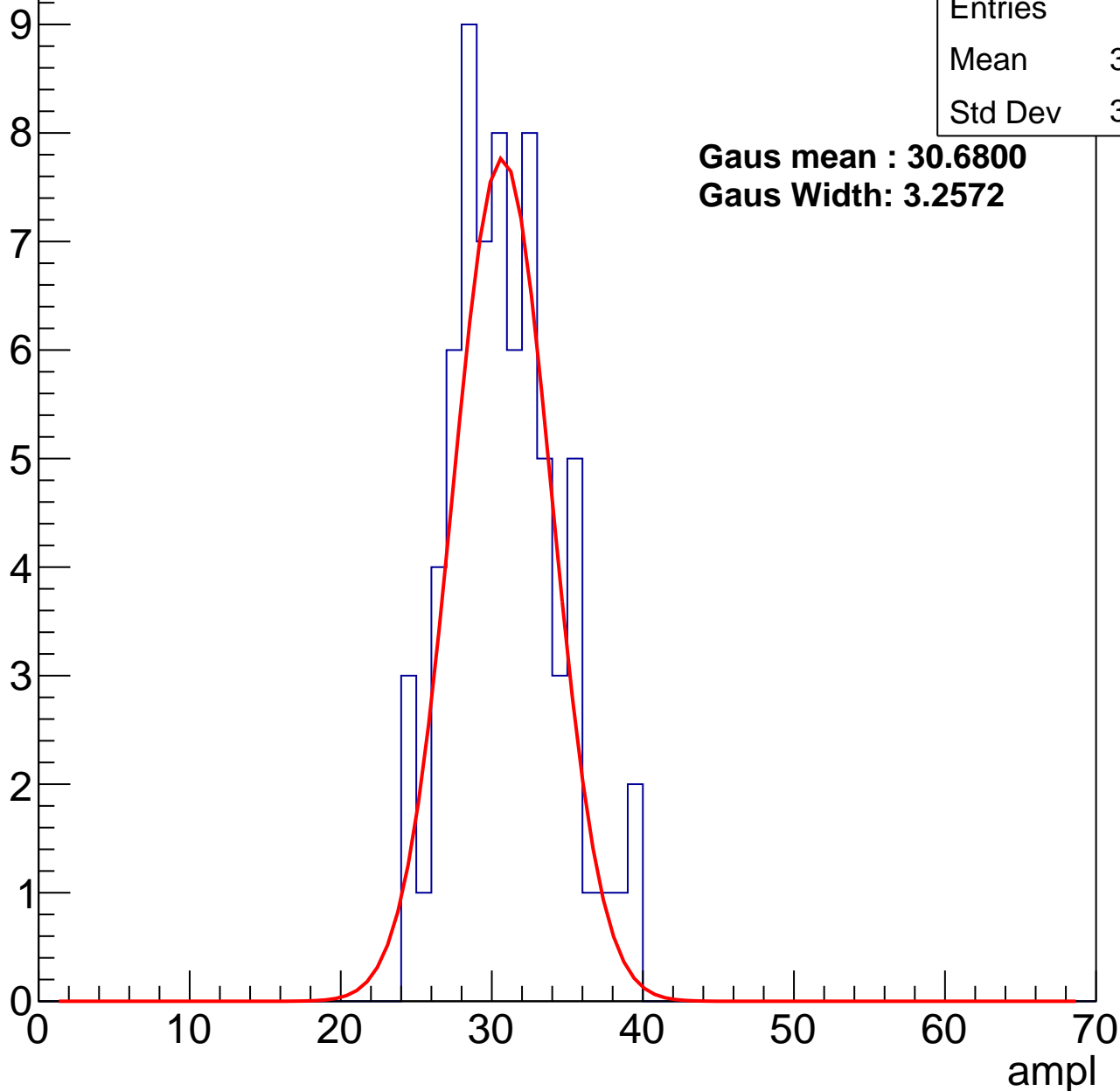
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.44
Std Dev	3.479

Gaus mean : 30.6800

Gaus Width: 3.2572



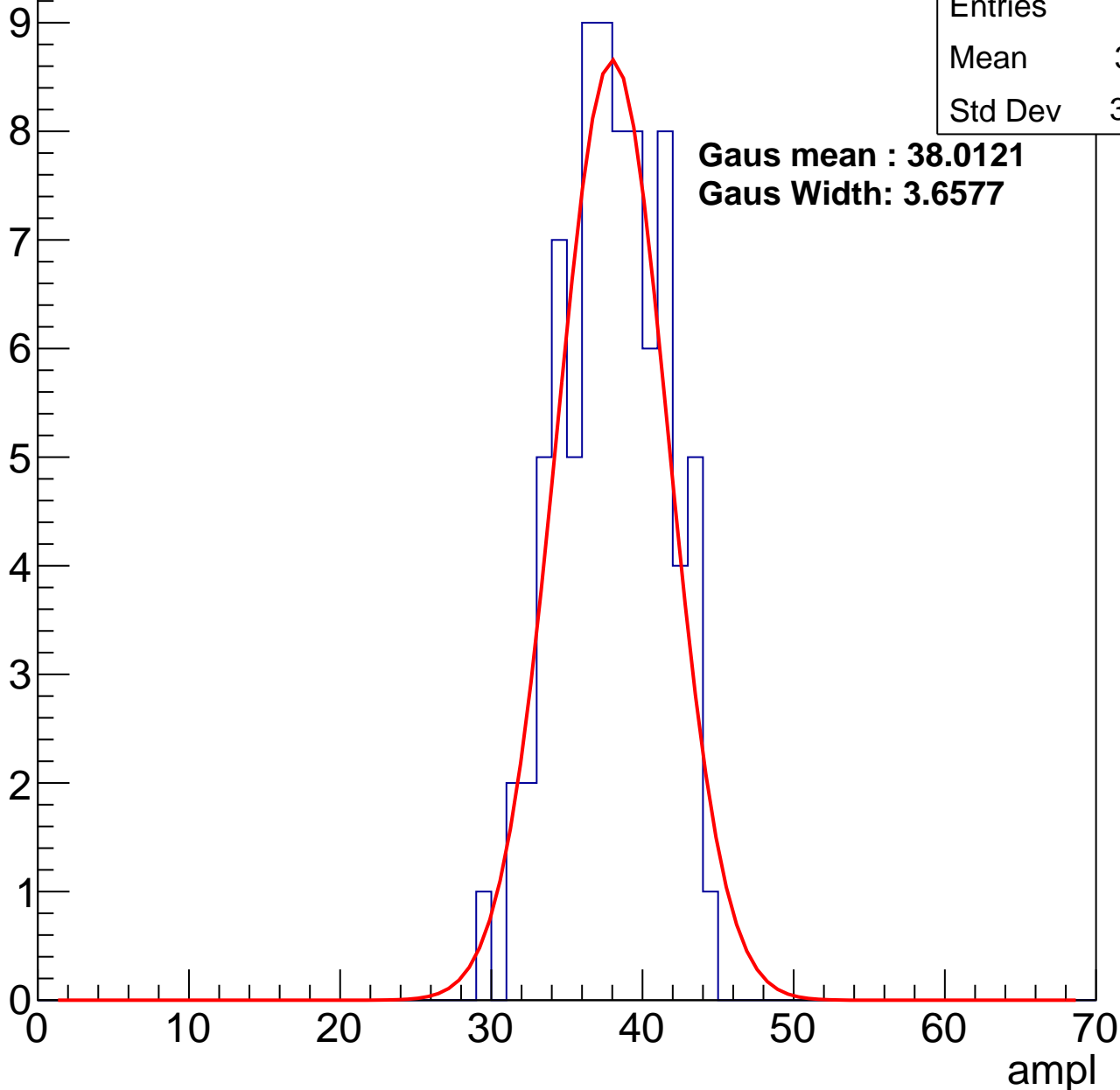
B1L100S, U13-ch45, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	37.51
Std Dev	3.332

Gaus mean : 38.0121
Gaus Width: 3.6577



B1L100S, U13-ch45, adc2

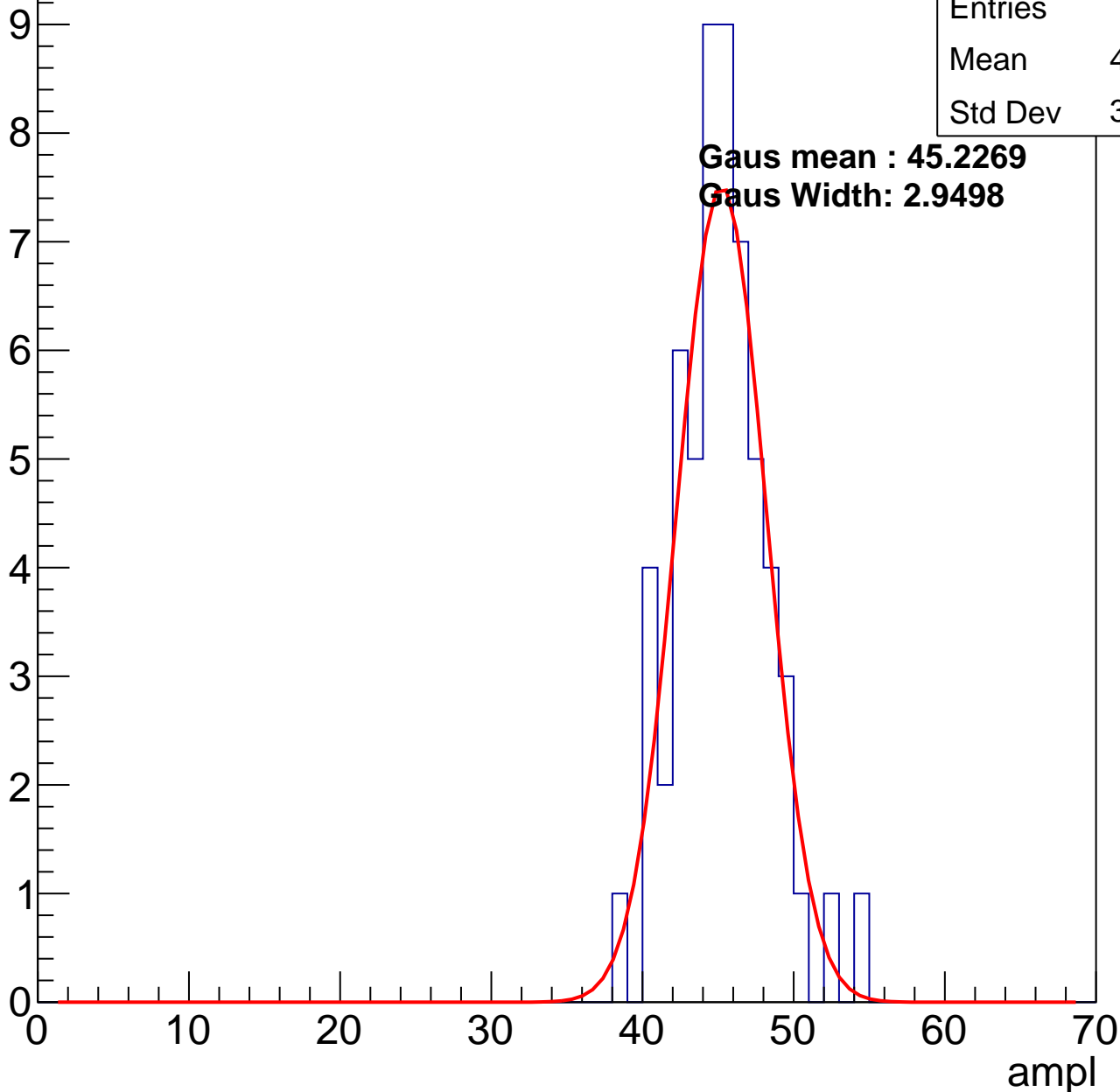
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	44.83
Std Dev	3.018

Gaus mean : 45.2269

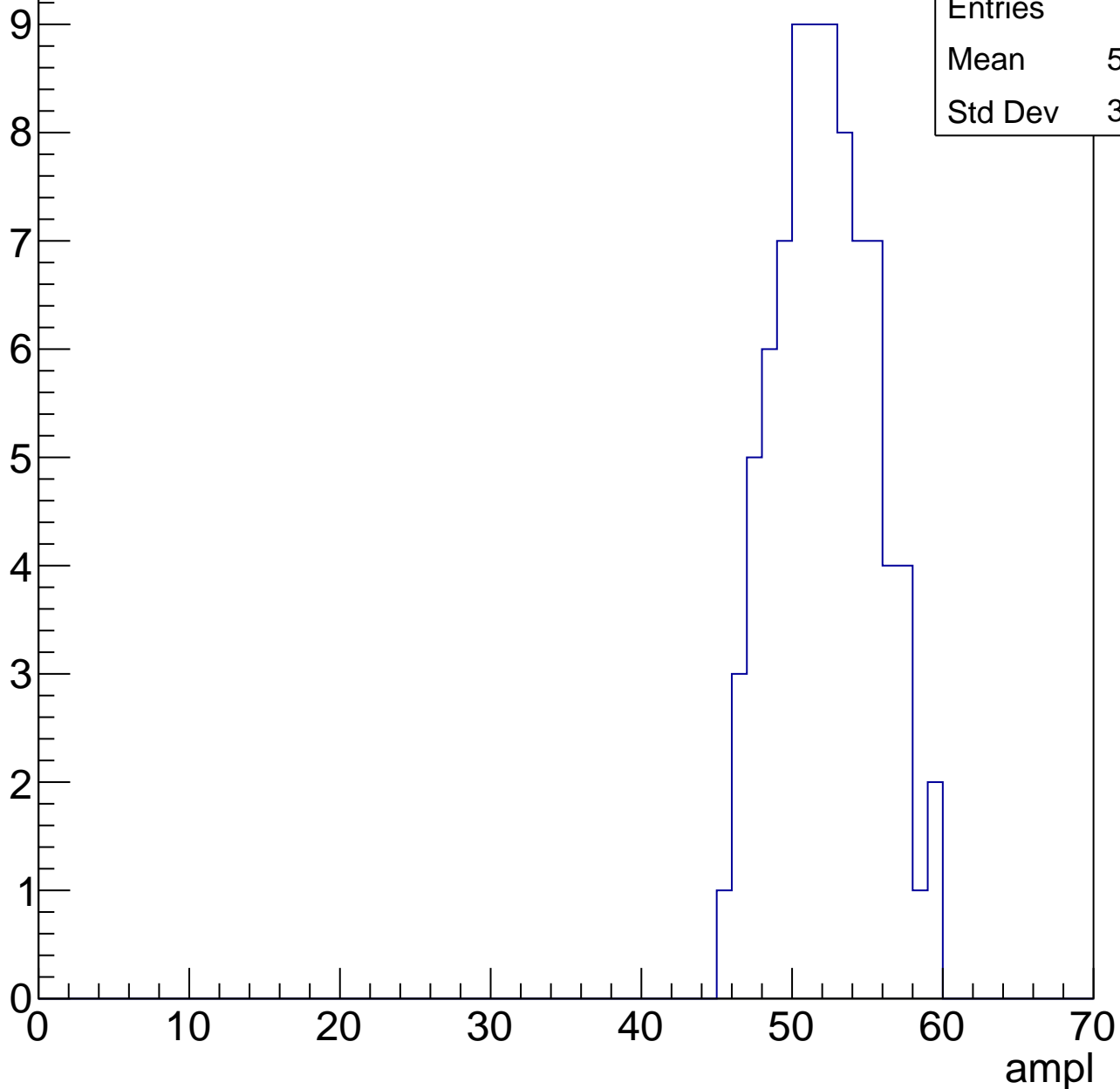
Gaus Width: 2.9498



B1L100S, U13-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



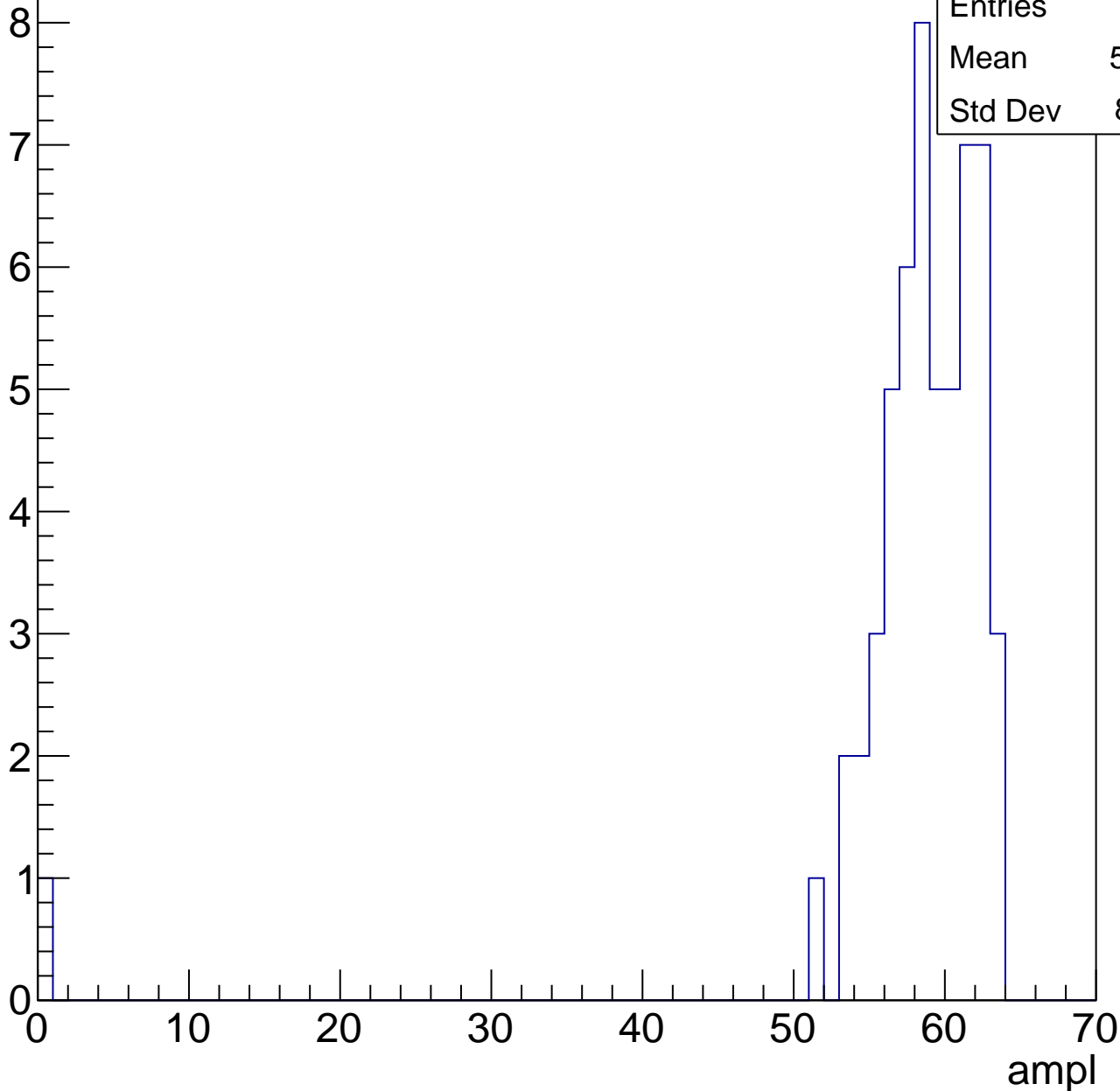
Entries	82
Mean	51.72
Std Dev	3.269

B1L100S, U13-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.47
Std Dev	8.321



B1L100S, U13-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	23
Mean	60.91
Std Dev	1.886

ampl

B1L100S, U13-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch46, adc0

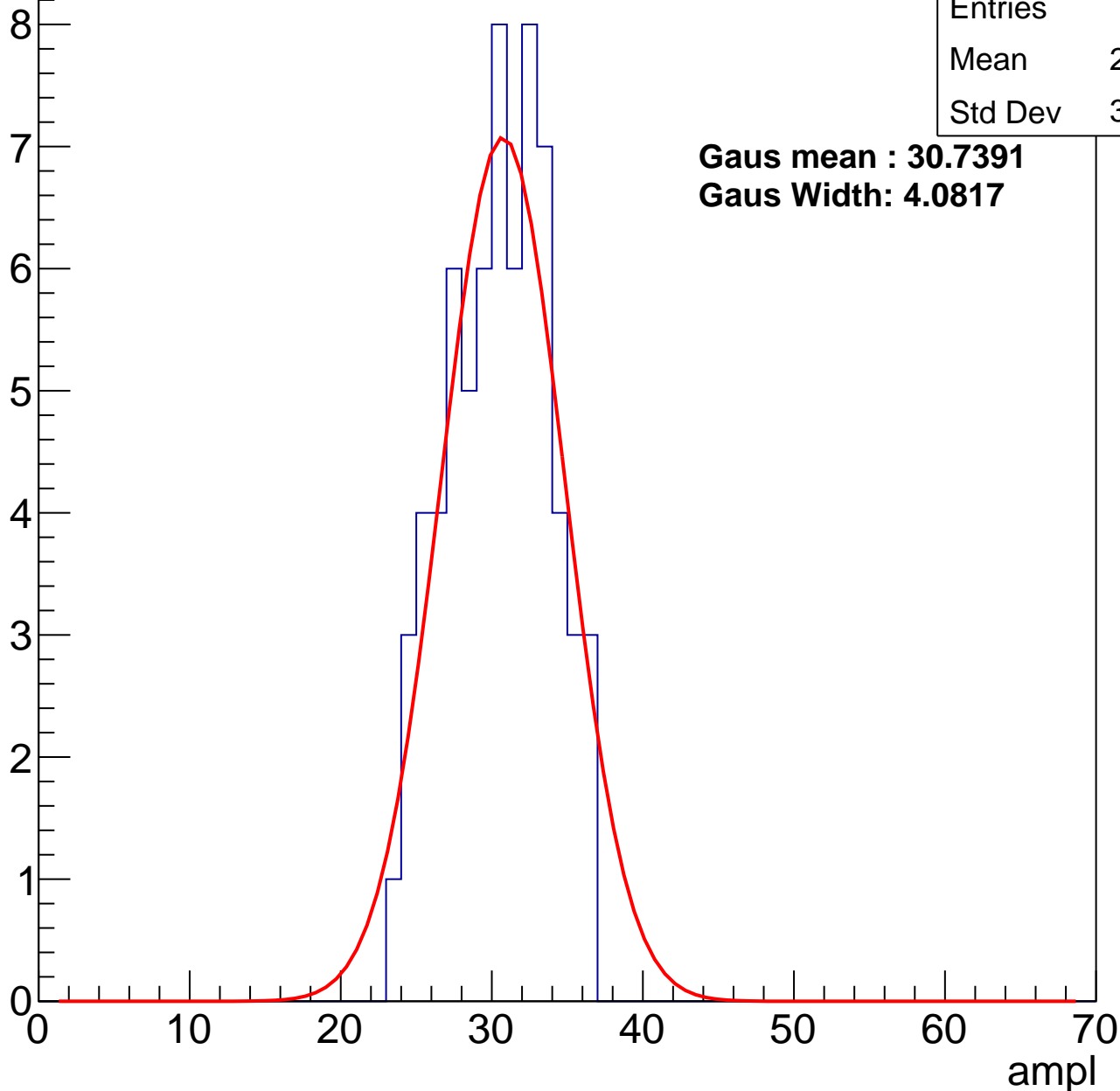
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.96
Std Dev	3.319

Gaus mean : 30.7391

Gaus Width: 4.0817



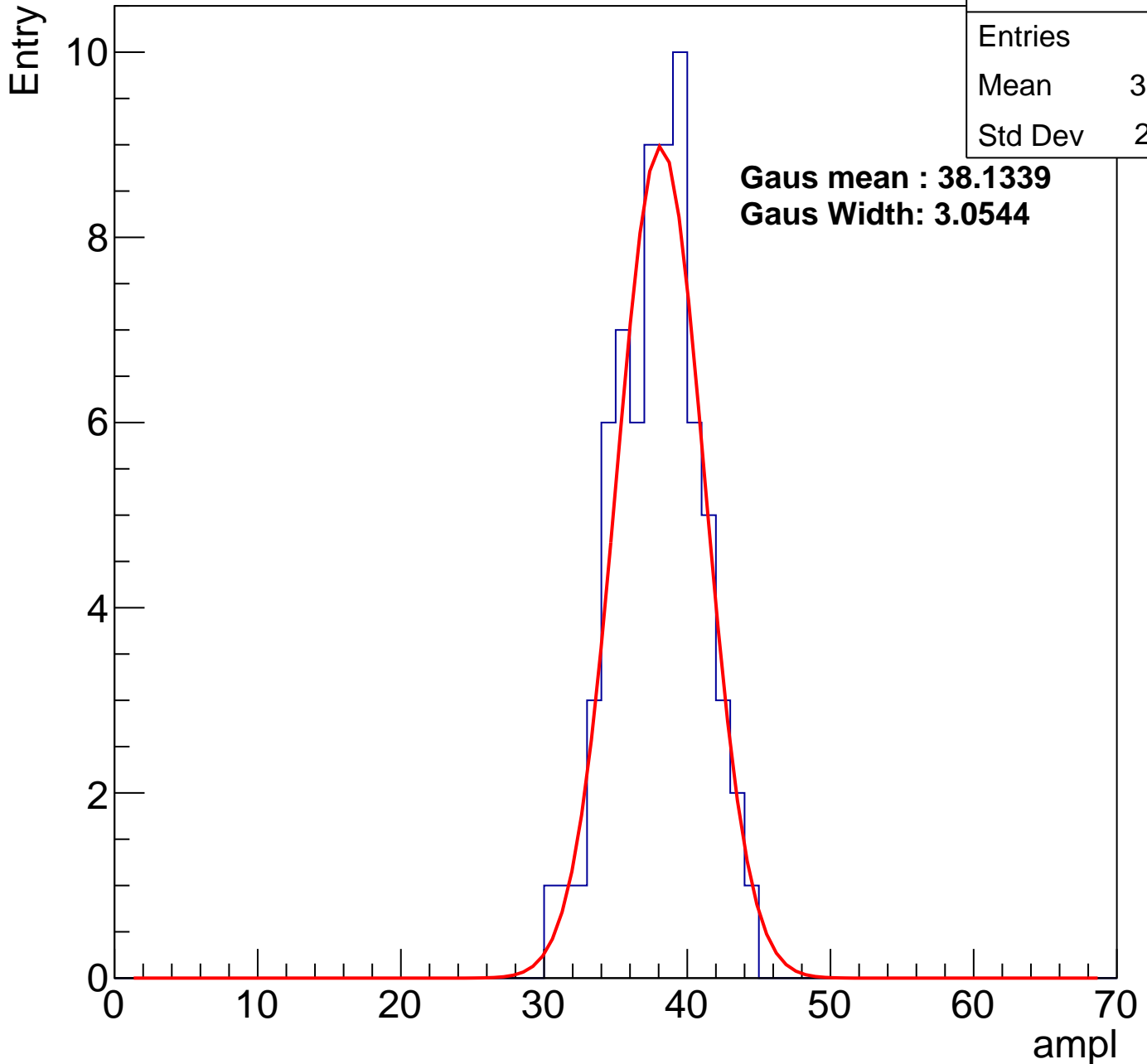
B1L100S, U13-ch46, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	37.47
Std Dev	2.951

Gaus mean : 38.1339

Gaus Width: 3.0544



B1L100S, U13-ch46, adc2

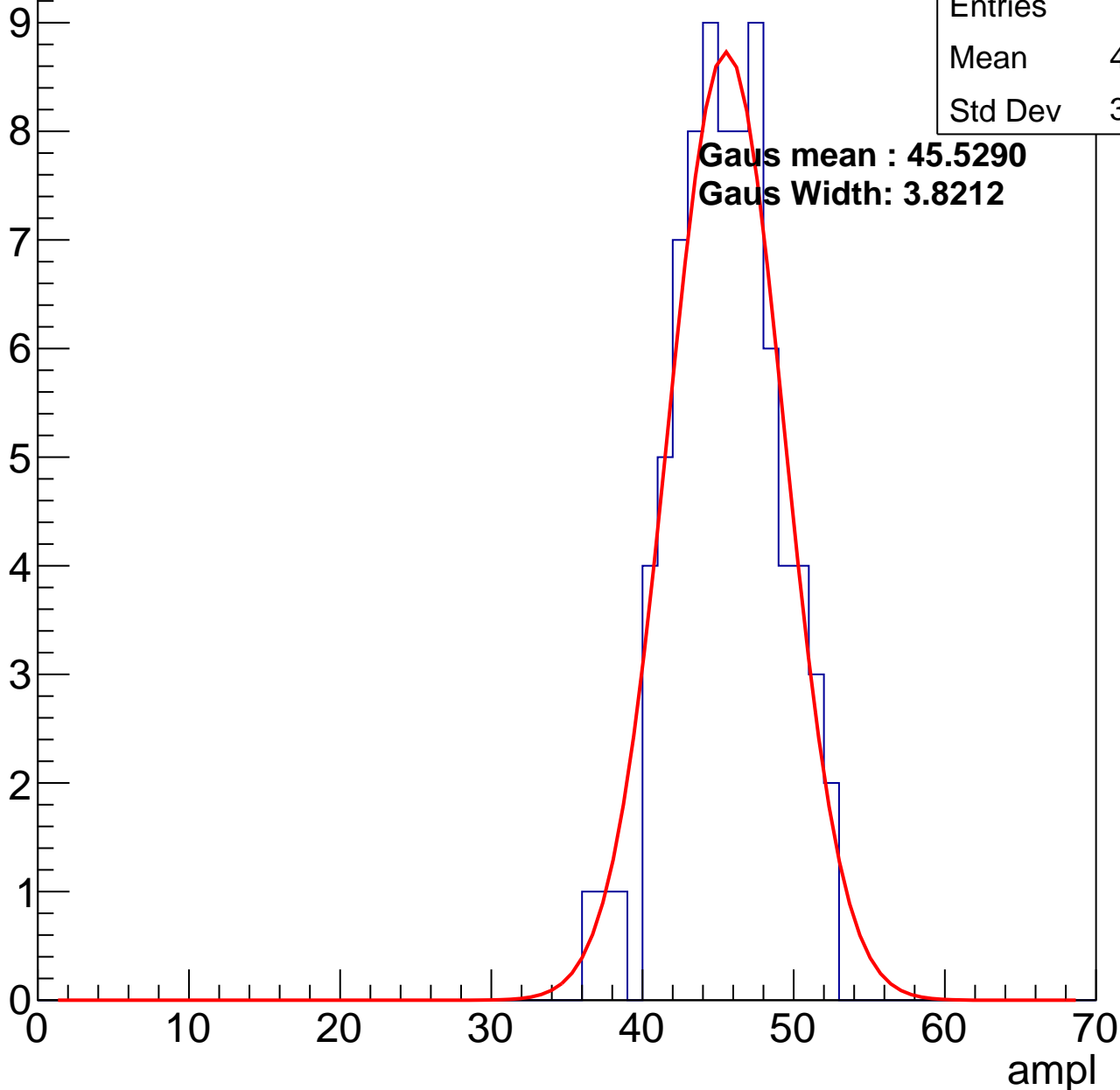
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	45.02
Std Dev	3.439

Gaus mean : 45.5290

Gaus Width: 3.8212

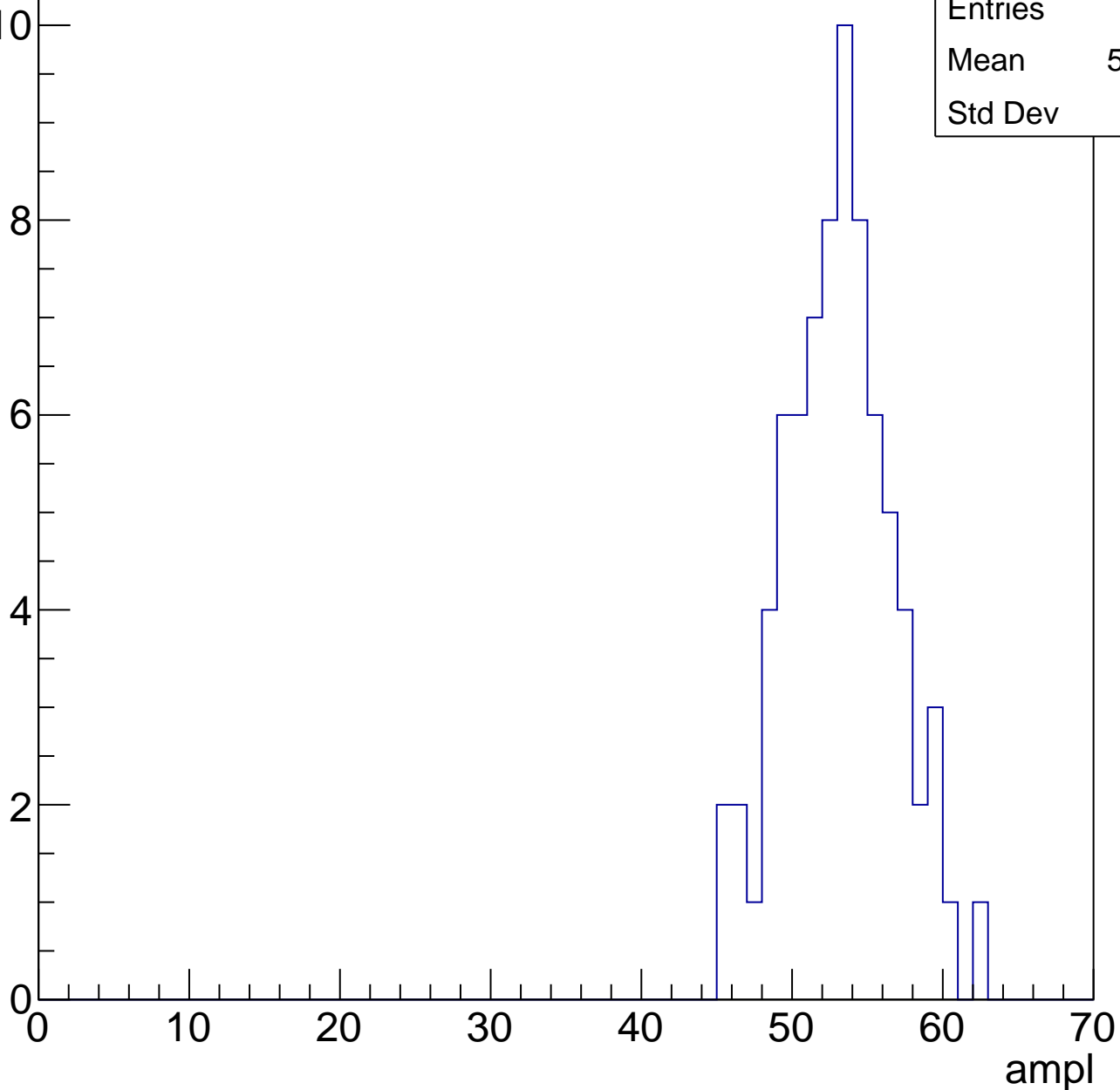


B1L100S, U13-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	52.67
Std Dev	3.57



B1L100S, U13-ch46, adc4

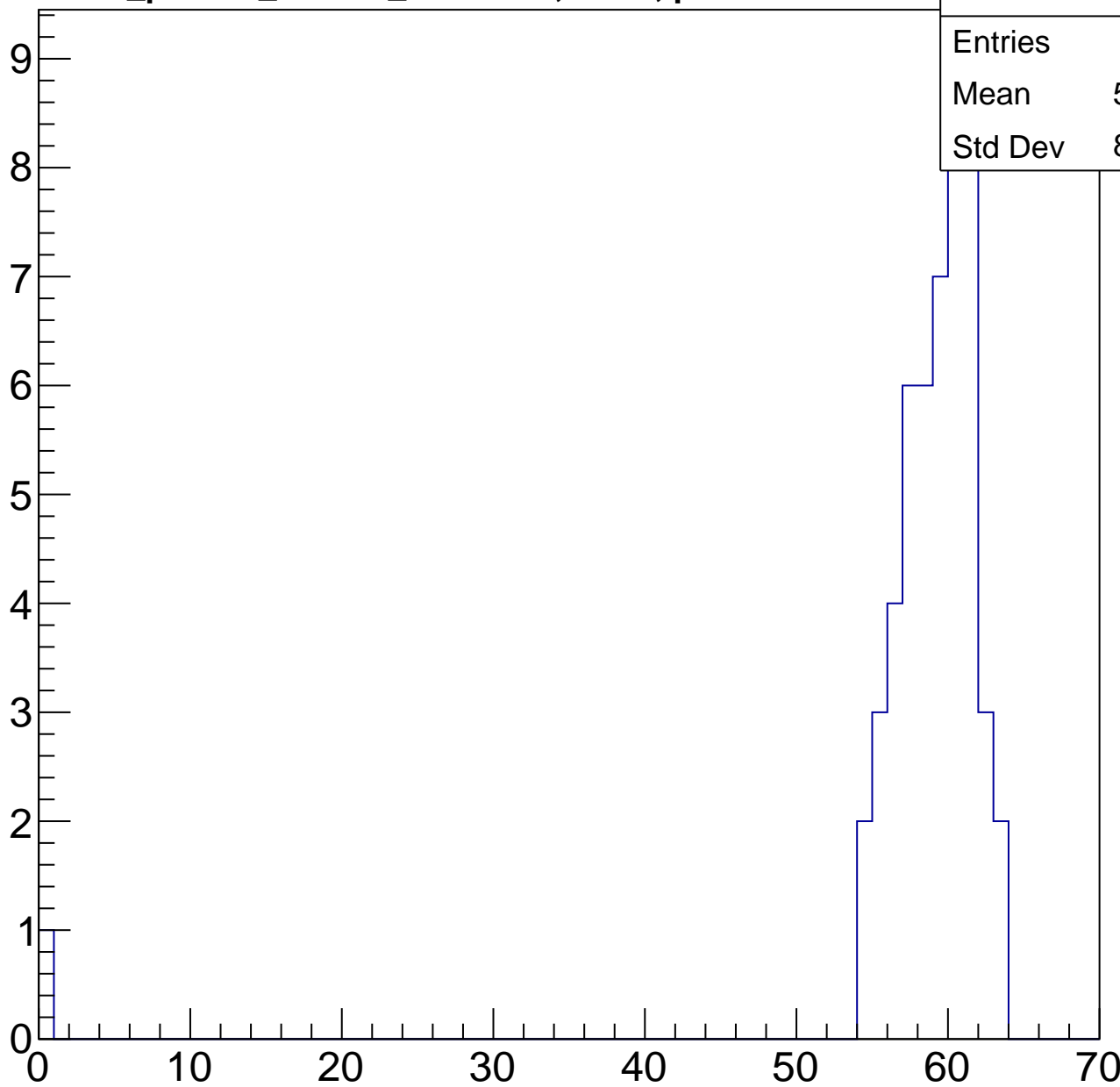
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	57.67
Std Dev	8.468

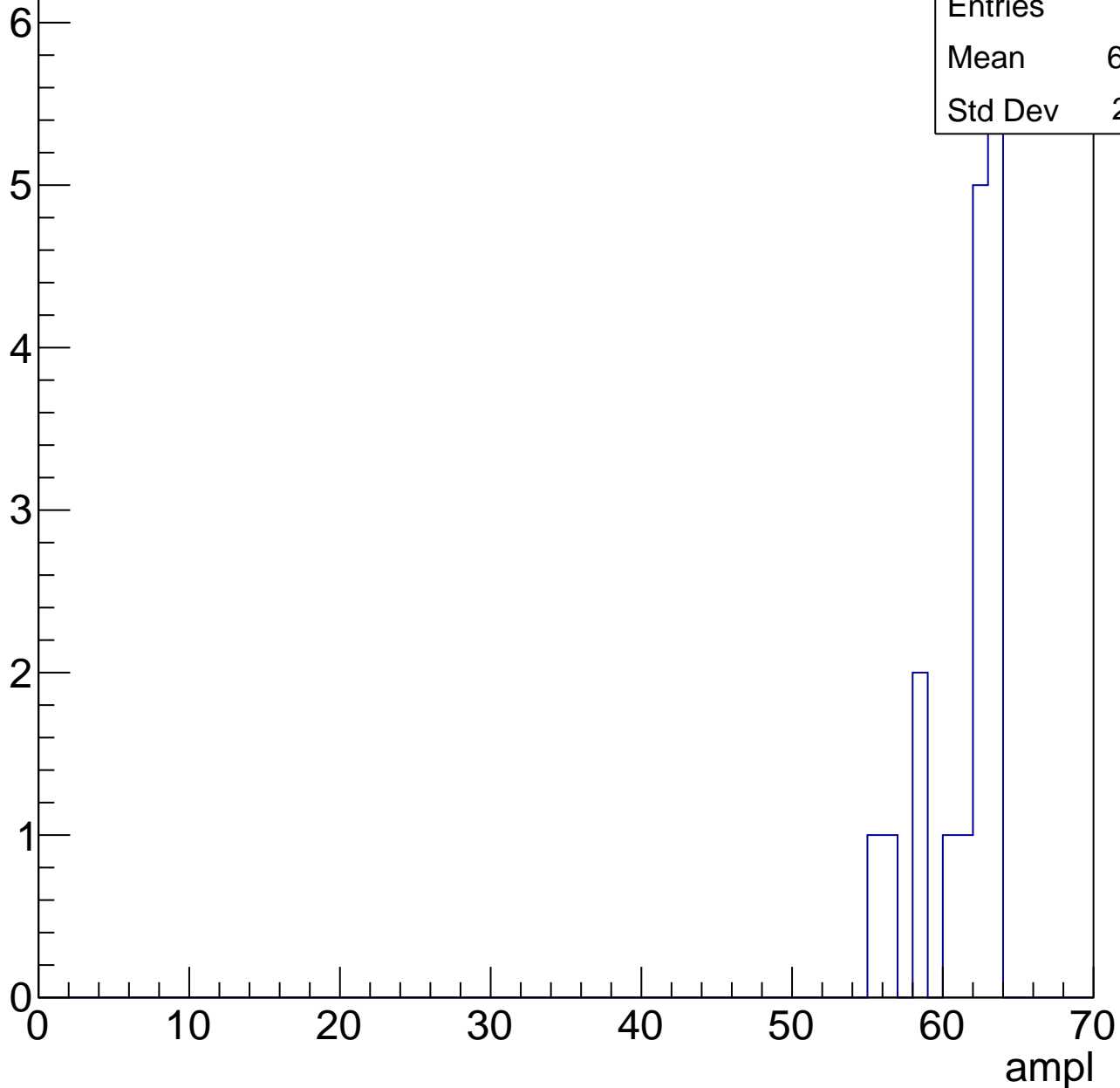
ampl



B1L100S, U13-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch47, adc0

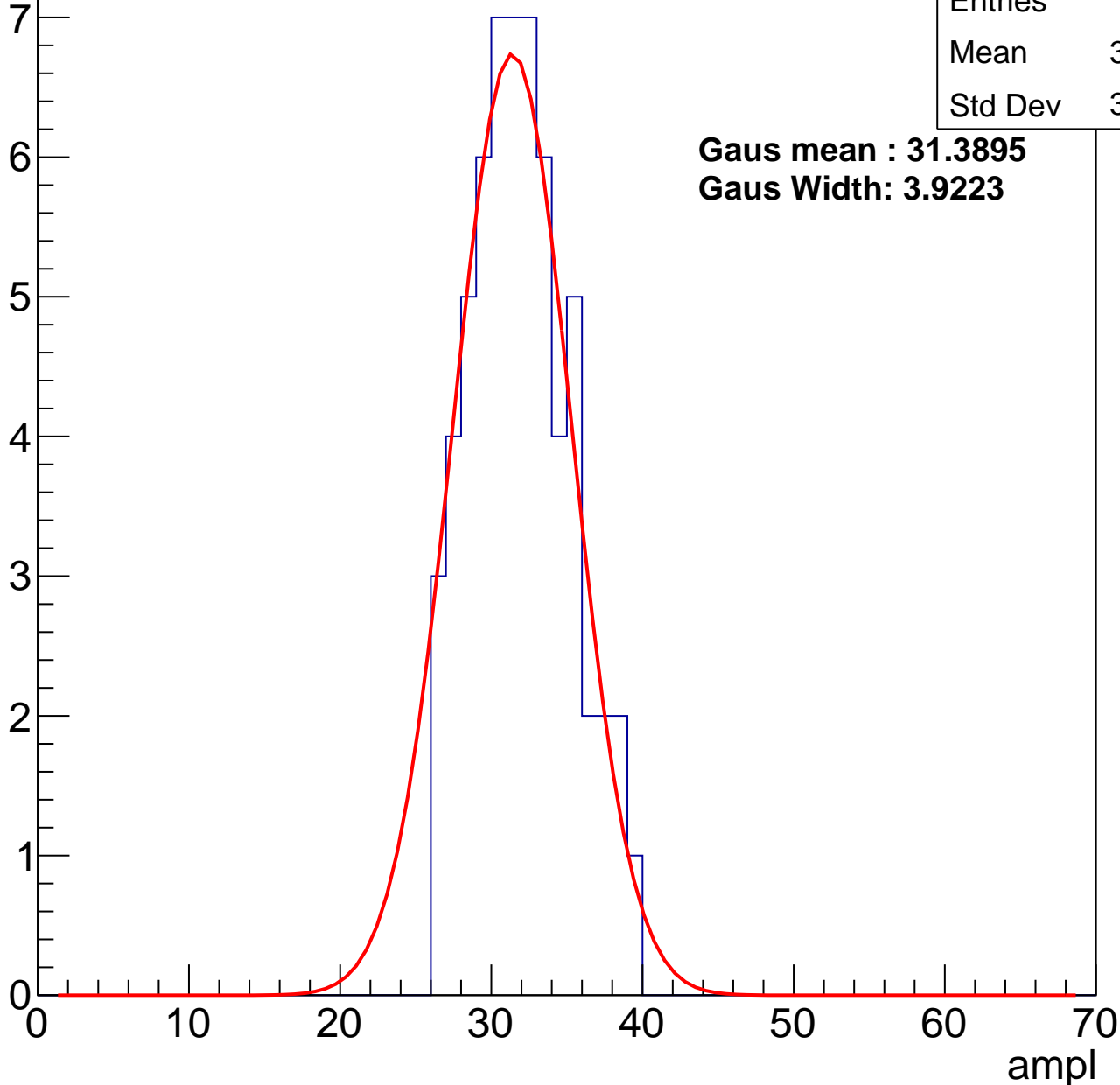
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	31.49
Std Dev	3.217

Gaus mean : 31.3895

Gaus Width: 3.9223



B1L100S, U13-ch47, adc1

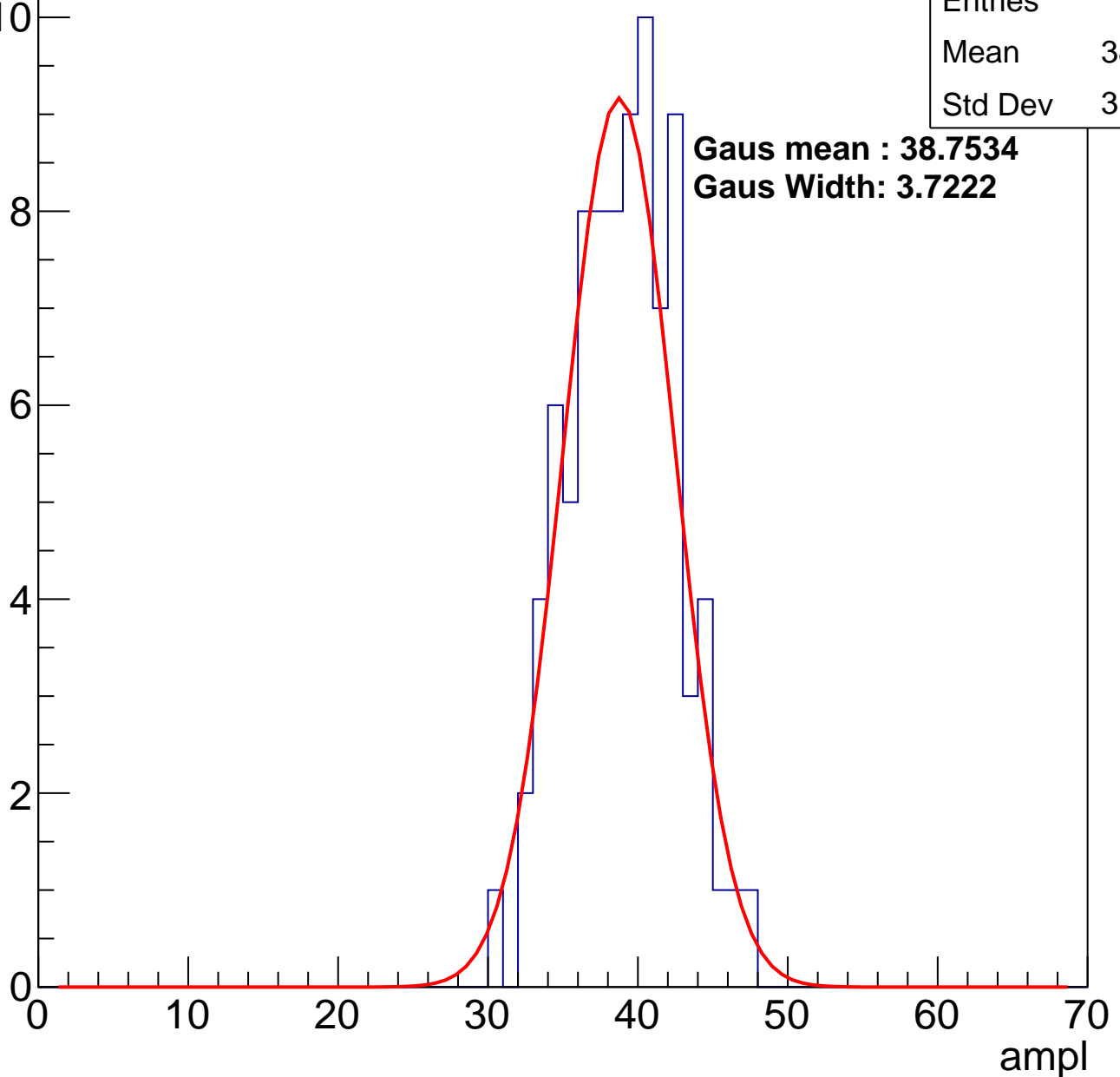
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	38.53
Std Dev	3.493

Gaus mean : 38.7534

Gaus Width: 3.7222



B1L100S, U13-ch47, adc2

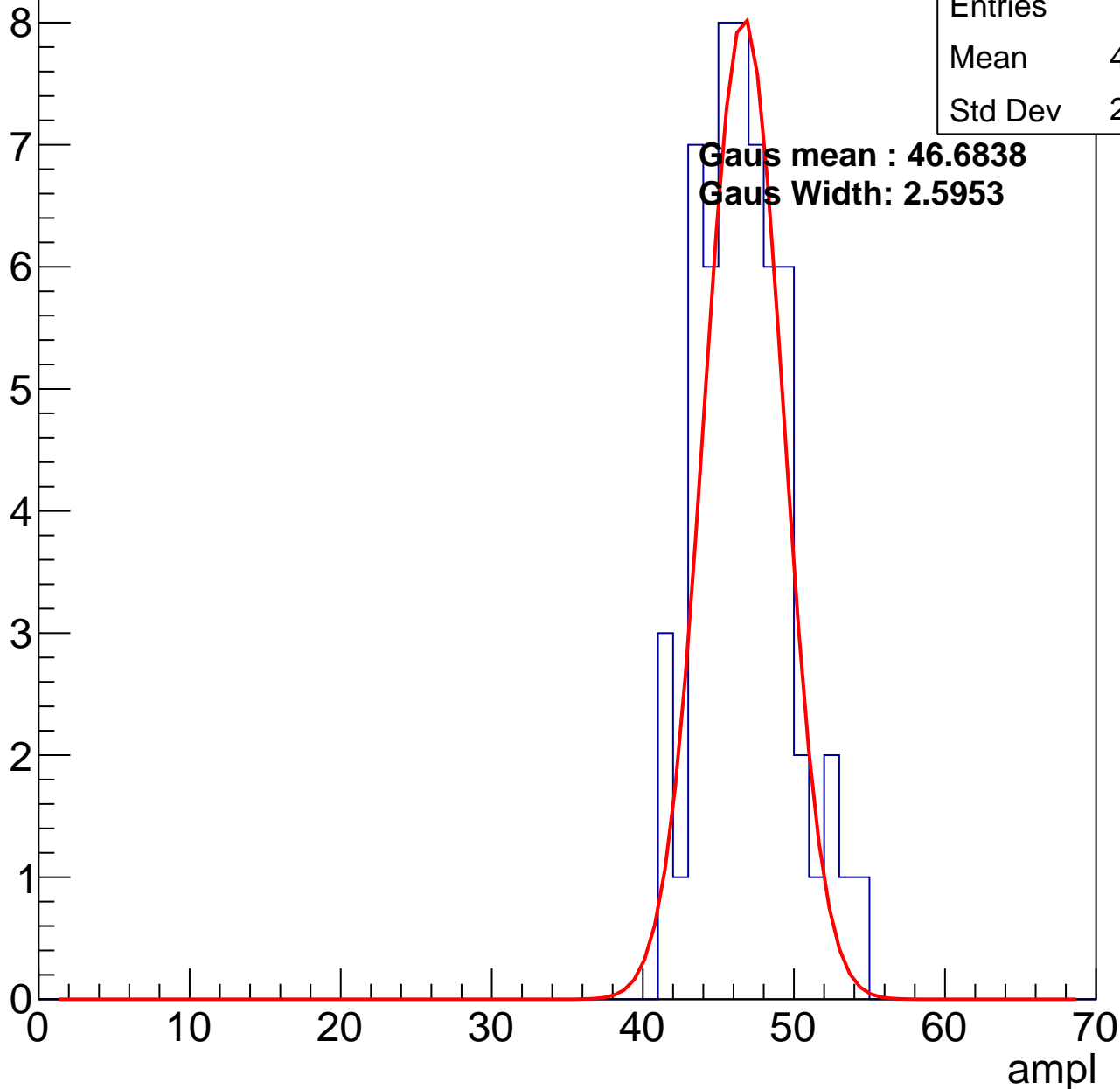
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	46.29
Std Dev	2.935

Gaus mean : 46.6838

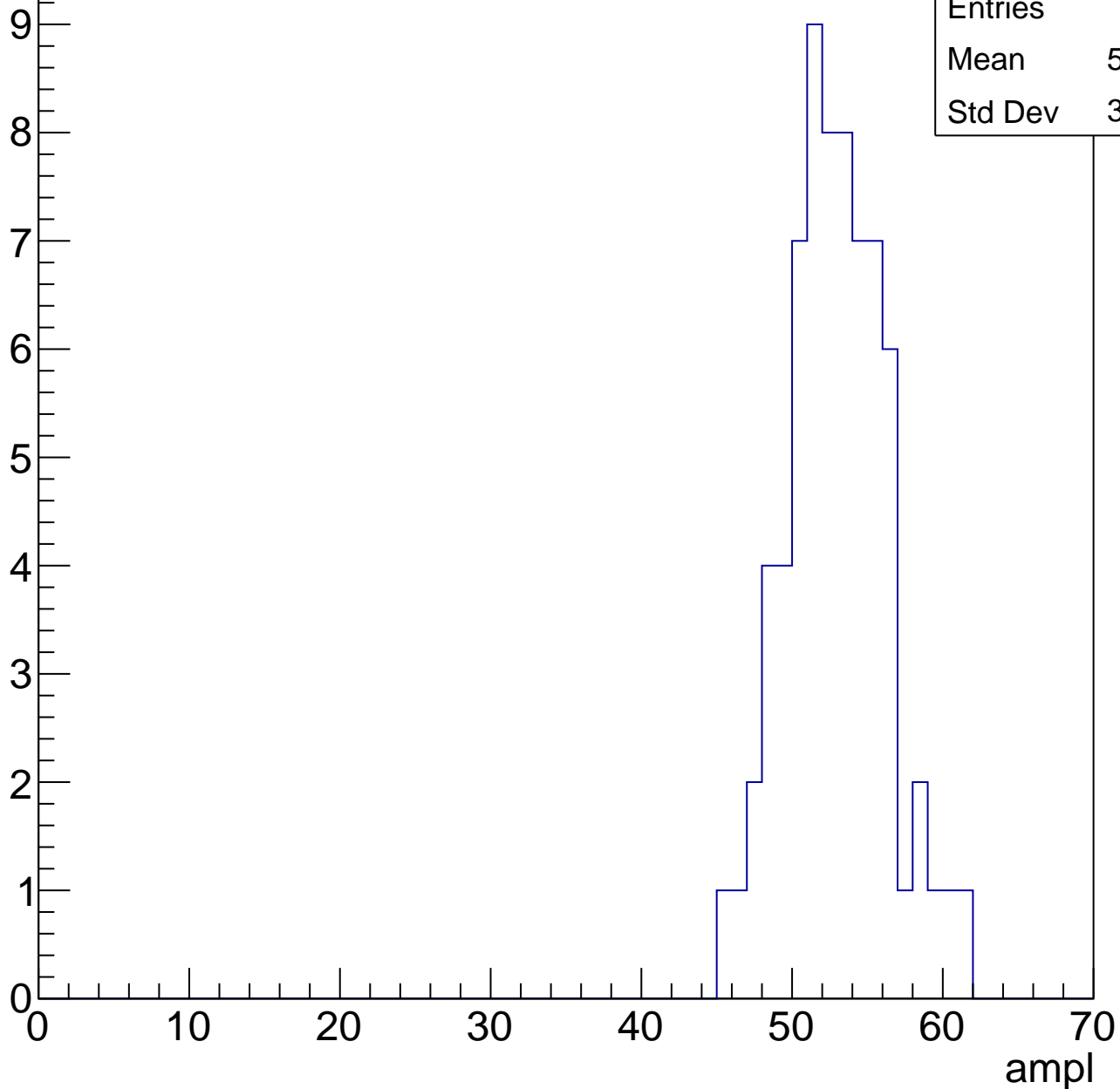
Gaus Width: 2.5953



B1L100S, U13-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

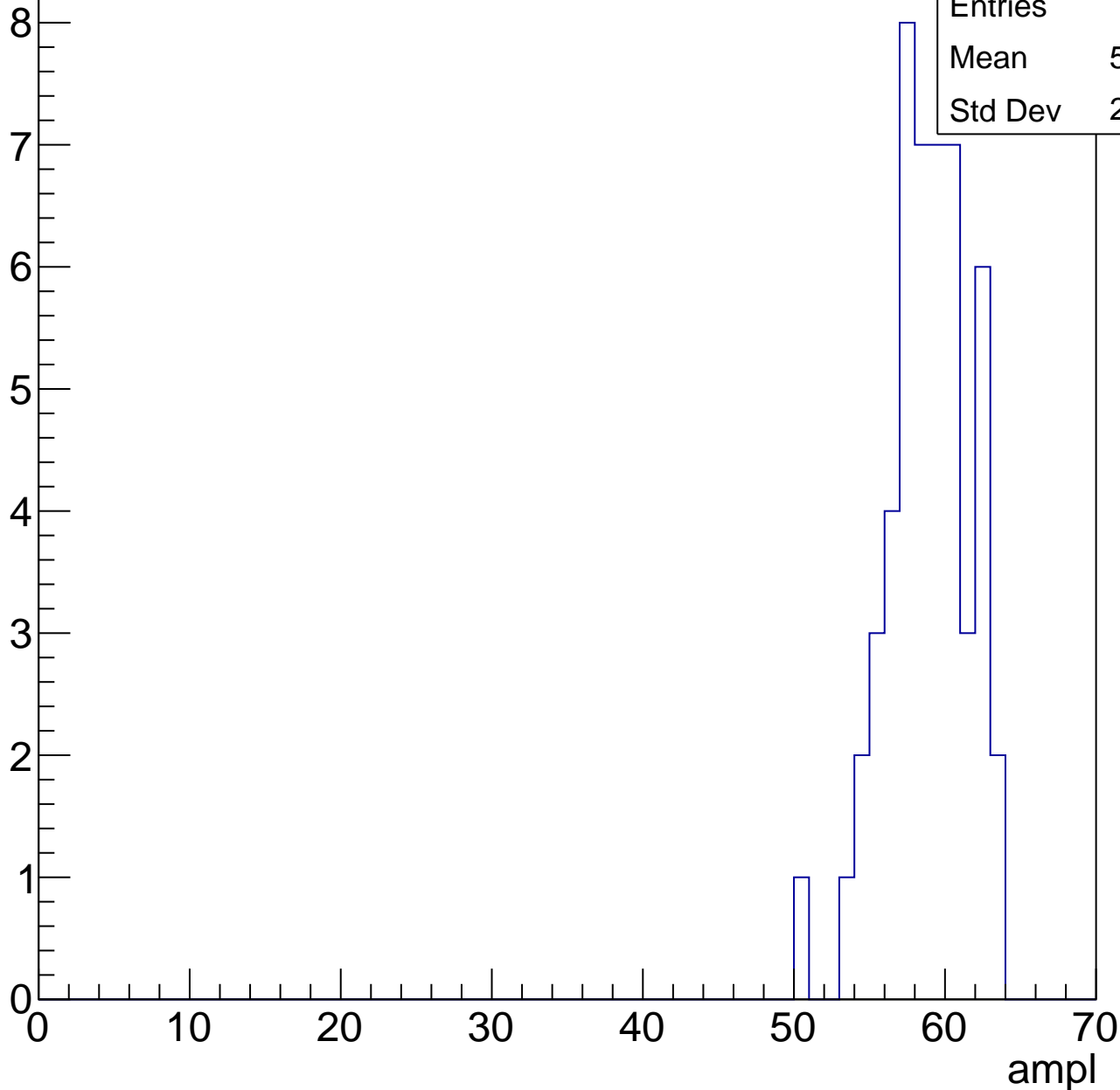
Entry



B1L100S, U13-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch47, adc5

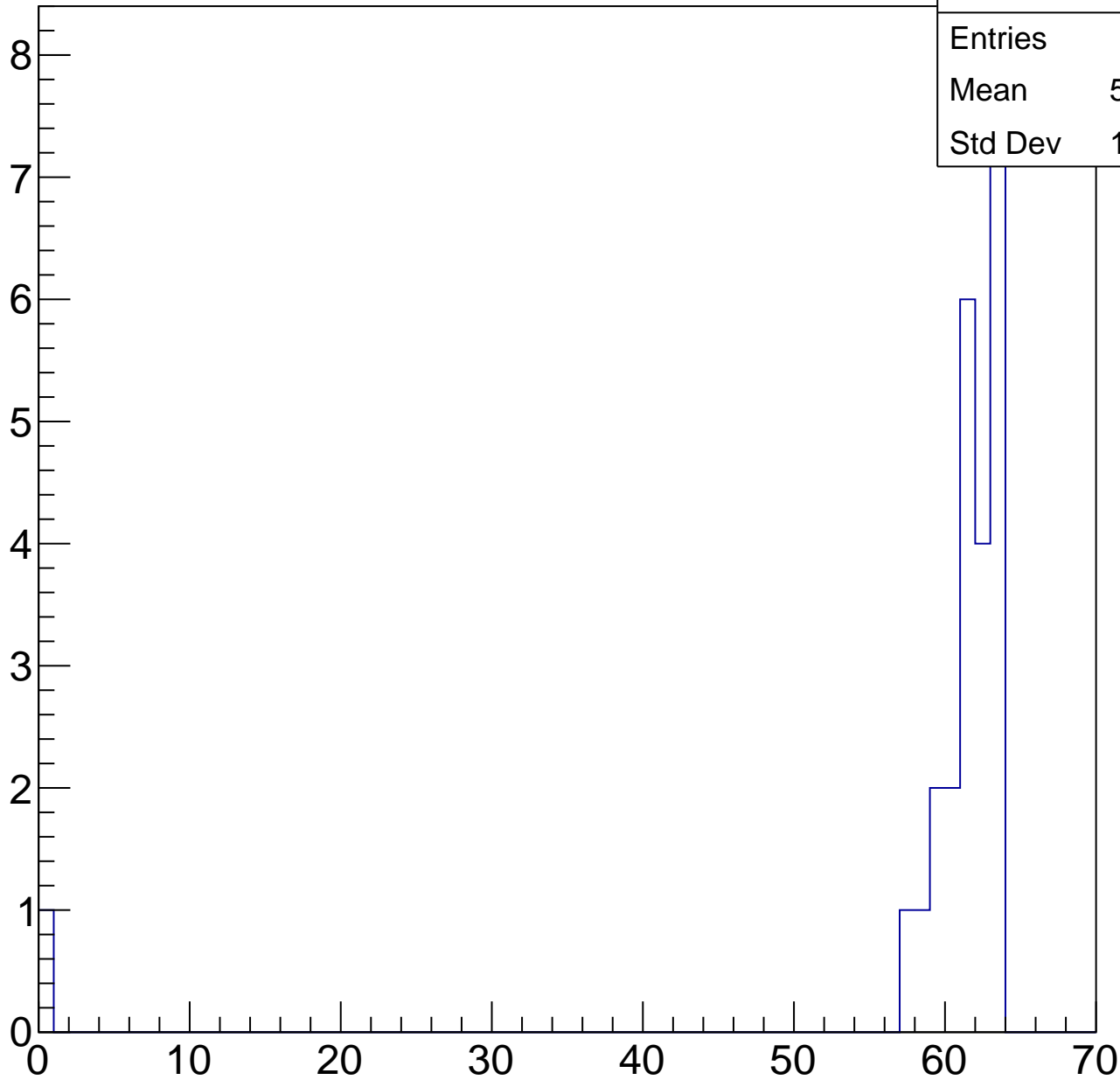
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	25
Mean	58.84
Std Dev	12.12

ampl



B1L100S, U13-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch47, adc7

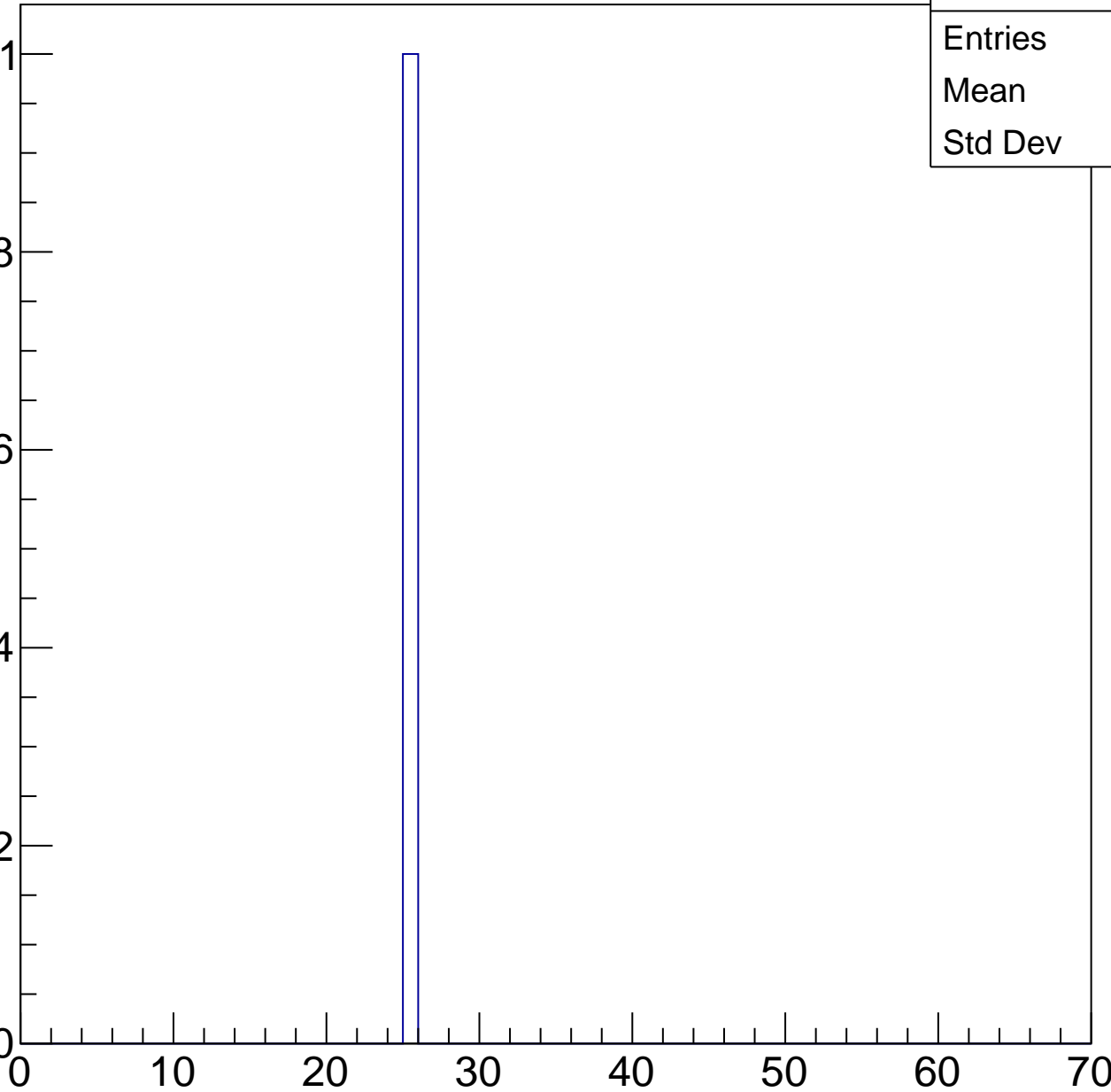
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L100S, U13-ch48, adc0

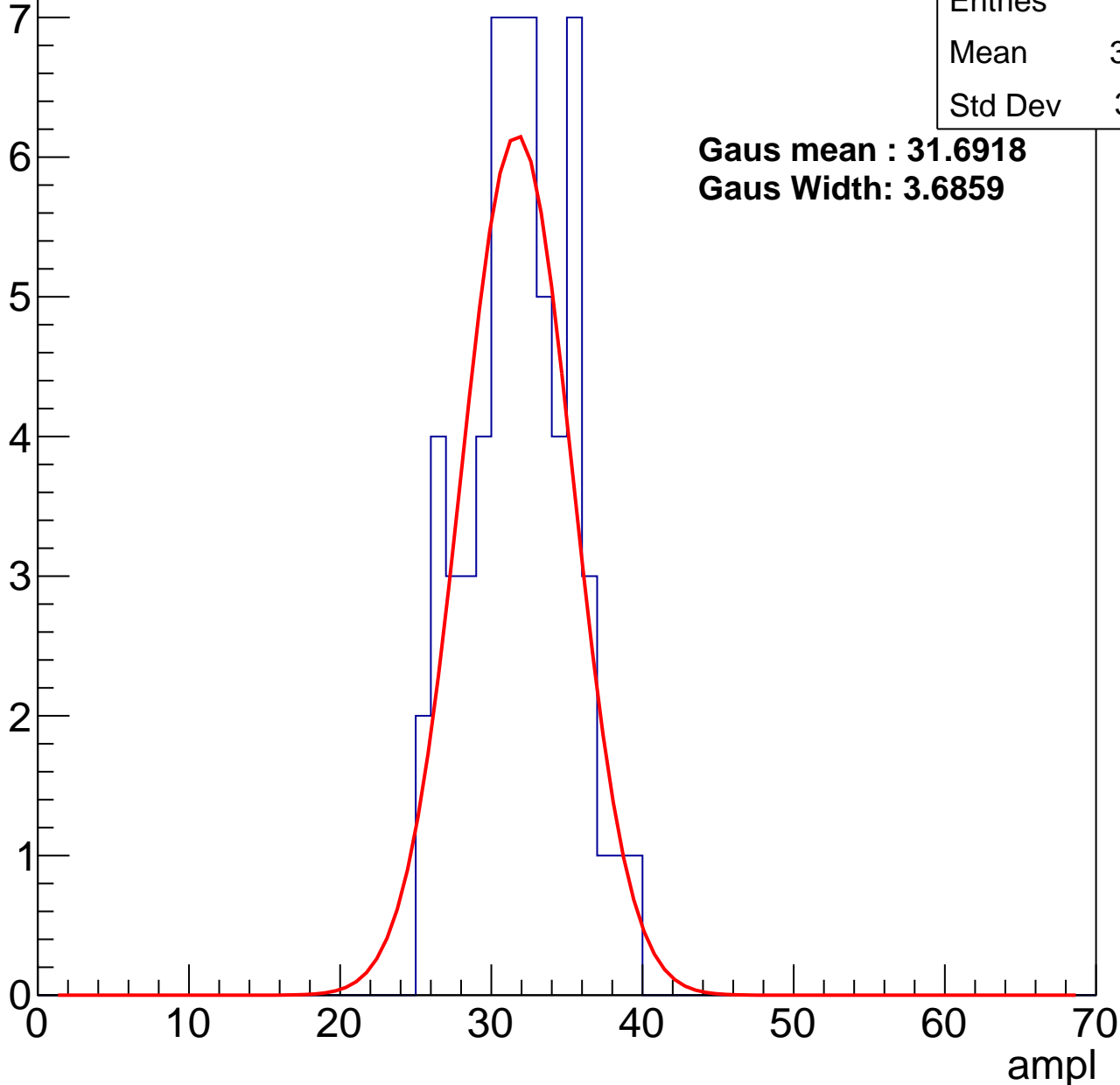
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	31.42
Std Dev	3.341

Gaus mean : 31.6918

Gaus Width: 3.6859



B1L100S, U13-ch48, adc1

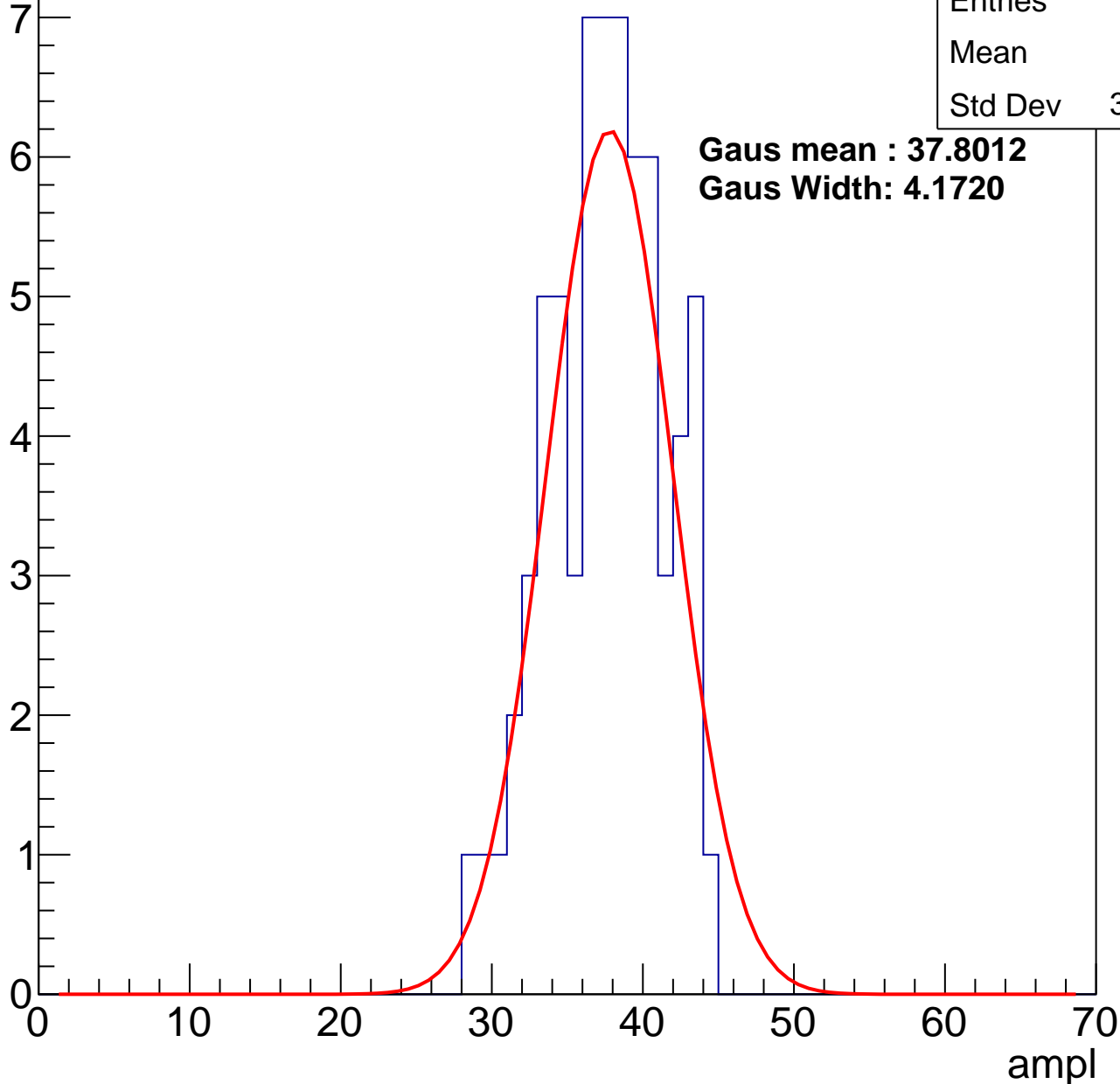
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.1
Std Dev	3.758

Gaus mean : 37.8012

Gaus Width: 4.1720



B1L100S, U13-ch48, adc2

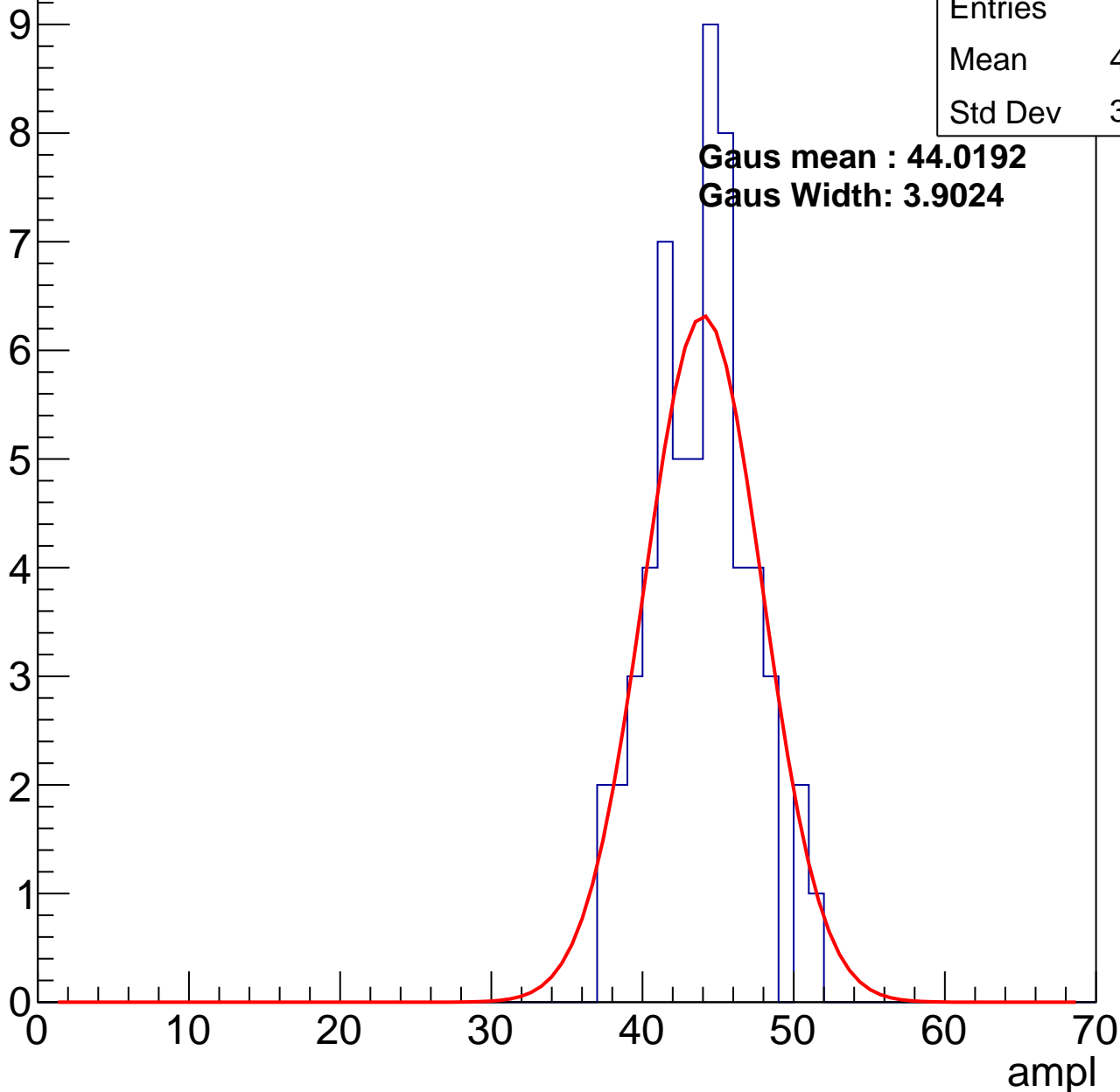
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.42
Std Dev	3.206

Gaus mean : 44.0192

Gaus Width: 3.9024



B1L100S, U13-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	50.83
Std Dev	3.366

Entry

10

8

6

4

2

0

0

10

20

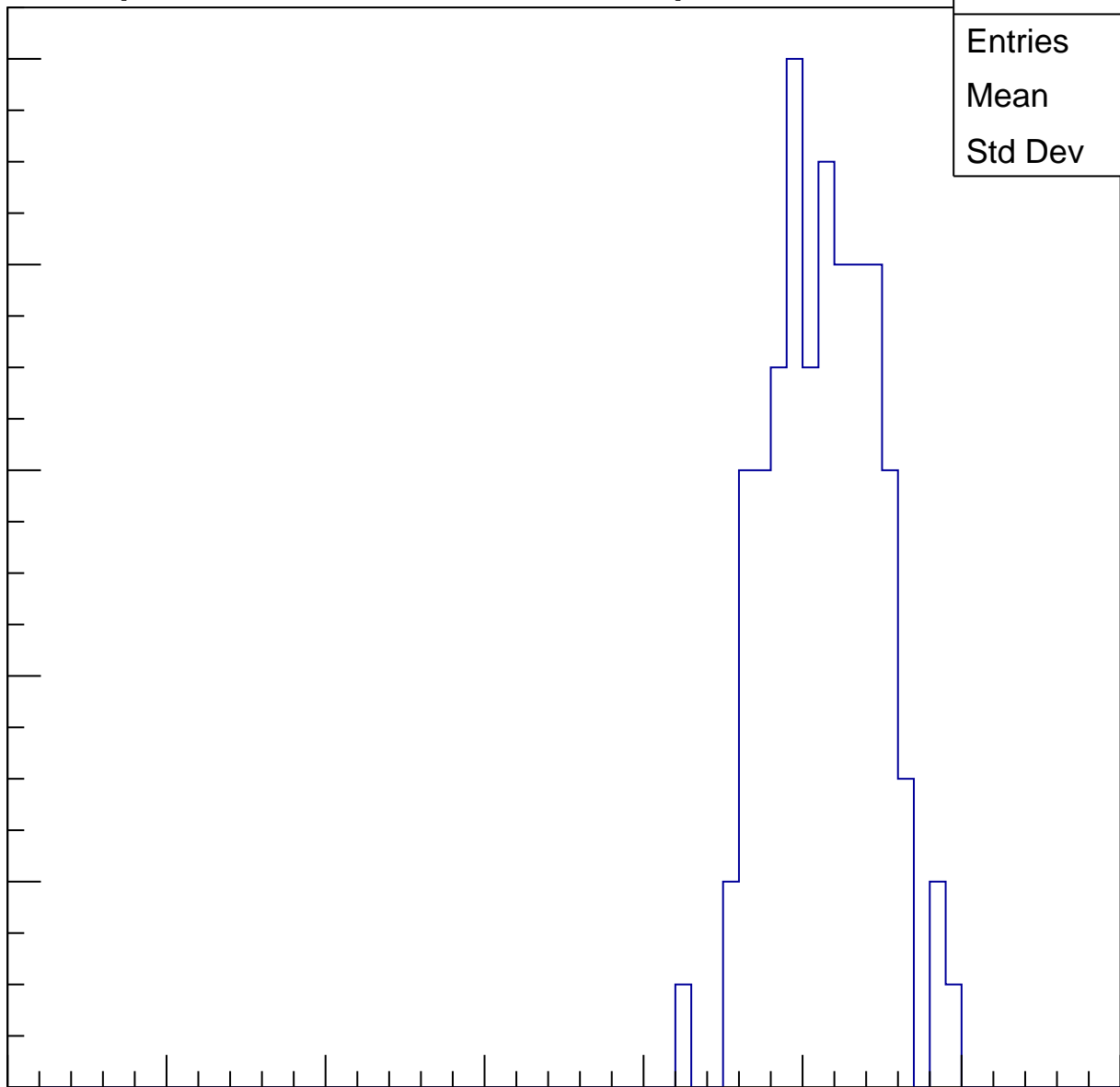
30

40

50

60

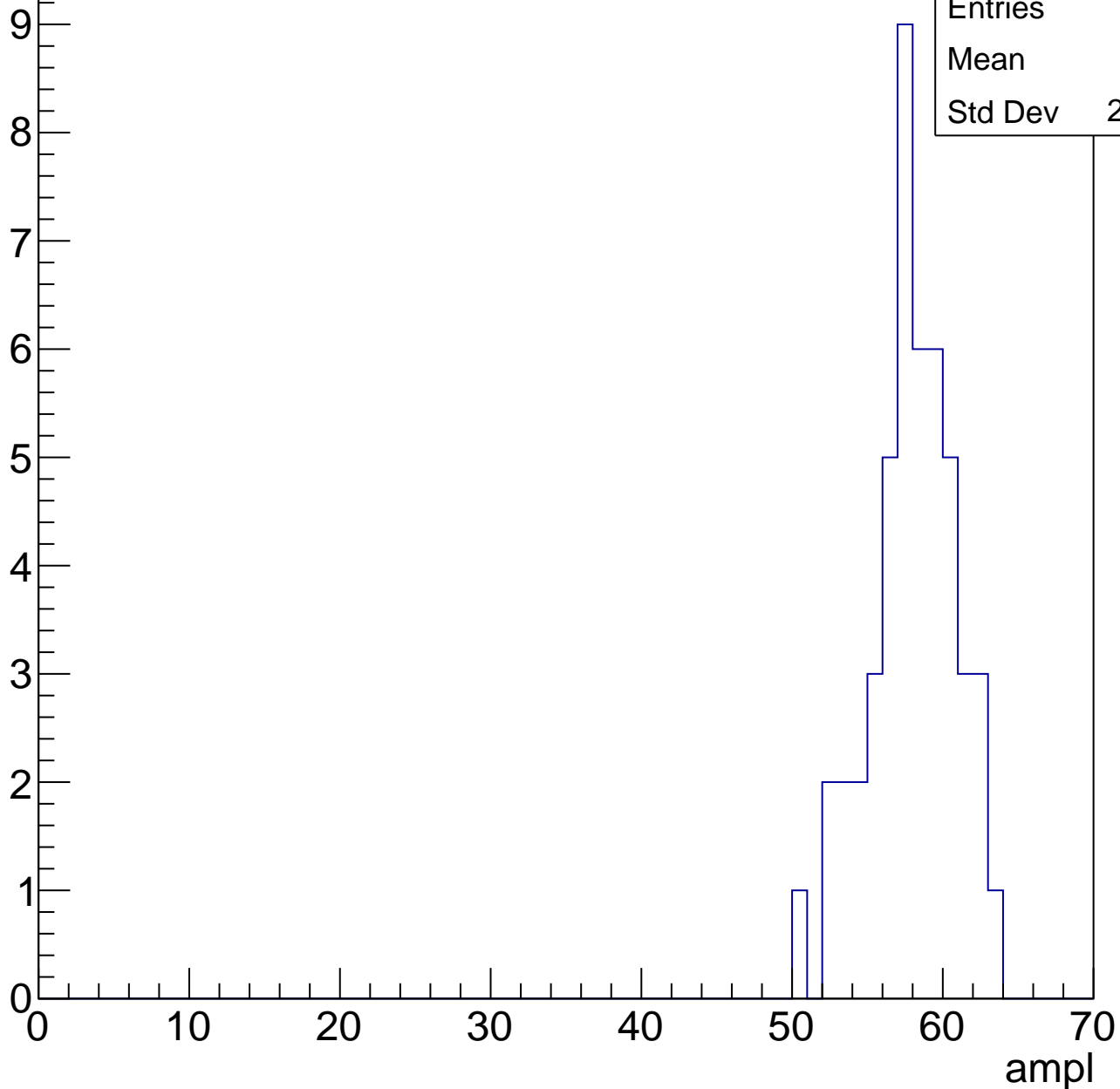
ampl



B1L100S, U13-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



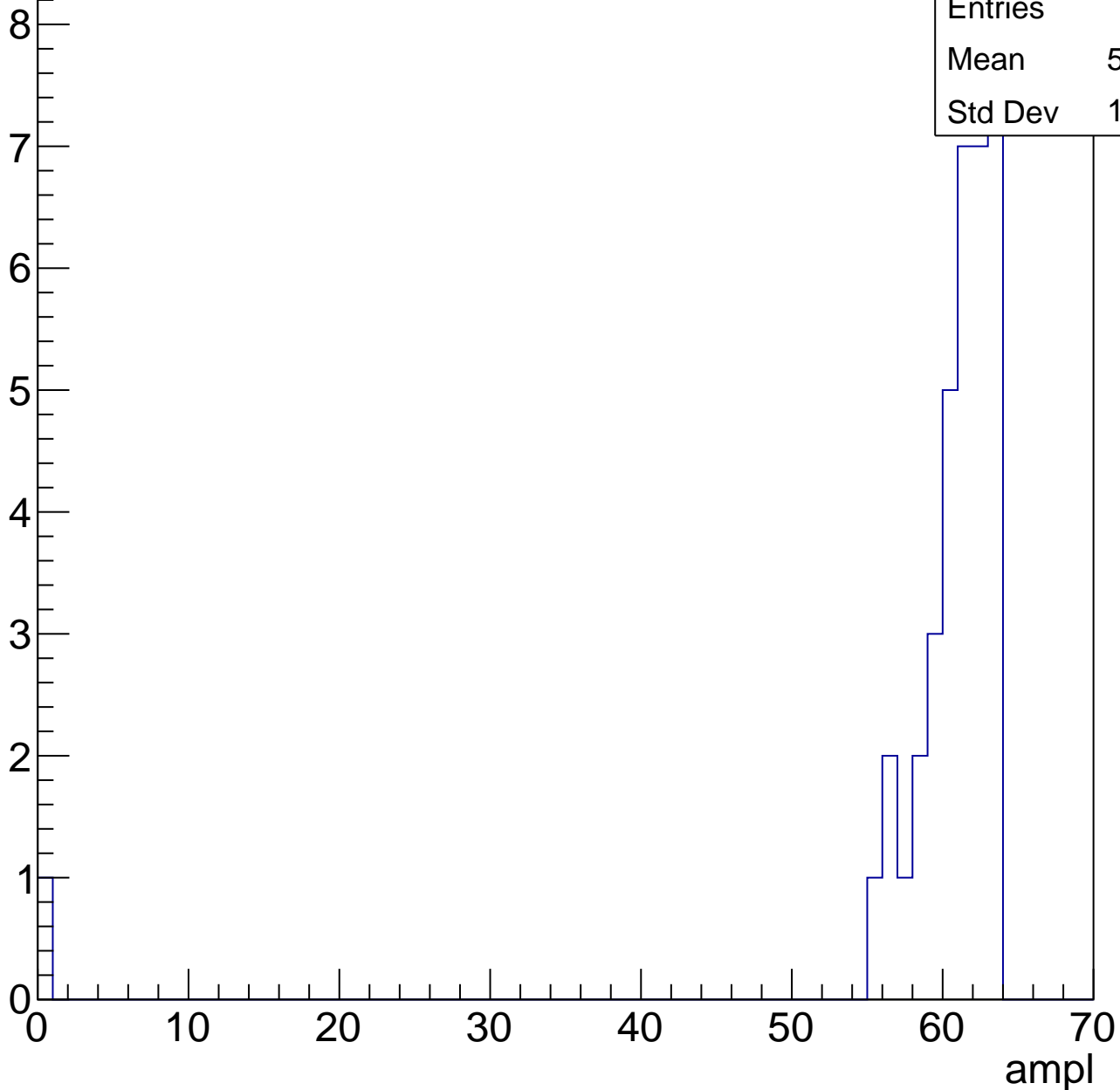
Entries	48
Mean	57.5
Std Dev	2.843

B1L100S, U13-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.97
Std Dev	10.06



B1L100S, U13-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch49, adc0

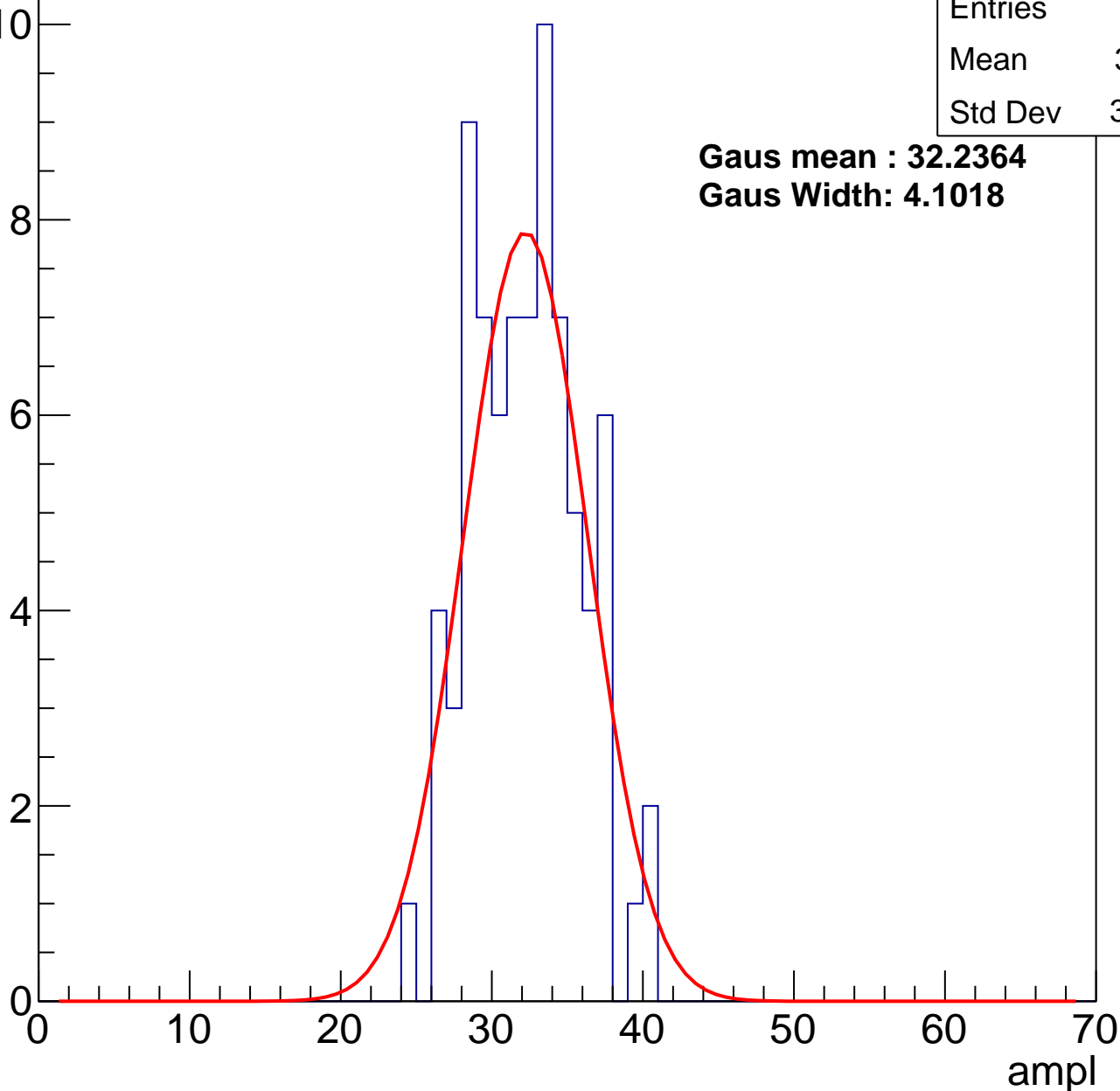
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	31.81
Std Dev	3.544

Gaus mean : 32.2364

Gaus Width: 4.1018



B1L100S, U13-ch49, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	38.92
Std Dev	3.774

Gaus mean : 39.3020

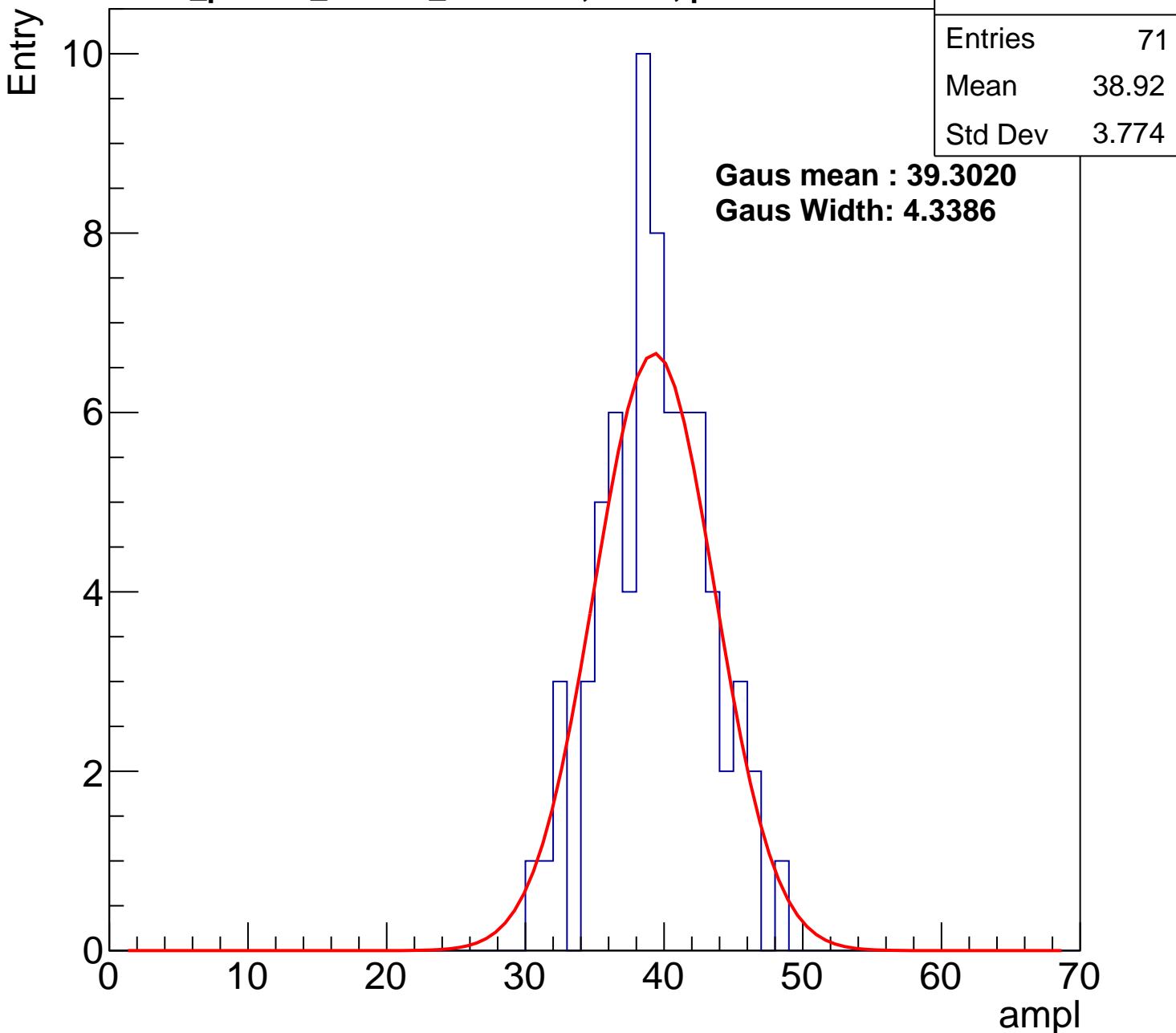
Gaus Width: 4.3386

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch49, adc2

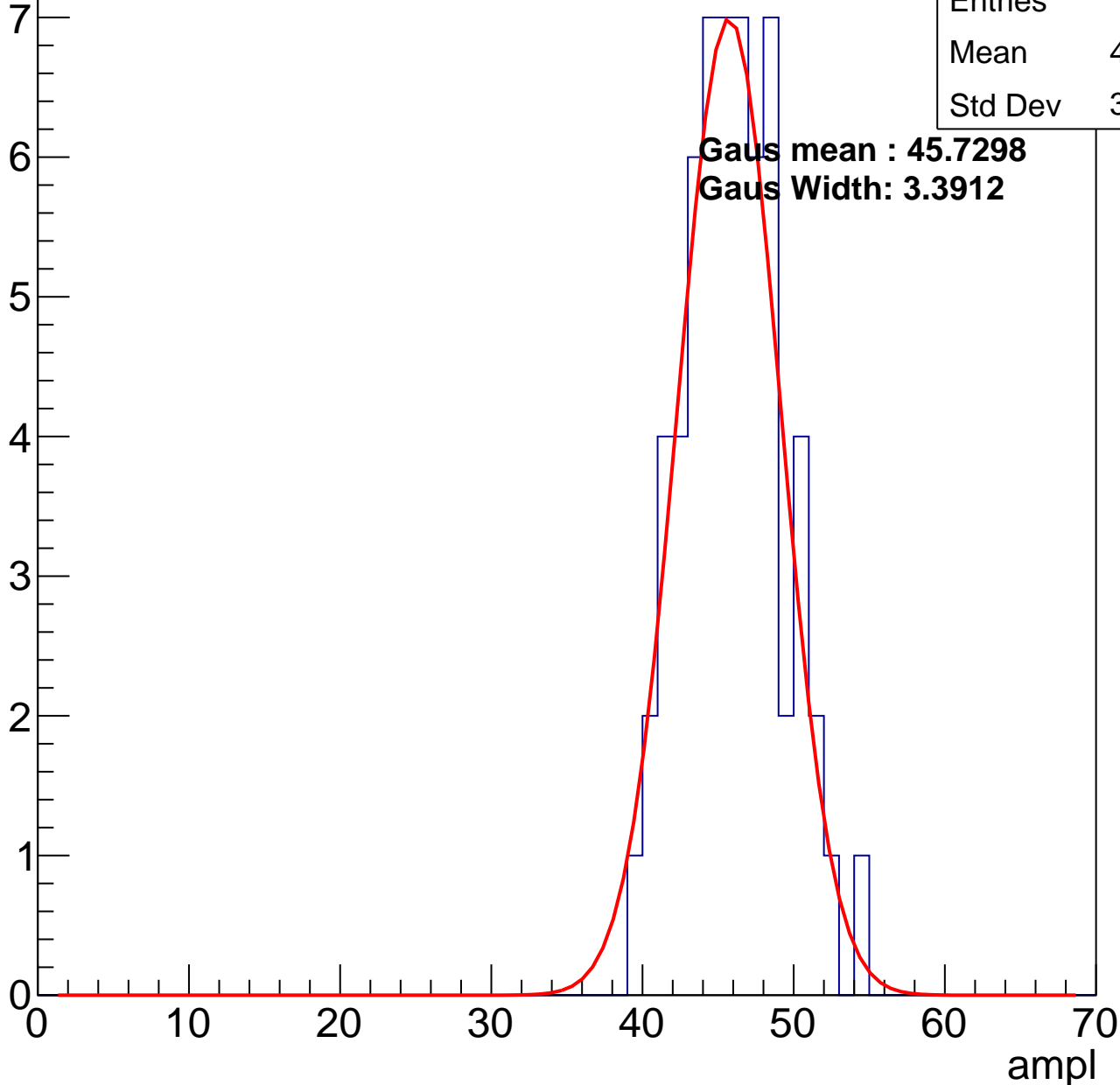
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	45.54
Std Dev	3.206

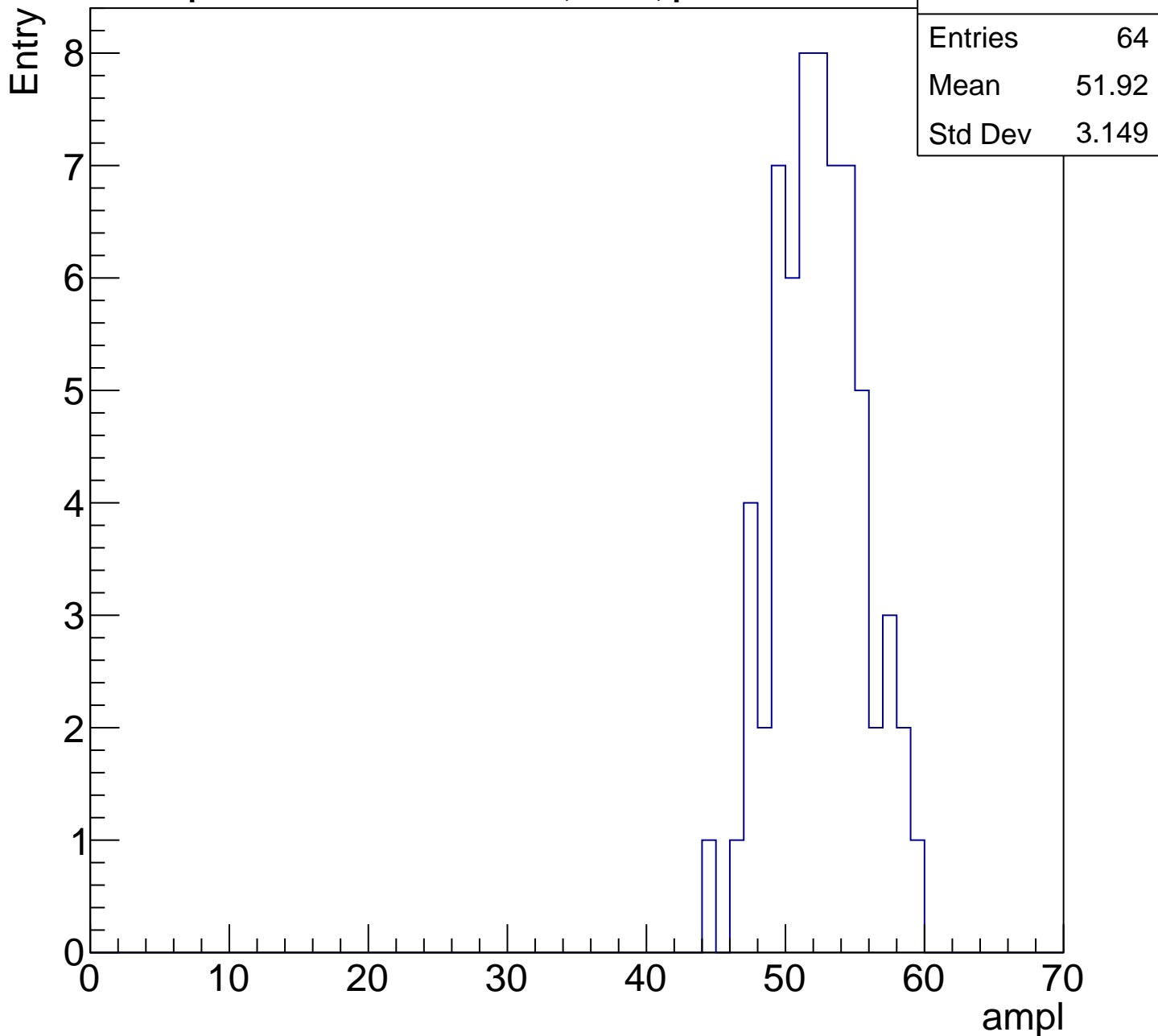
Gaus mean : 45.7298

Gaus Width: 3.3912



B1L100S, U13-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

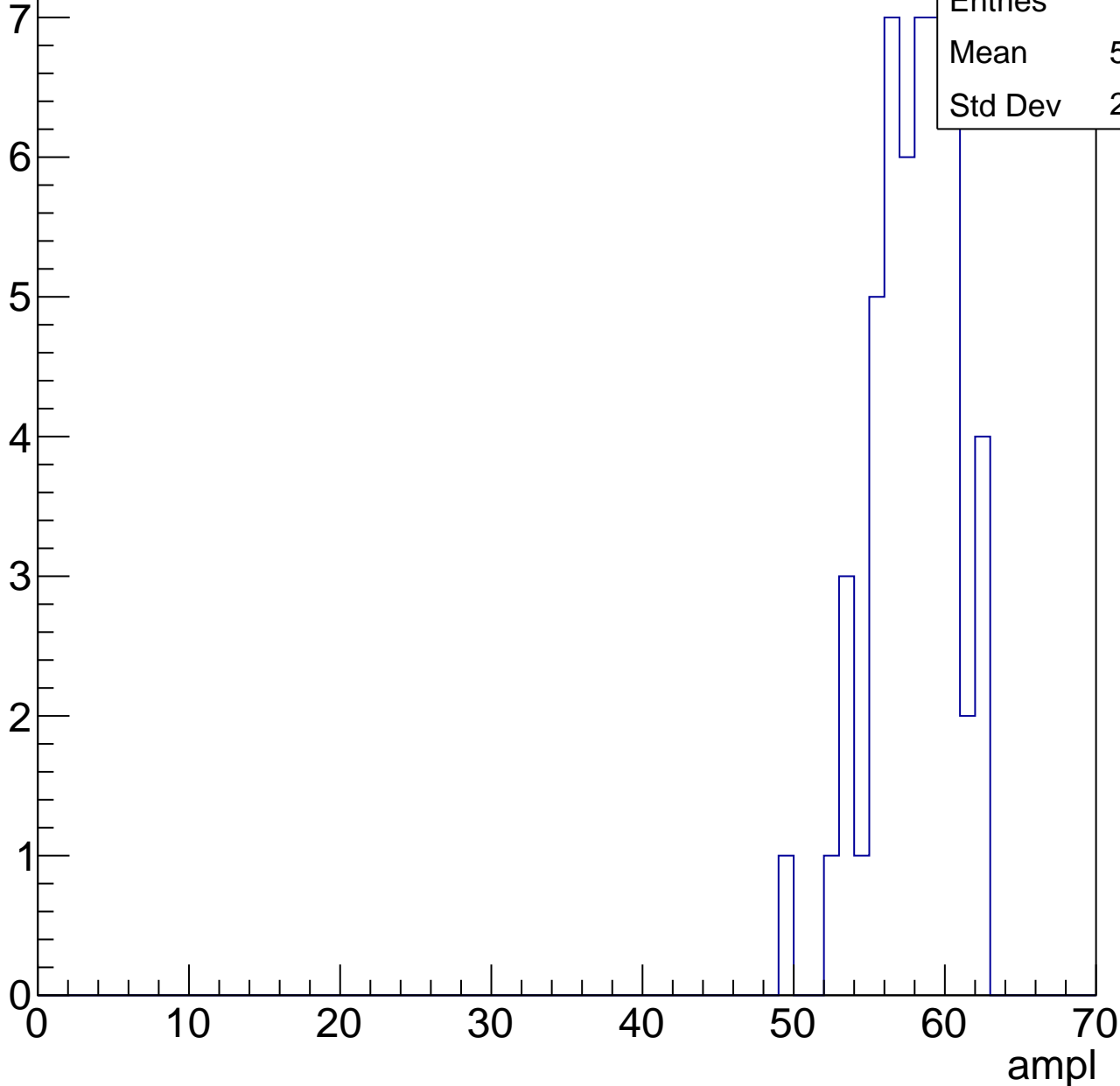


B1L100S, U13-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	57.49
Std Dev	2.768

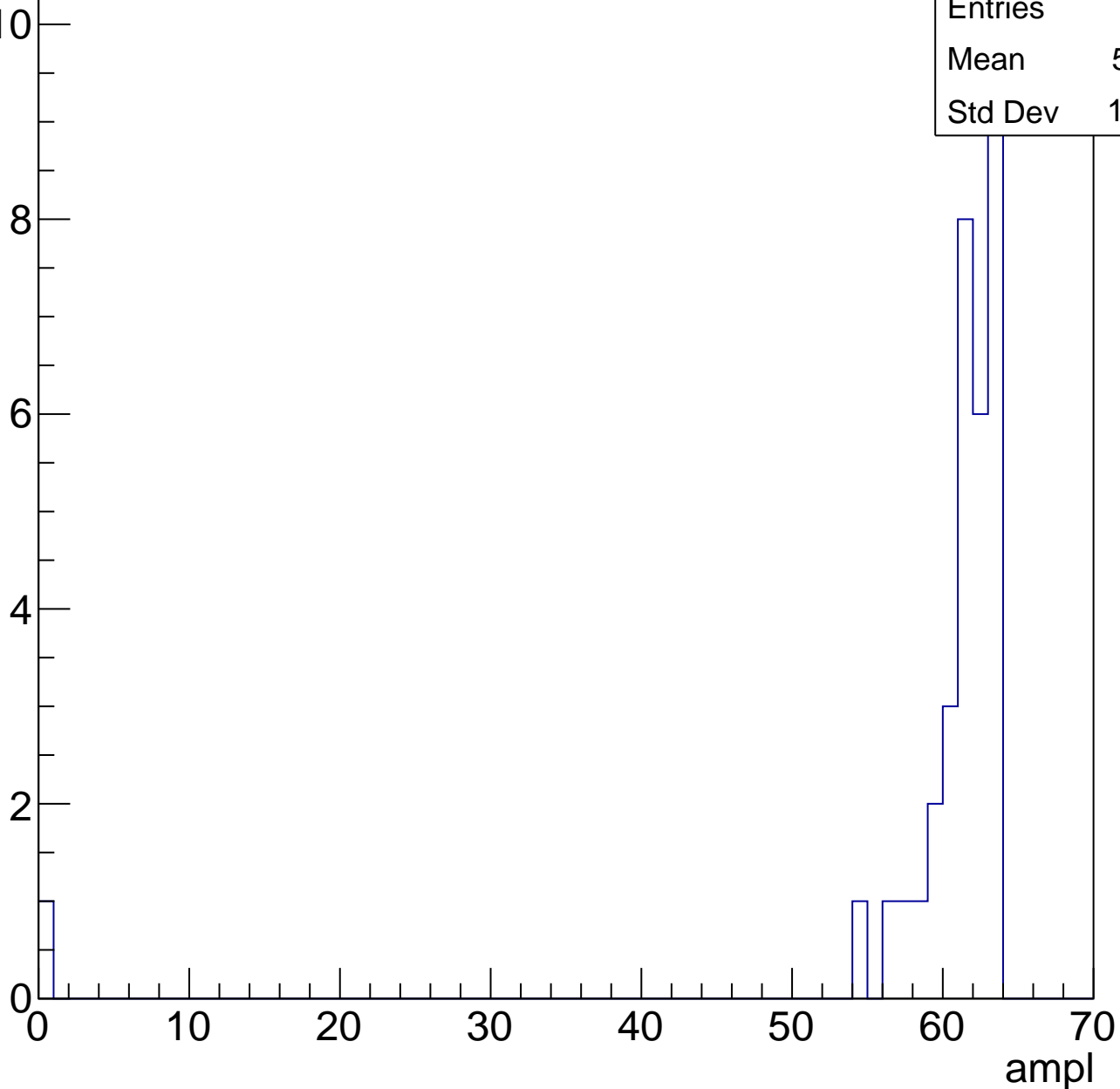


B1L100S, U13-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59.21
Std Dev	10.53



B1L100S, U13-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch50, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	101
Mean	32.02
Std Dev	6.124

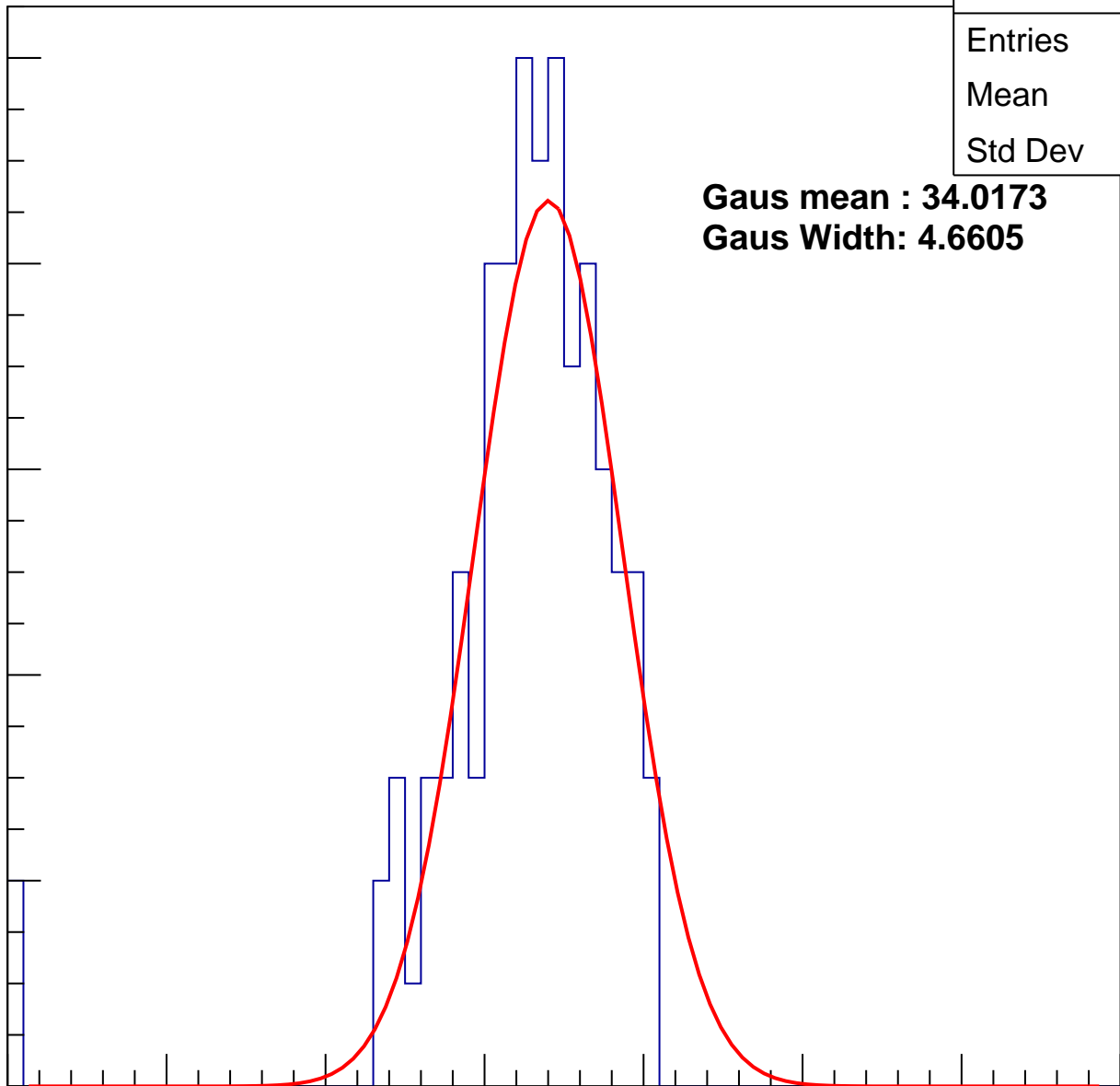
Gaus mean : 34.0173

Gaus Width: 4.6605

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch50, adc1

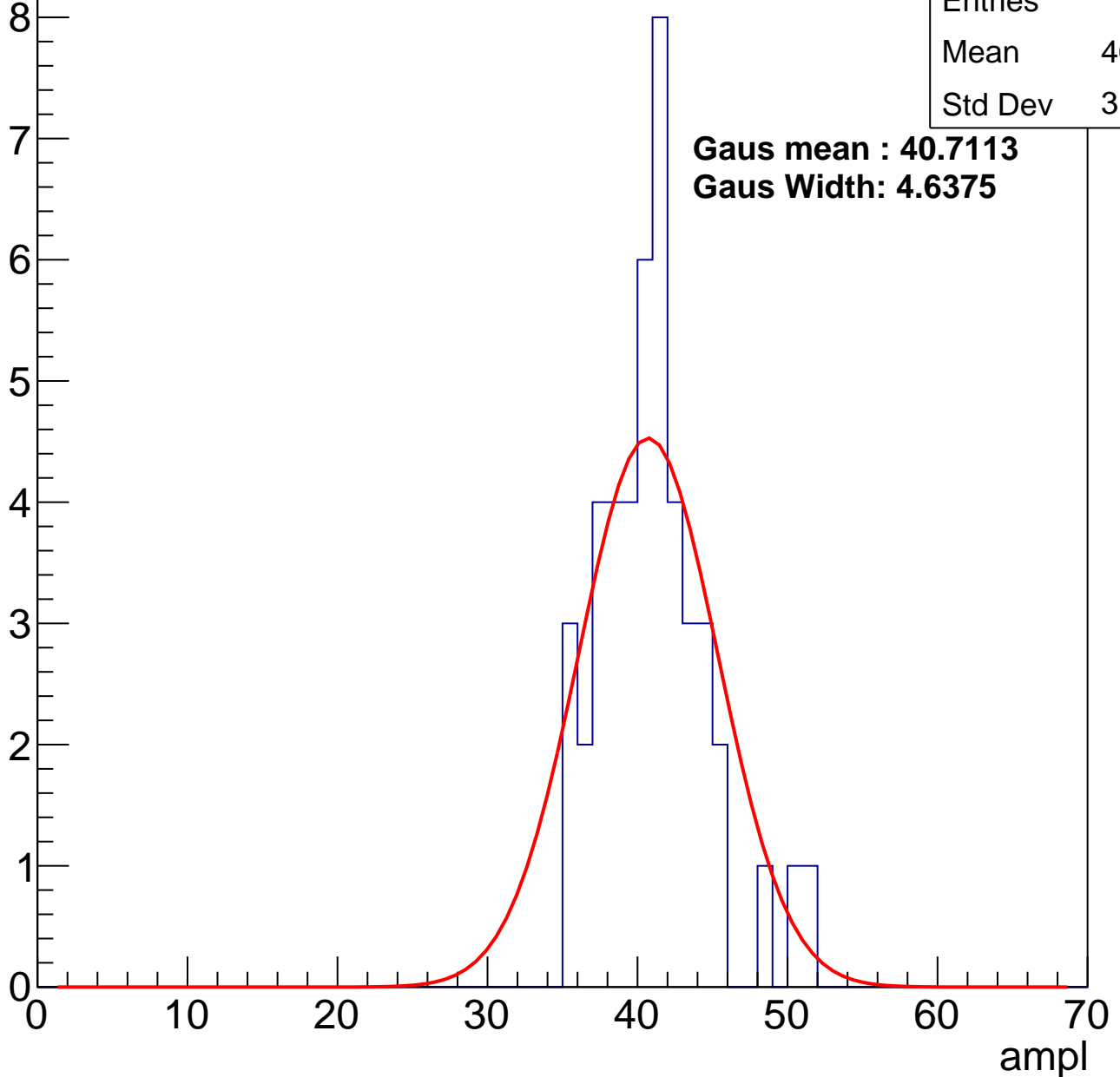
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	40.63
Std Dev	3.547

Gaus mean : 40.7113

Gaus Width: 4.6375



B1L100S, U13-ch50, adc2

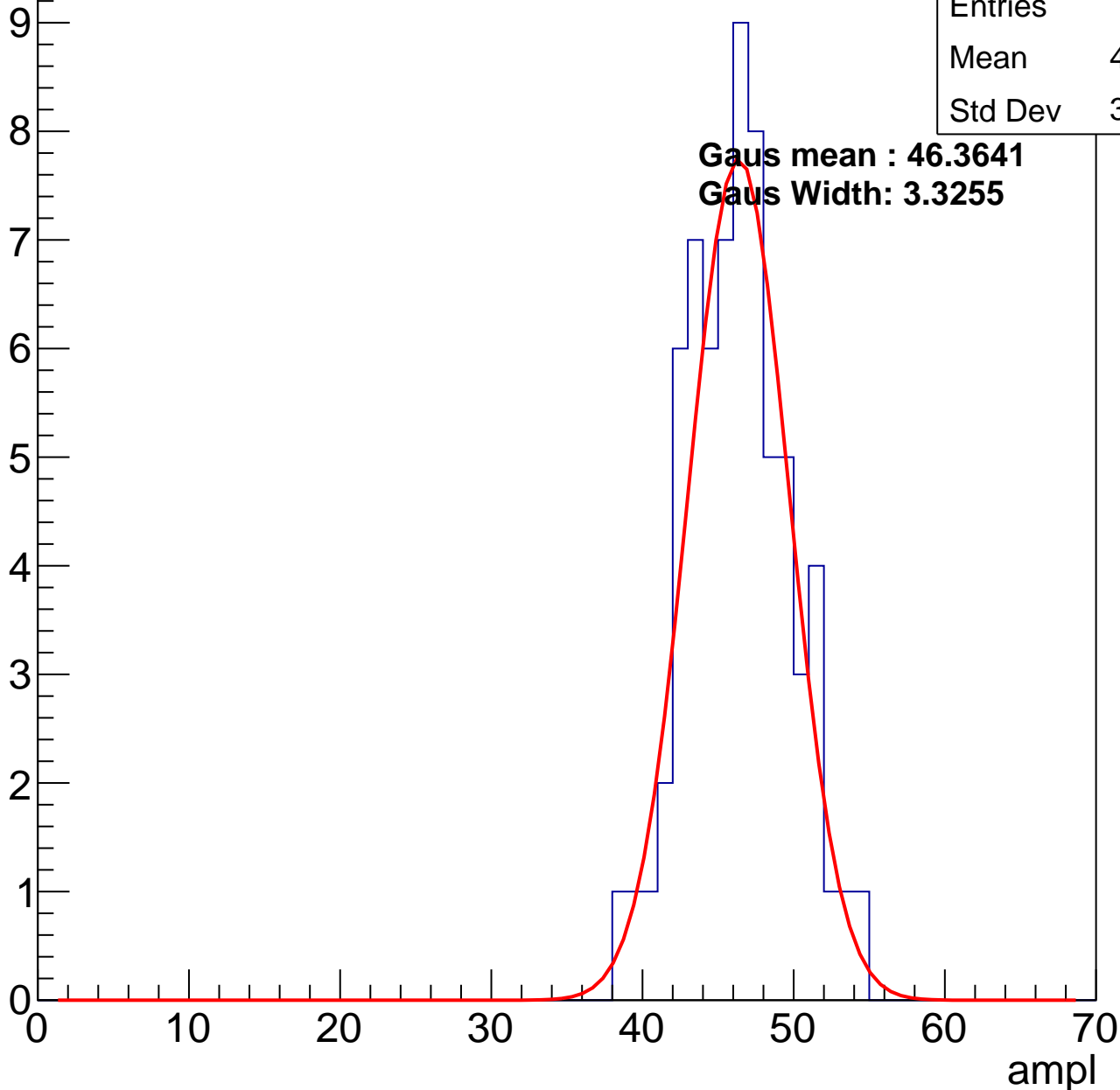
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	45.87
Std Dev	3.338

Gaus mean : 46.3641

Gaus Width: 3.3255

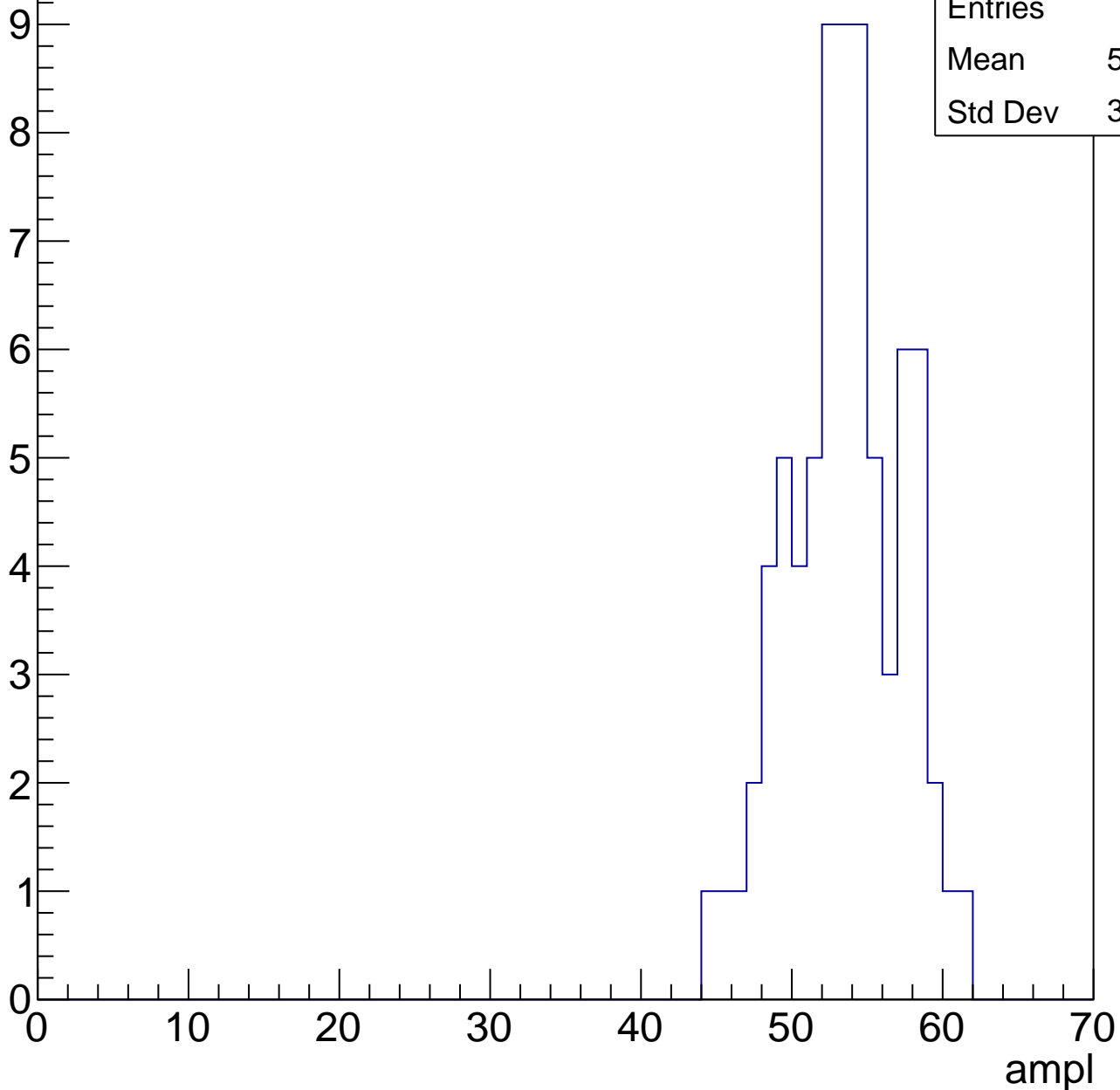


B1L100S, U13-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

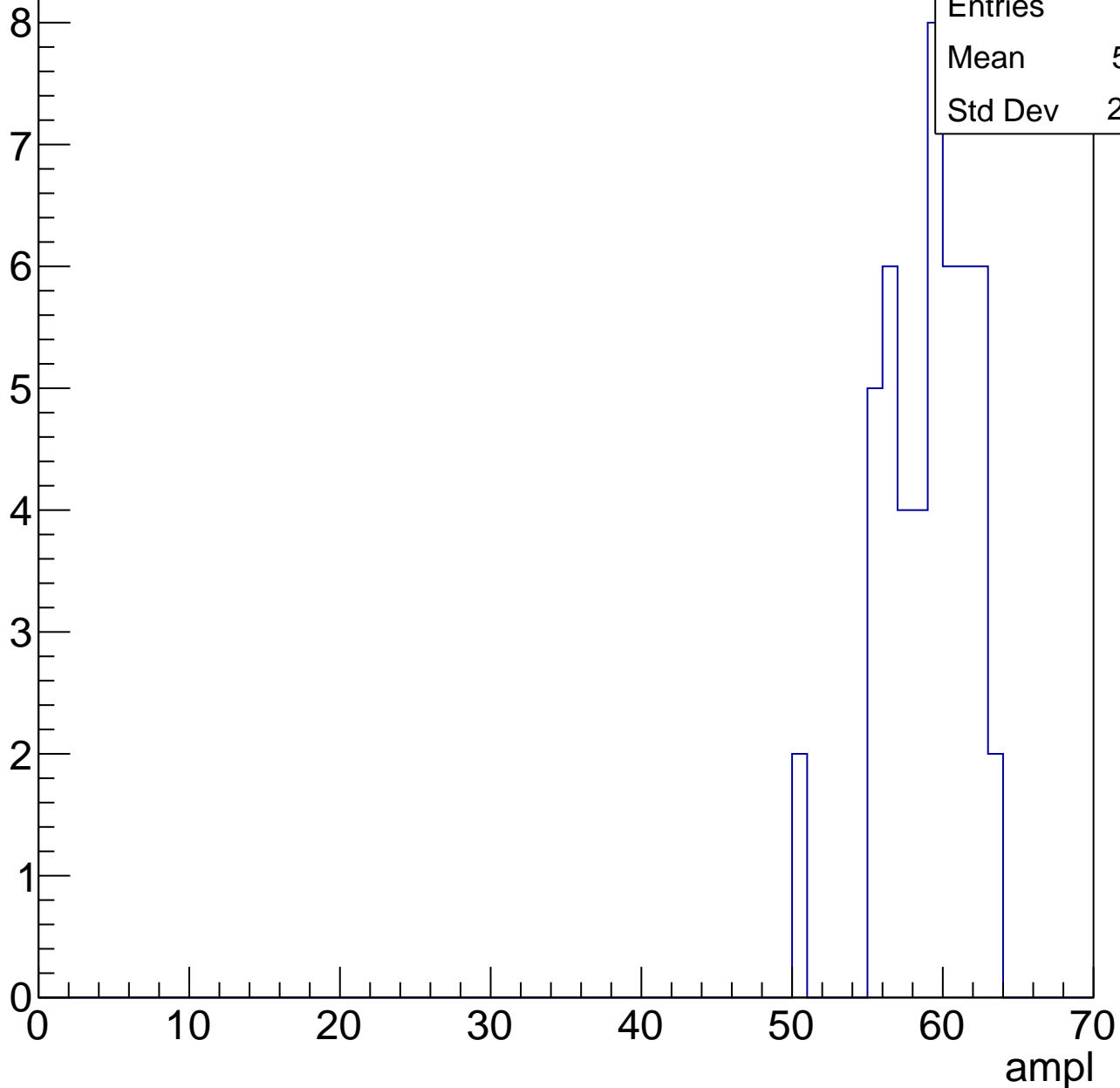
Entries	74
Mean	53.03
Std Dev	3.672



B1L100S, U13-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

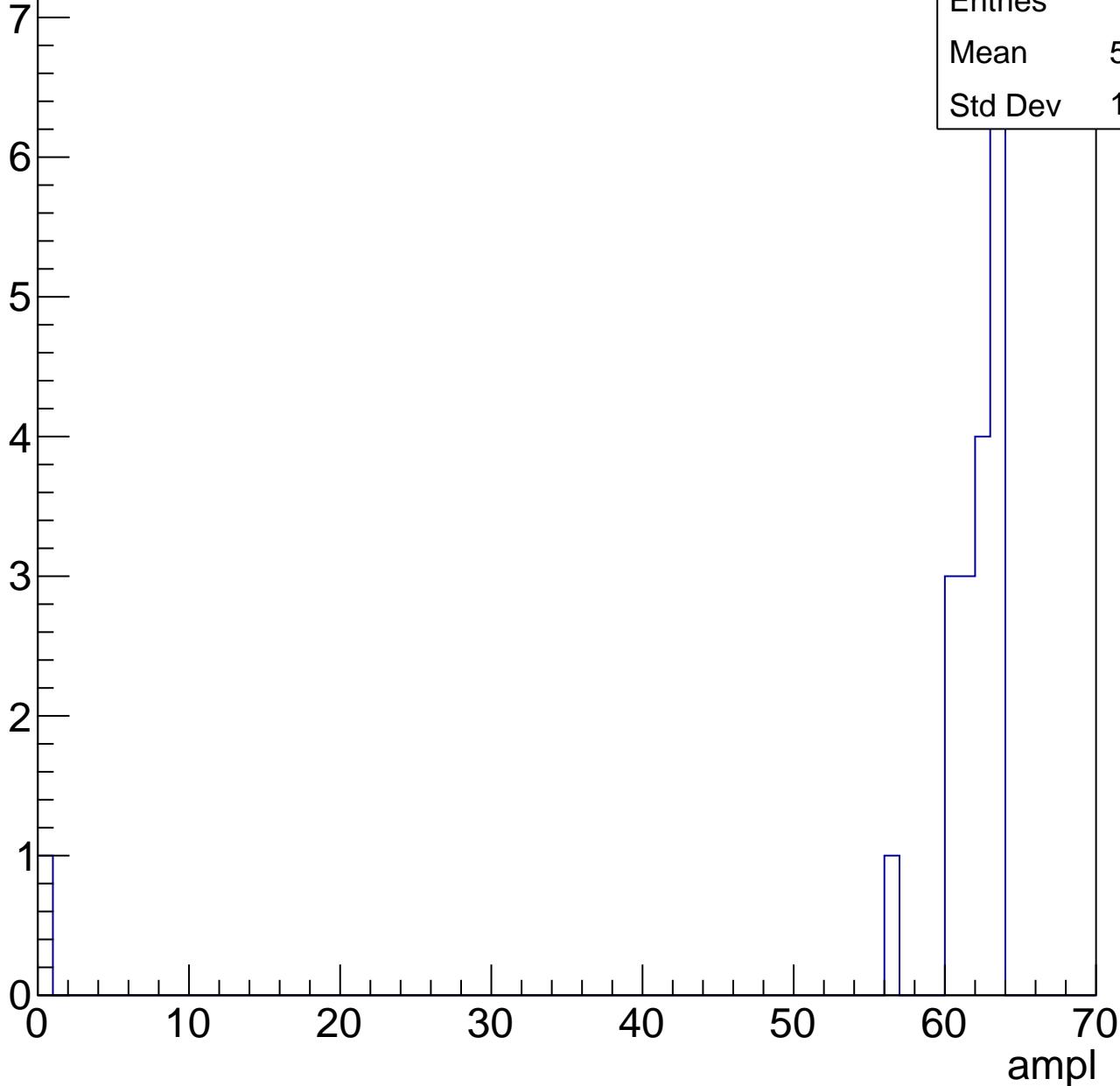


B1L100S, U13-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	58.32
Std Dev	13.85



B1L100S, U13-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch51, adc0

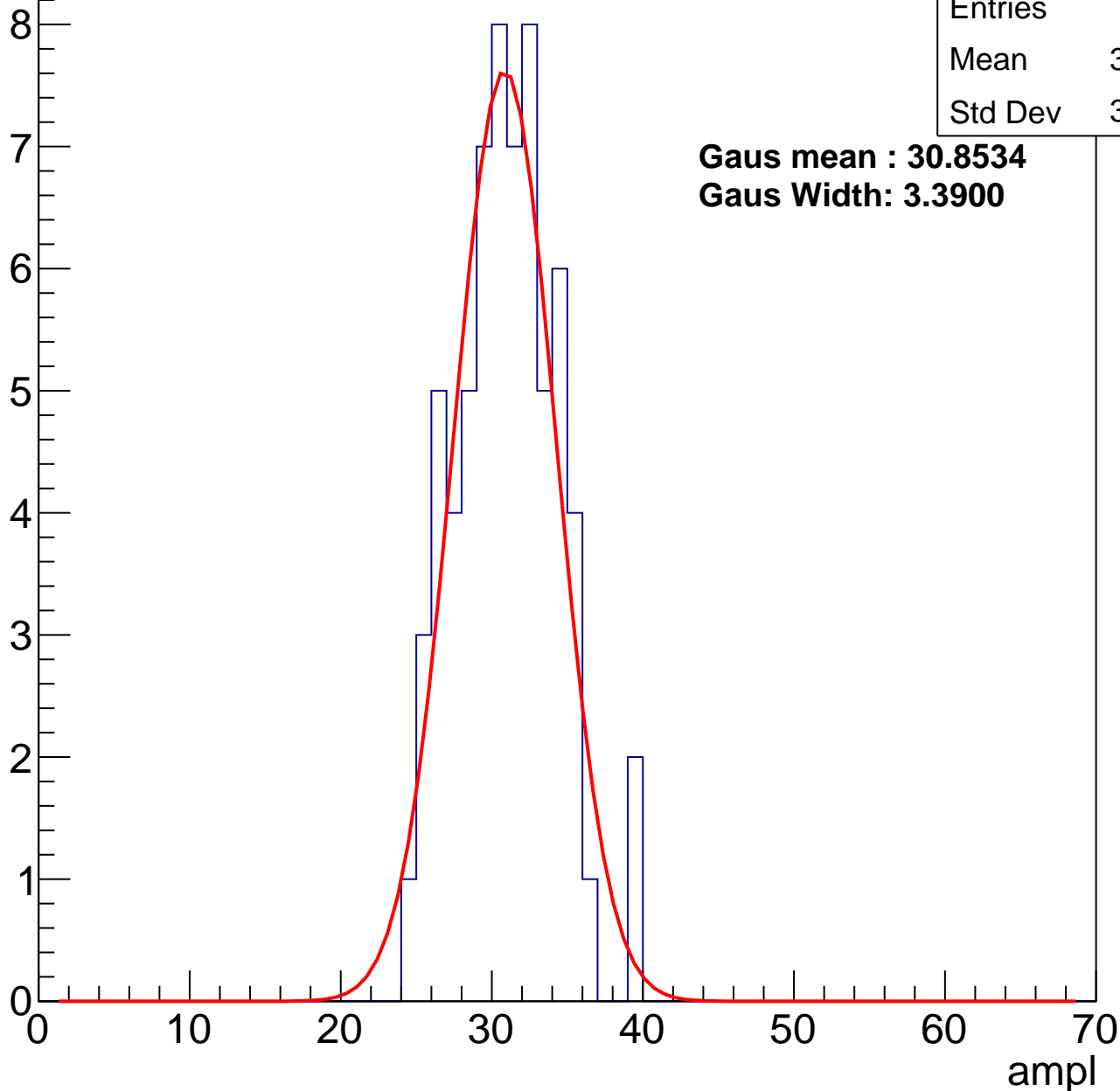
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.55
Std Dev	3.285

Gaus mean : 30.8534

Gaus Width: 3.3900



B1L100S, U13-ch51, adc1

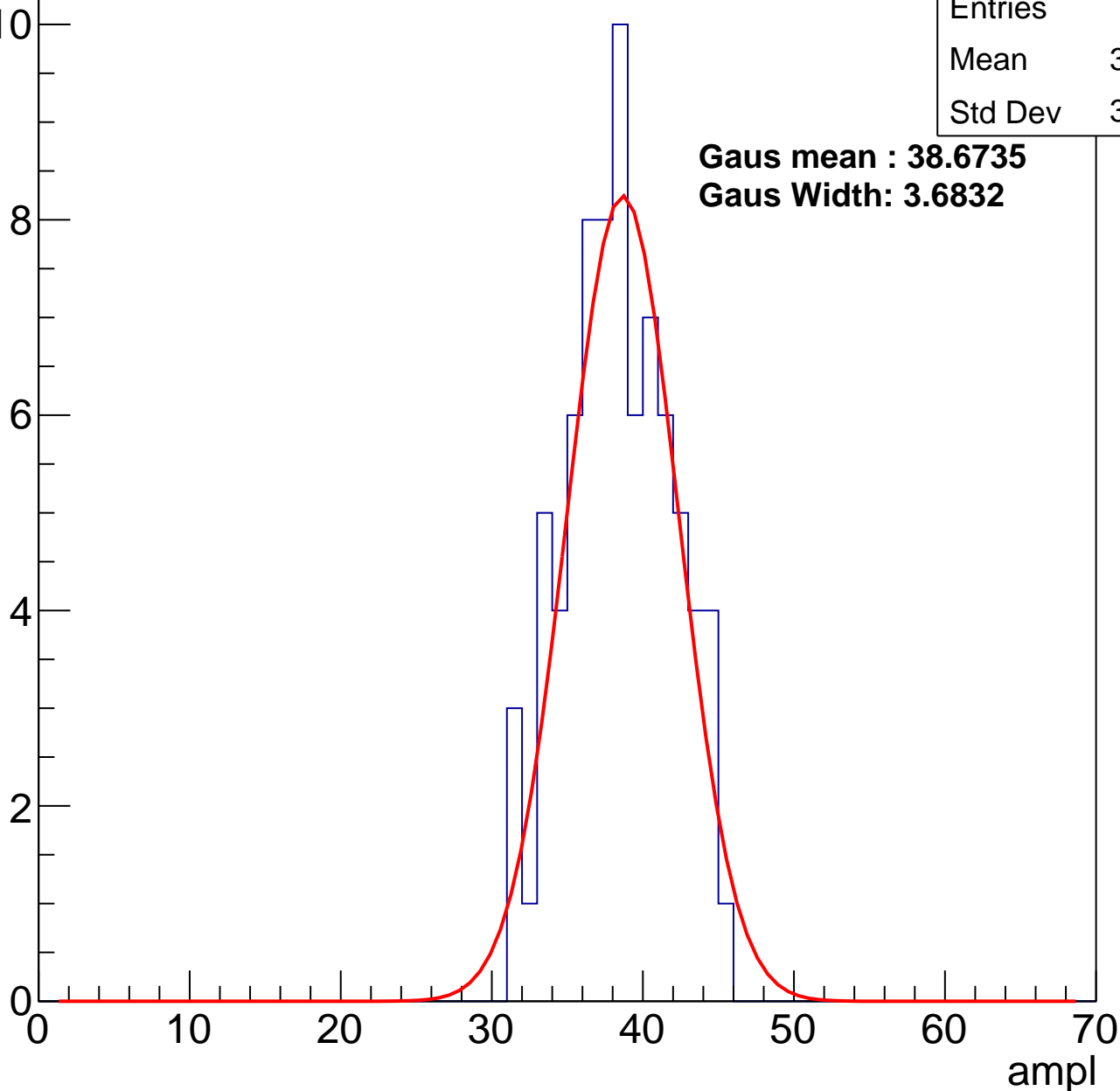
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	37.99
Std Dev	3.447

Gaus mean : 38.6735

Gaus Width: 3.6832



B1L100S, U13-ch51, adc2

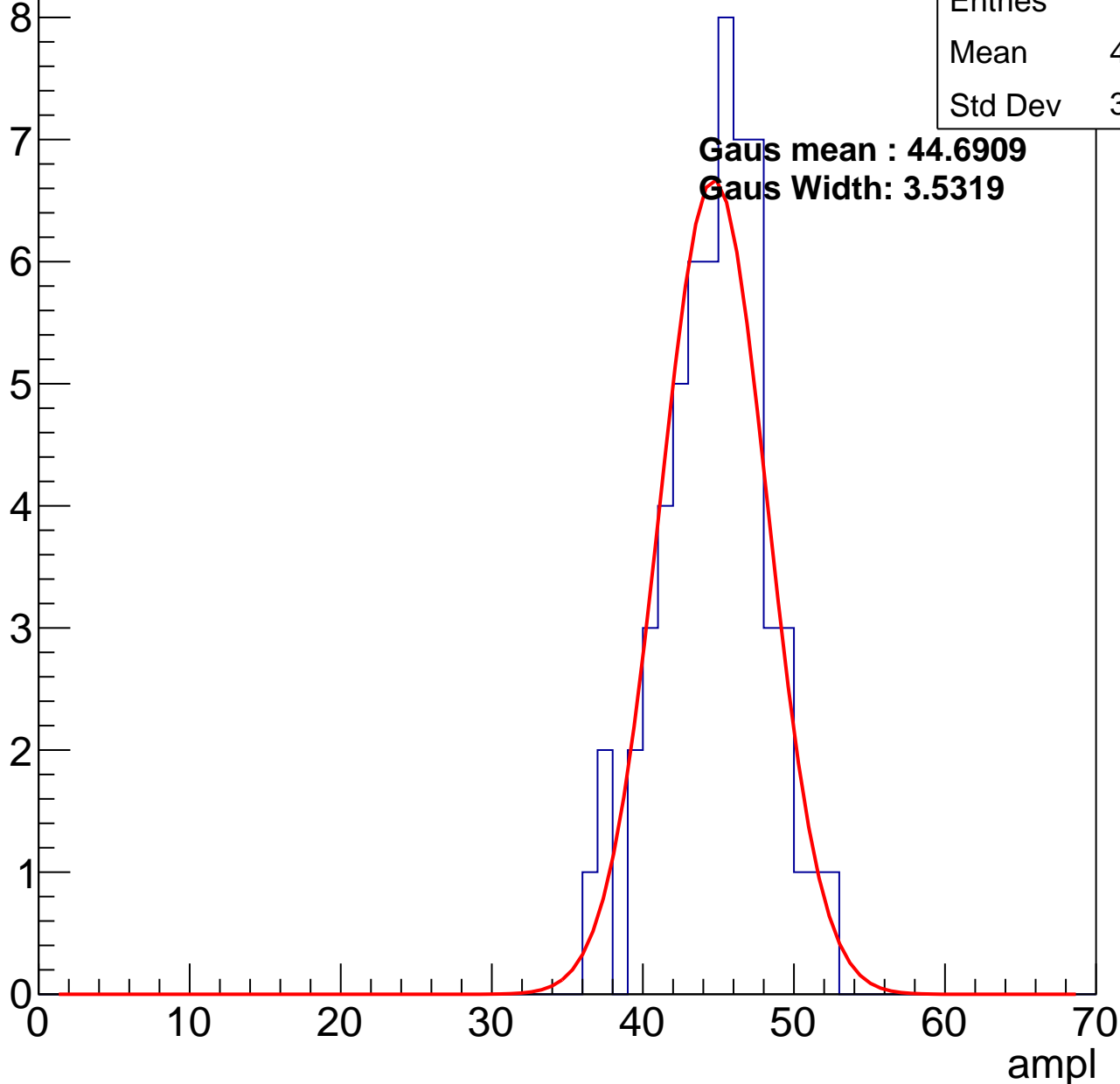
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.32
Std Dev	3.384

Gaus mean : 44.6909

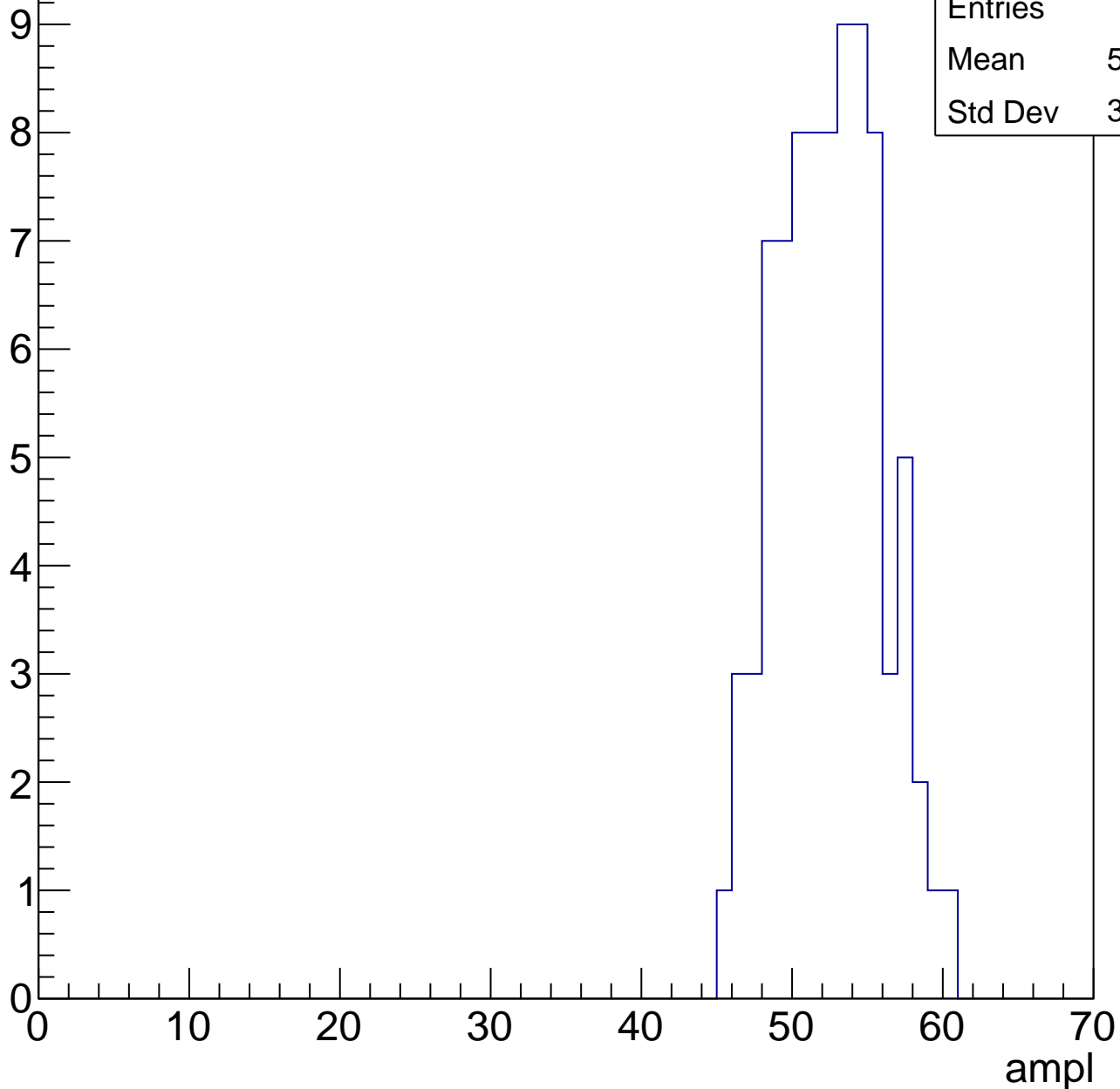
Gaus Width: 3.5319



B1L100S, U13-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

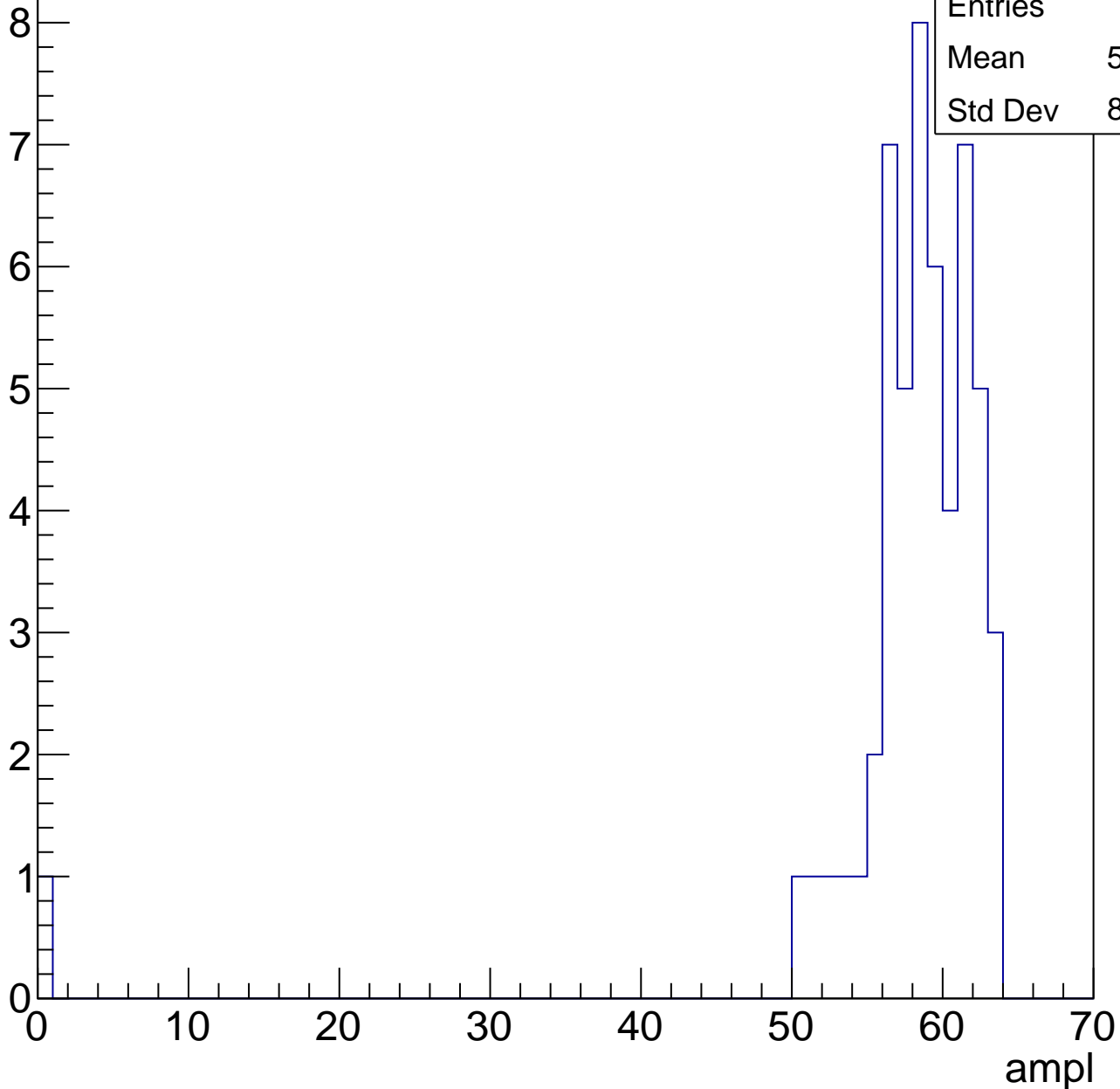


B1L100S, U13-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

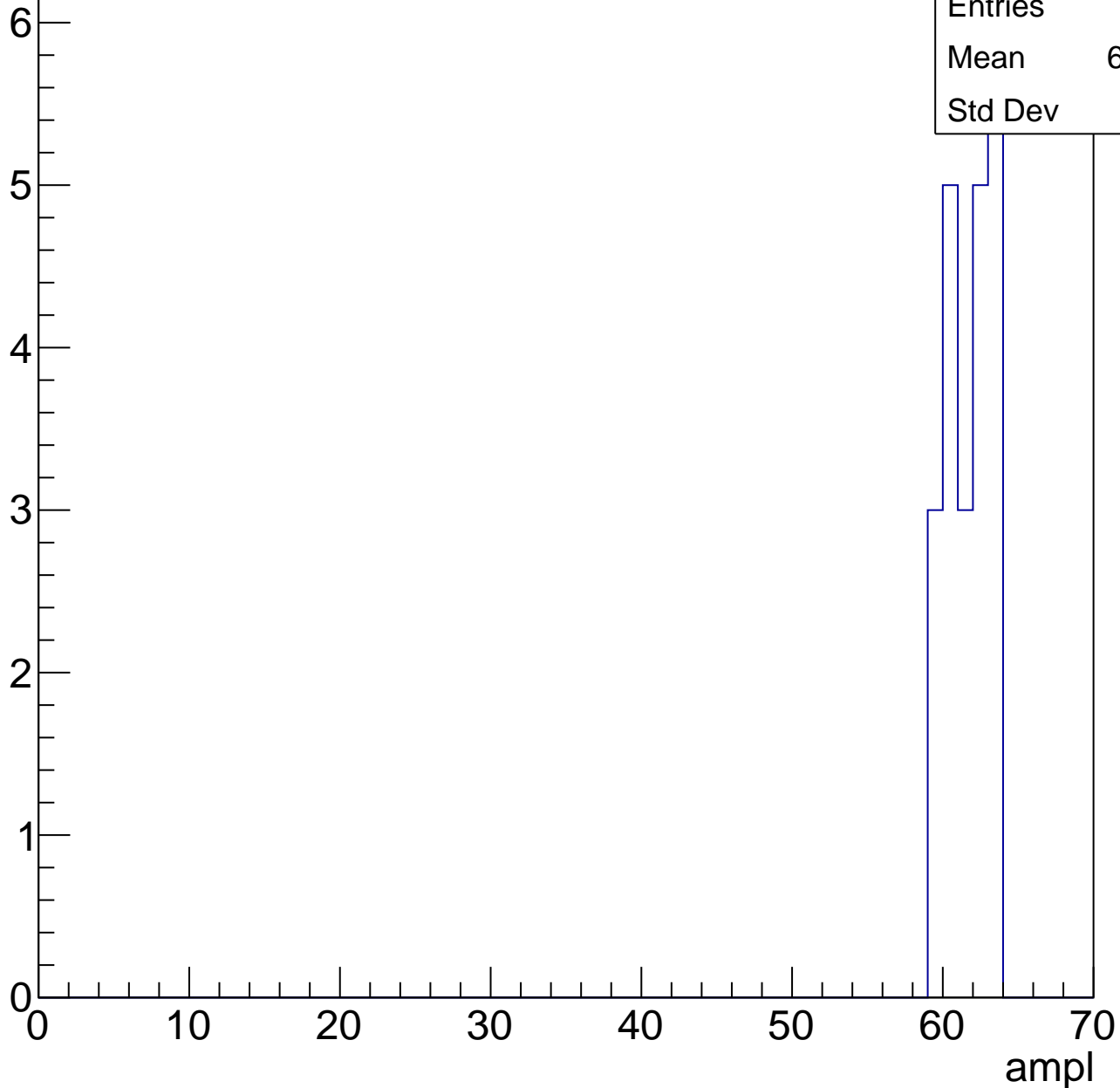
Entries	53
Mean	57.19
Std Dev	8.479



B1L100S, U13-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U13-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch52, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	31.18
Std Dev	3.228

Gaus mean : 31.4390

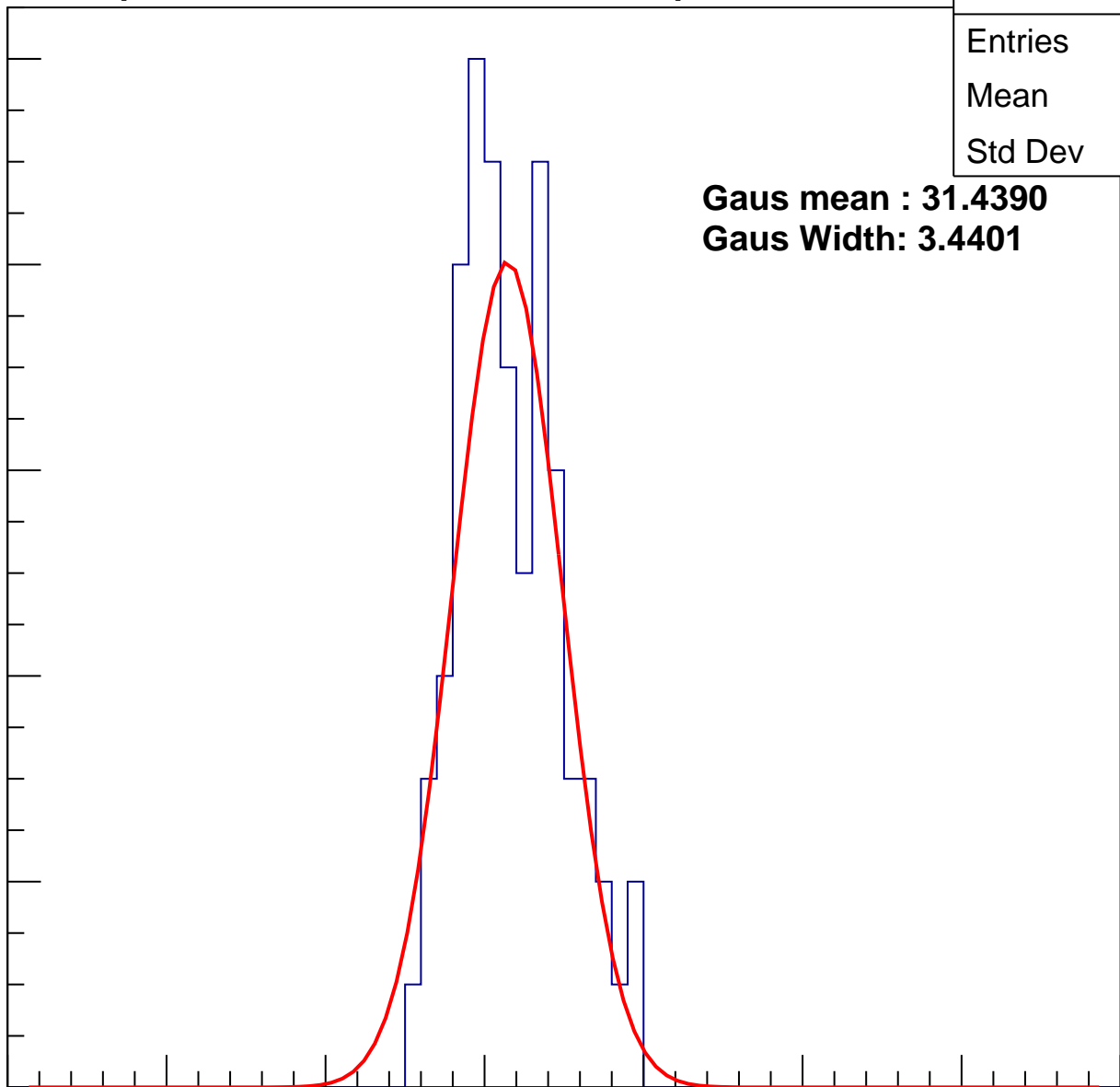
Gaus Width: 3.4401

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch52, adc1

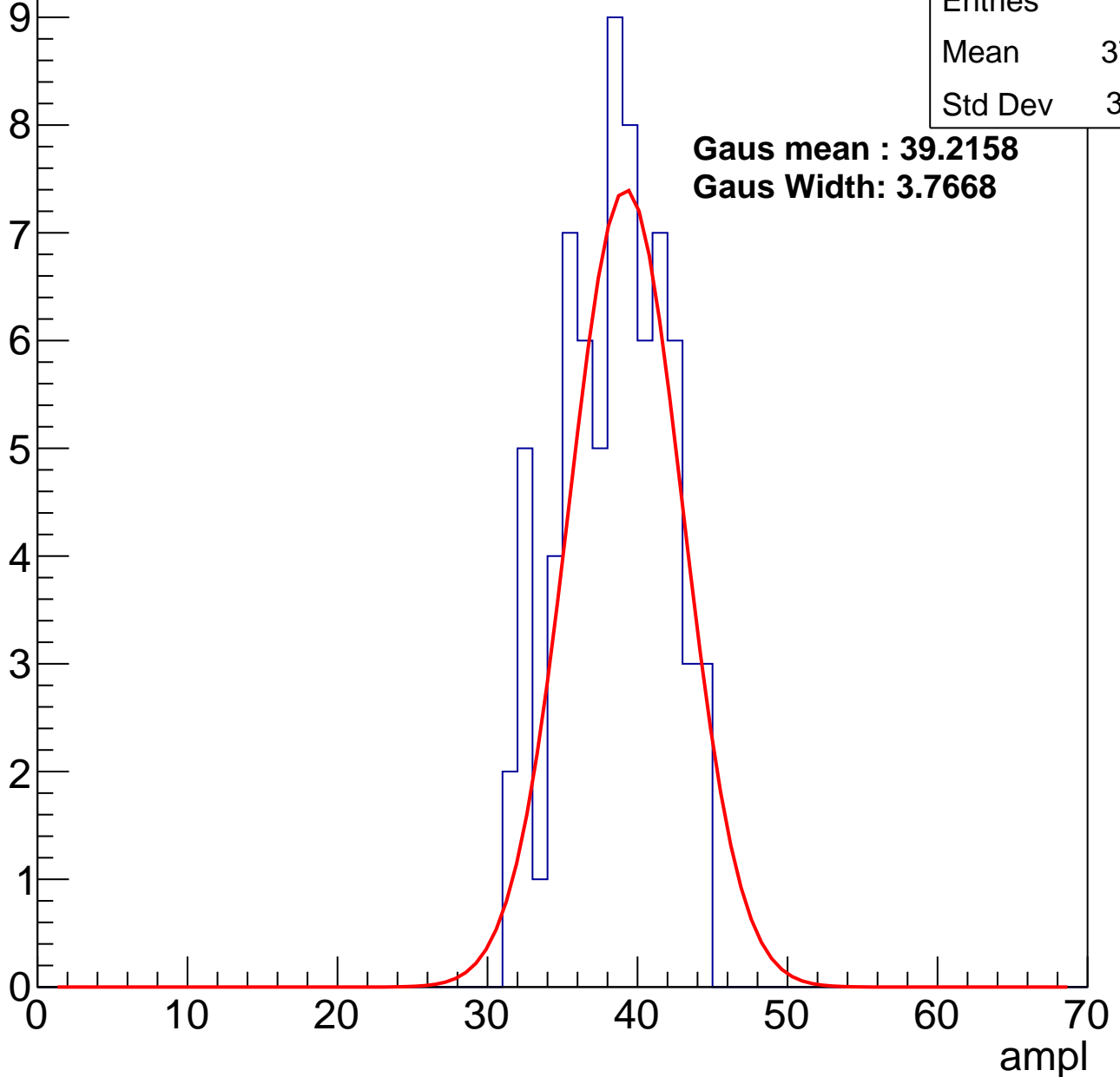
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.93
Std Dev	3.401

Gaus mean : 39.2158

Gaus Width: 3.7668



B1L100S, U13-ch52, adc2

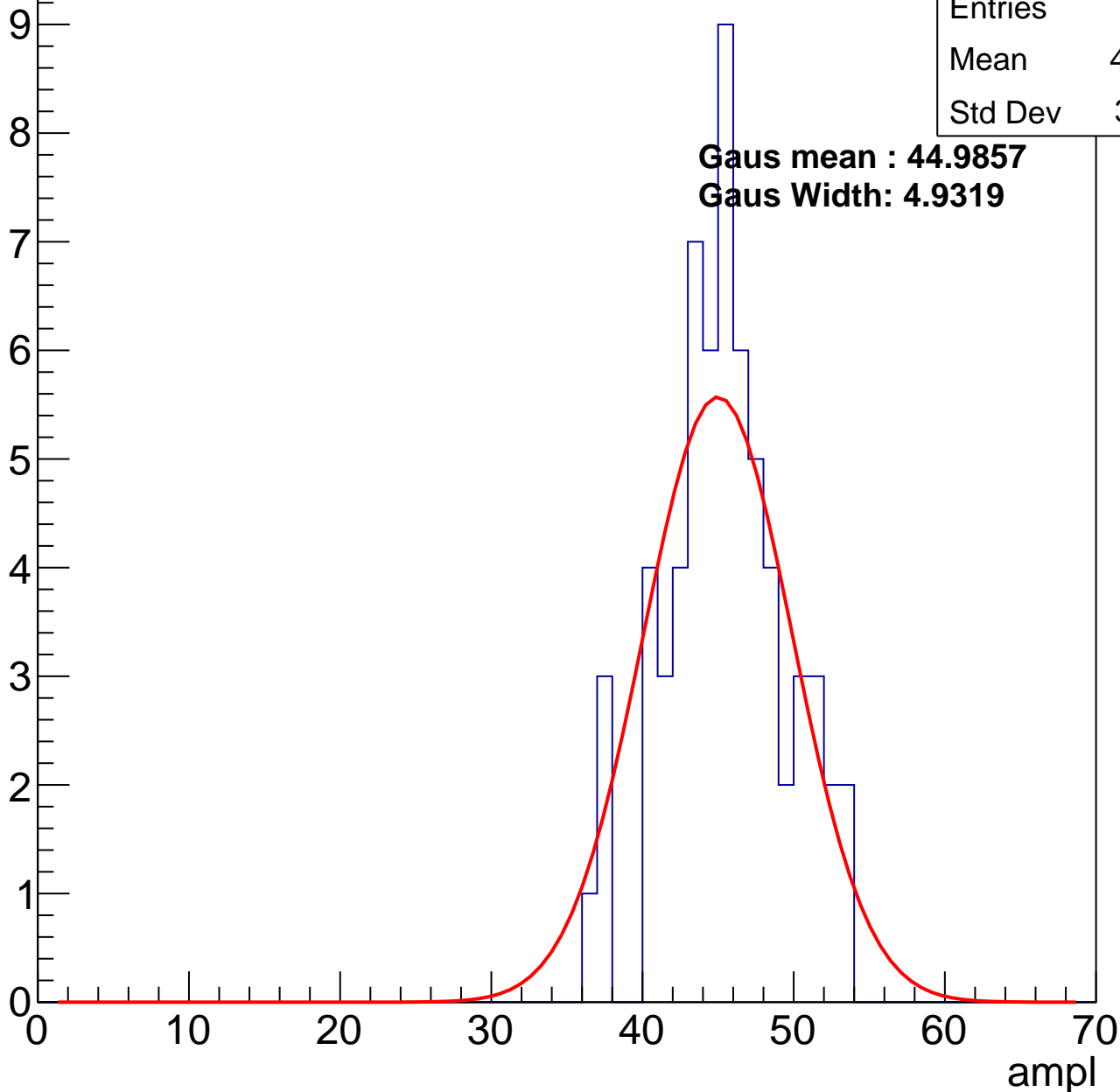
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	45.03
Std Dev	3.941

Gaus mean : 44.9857

Gaus Width: 4.9319

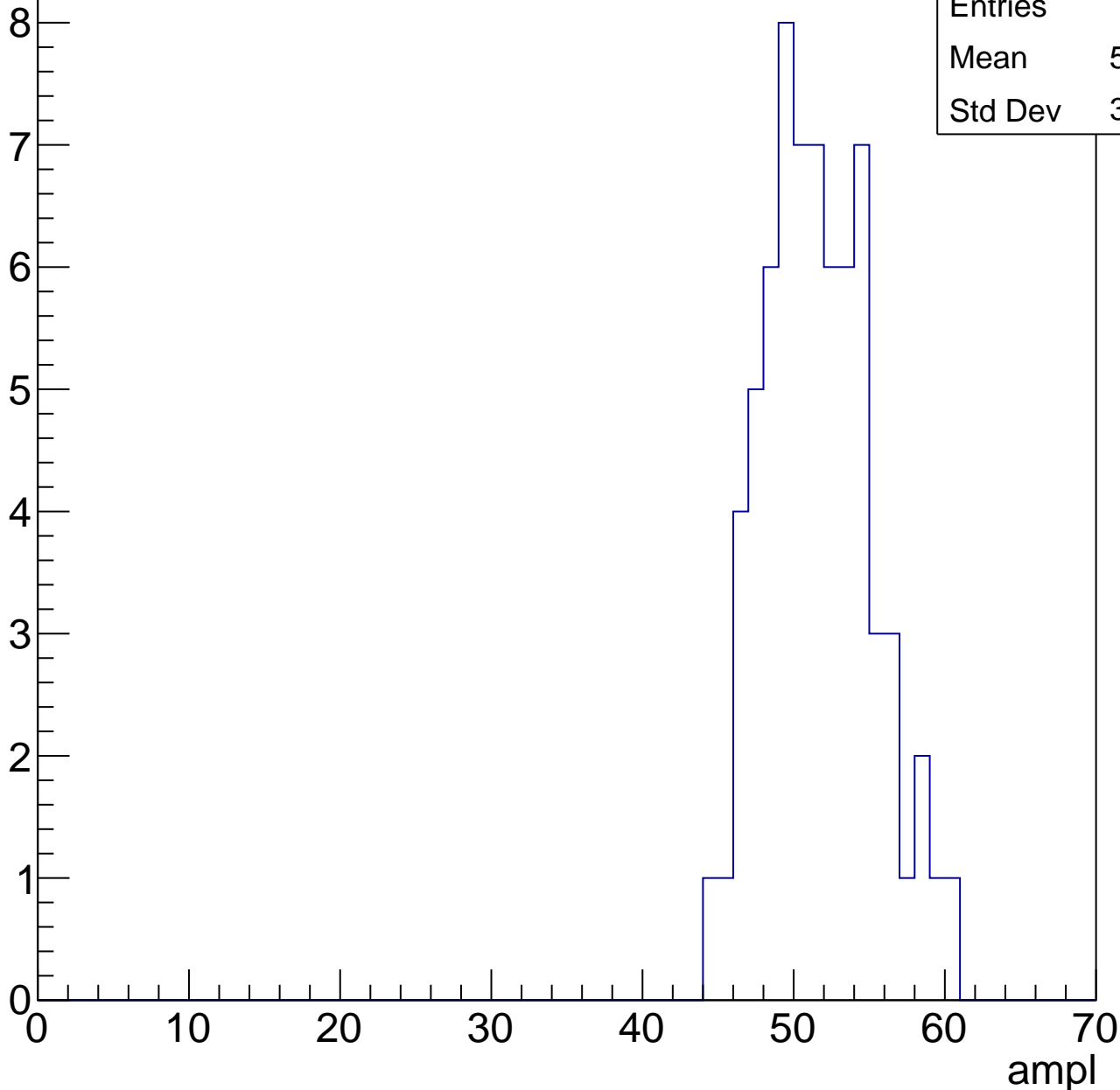


B1L100S, U13-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

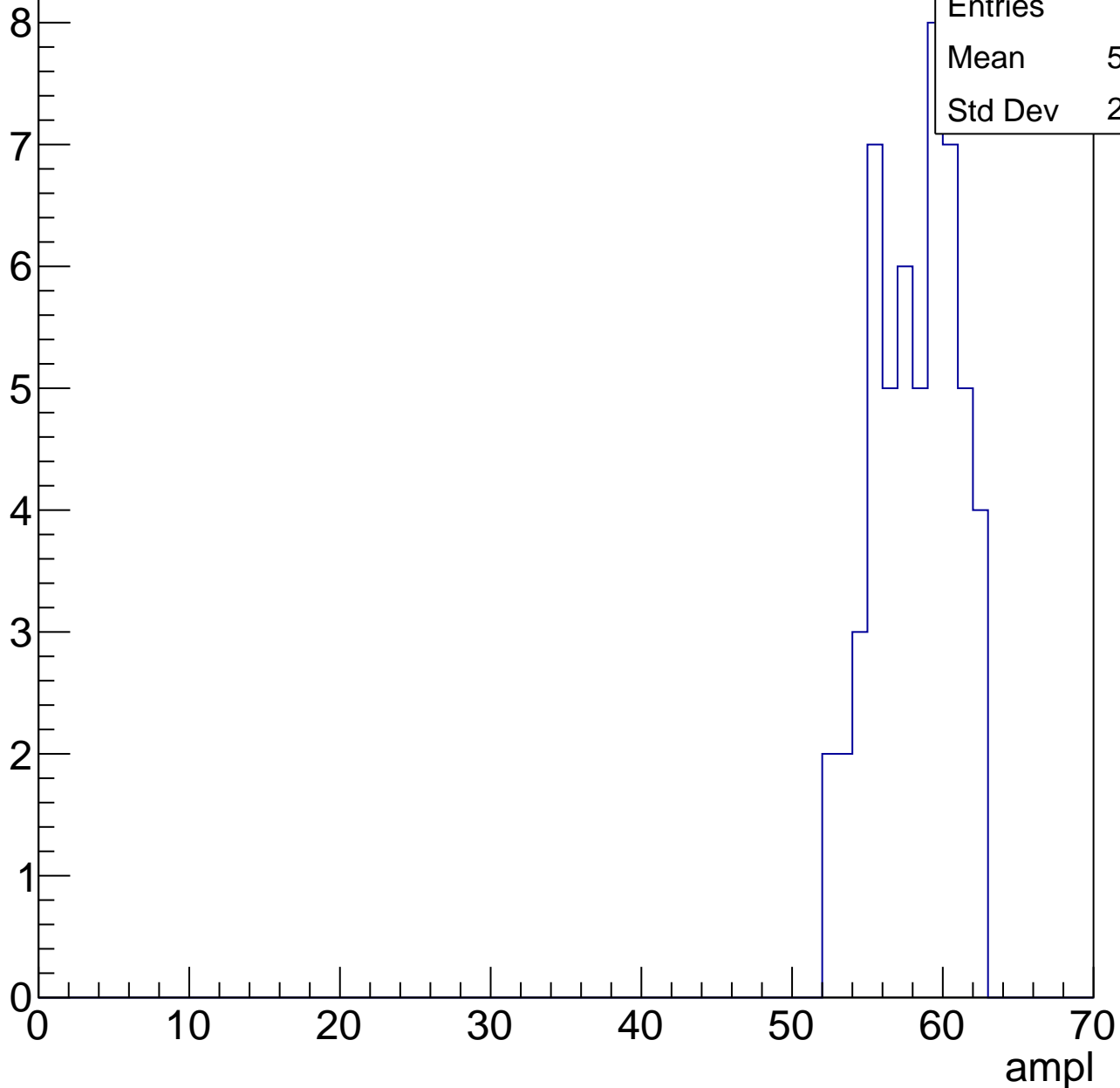
Entries	69
Mean	51.13
Std Dev	3.514



B1L100S, U13-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



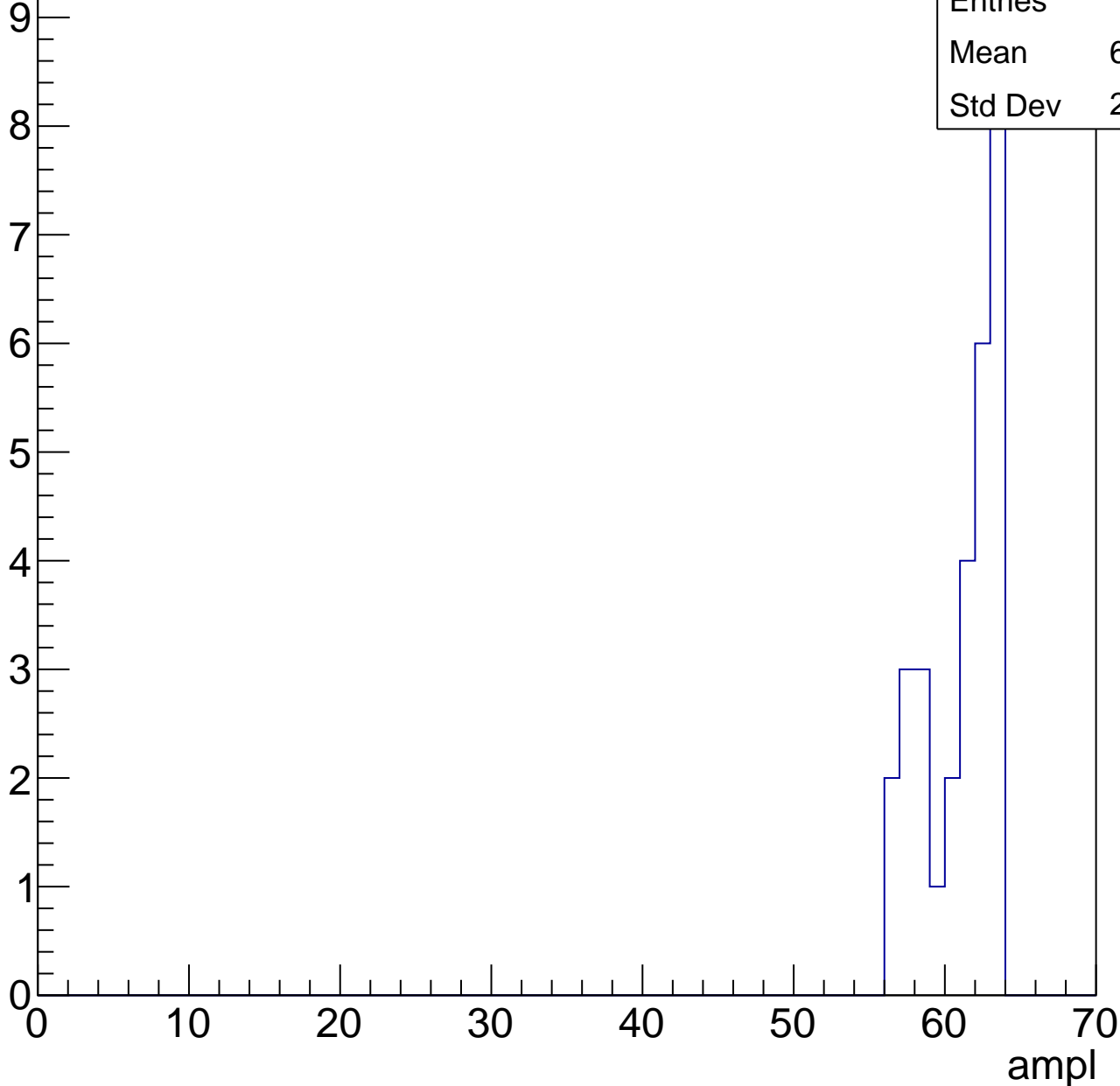
Entries	54
Mean	57.67
Std Dev	2.715

B1L100S, U13-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.63
Std Dev	2.373



B1L100S, U13-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch53, adc0

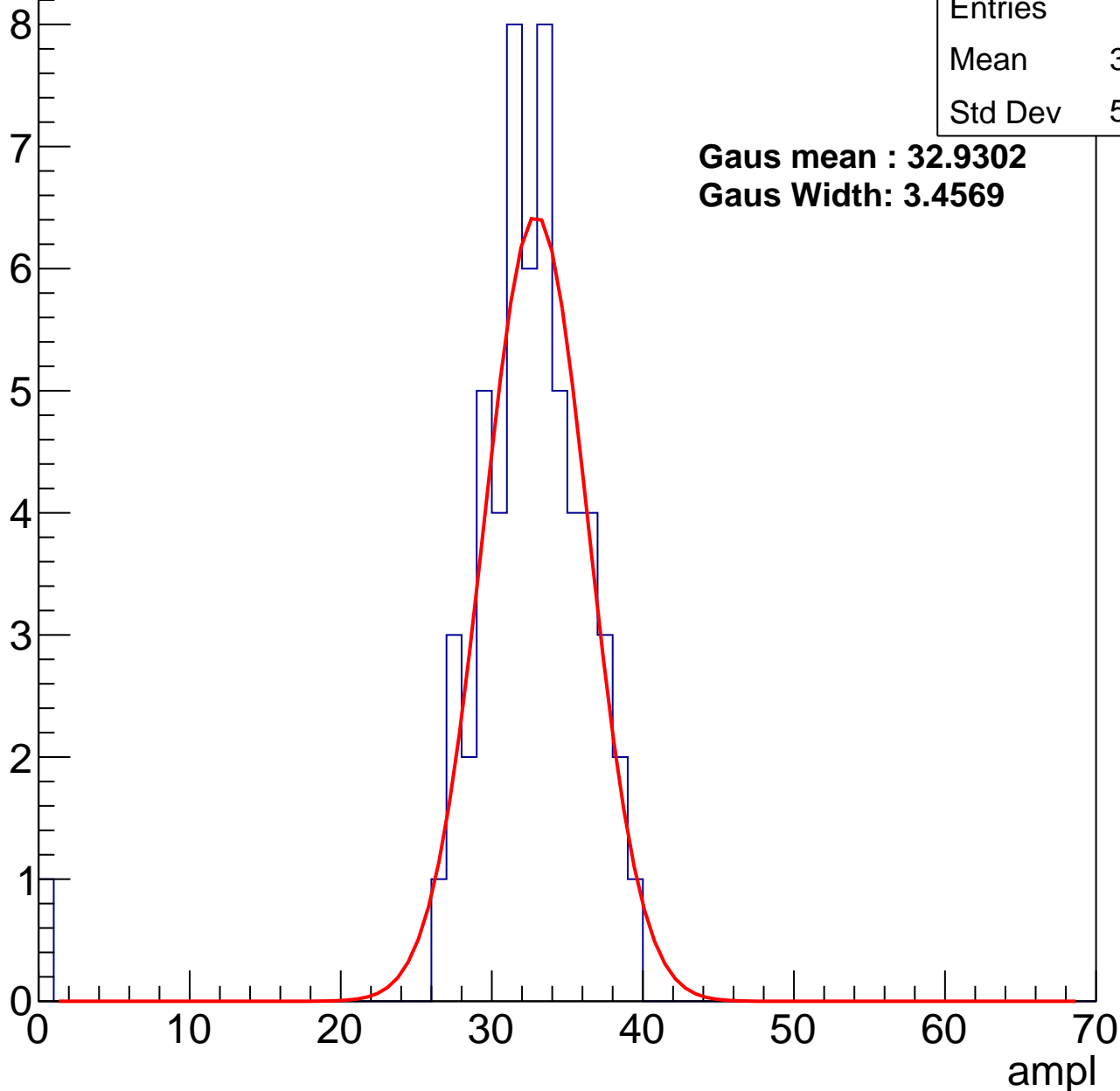
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	31.79
Std Dev	5.227

Gaus mean : 32.9302

Gaus Width: 3.4569



B1L100S, U13-ch53, adc1

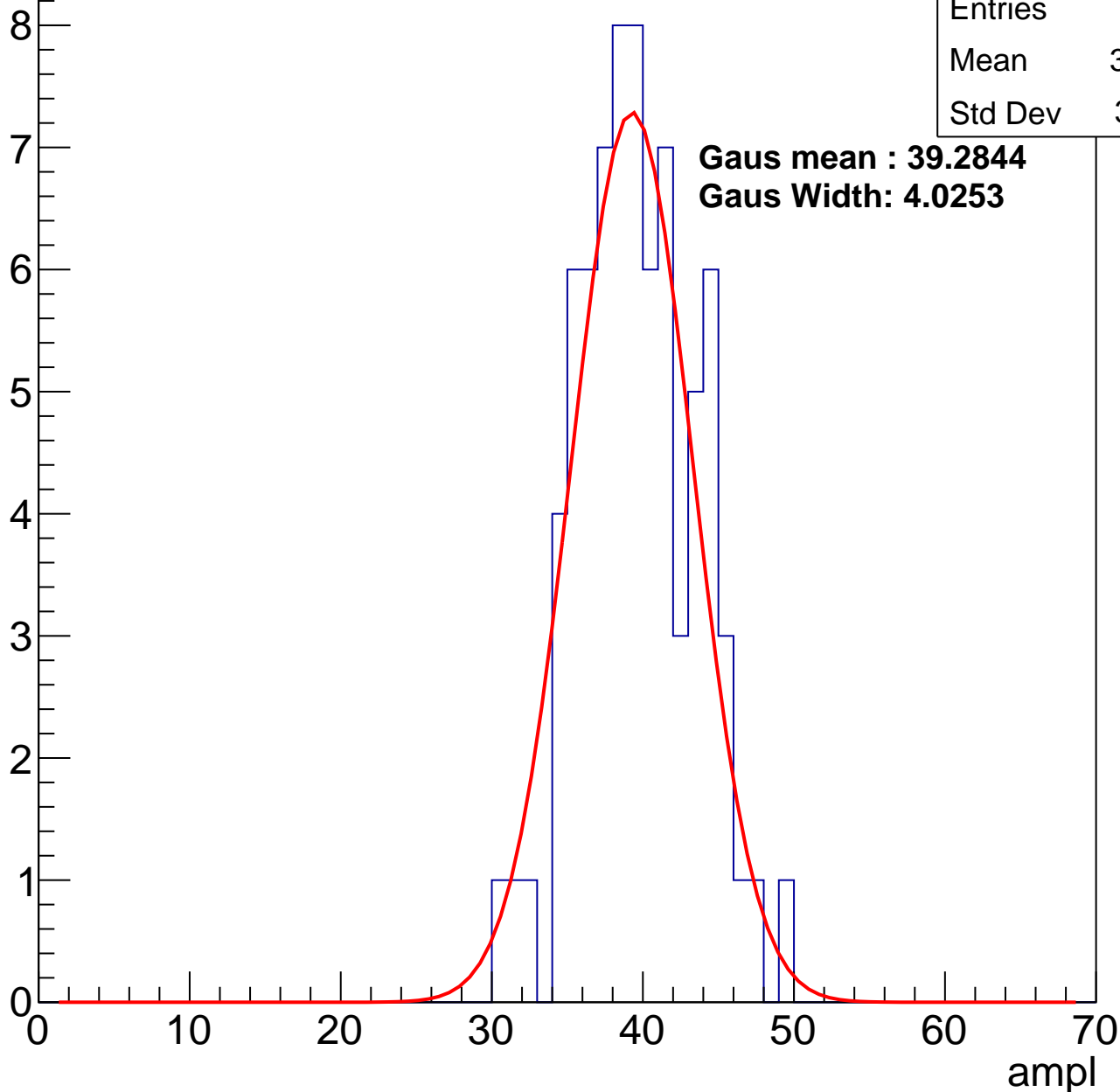
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	39.19
Std Dev	3.811

Gaus mean : 39.2844

Gaus Width: 4.0253



B1L100S, U13-ch53, adc2

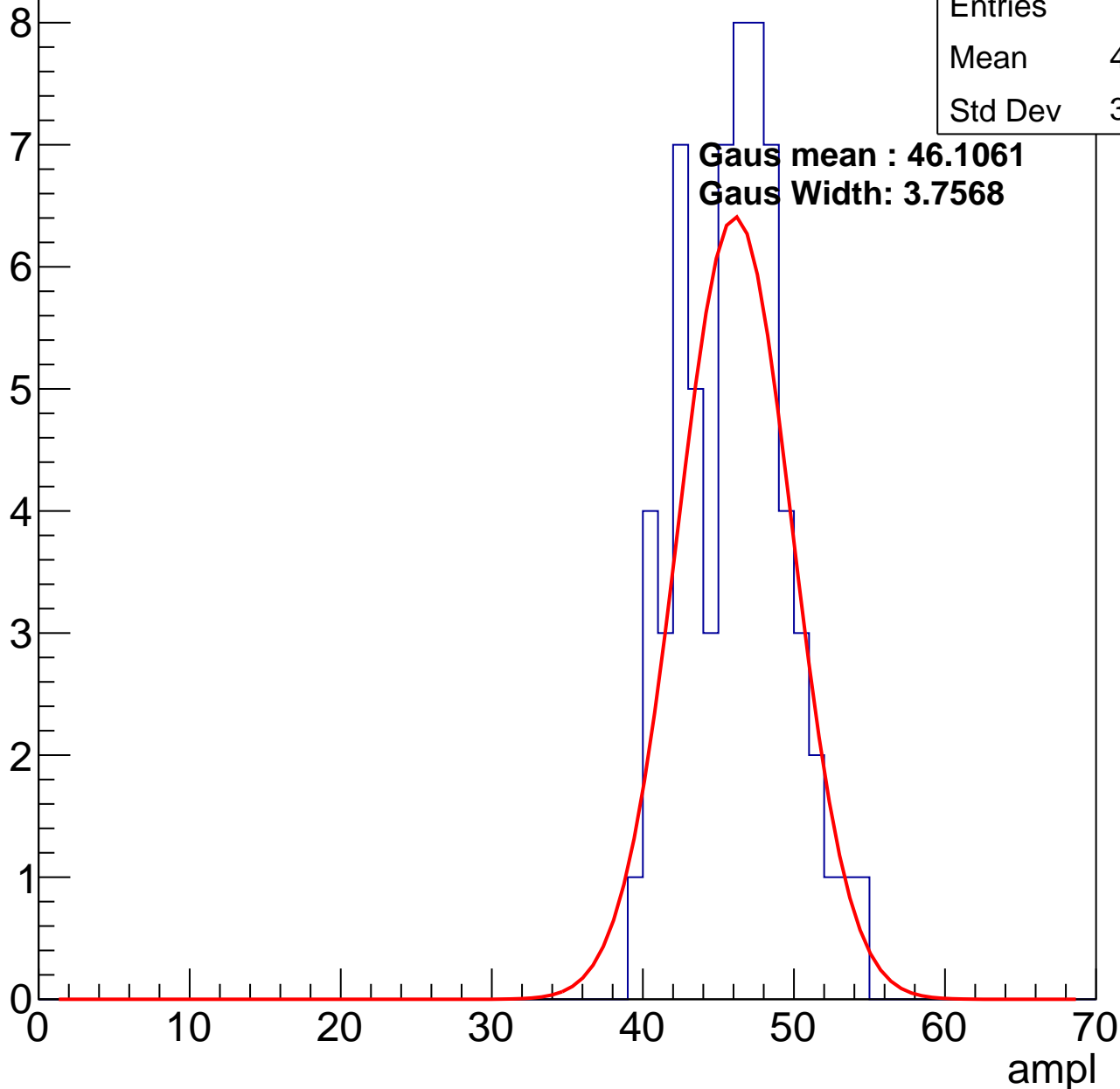
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	45.62
Std Dev	3.404

Gaus mean : 46.1061

Gaus Width: 3.7568

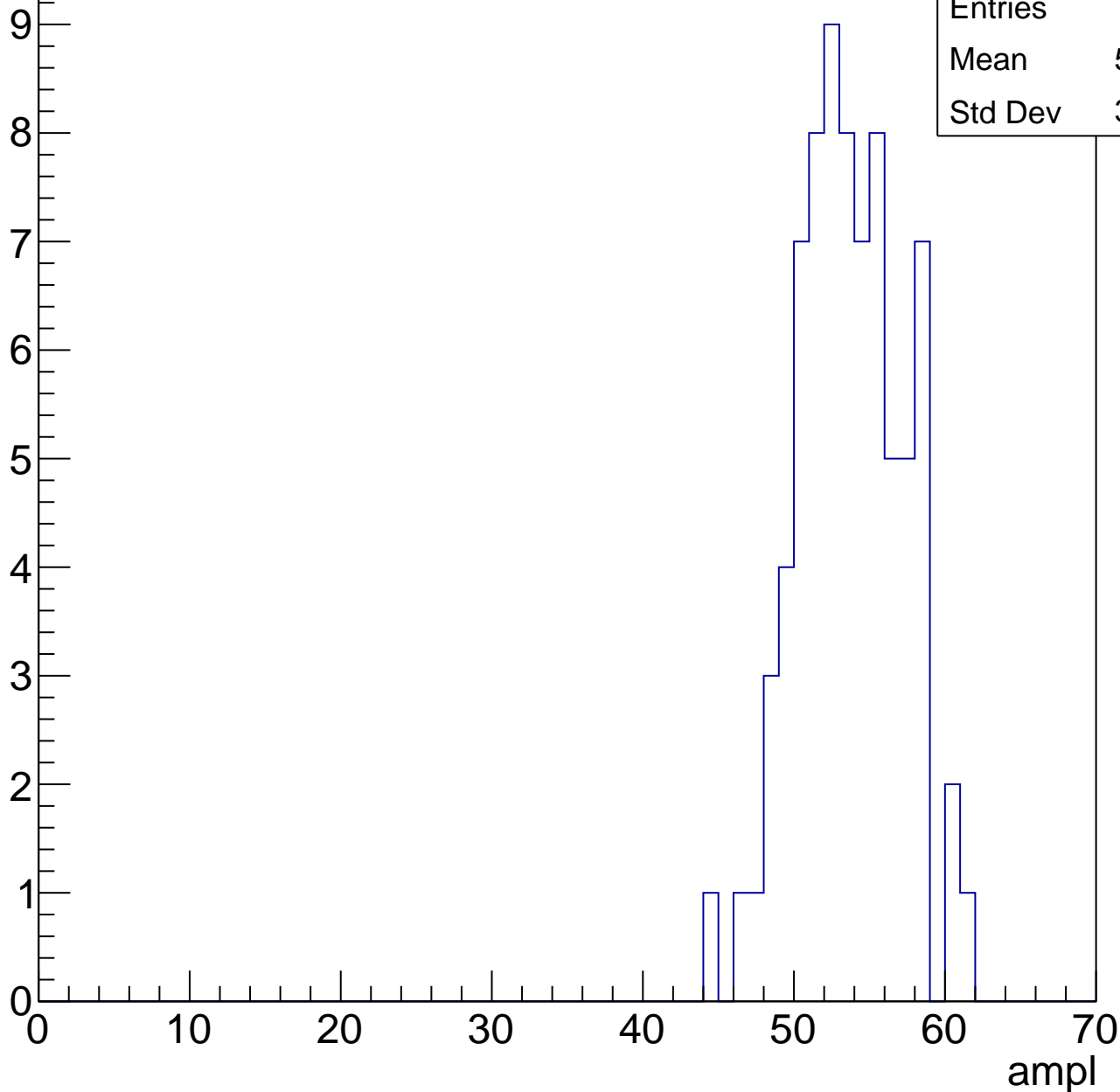


B1L100S, U13-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	53.21
Std Dev	3.431



B1L100S, U13-ch53, adc4

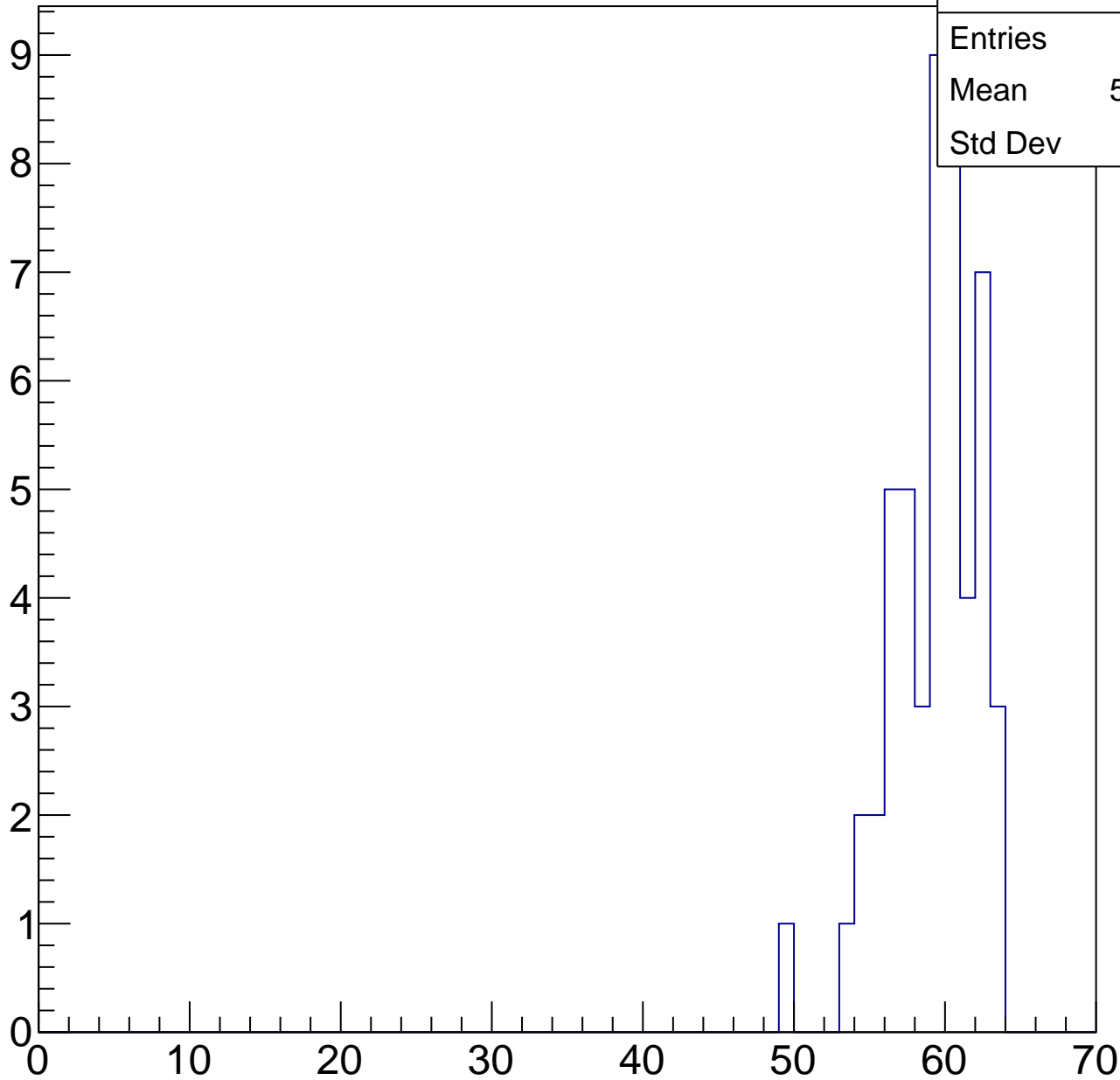
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.74
Std Dev	2.89

ampl

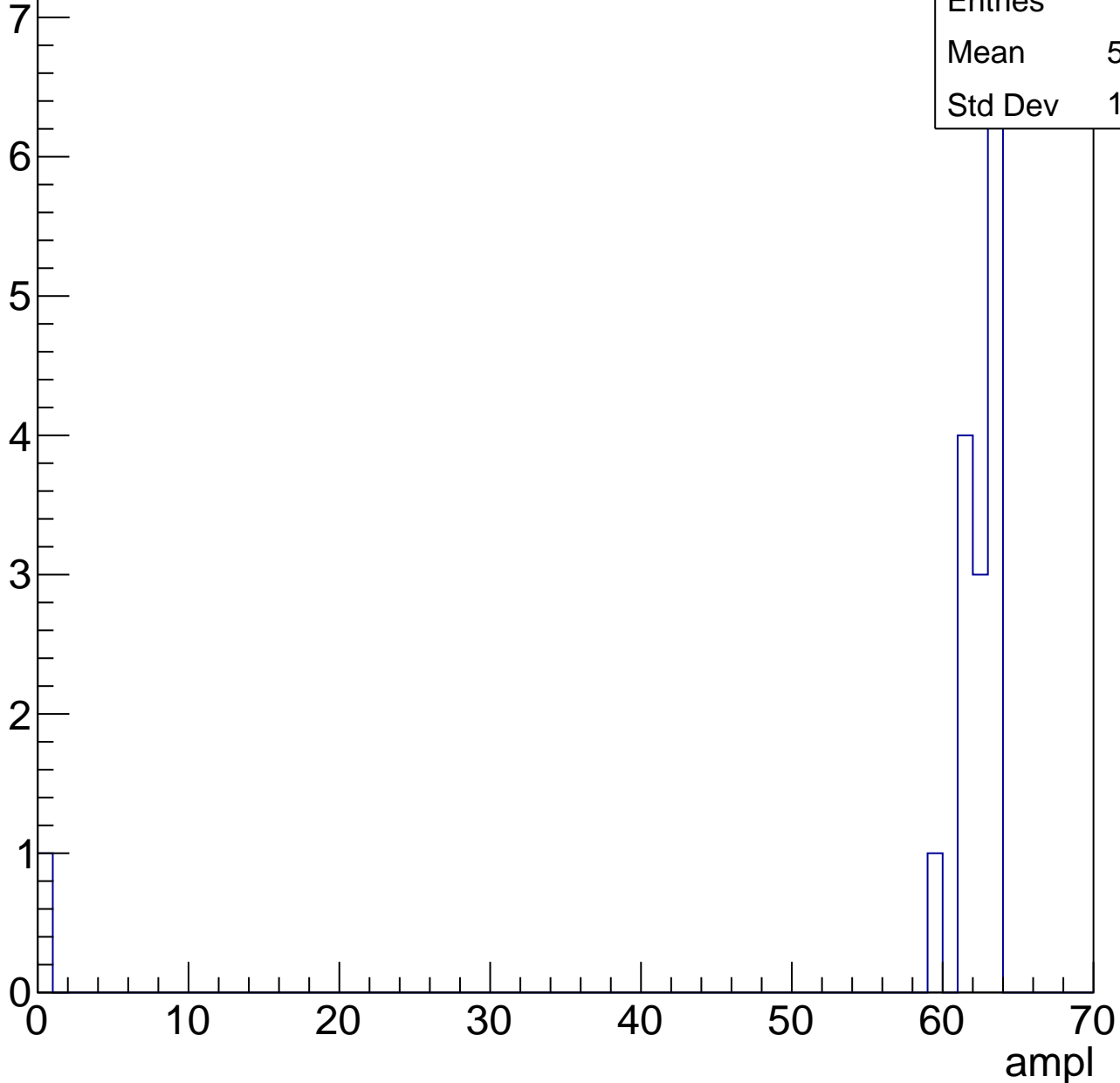


B1L100S, U13-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	58.12
Std Dev	15.05



B1L100S, U13-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch54, adc0

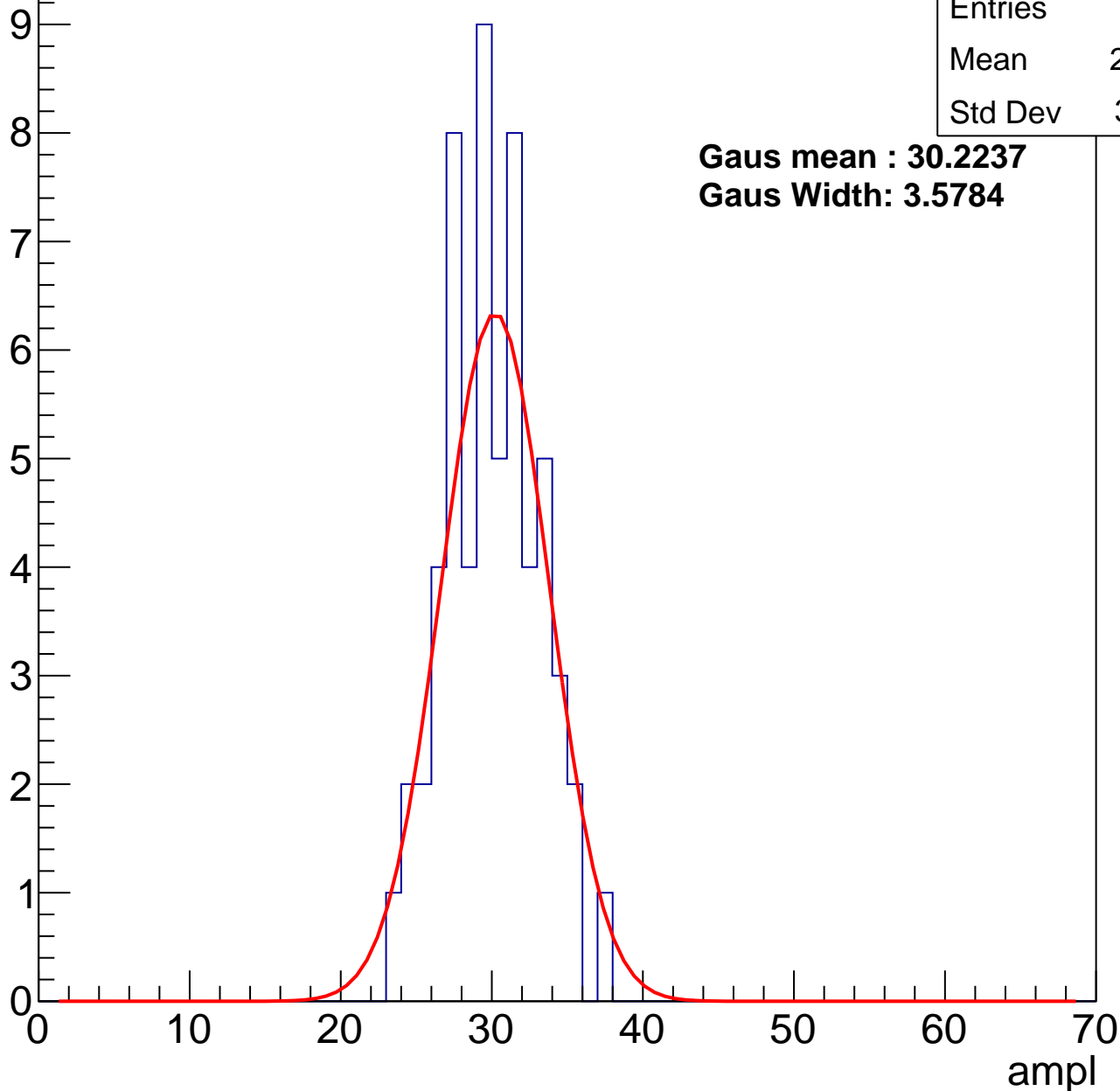
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.55
Std Dev	3.041

Gaus mean : 30.2237

Gaus Width: 3.5784



B1L100S, U13-ch54, adc1

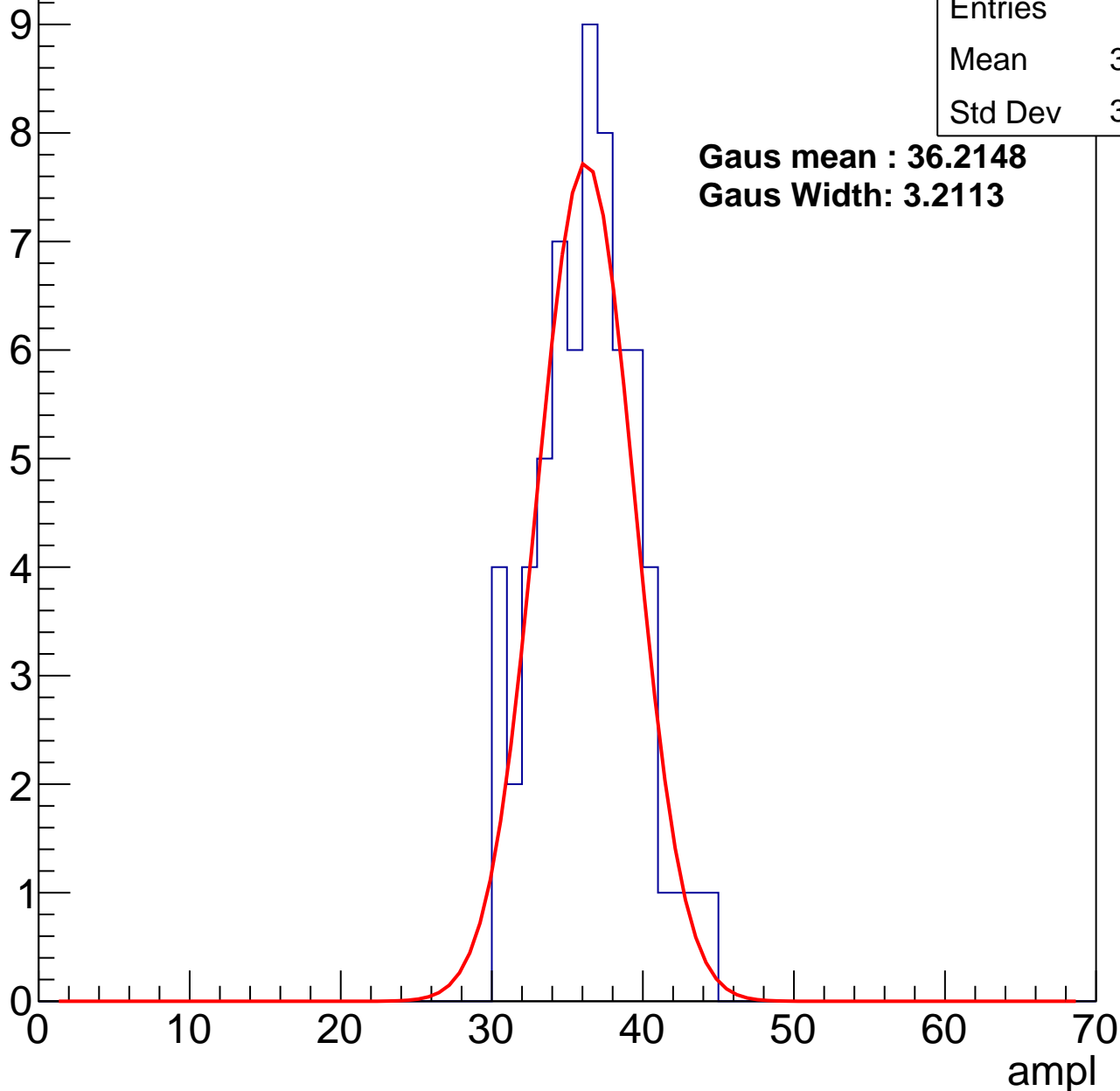
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.92
Std Dev	3.188

Gaus mean : 36.2148

Gaus Width: 3.2113



B1L100S, U13-ch54, adc2

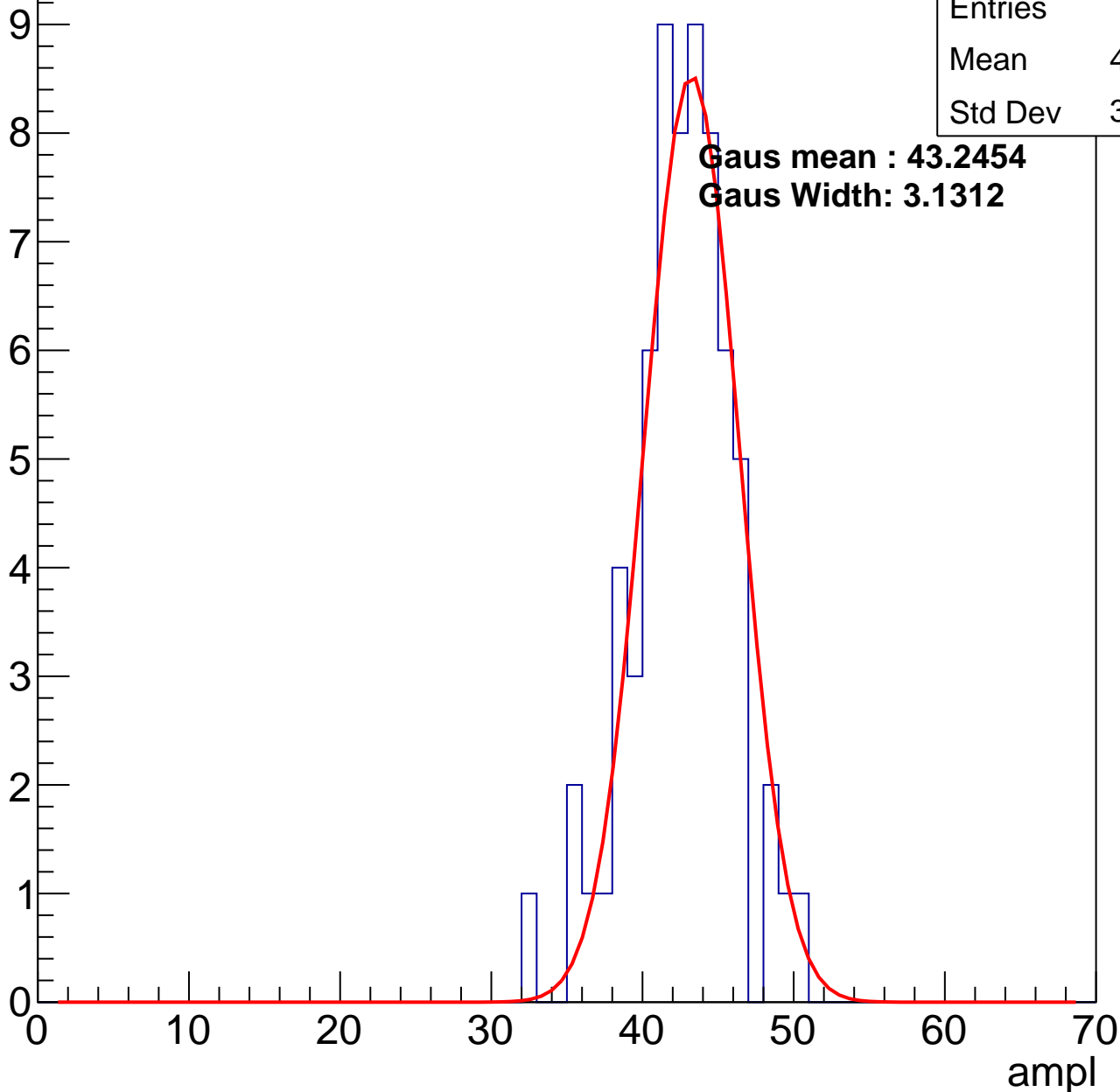
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.13
Std Dev	3.327

Gaus mean : 43.2454

Gaus Width: 3.1312



B1L100S, U13-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	49.8
Std Dev	3.389

Entry

10

8

6

4

2

0

0

10

20

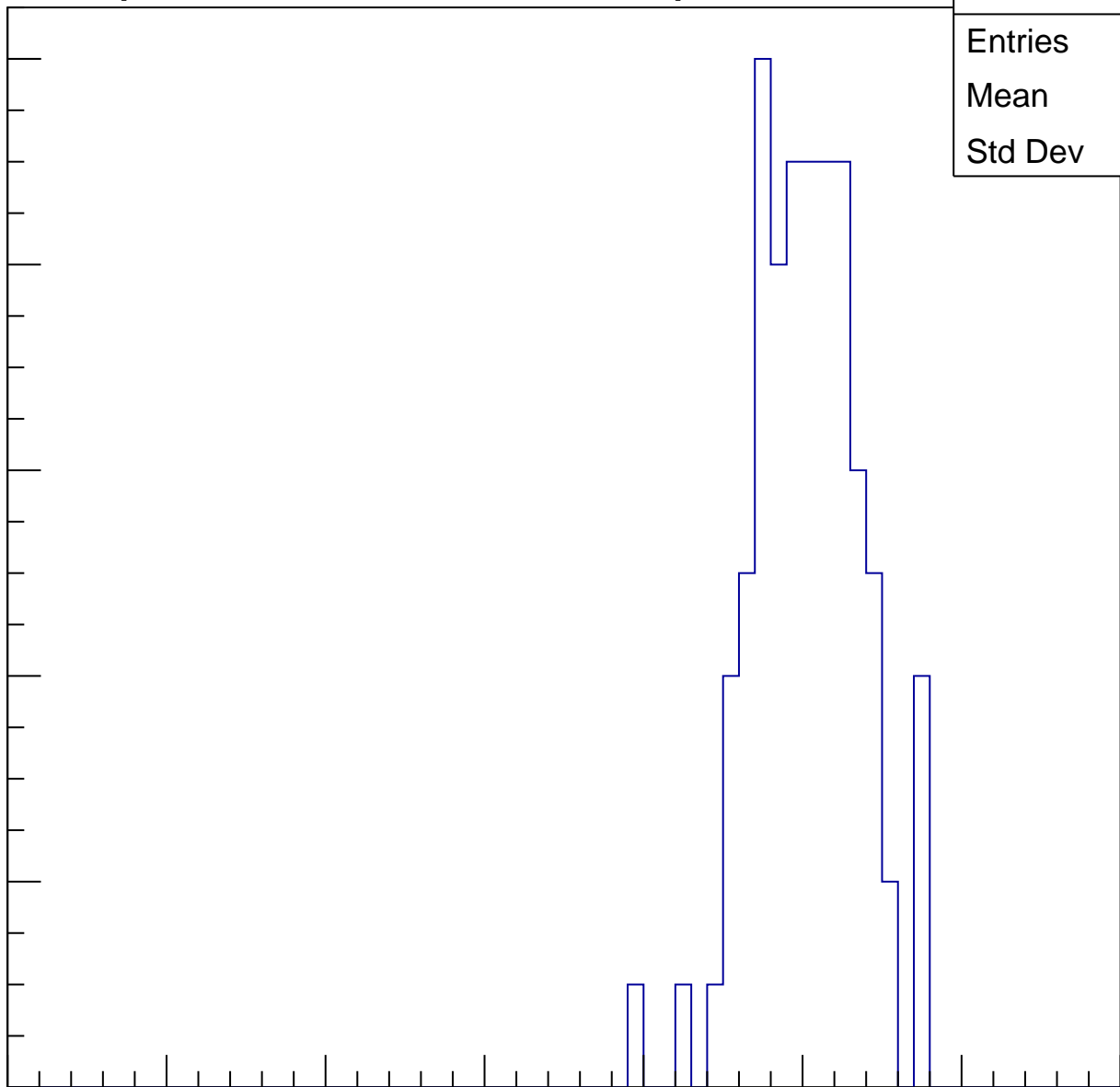
30

40

50

60

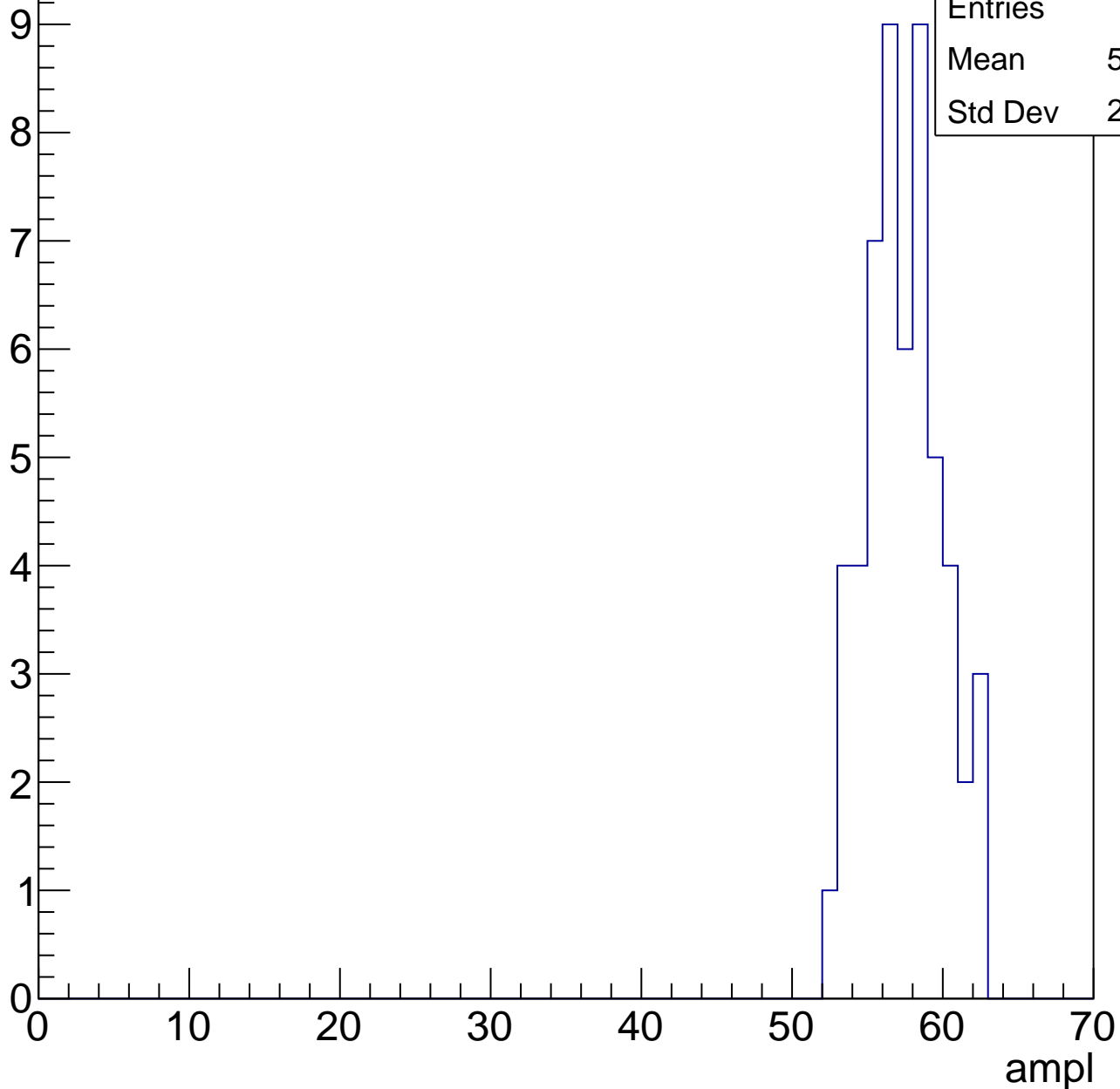
ampl



B1L100S, U13-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



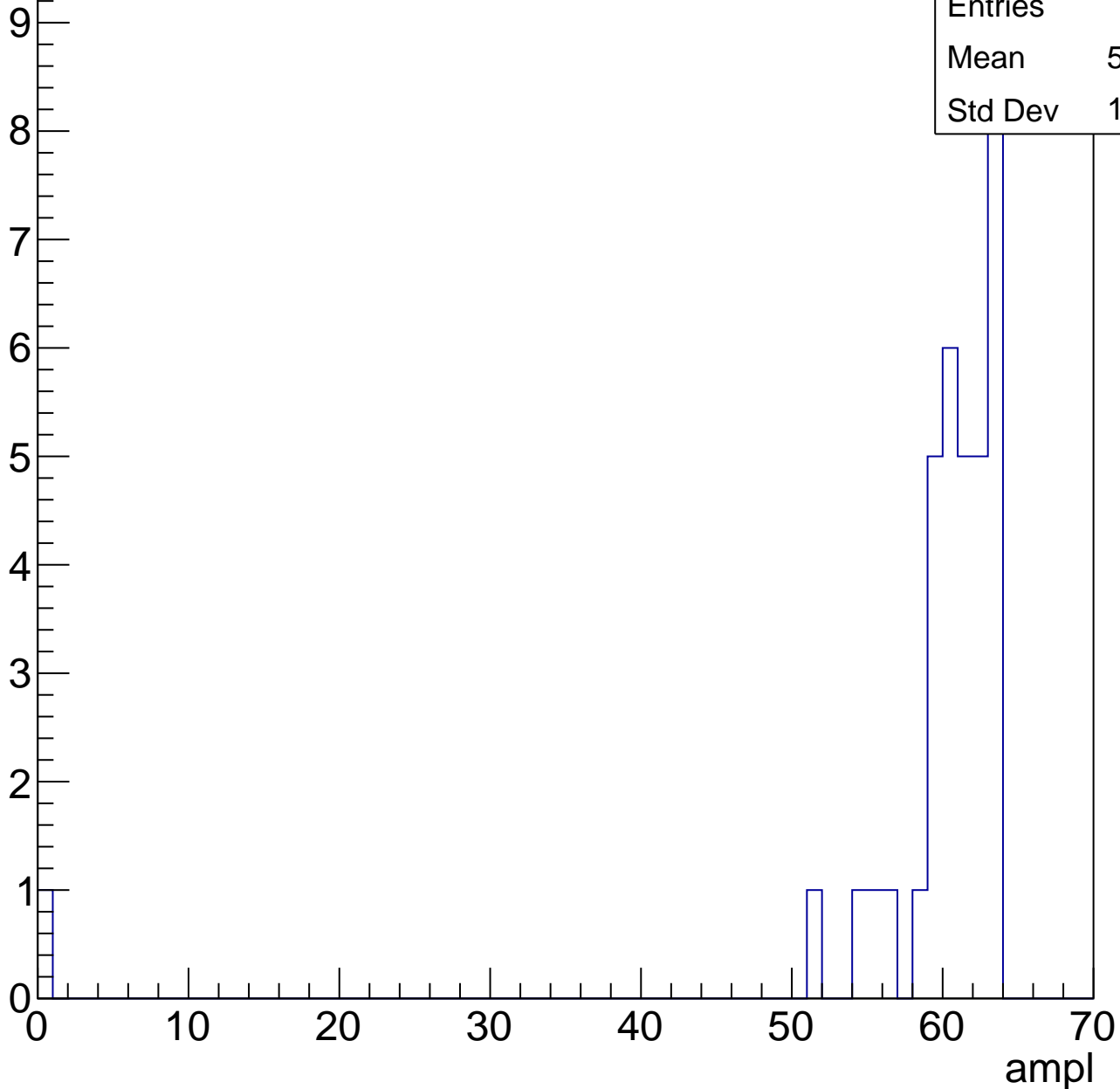
Entries	54
Mean	56.96
Std Dev	2.487

B1L100S, U13-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

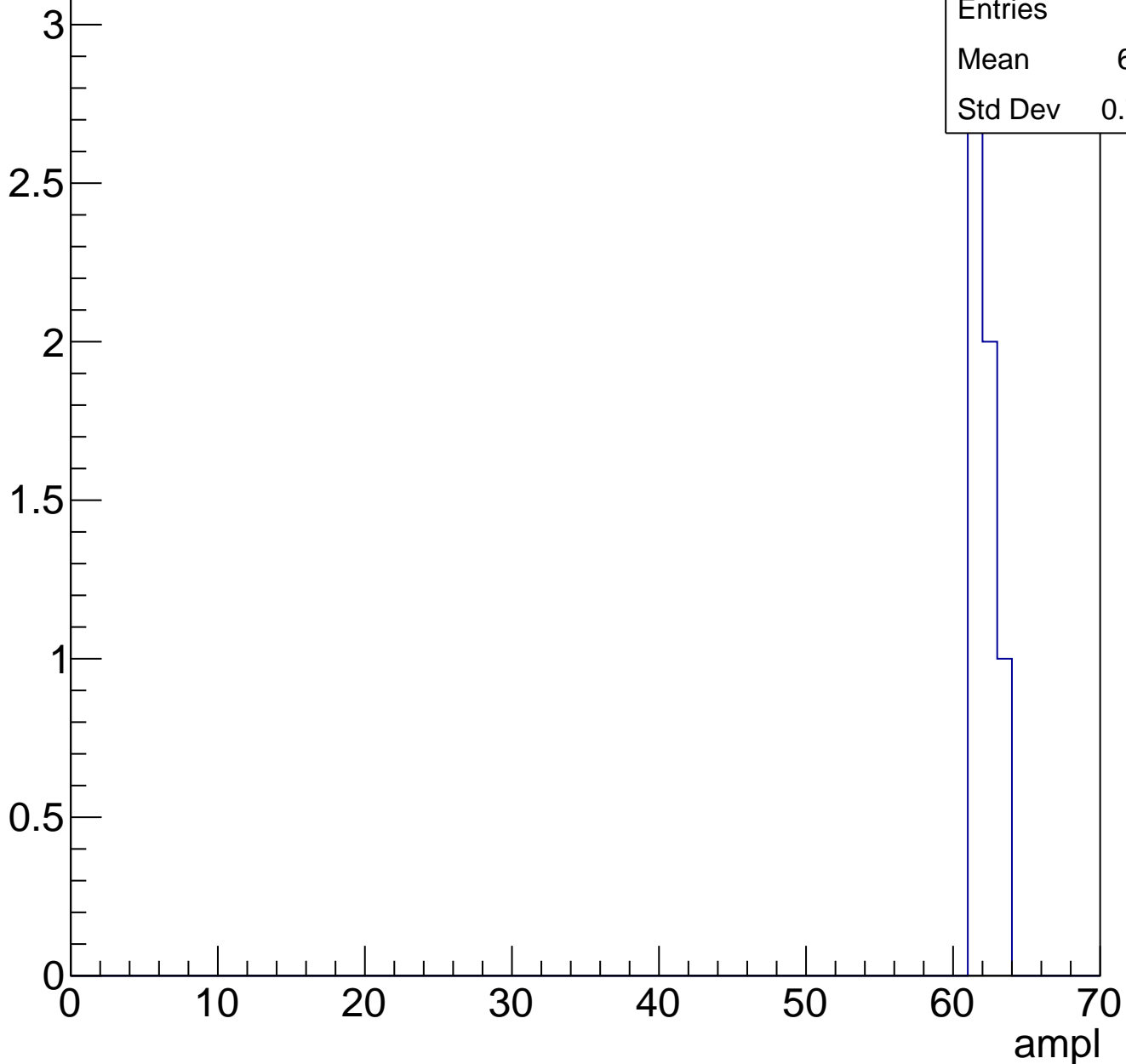
Entries	36
Mean	58.64
Std Dev	10.28



B1L100S, U13-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U13-ch55, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	29.37
Std Dev	5.041

Gaus mean : 30.5666

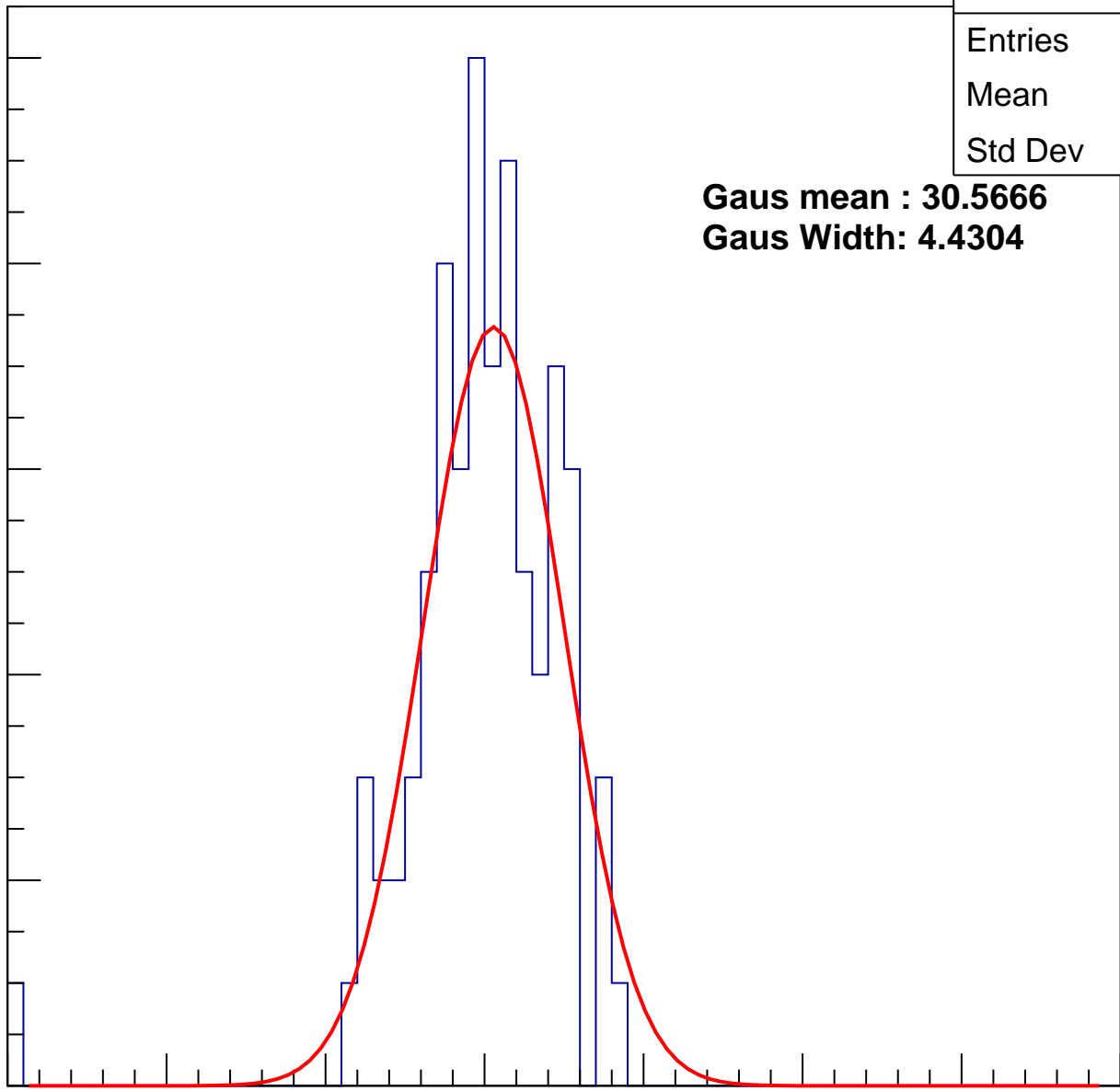
Gaus Width: 4.4304

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch55, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	37.08
Std Dev	3.614

Gaus mean : 37.7020

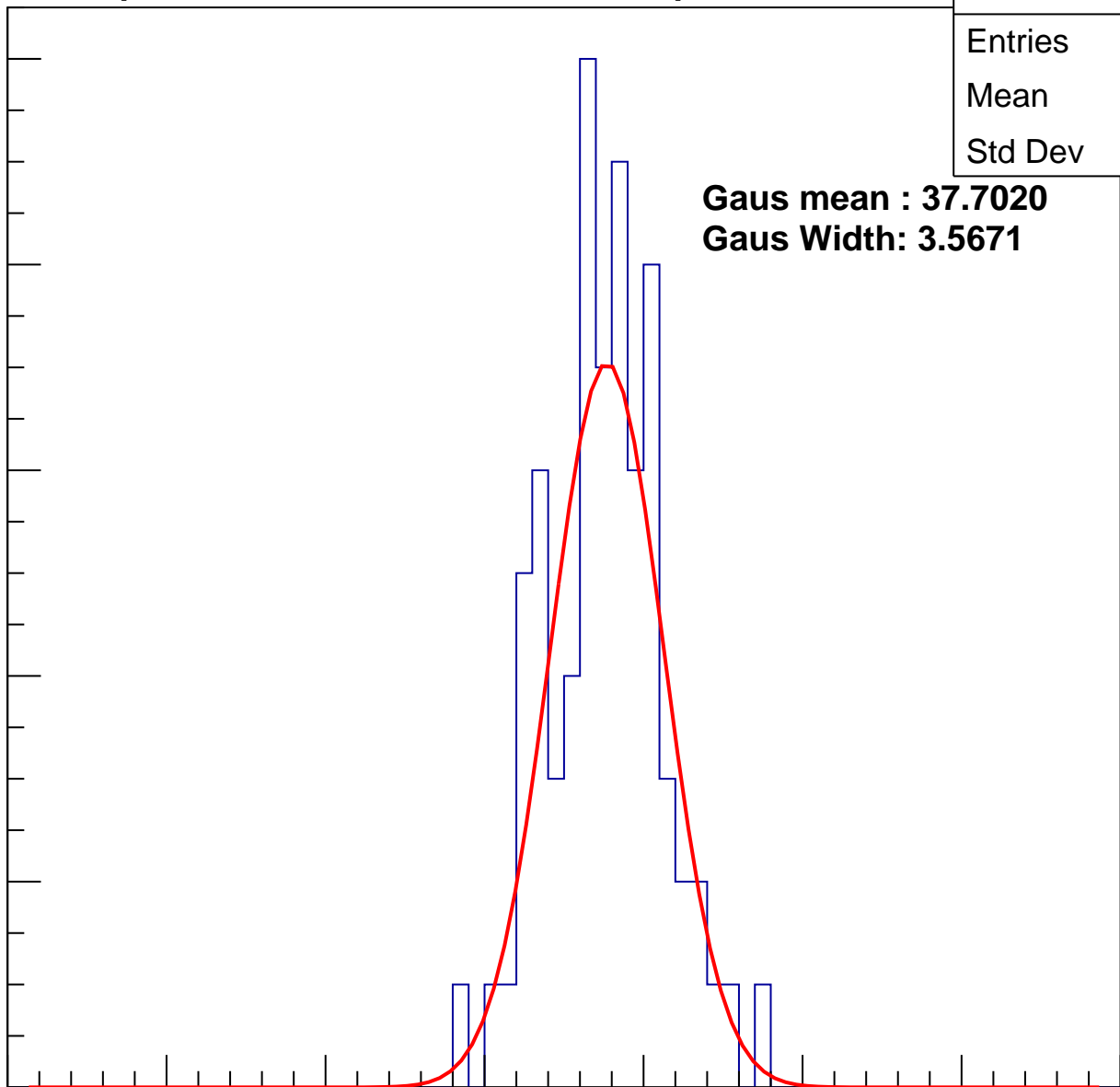
Gaus Width: 3.5671

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch55, adc2

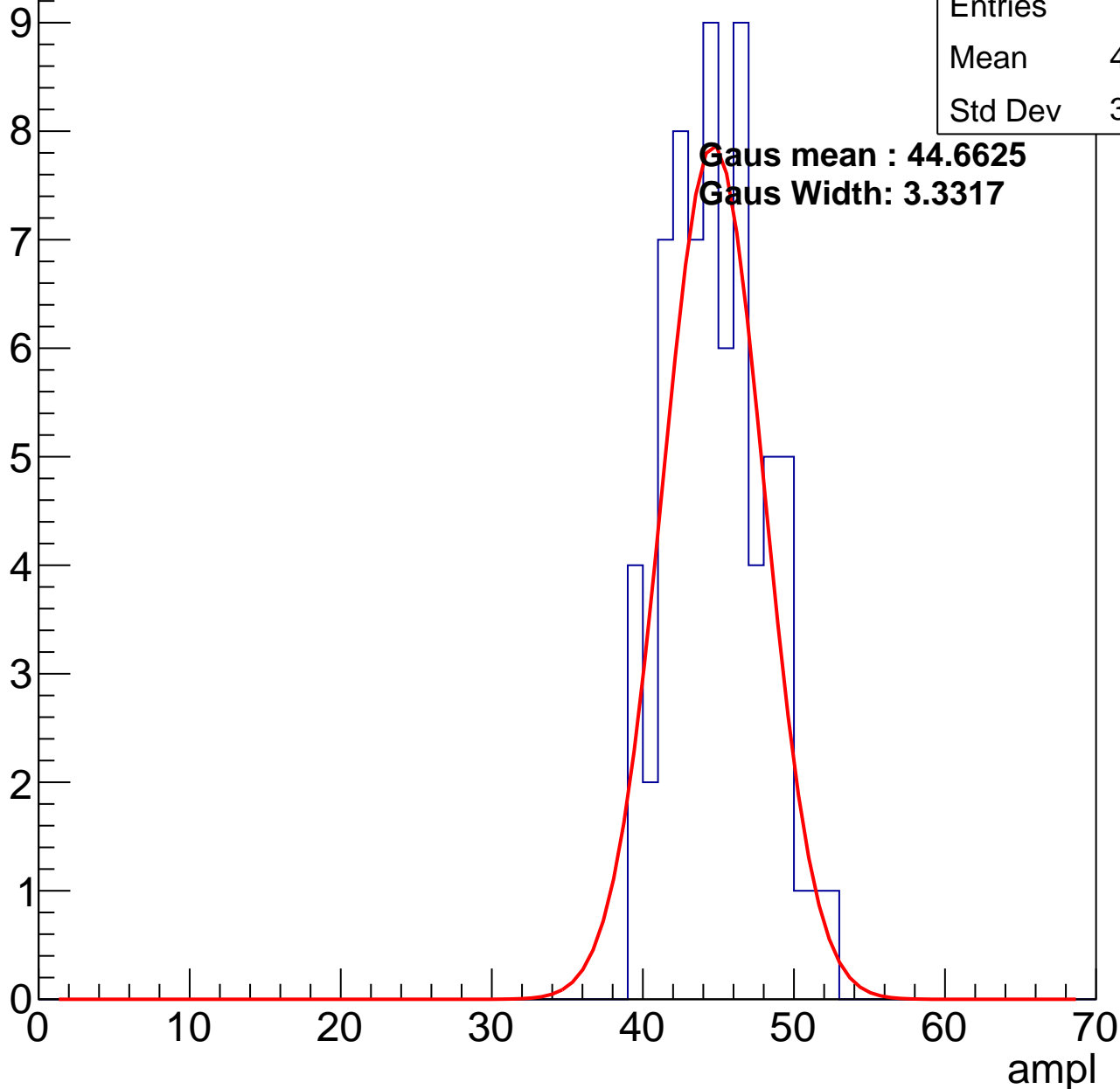
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.43
Std Dev	3.076

Gaus mean : 44.6625

Gaus Width: 3.3317

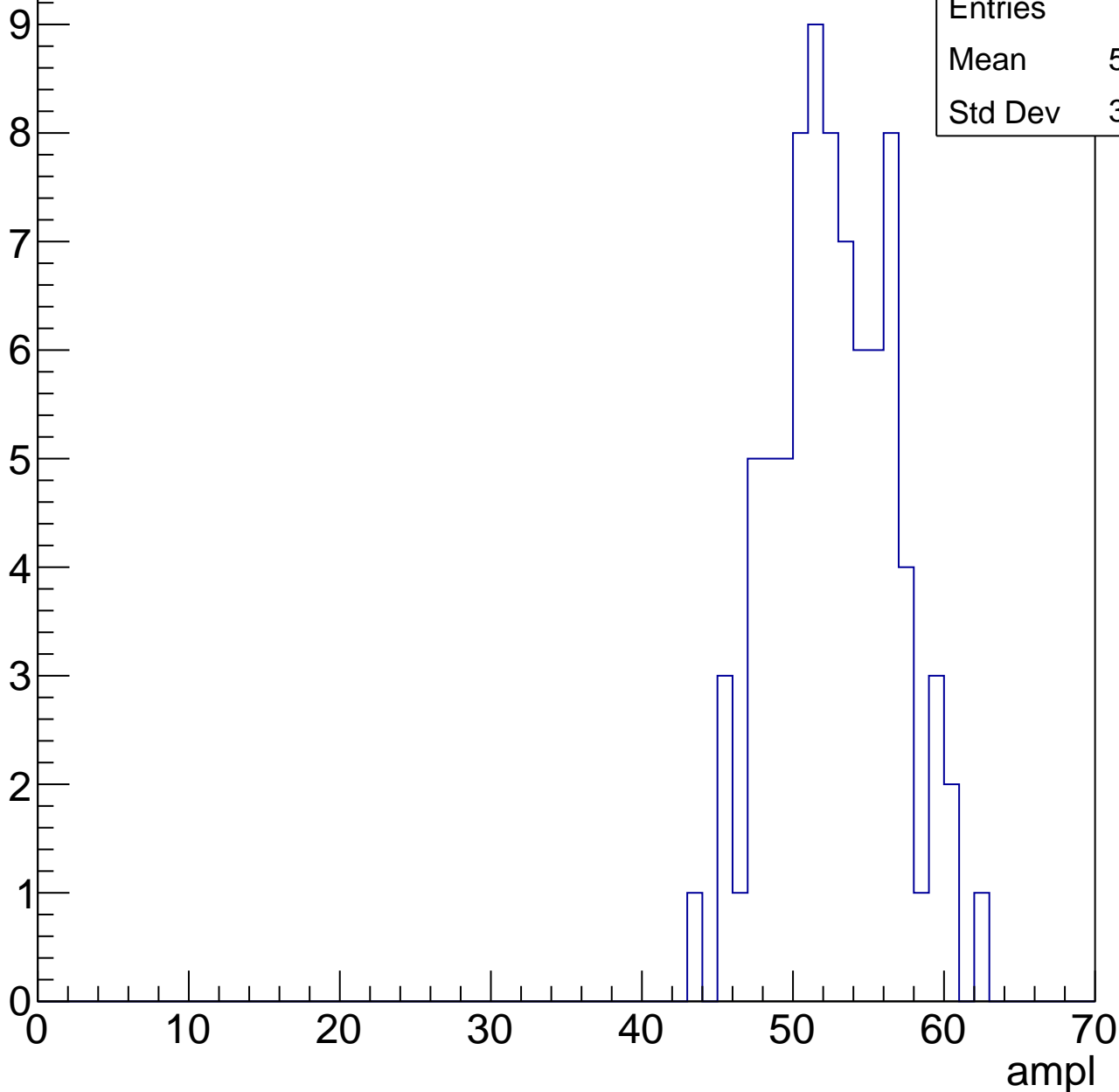


B1L100S, U13-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	52.25
Std Dev	3.917

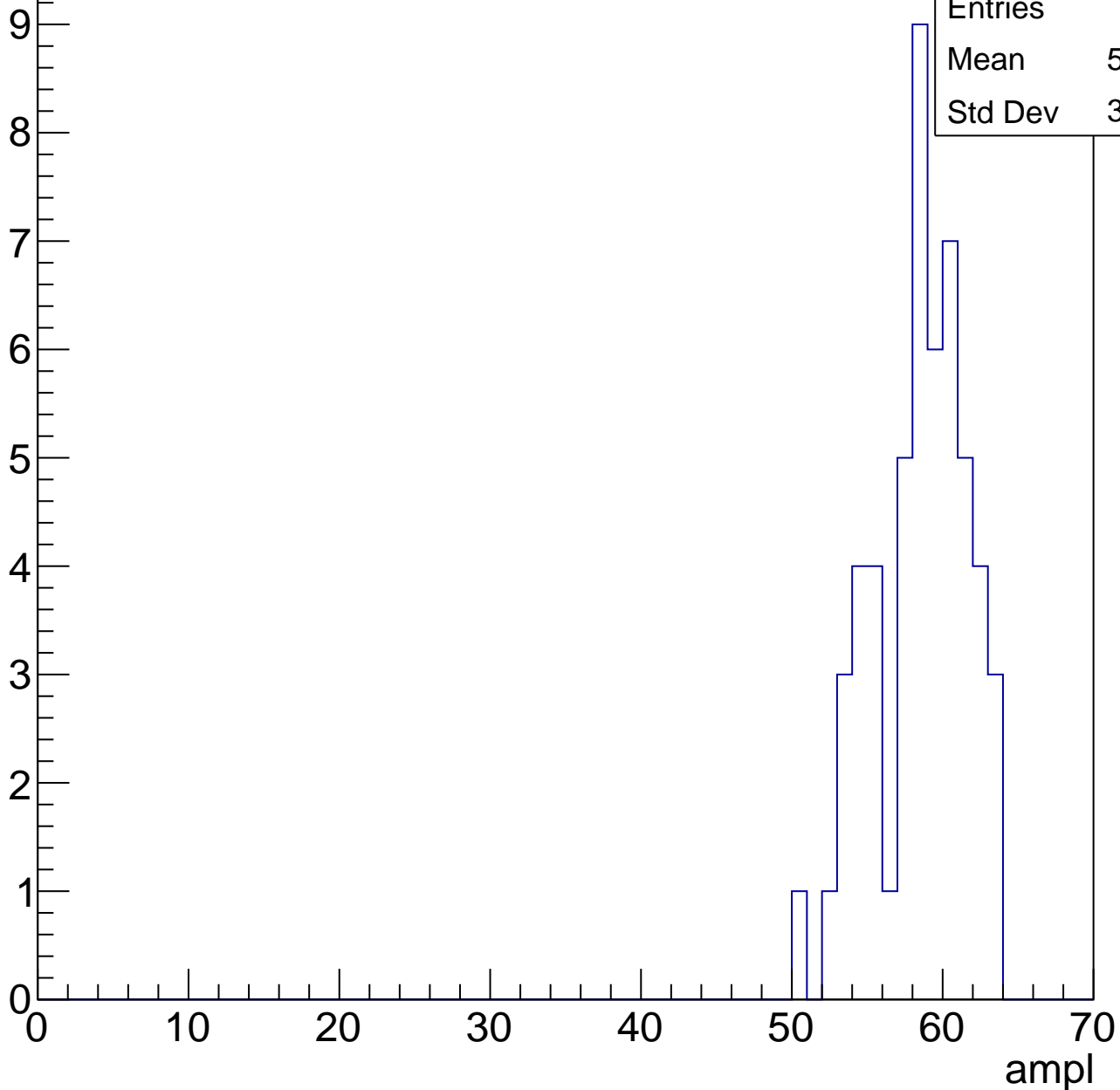


B1L100S, U13-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.04
Std Dev	3.077

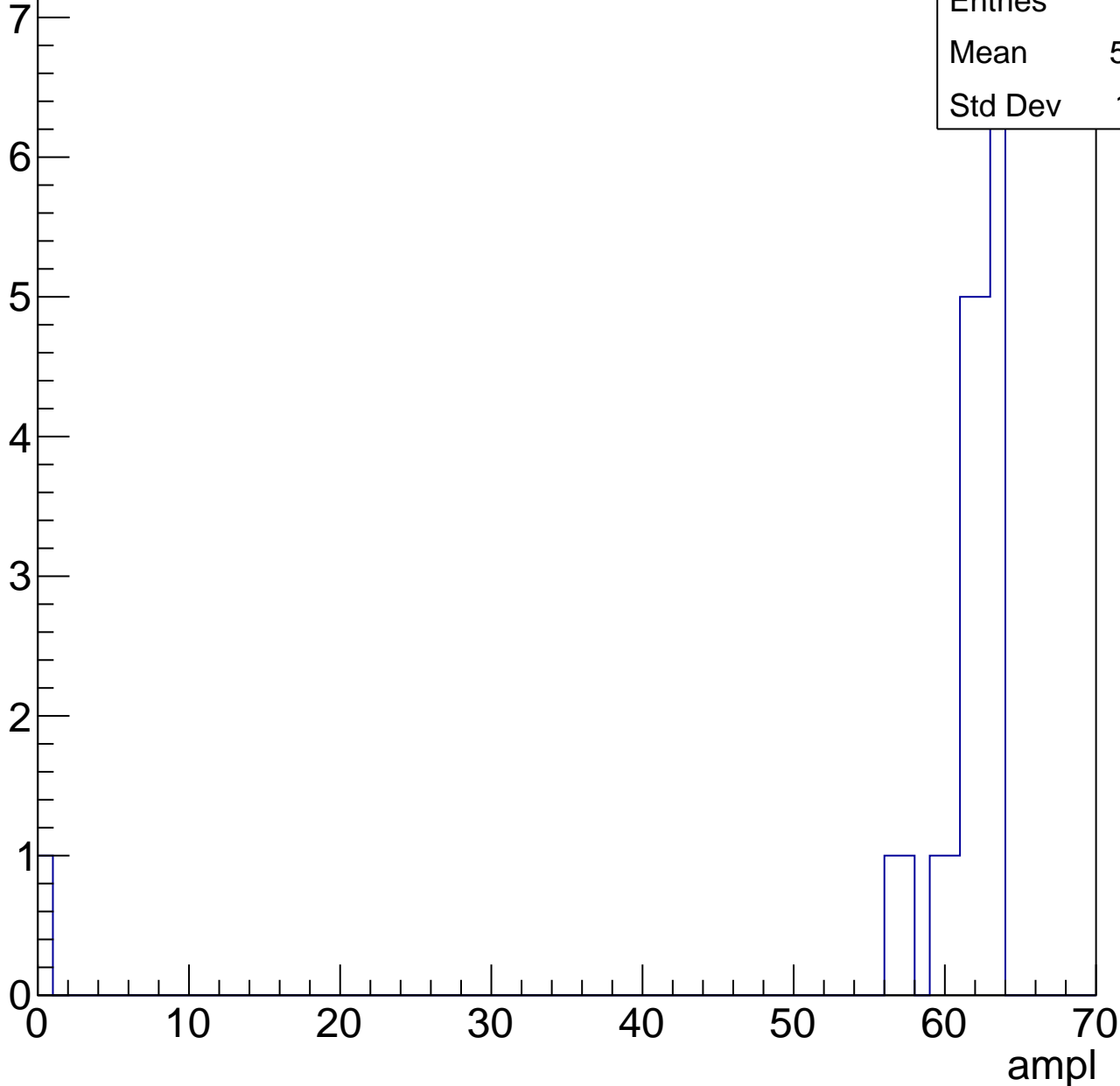


B1L100S, U13-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.55
Std Dev	12.91



B1L100S, U13-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch56, adc0

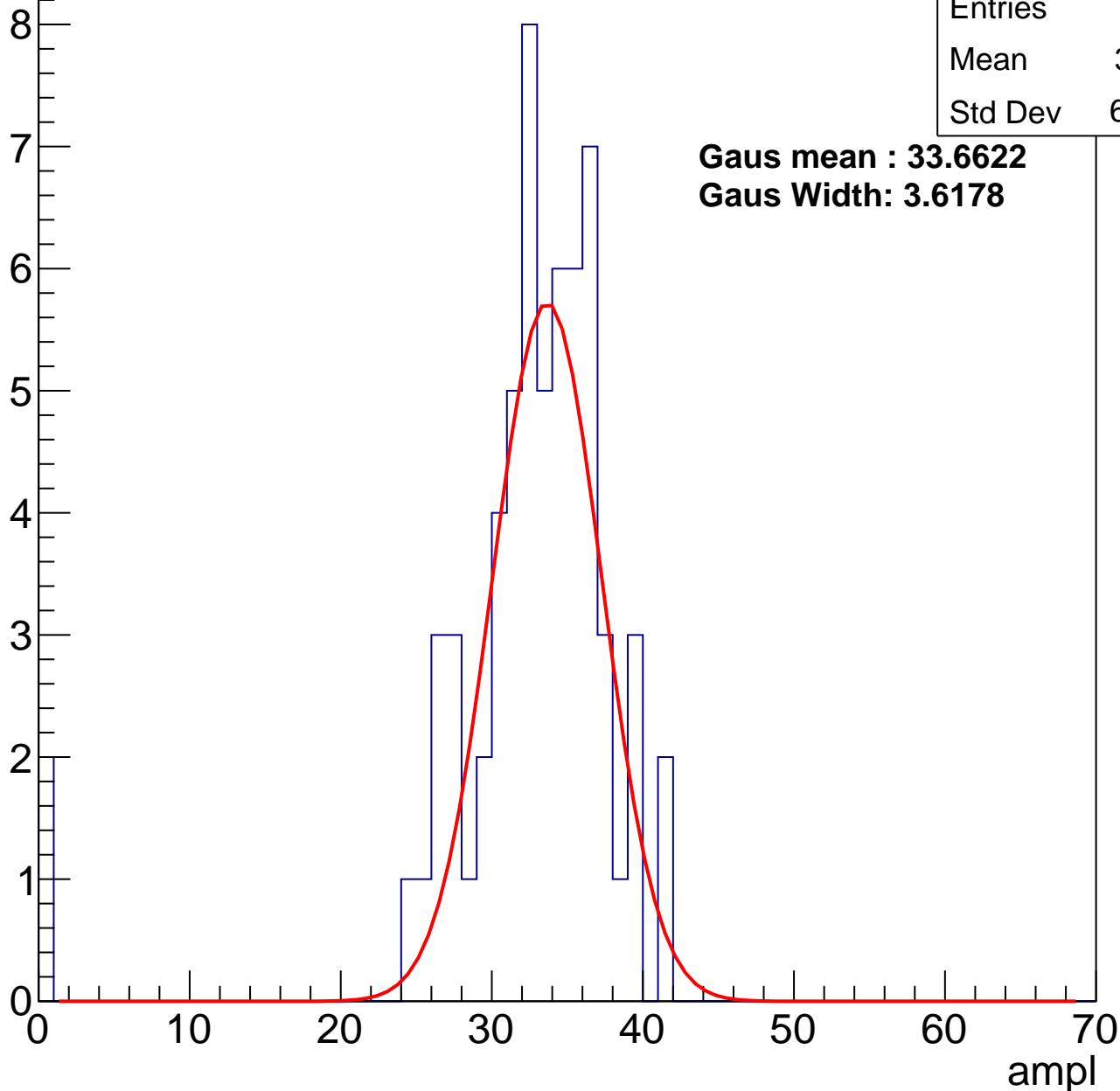
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	31.81
Std Dev	6.914

Gaus mean : 33.6622

Gaus Width: 3.6178



B1L100S, U13-ch56, adc1

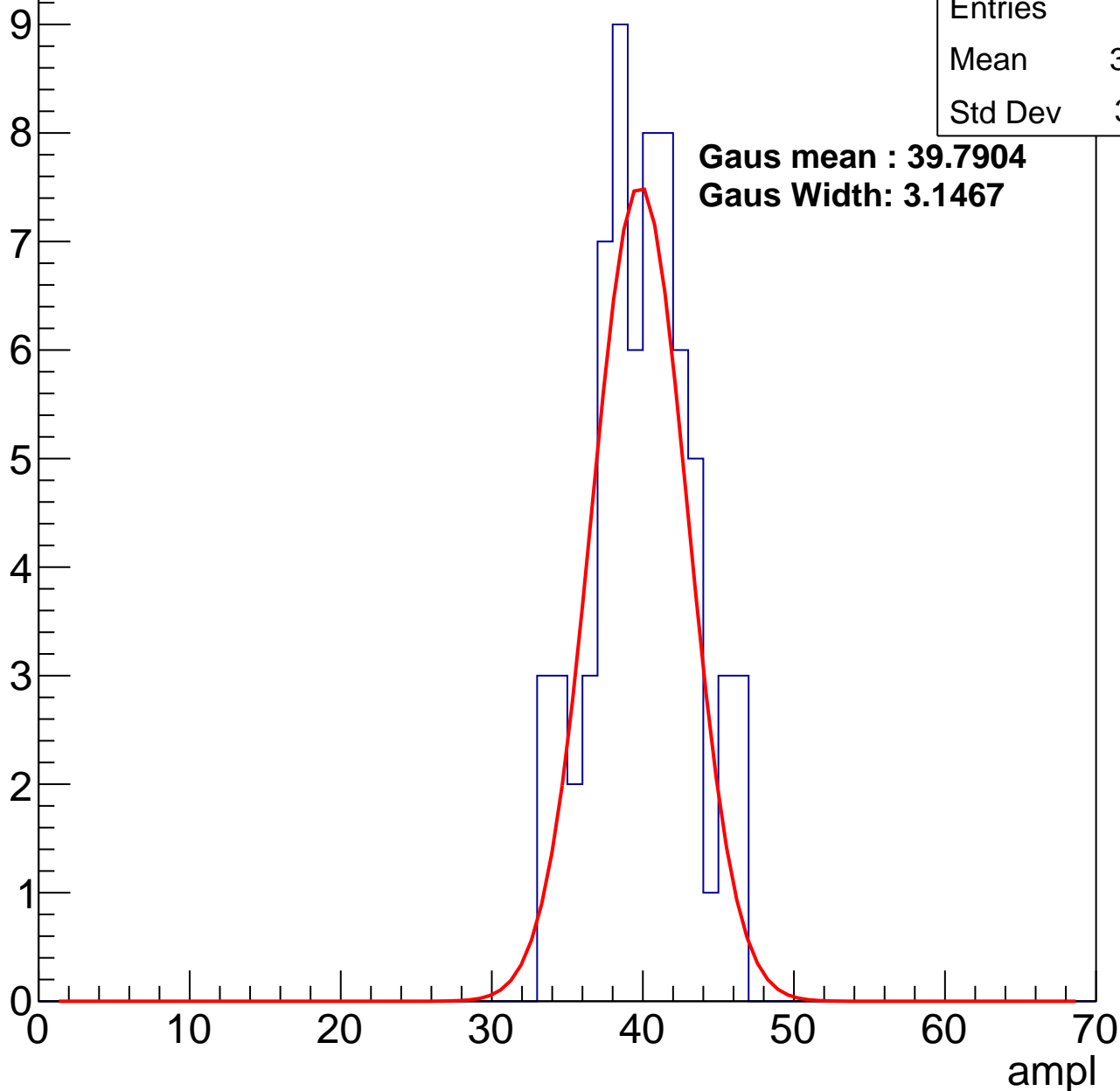
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	39.49
Std Dev	3.271

Gaus mean : 39.7904

Gaus Width: 3.1467



B1L100S, U13-ch56, adc2

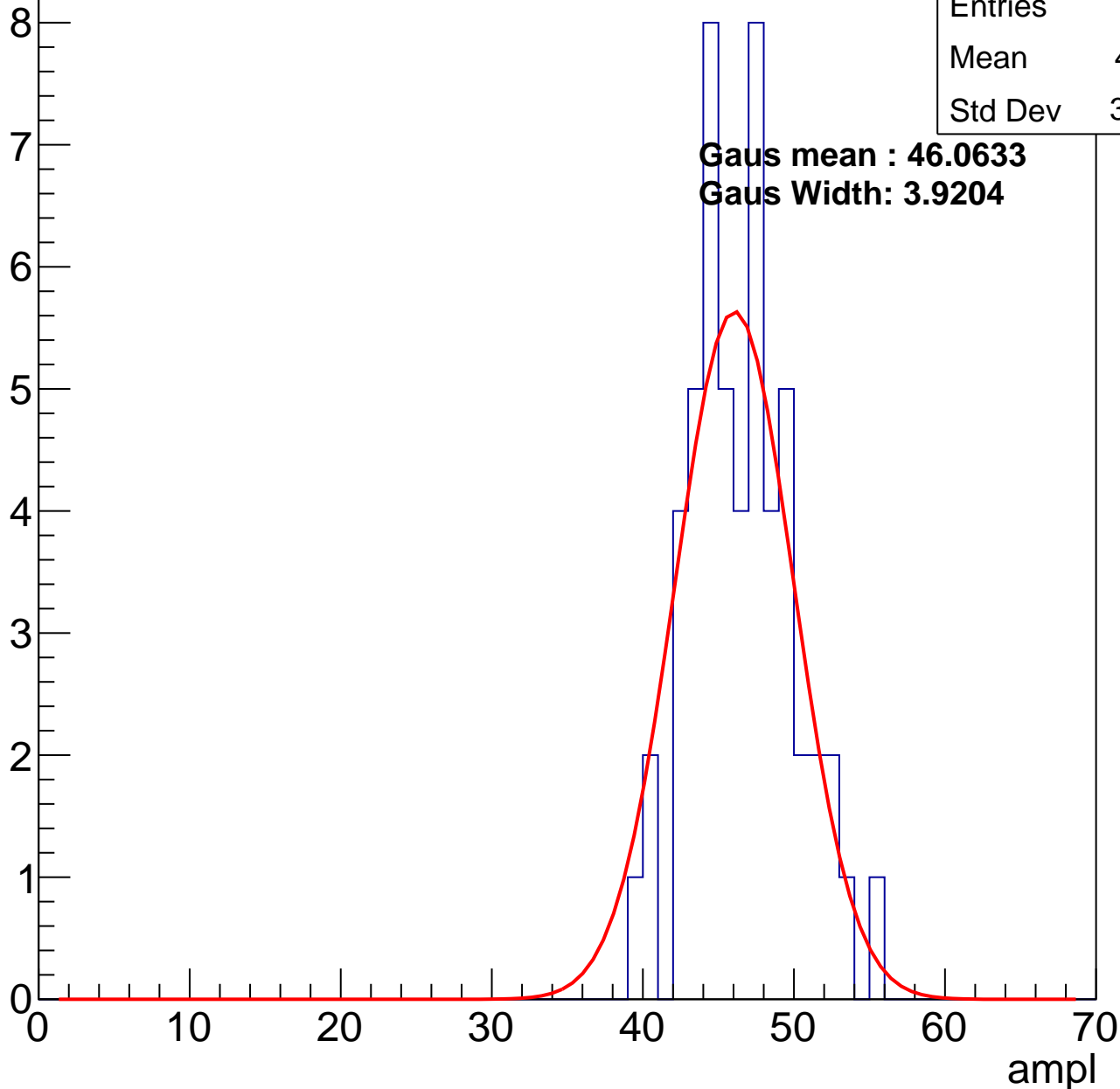
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	46.11
Std Dev	3.387

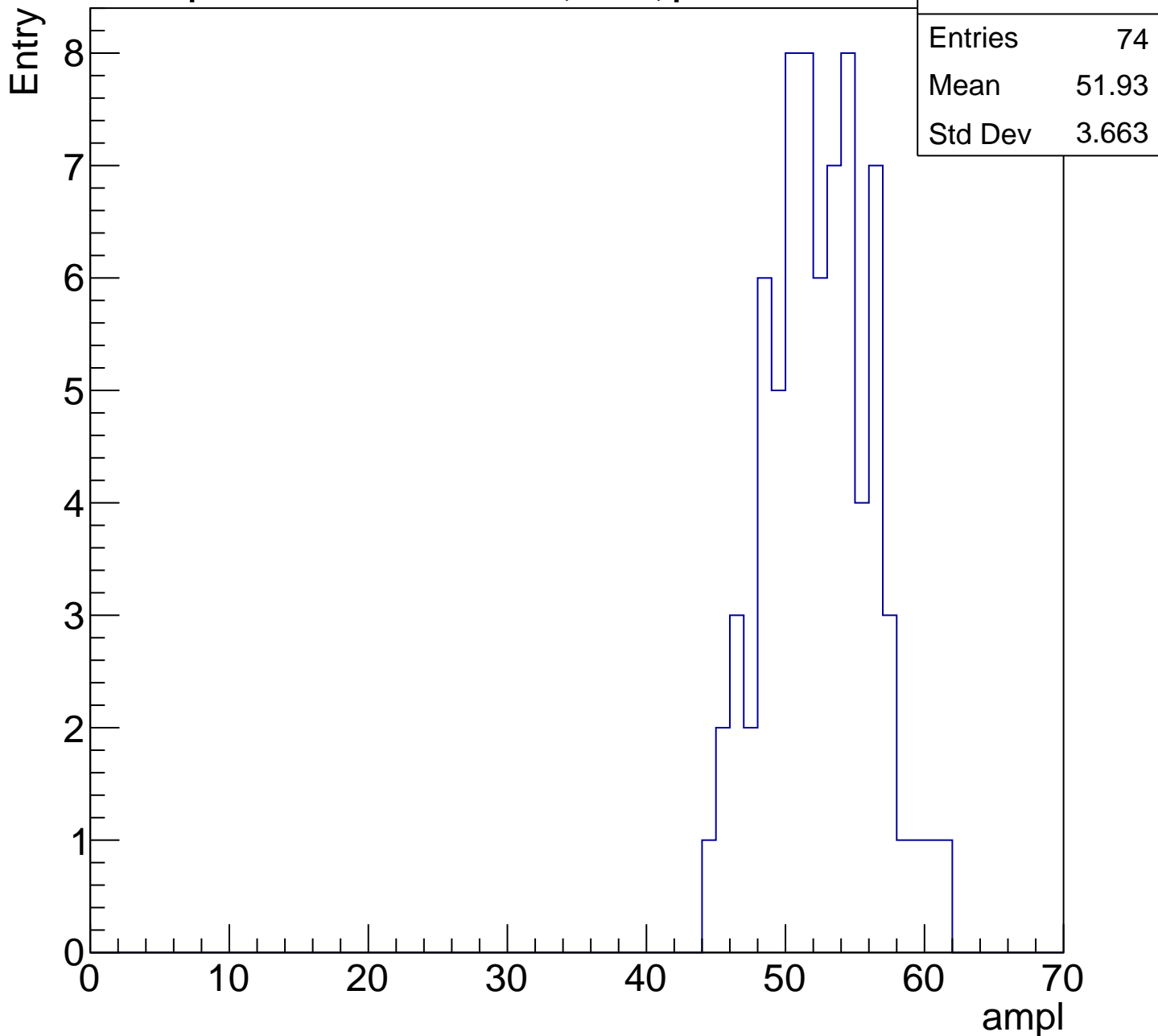
Gaus mean : 46.0633

Gaus Width: 3.9204



B1L100S, U13-ch56, adc3

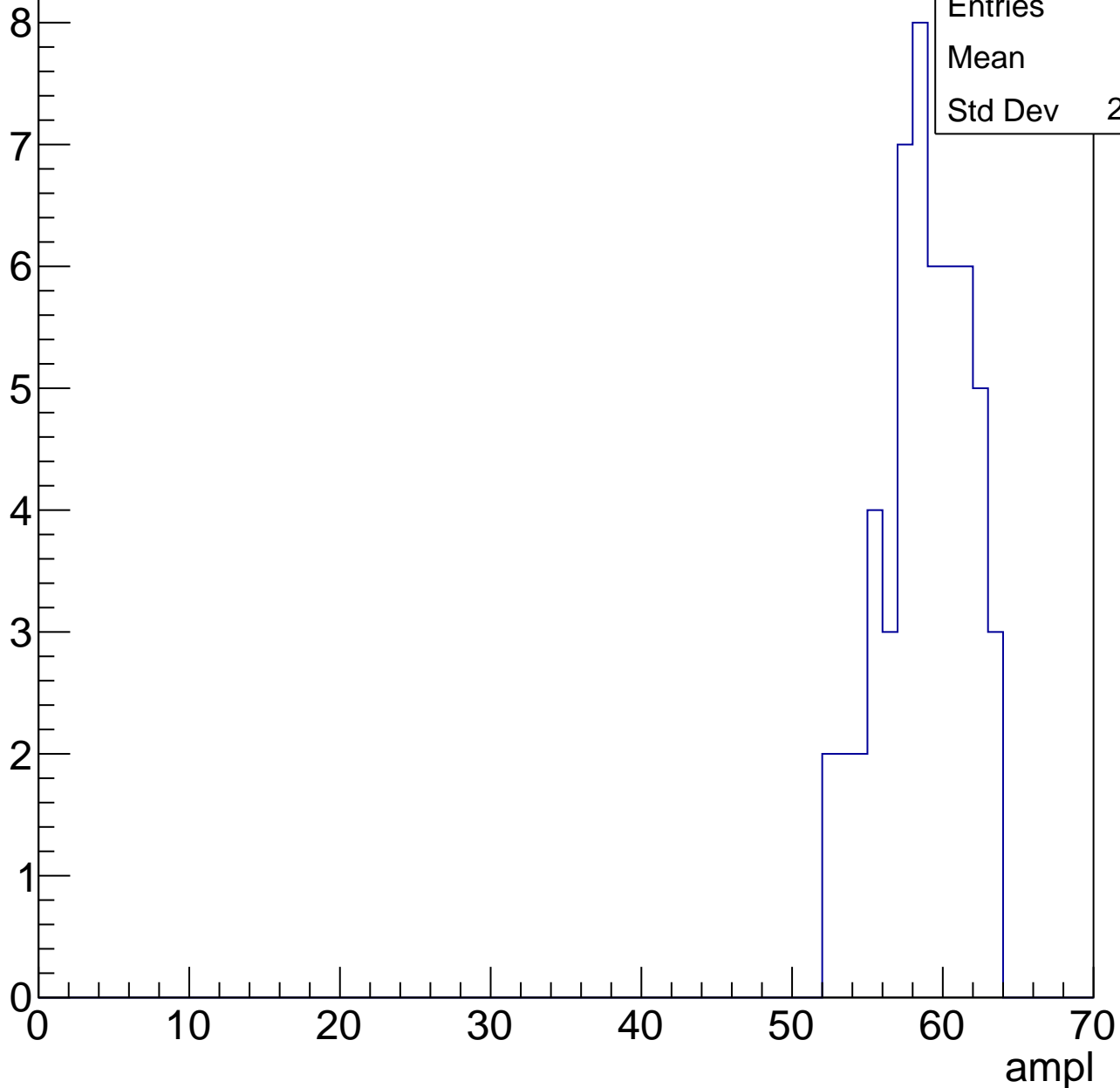
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



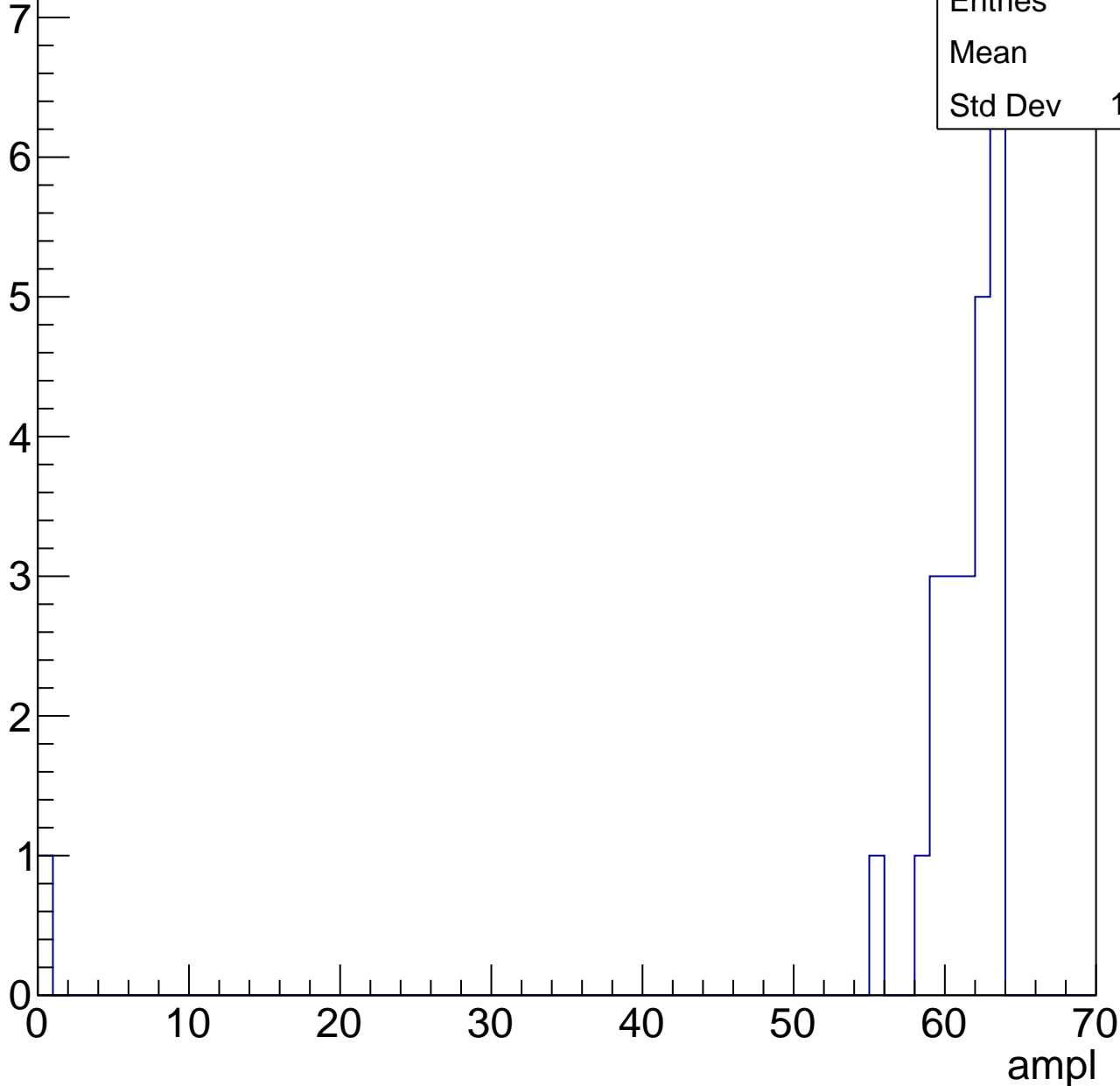
Entries	54
Mean	58.3
Std Dev	2.865

B1L100S, U13-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.5
Std Dev	12.36



B1L100S, U13-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch57, adc0

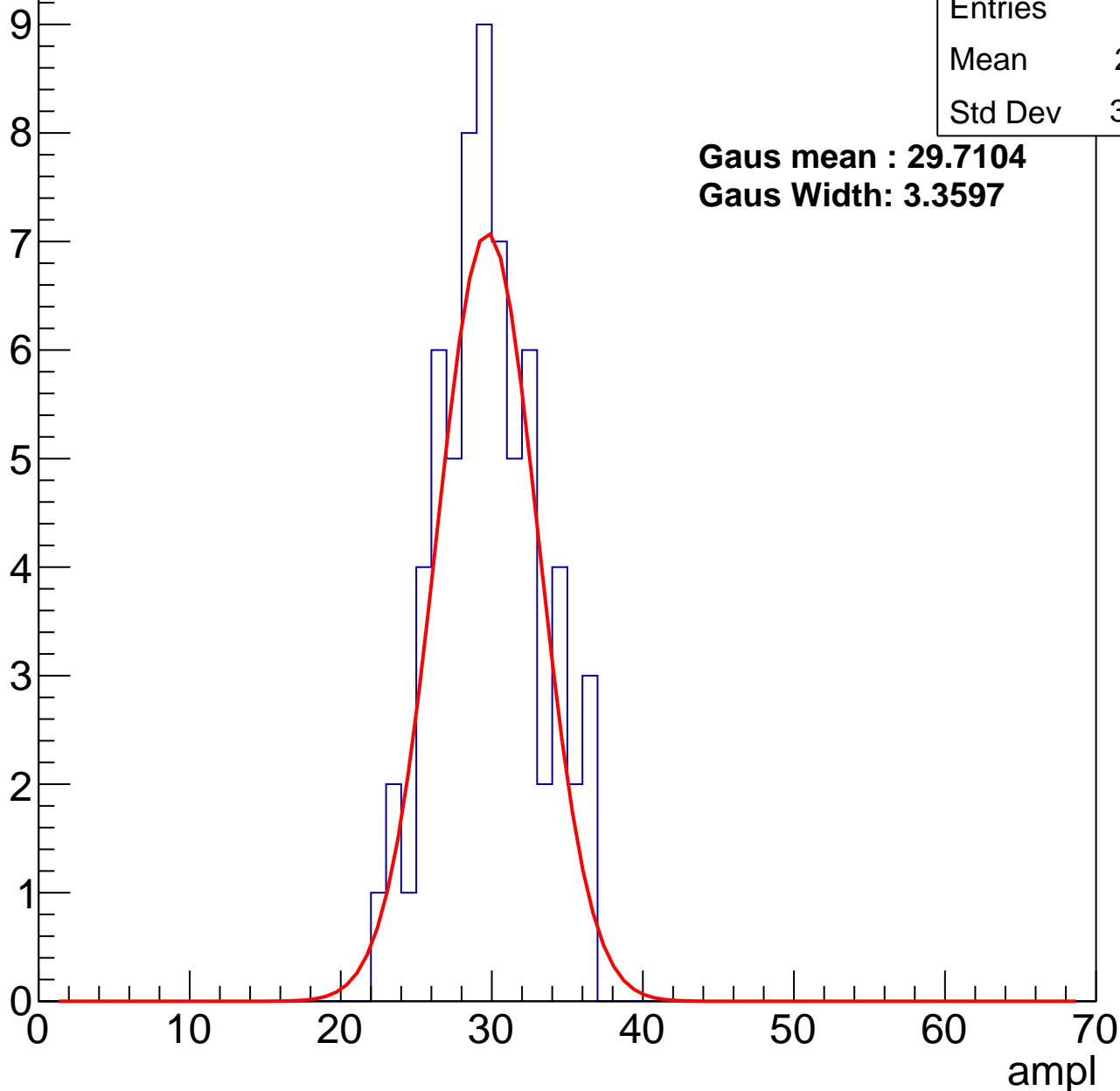
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.31
Std Dev	3.323

Gaus mean : 29.7104

Gaus Width: 3.3597



B1L100S, U13-ch57, adc1

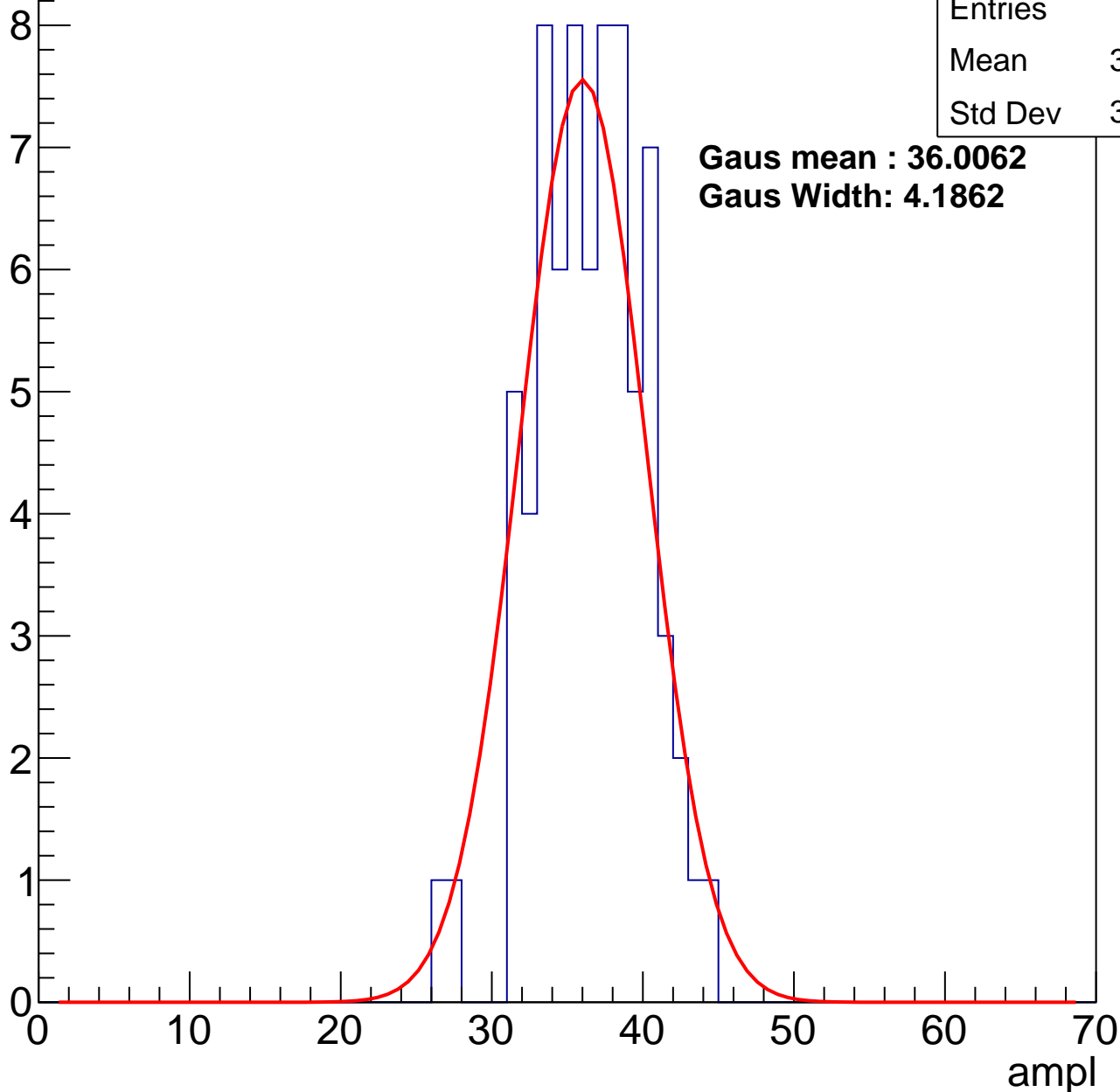
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.07
Std Dev	3.546

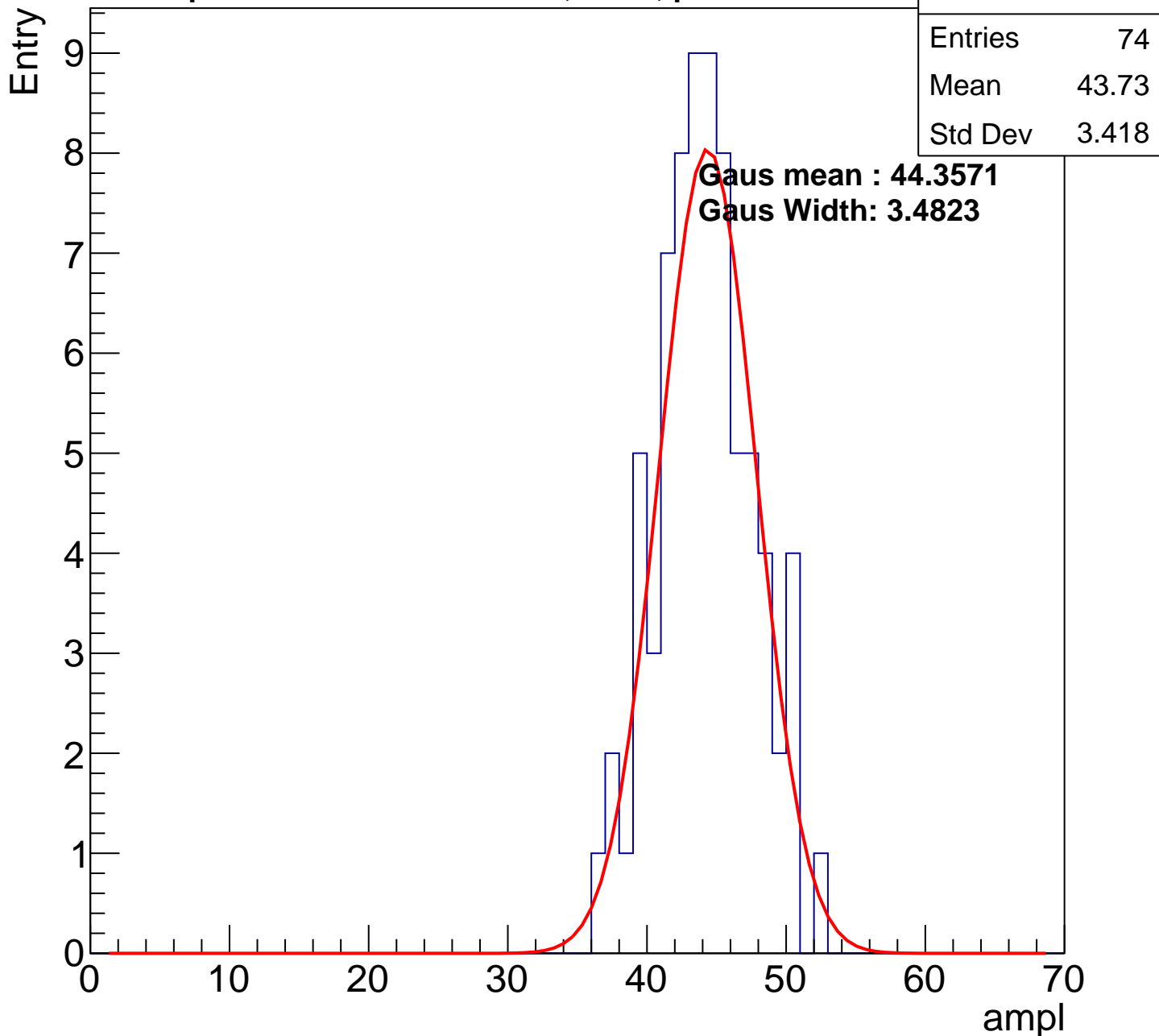
Gaus mean : 36.0062

Gaus Width: 4.1862



B1L100S, U13-ch57, adc2

calib_packv5_042523_0143.root, FC#4, port A2

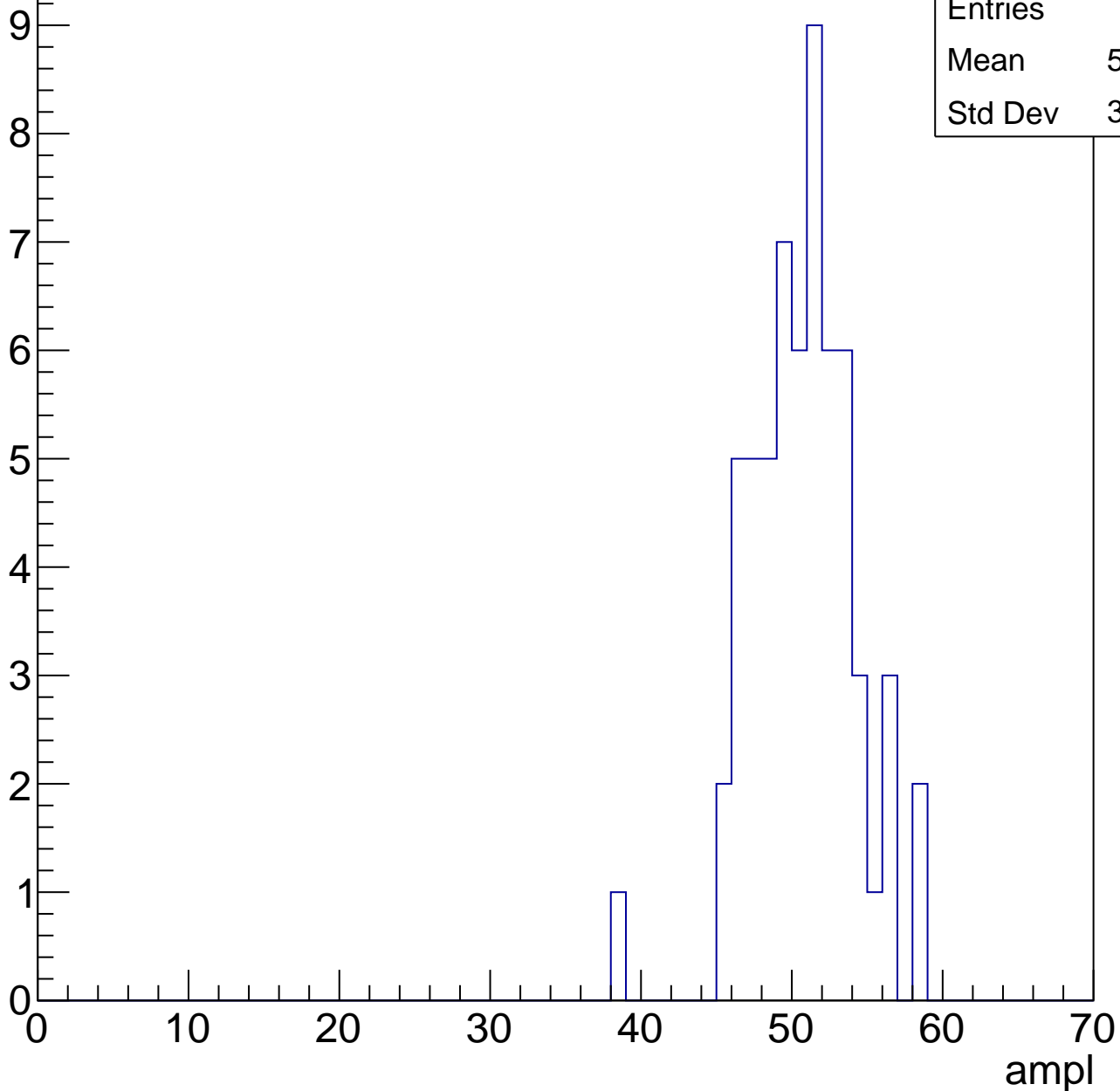


B1L100S, U13-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.26
Std Dev	3.478

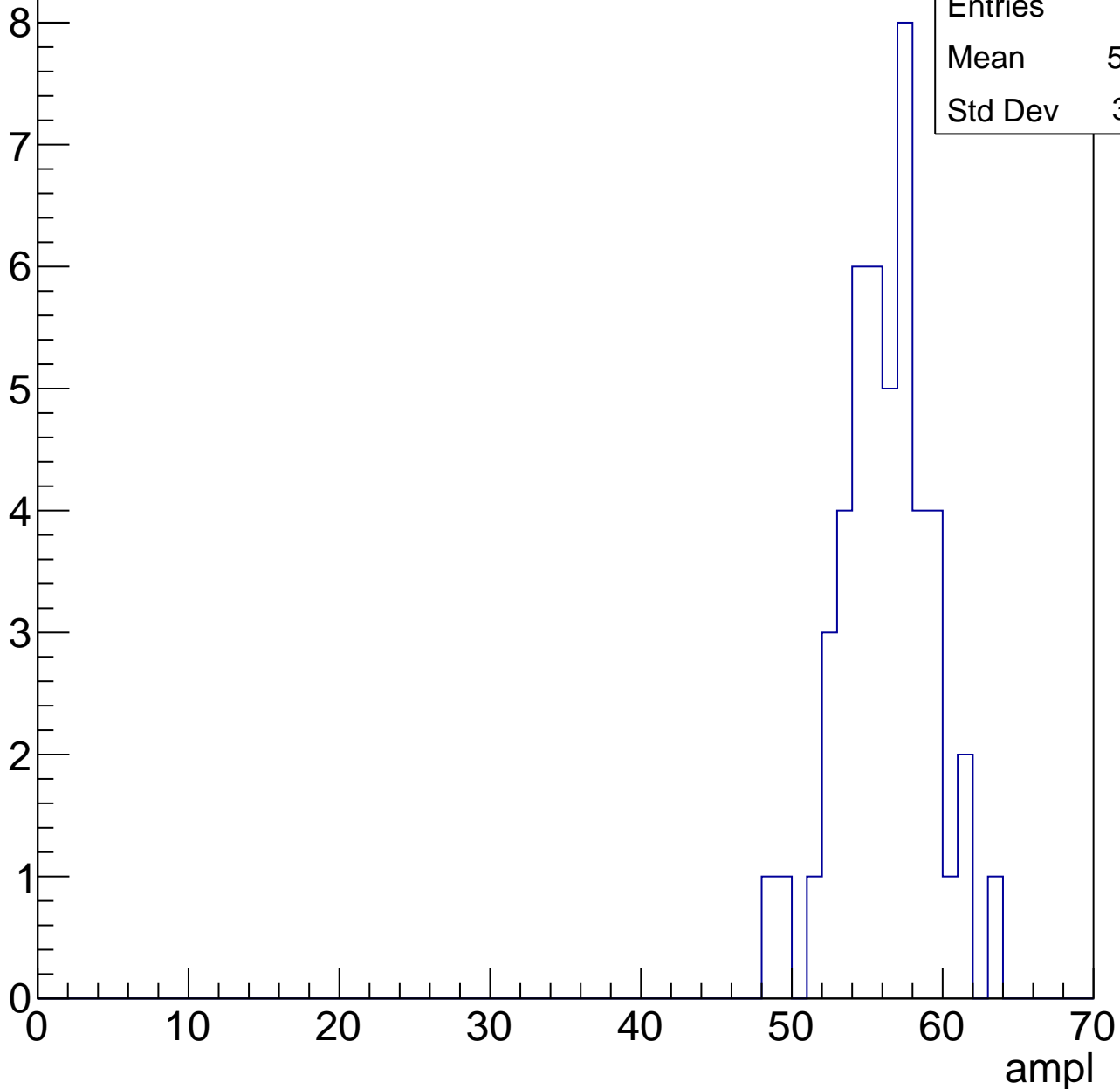


B1L100S, U13-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	55.72
Std Dev	3.001



B1L100S, U13-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.73
Std Dev	8.63

ampl

0

10

20

30

40

50

60

70

B1L100S, U13-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch58, adc0

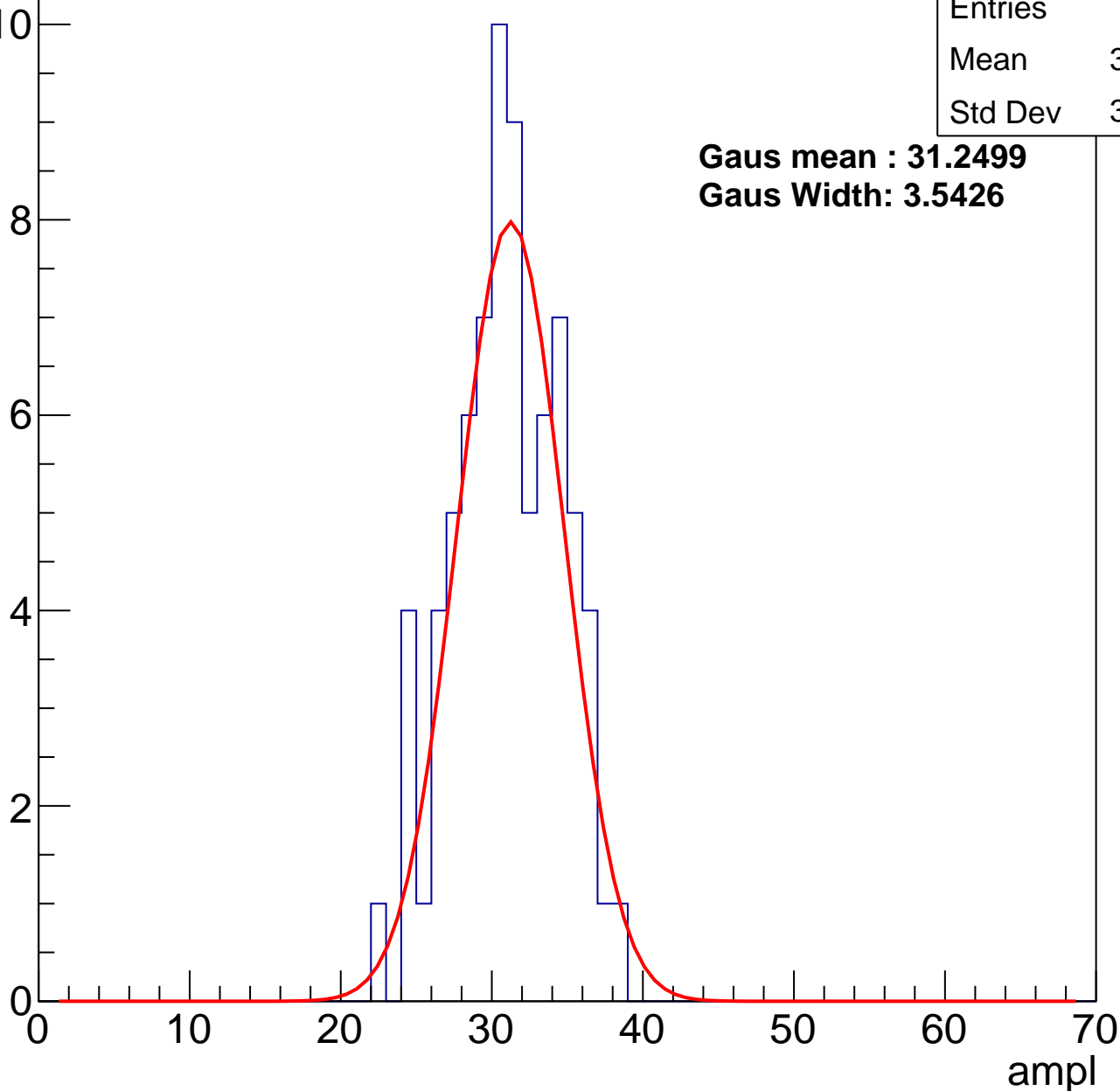
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.55
Std Dev	3.492

Gaus mean : 31.2499

Gaus Width: 3.5426



B1L100S, U13-ch58, adc1

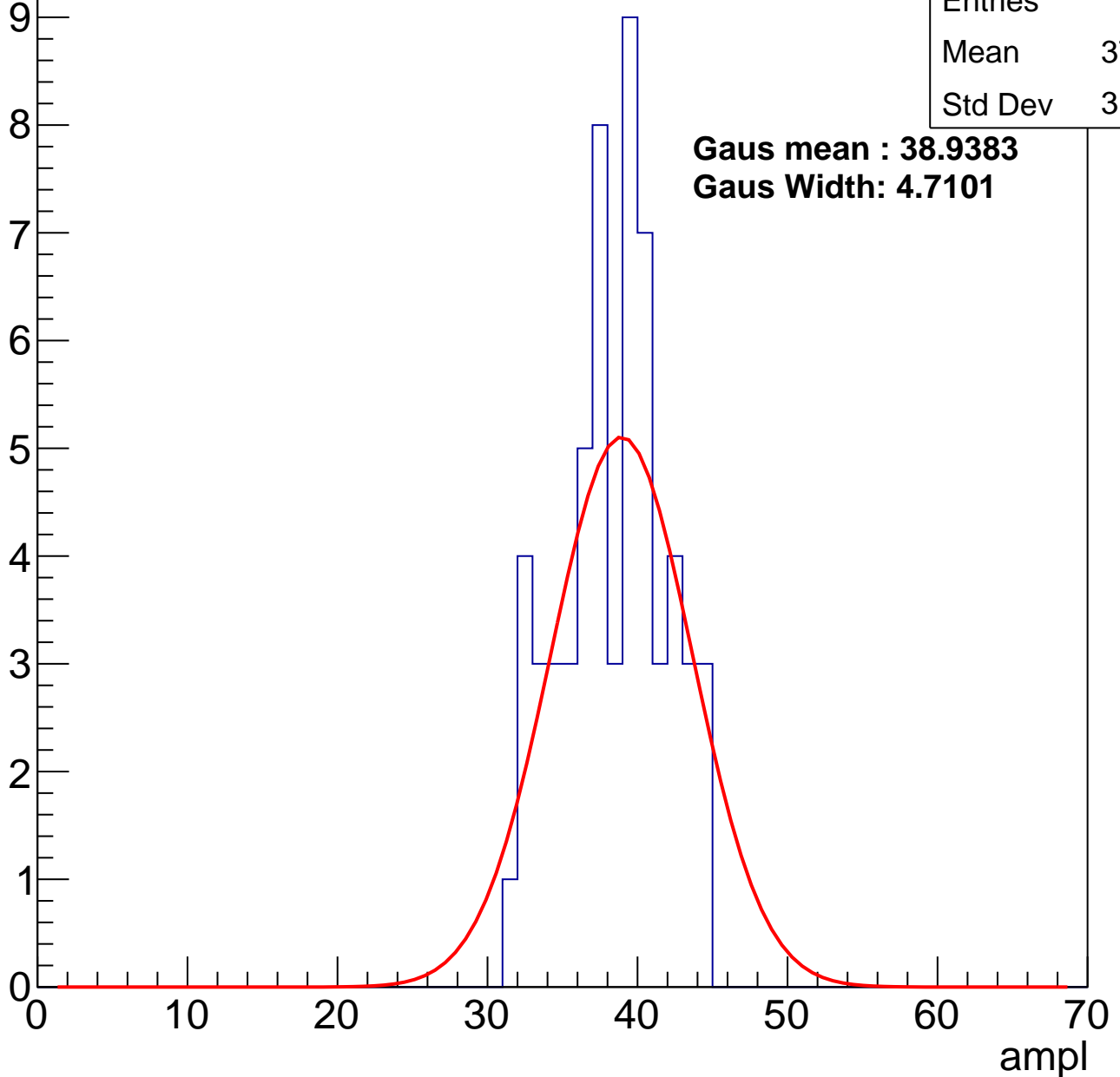
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.93
Std Dev	3.399

Gaus mean : 38.9383

Gaus Width: 4.7101



B1L100S, U13-ch58, adc2

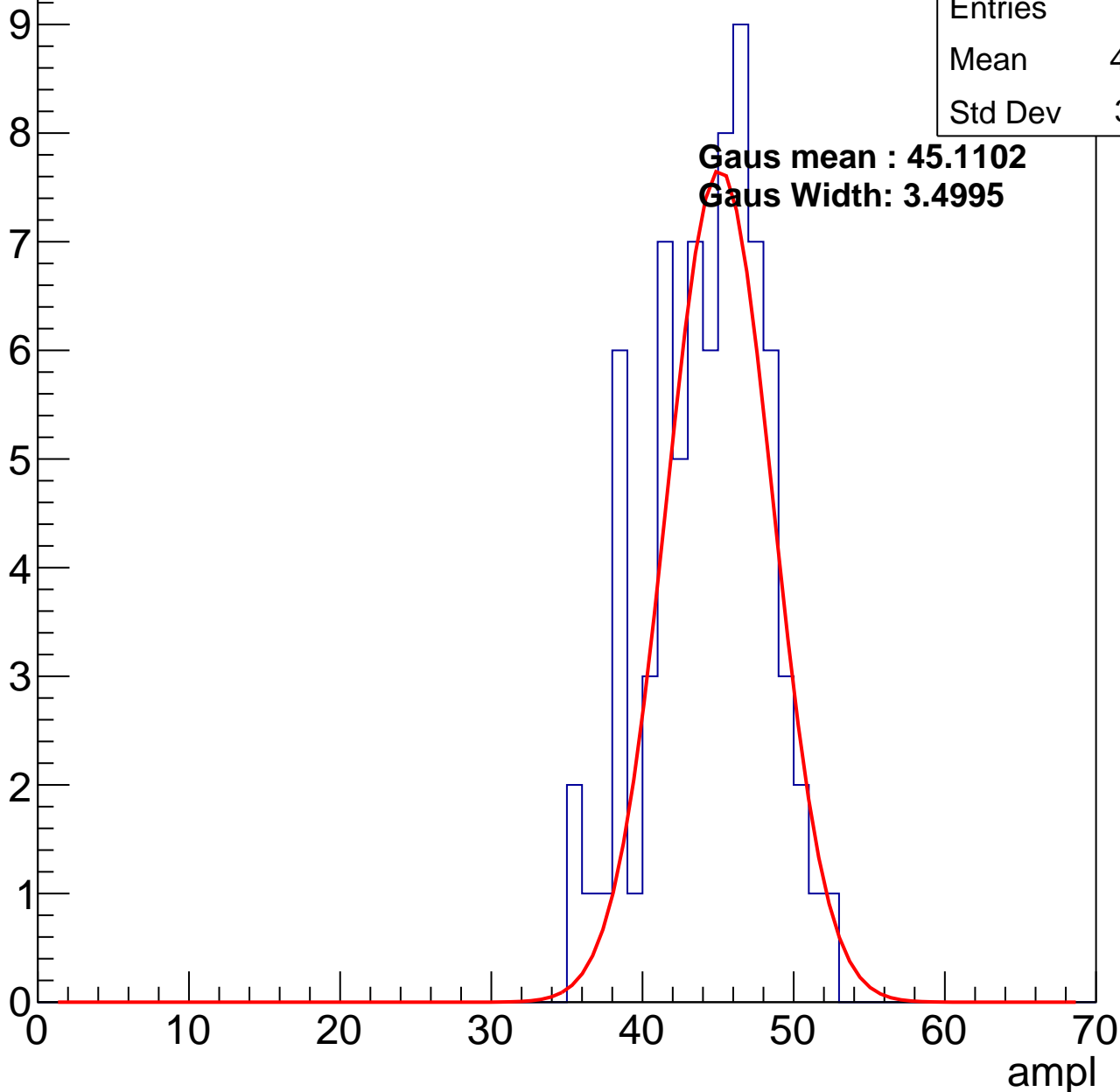
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	43.86
Std Dev	3.831

Gaus mean : 45.1102

Gaus Width: 3.4995

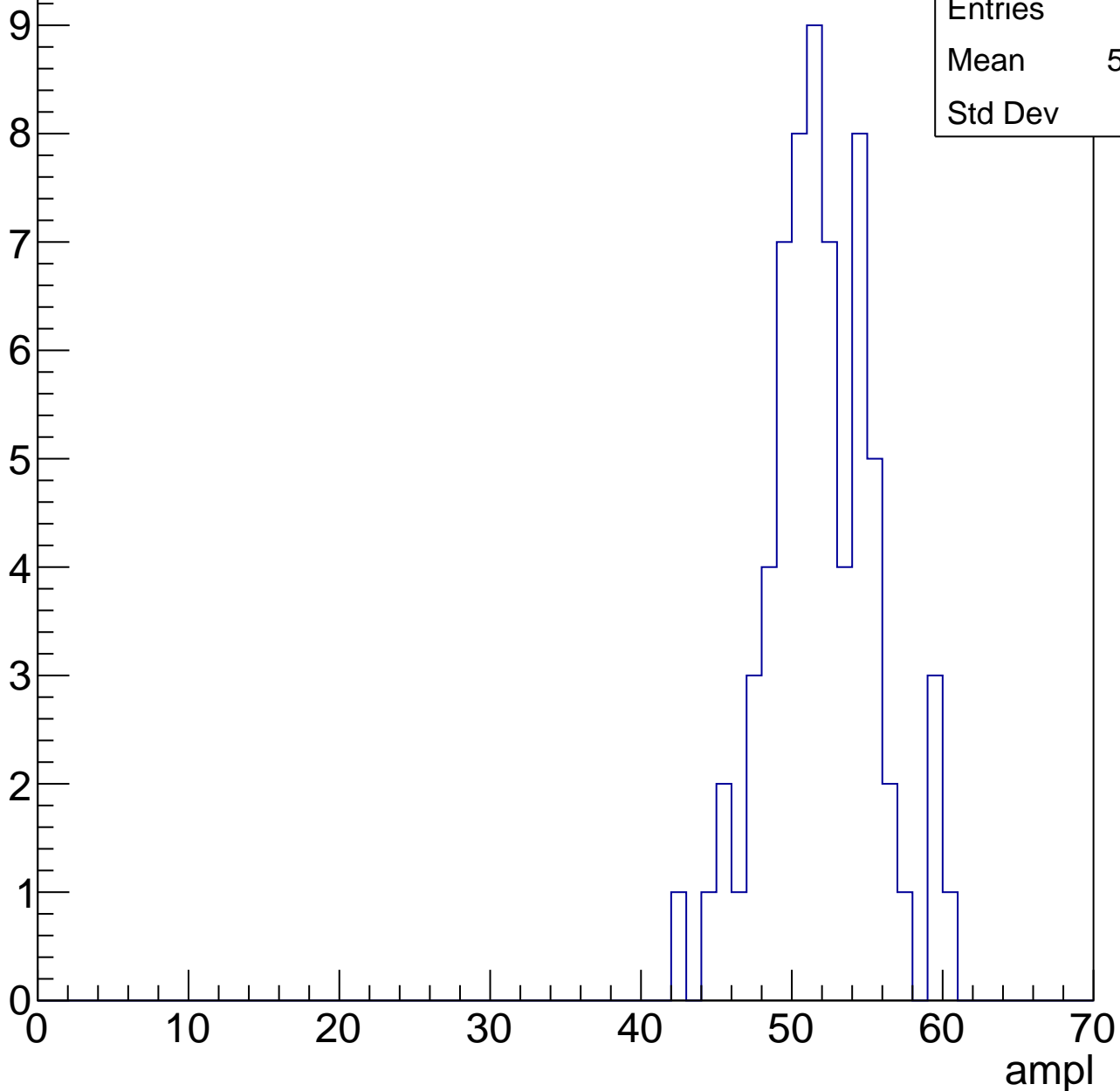


B1L100S, U13-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	51.43
Std Dev	3.6

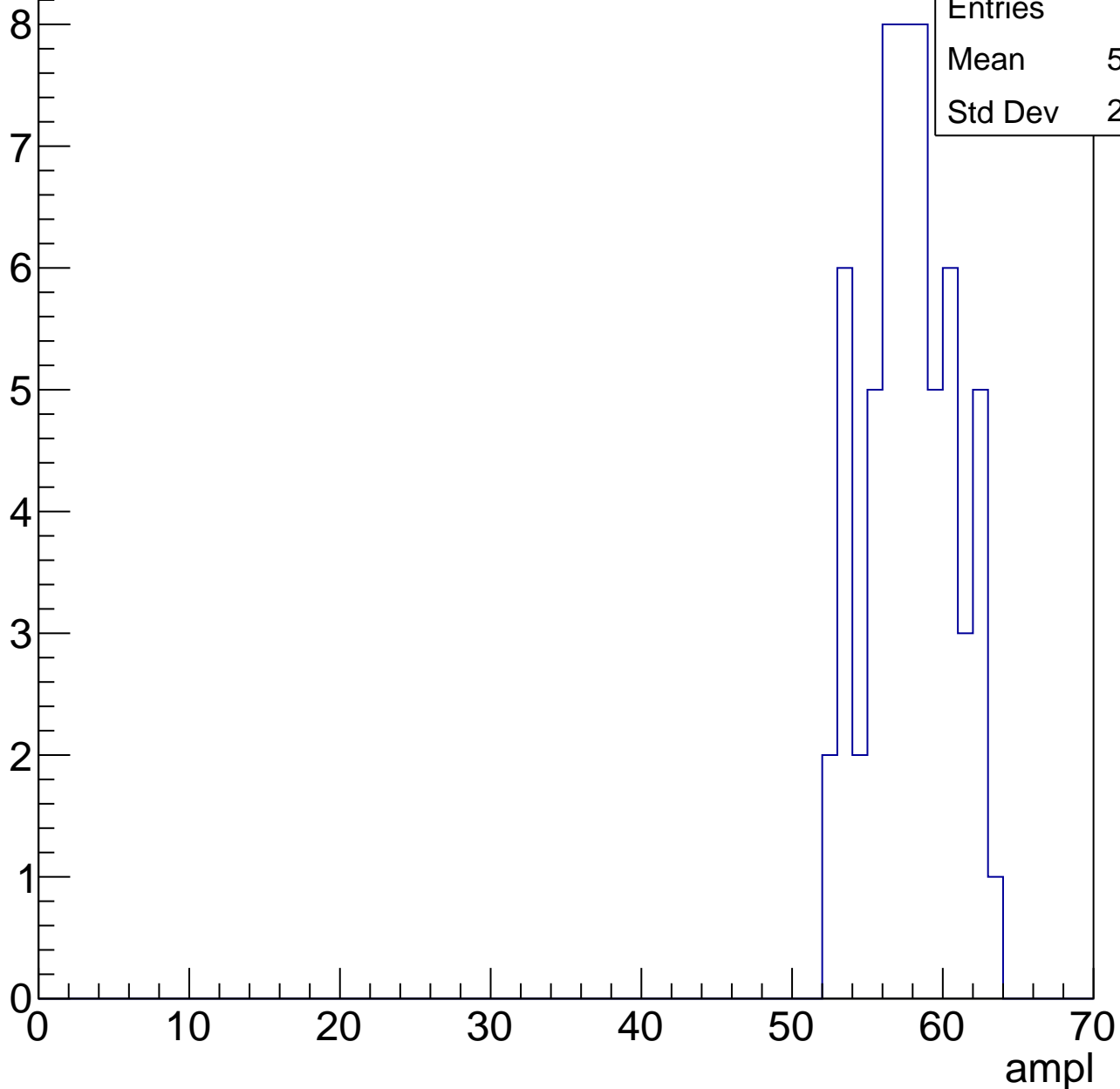


B1L100S, U13-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	57.36
Std Dev	2.839

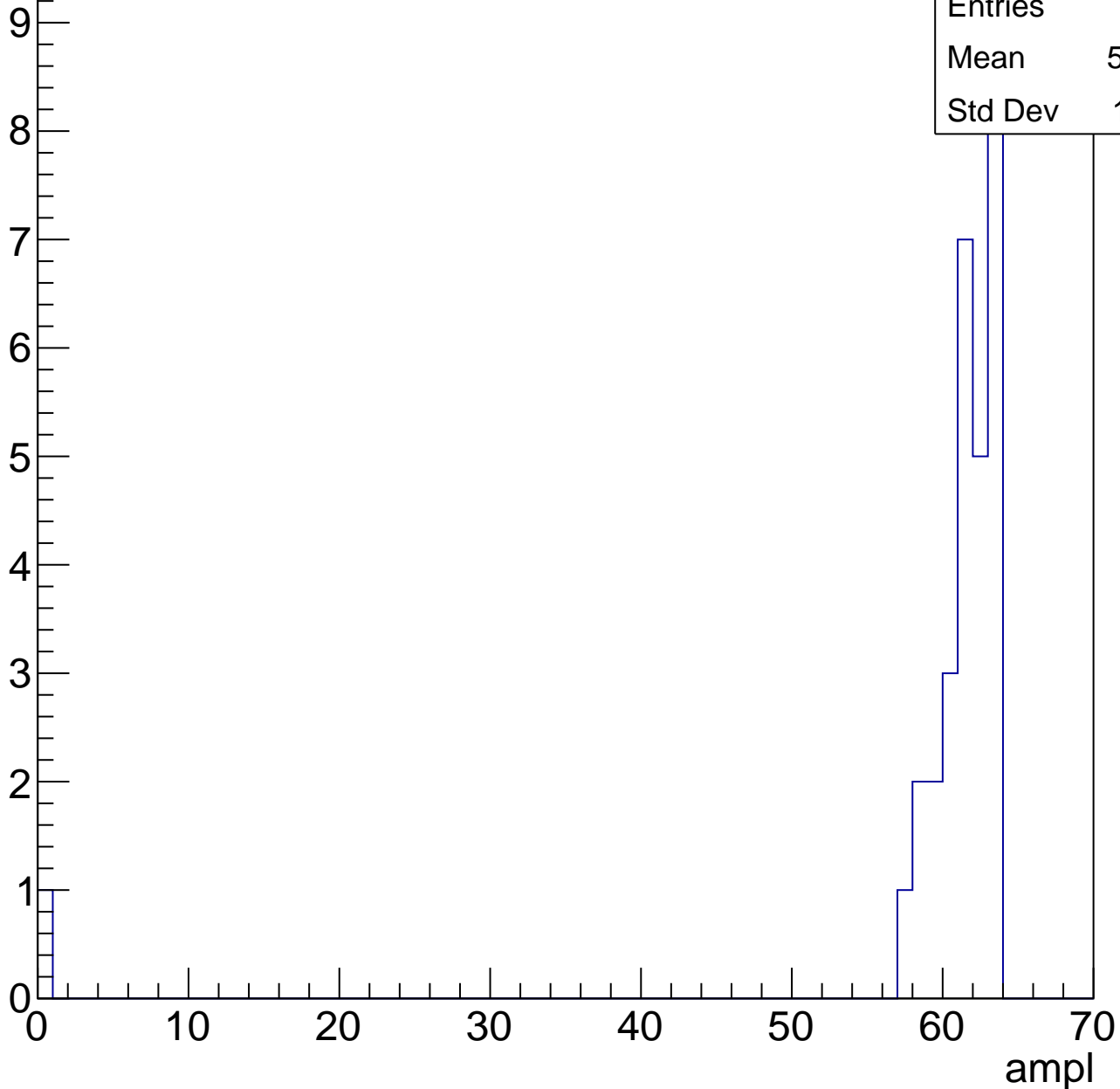


B1L100S, U13-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	59.17
Std Dev	11.11



B1L100S, U13-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

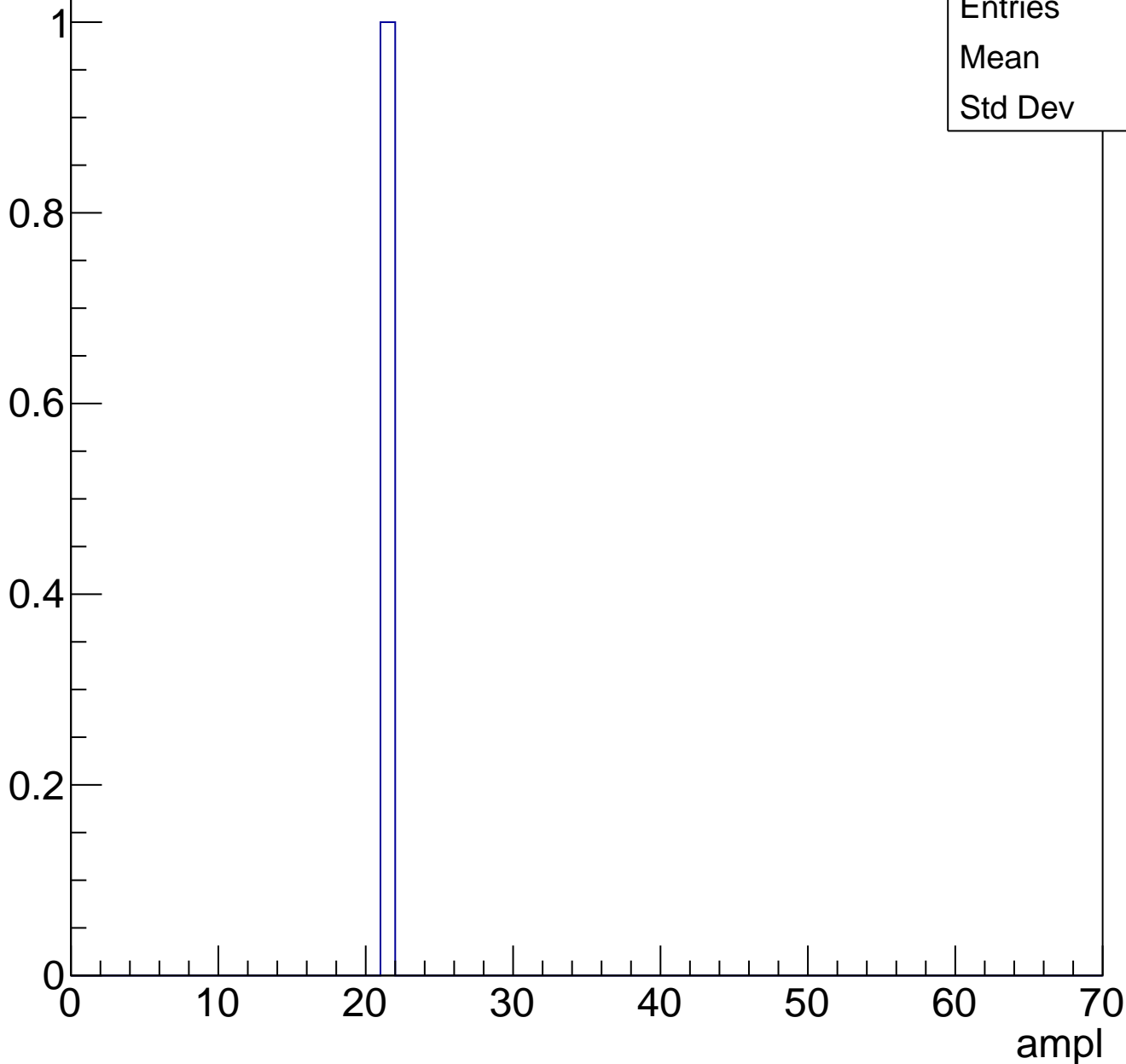


Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	21
Std Dev	0

B1L100S, U13-ch59, adc0

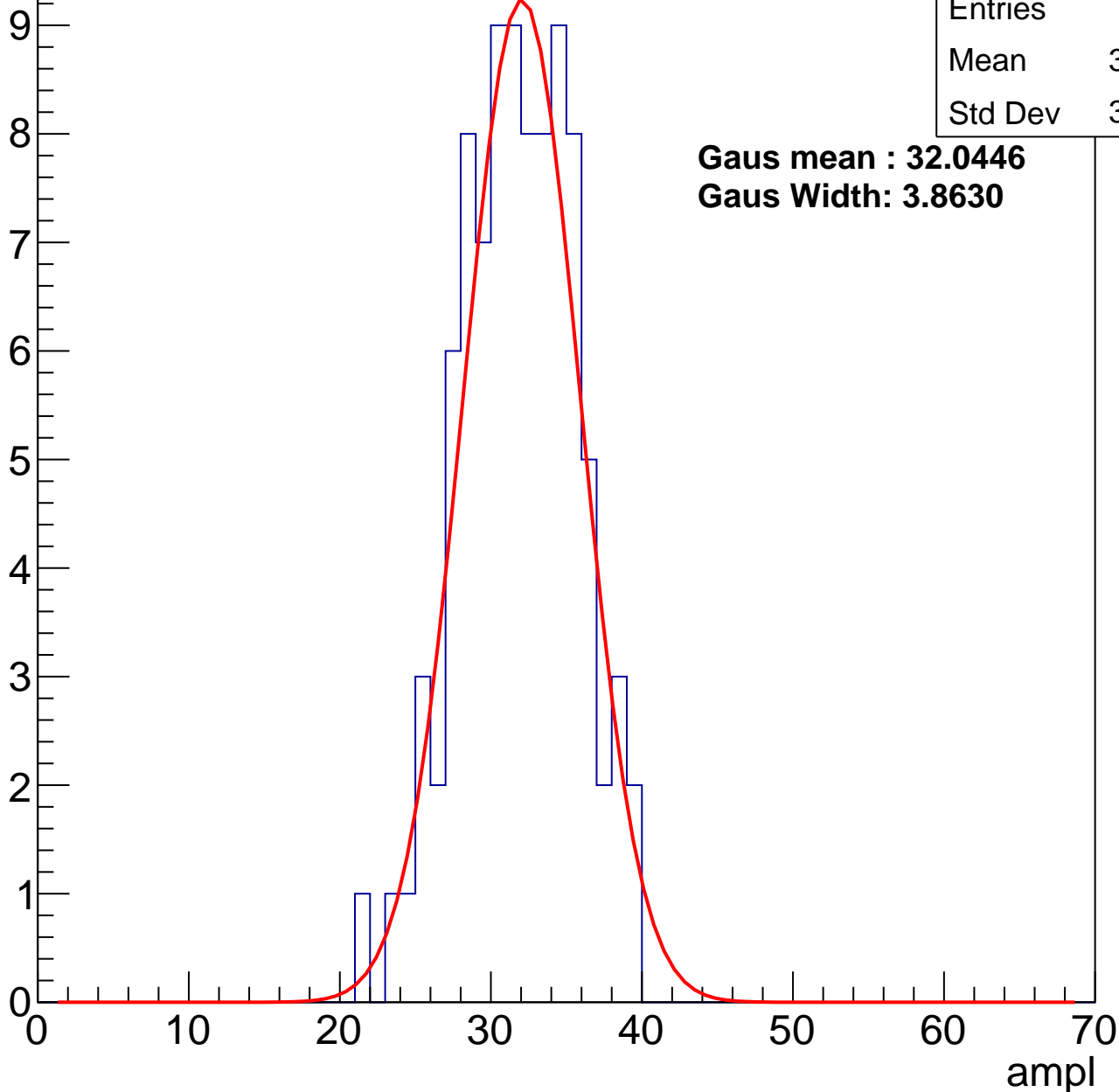
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	92
Mean	31.36
Std Dev	3.738

Gaus mean : 32.0446

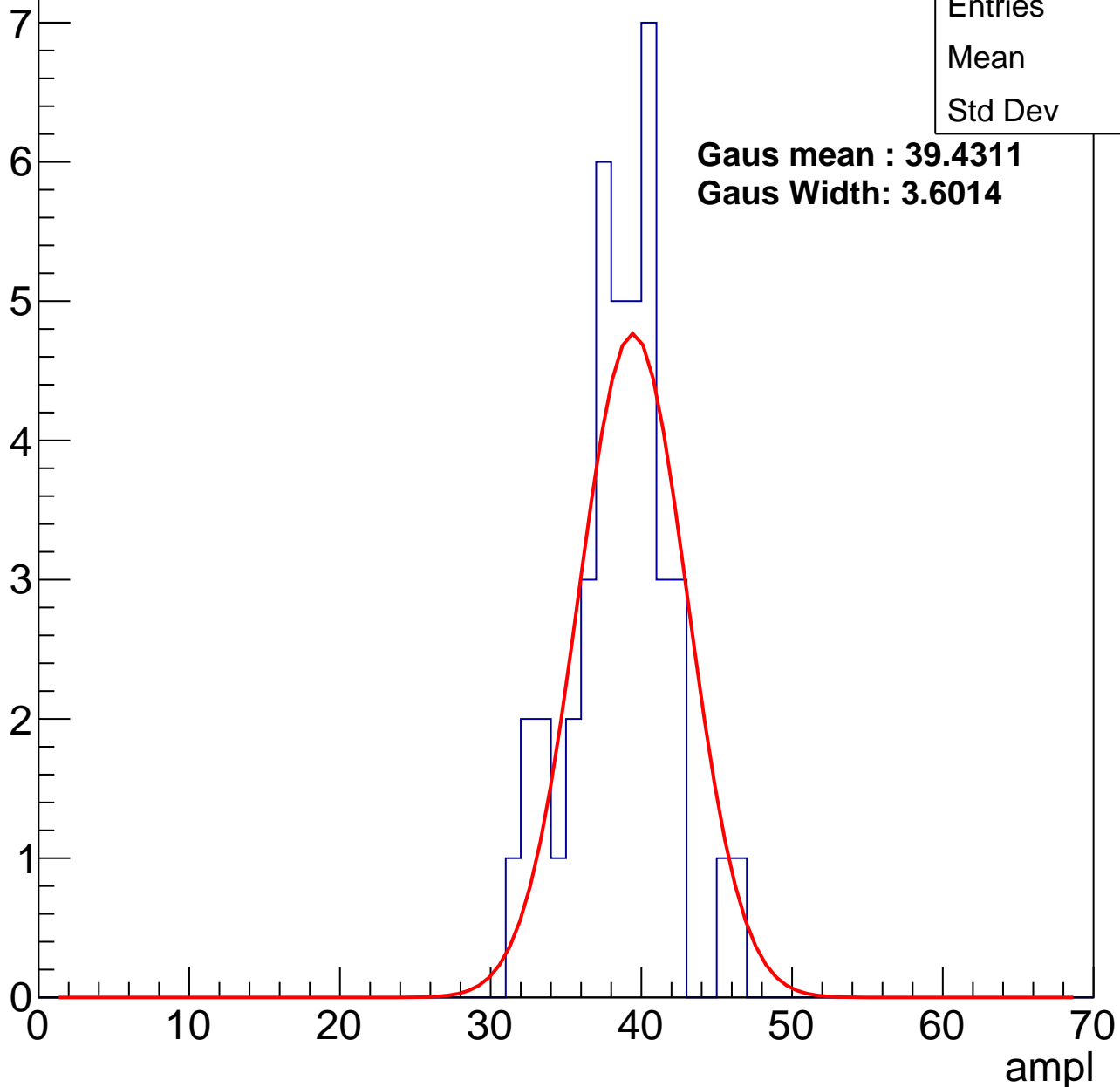
Gaus Width: 3.8630



B1L100S, U13-ch59, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch59, adc2

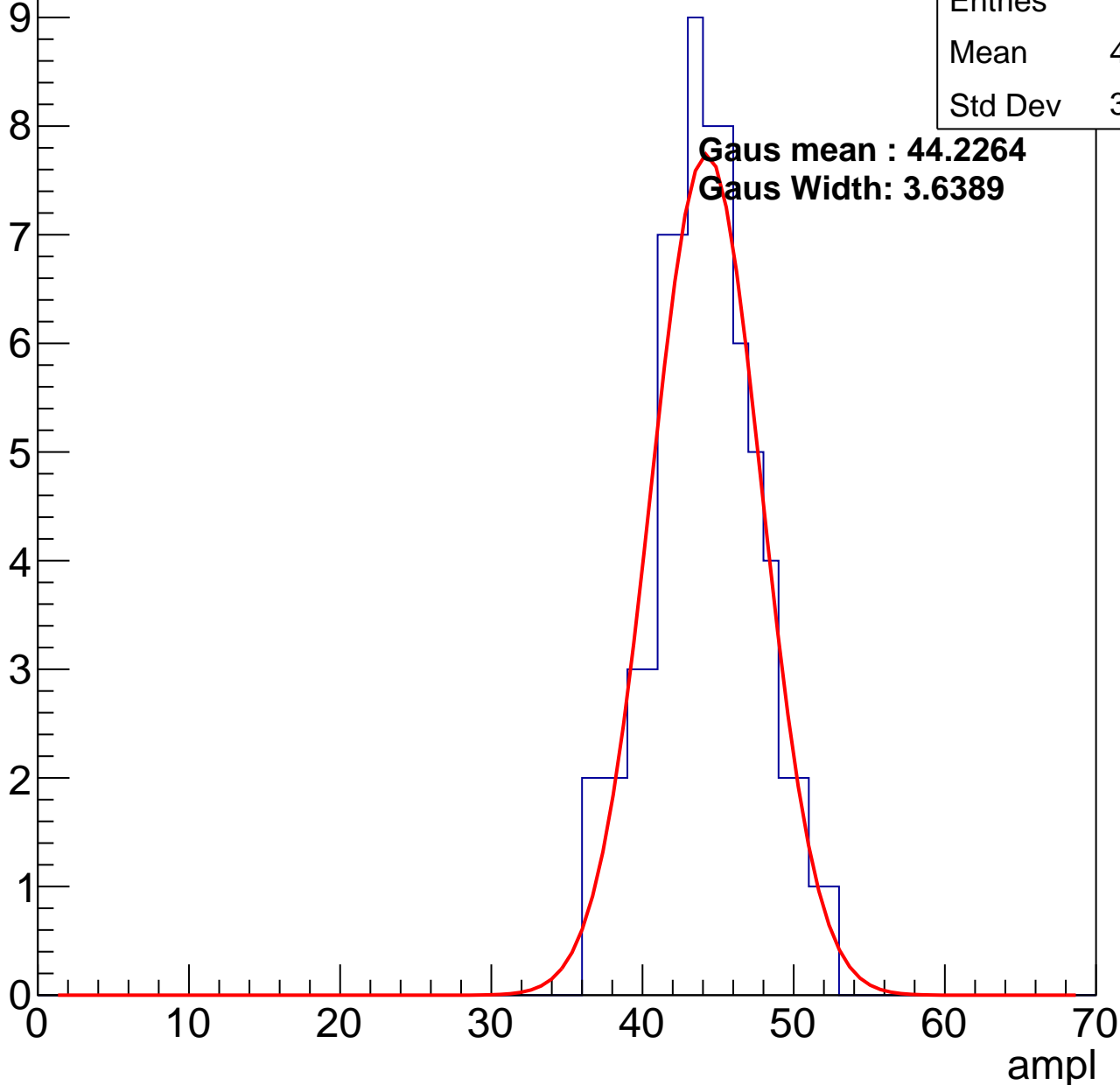
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.65
Std Dev	3.513

Gaus mean : 44.2264

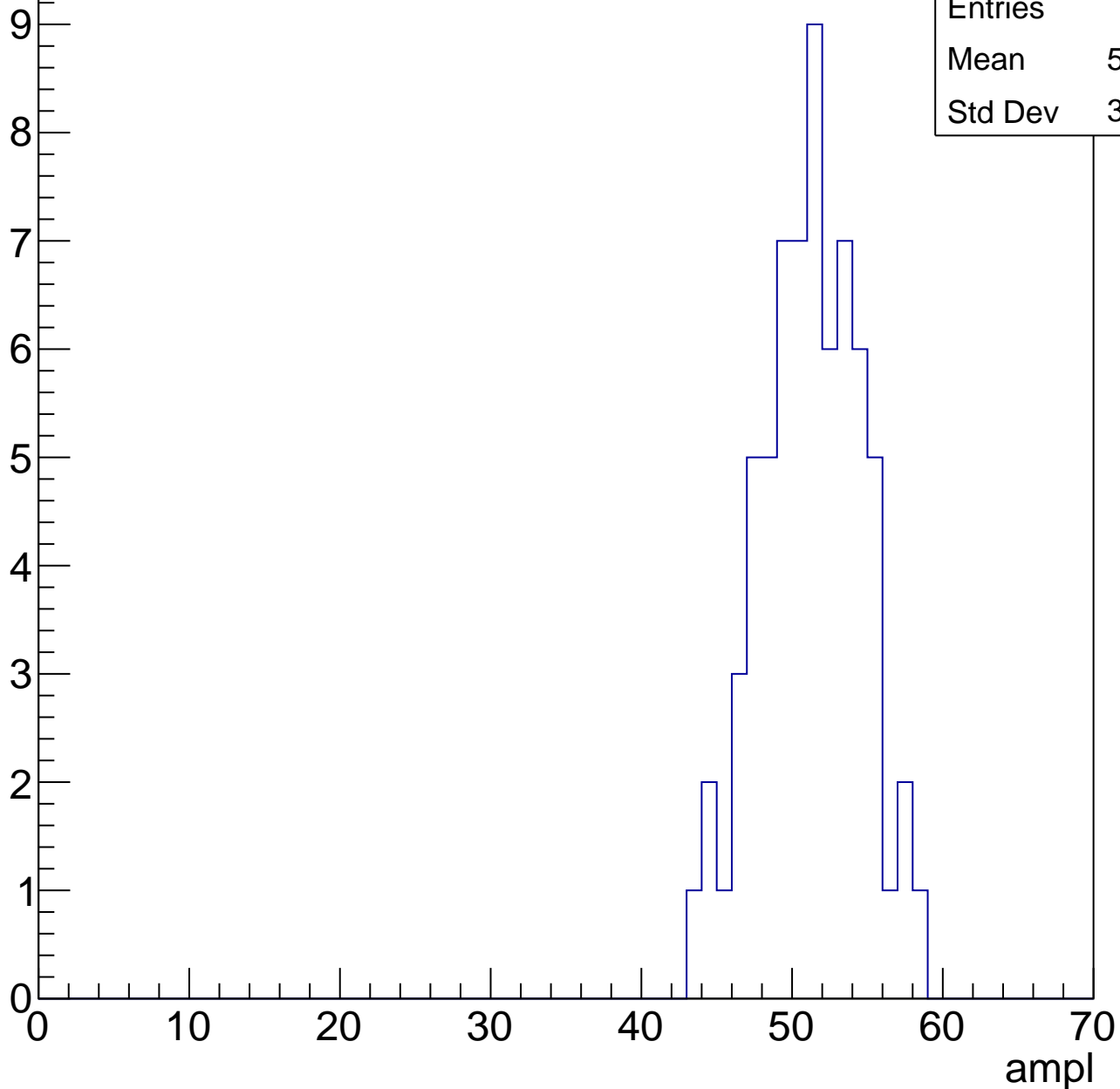
Gaus Width: 3.6389



B1L100S, U13-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	57
Mean	56.88
Std Dev	3.362

0

1

2

3

4

5

6

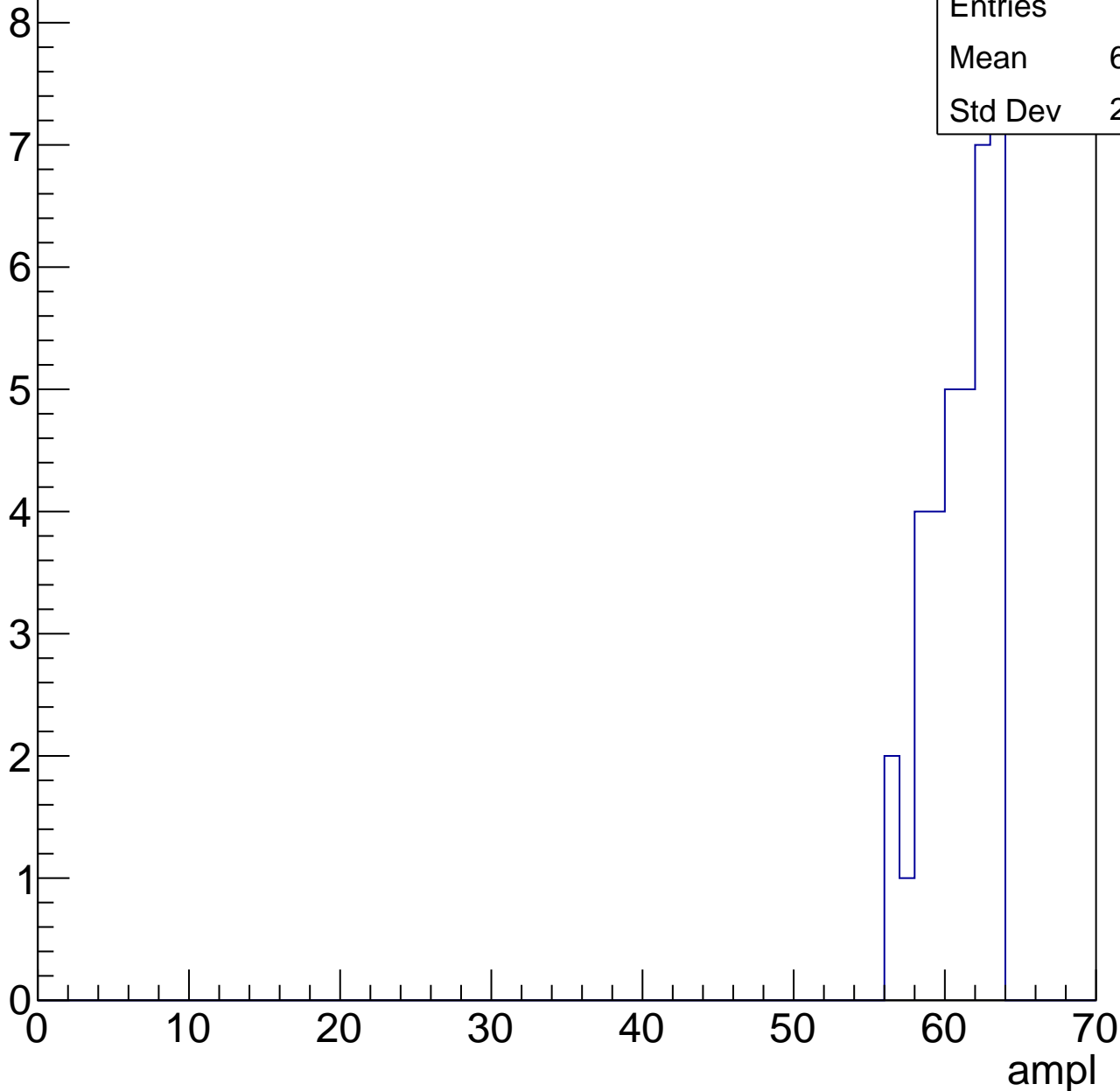
7

B1L100S, U13-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	60.56
Std Dev	2.074



B1L100S, U13-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch60, adc0

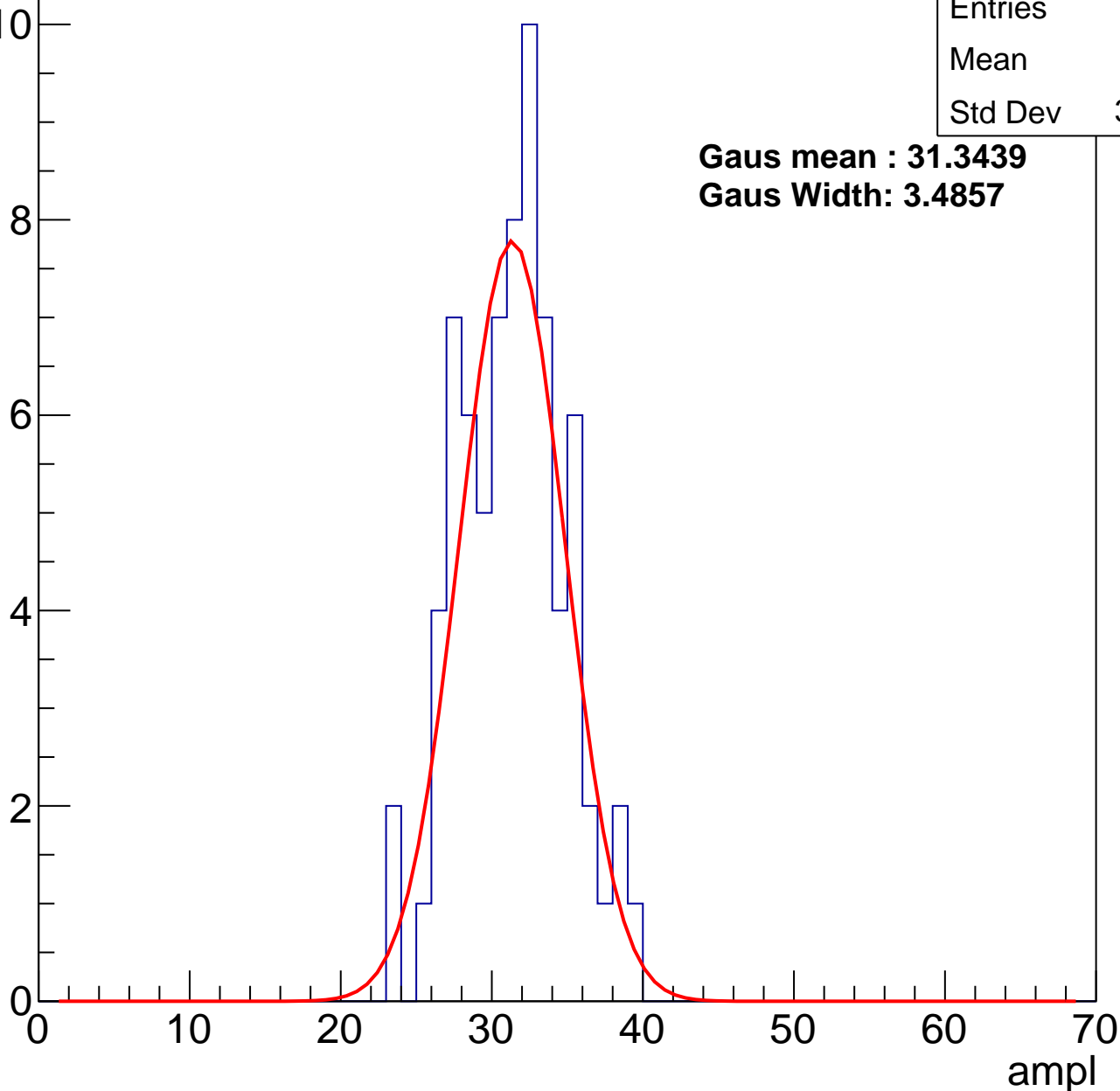
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.9
Std Dev	3.461

Gaus mean : 31.3439

Gaus Width: 3.4857



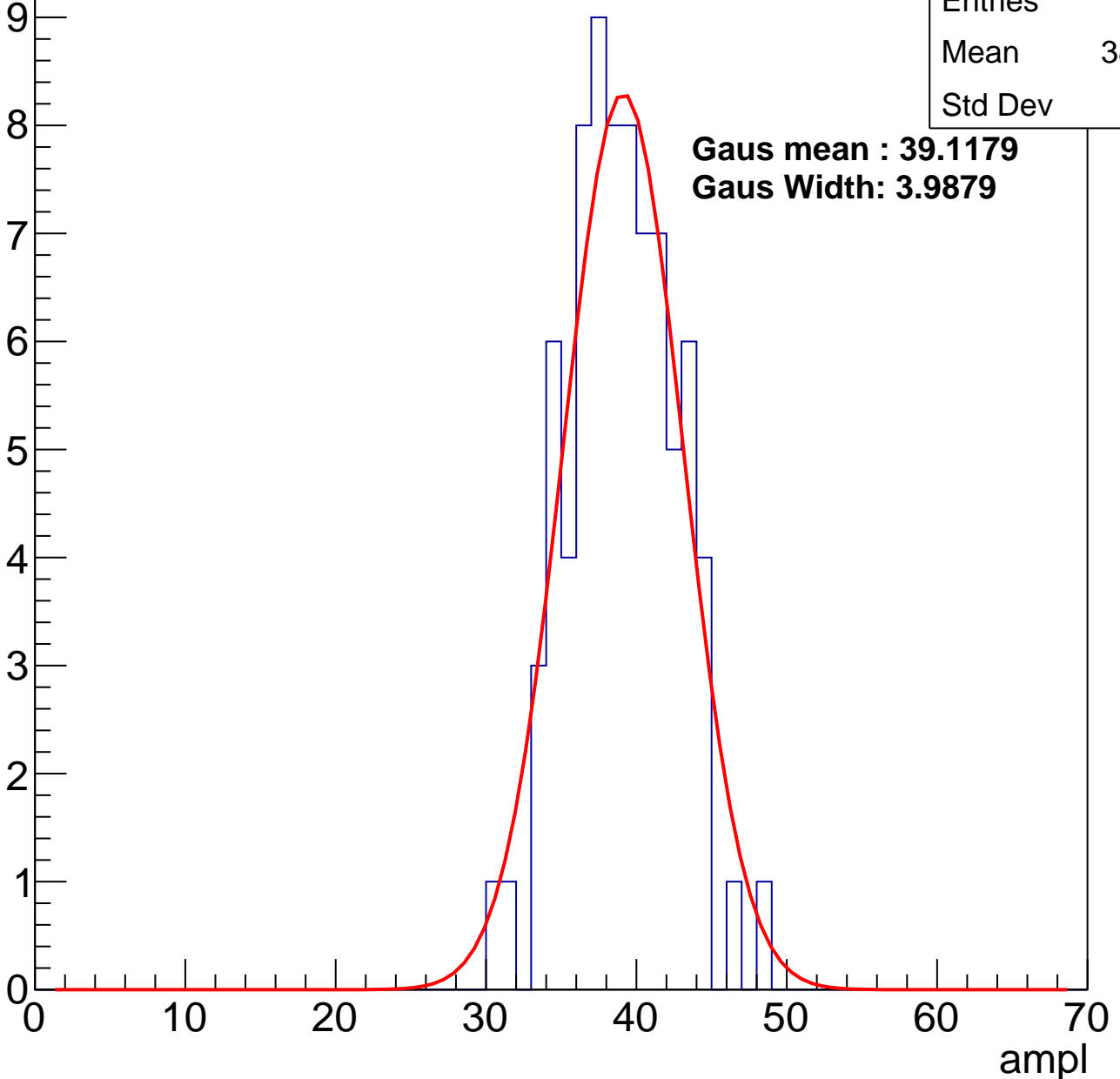
B1L100S, U13-ch60, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	38.56
Std Dev	3.51

Gaus mean : 39.1179
Gaus Width: 3.9879



B1L100S, U13-ch60, adc2

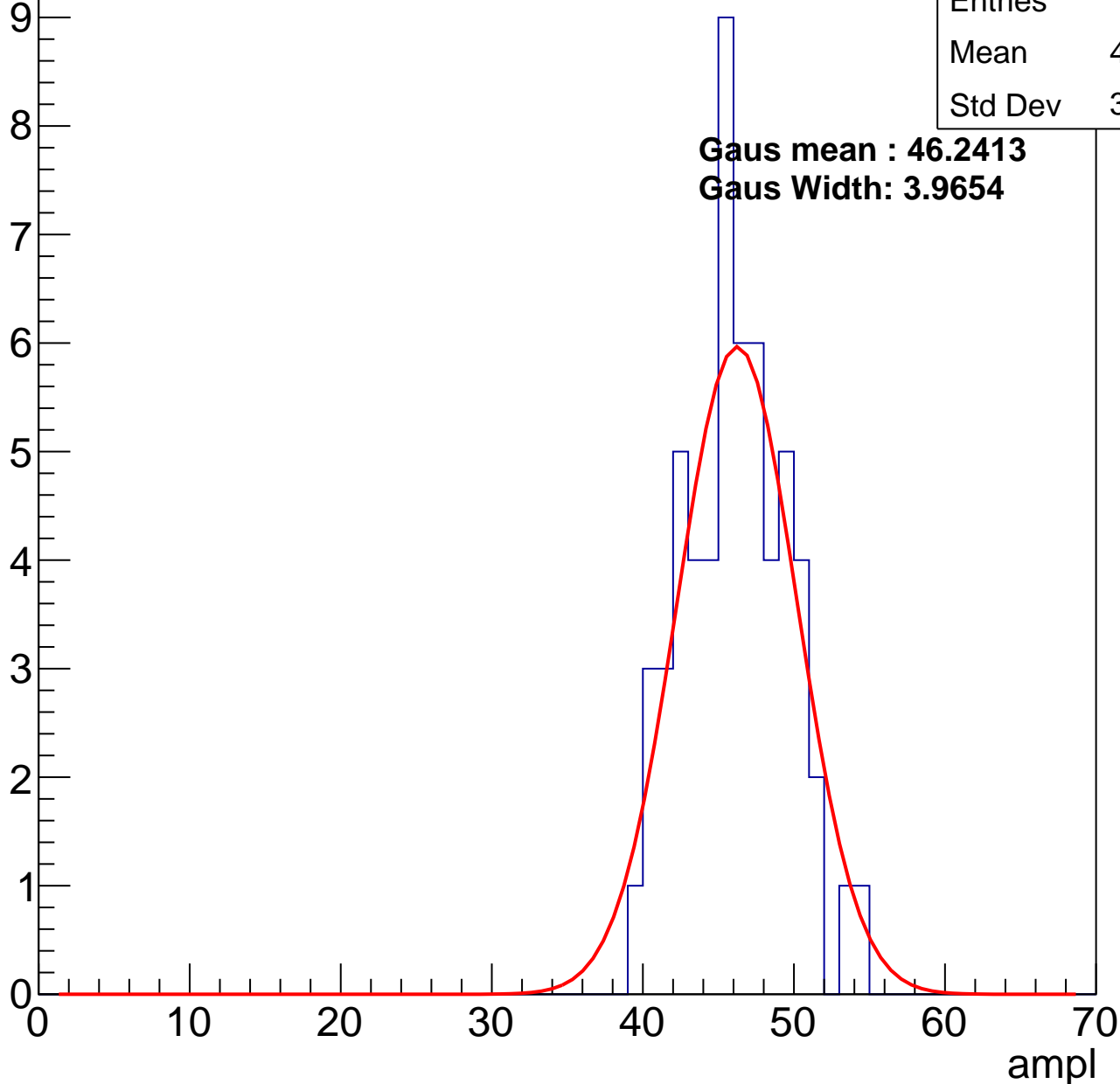
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	45.67
Std Dev	3.375

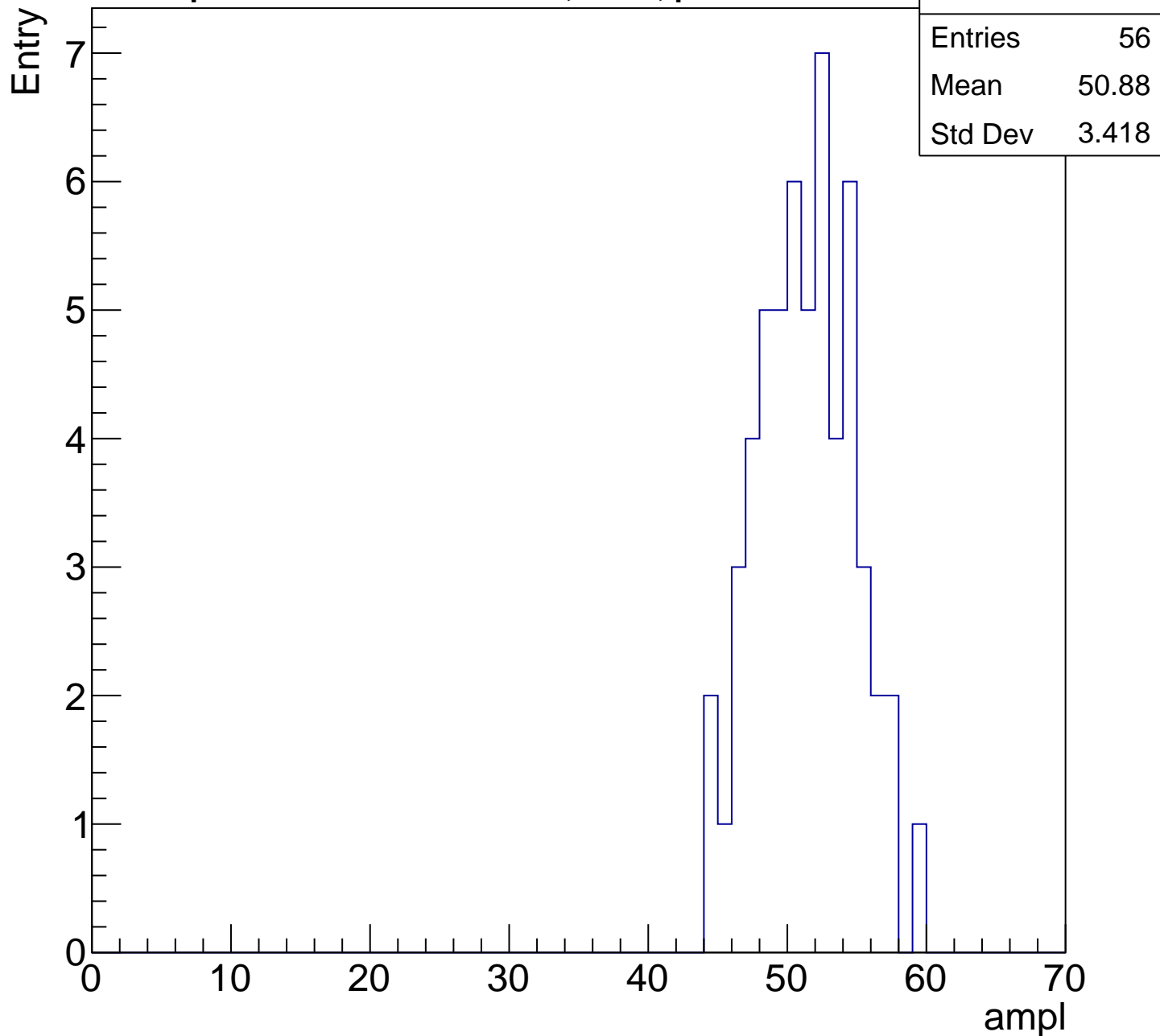
Gaus mean : 46.2413

Gaus Width: 3.9654



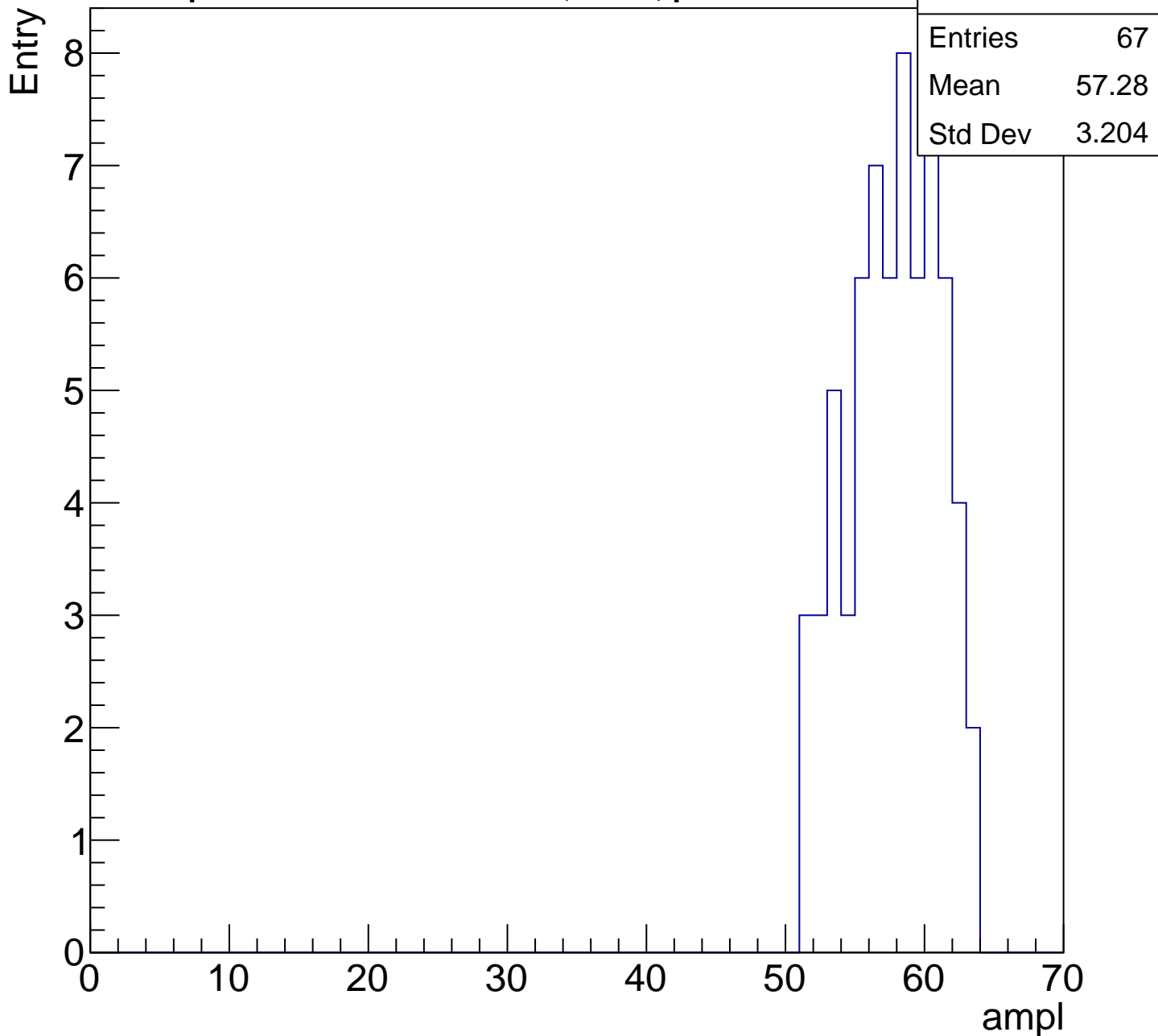
B1L100S, U13-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

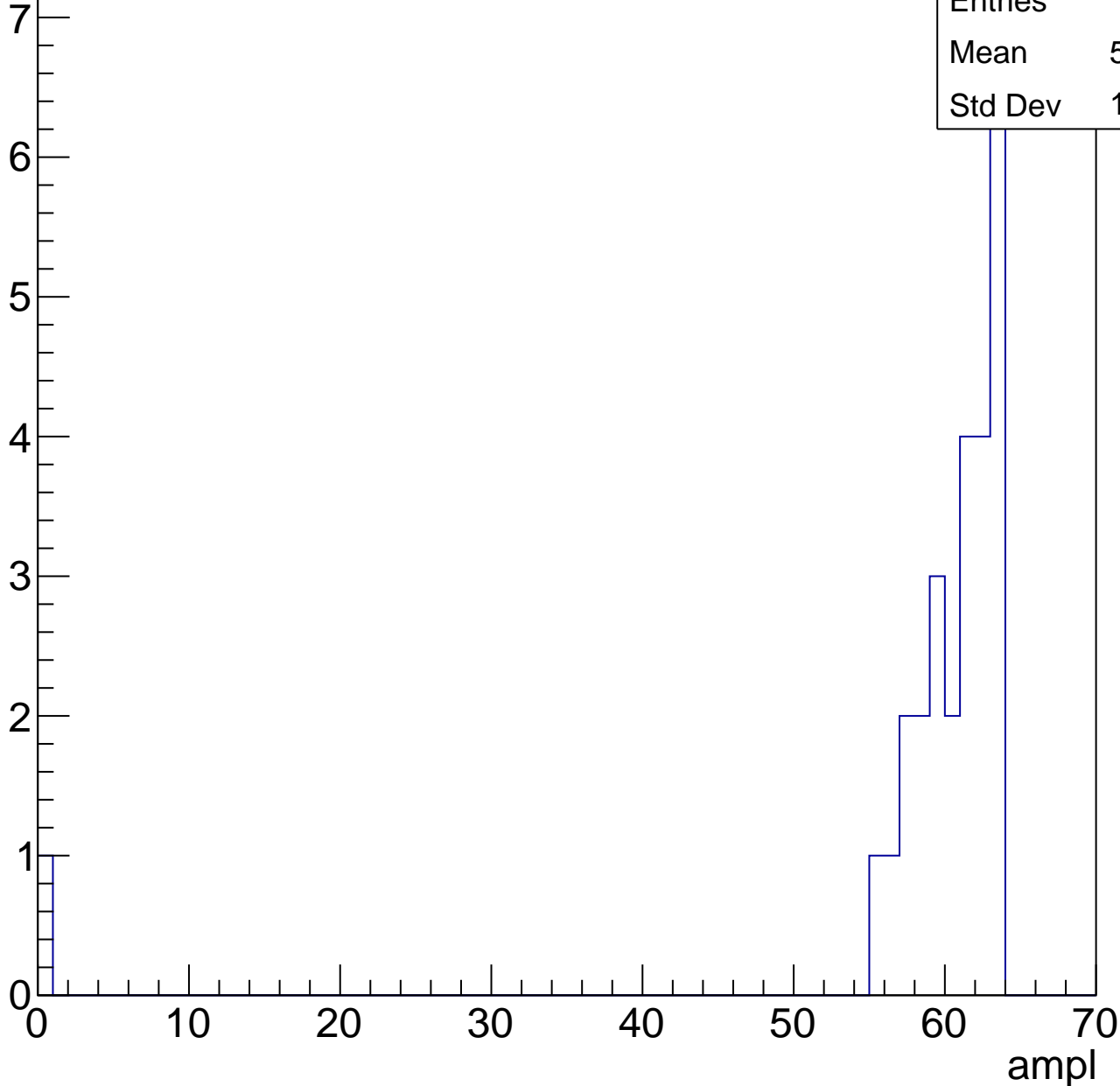


B1L100S, U13-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.19
Std Dev	11.65



B1L100S, U13-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U13-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch61, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	31.78
Std Dev	4.121

Gaus mean : 32.4050

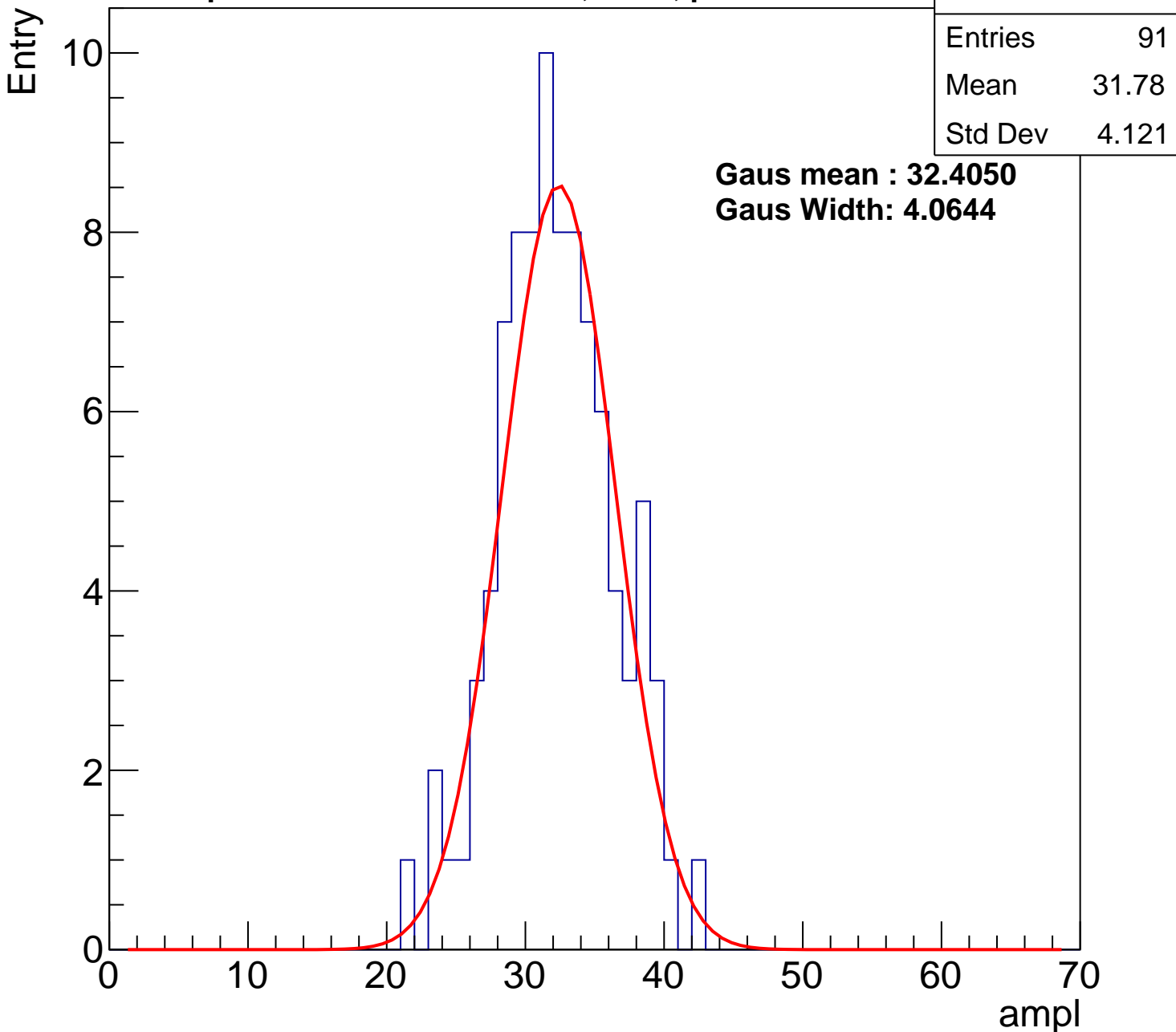
Gaus Width: 4.0644

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch61, adc1

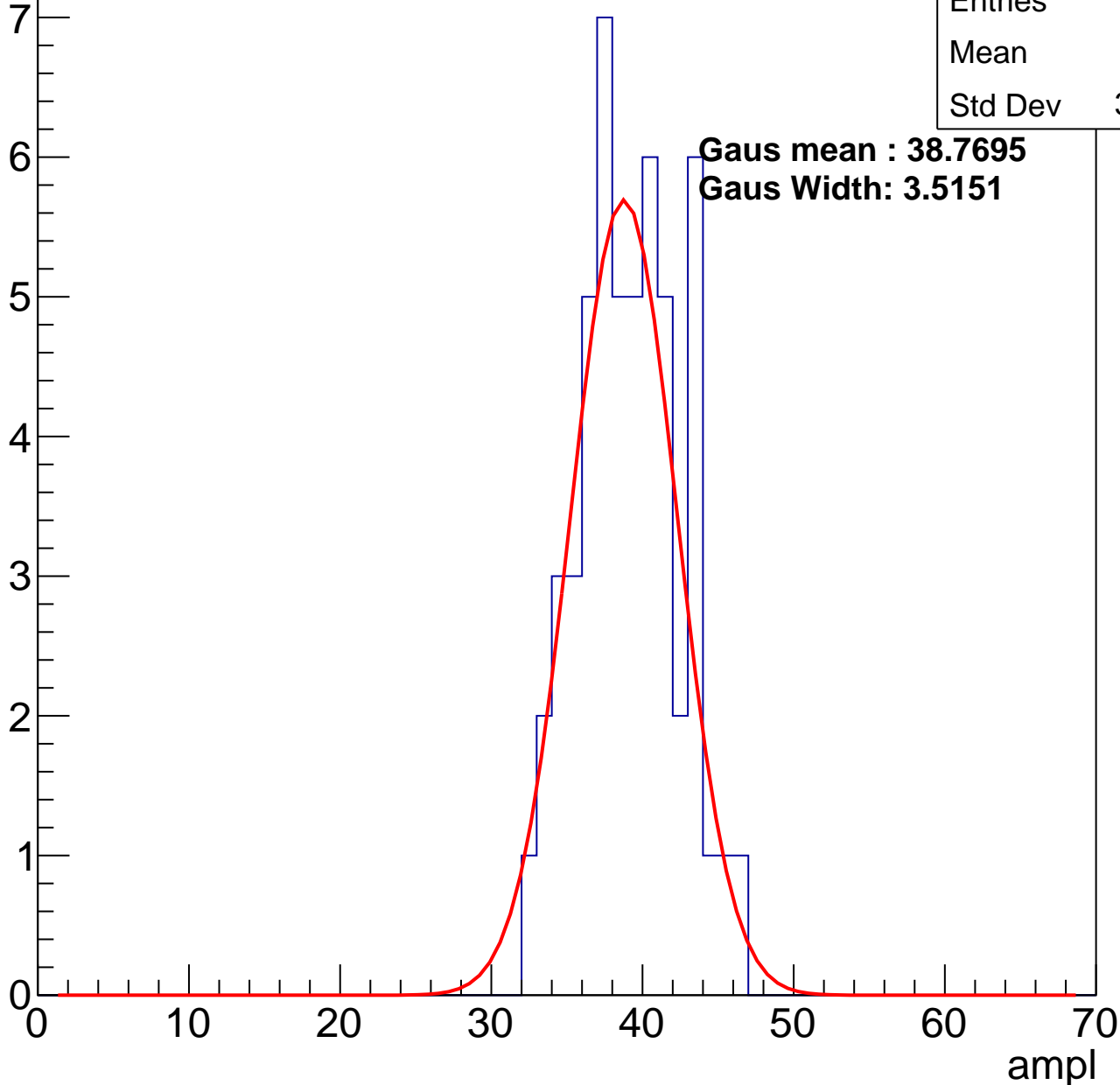
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	38.7
Std Dev	3.271

Gaus mean : 38.7695

Gaus Width: 3.5151



B1L100S, U13-ch61, adc2

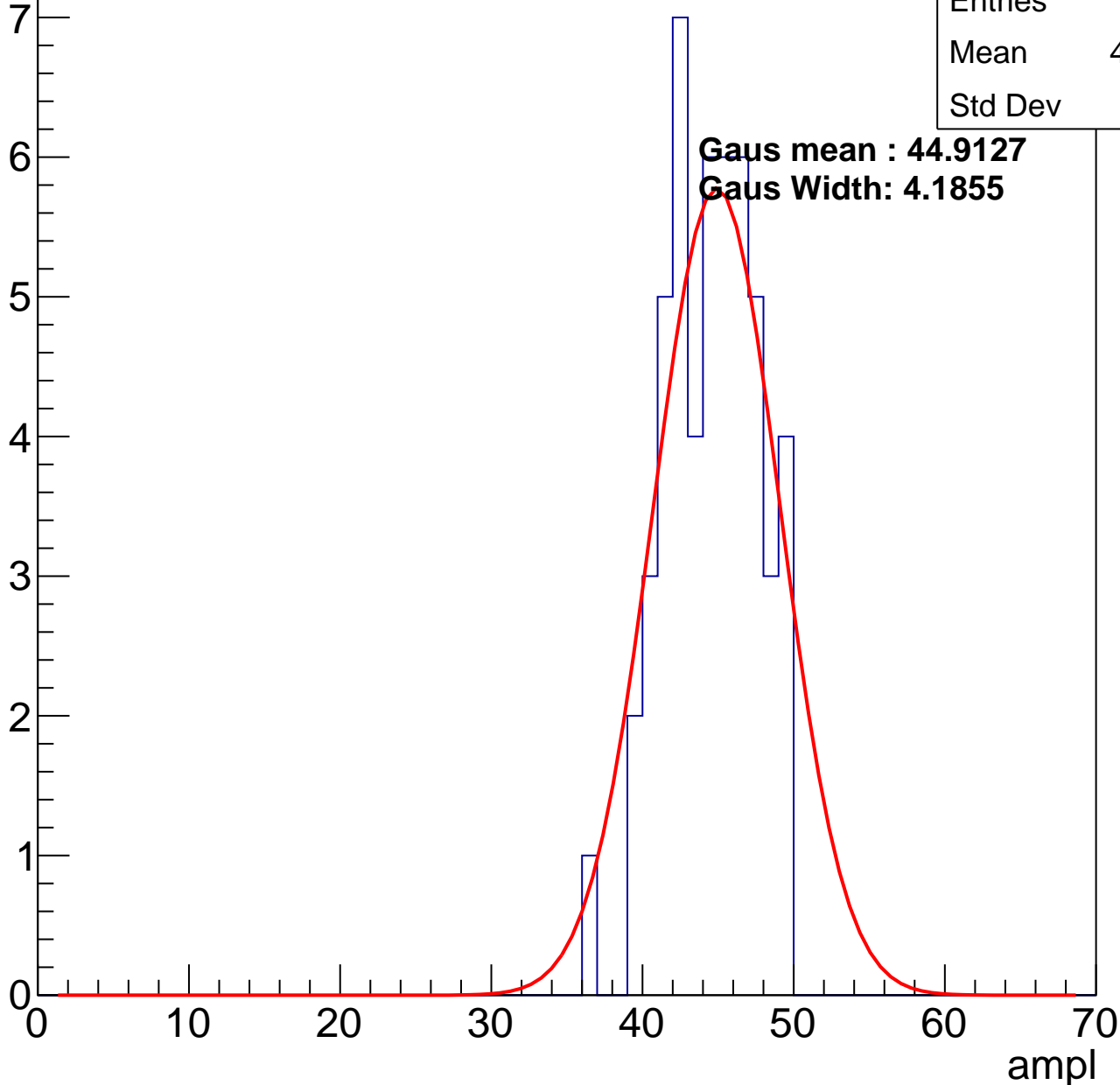
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	44.04
Std Dev	2.98

Gaus mean : 44.9127

Gaus Width: 4.1855

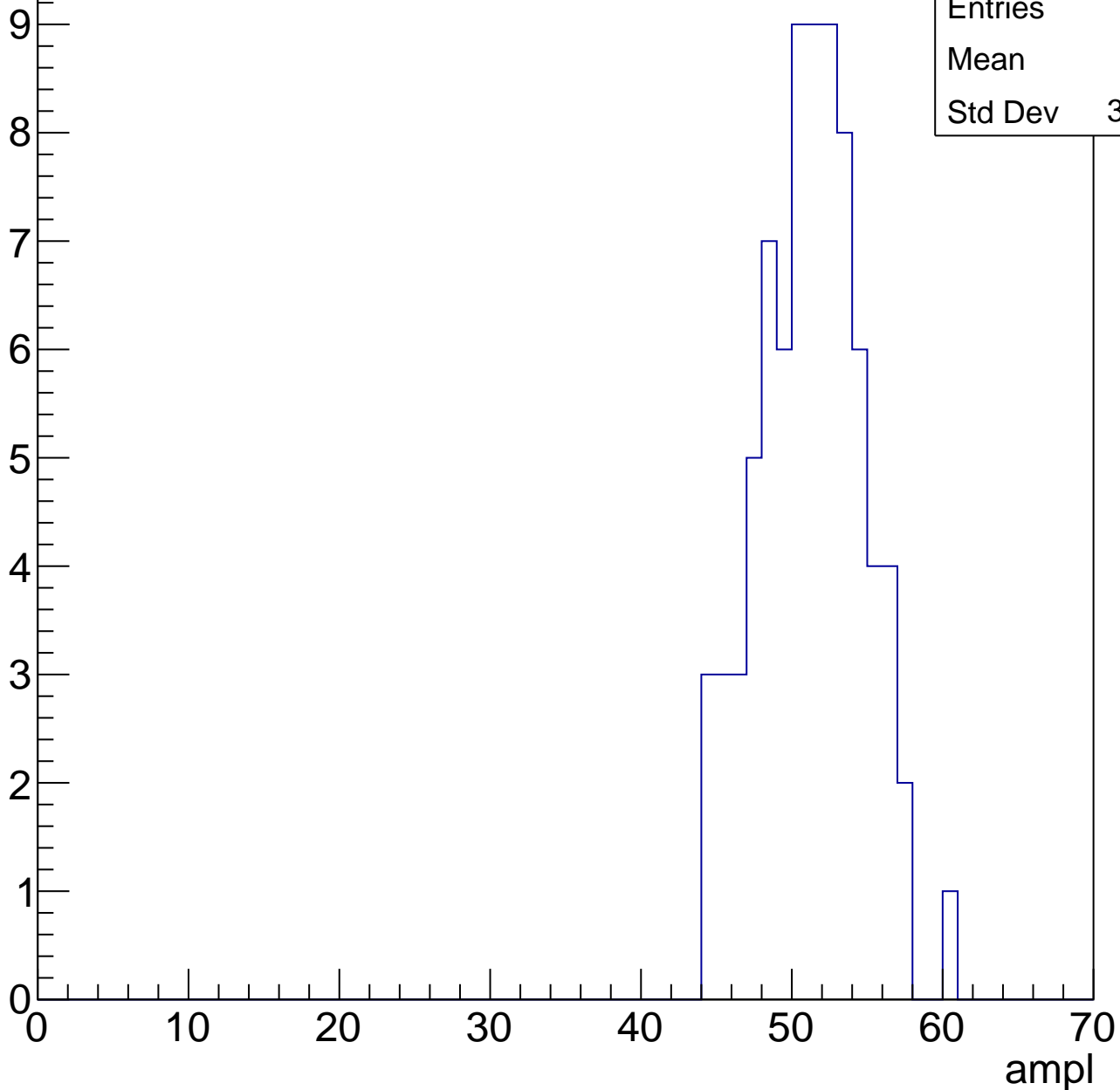


B1L100S, U13-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

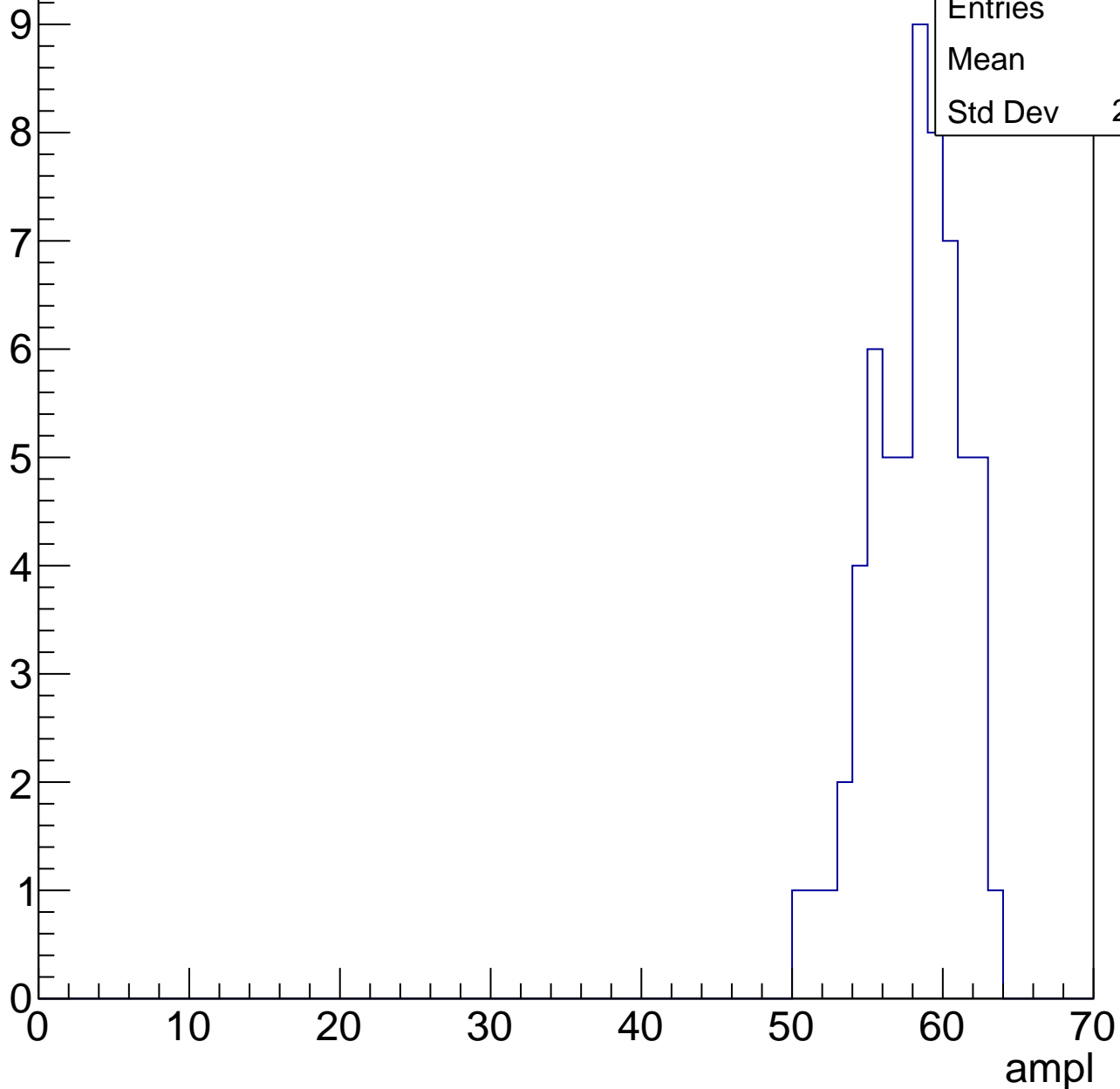
Entries	79
Mean	50.8
Std Dev	3.403



B1L100S, U13-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

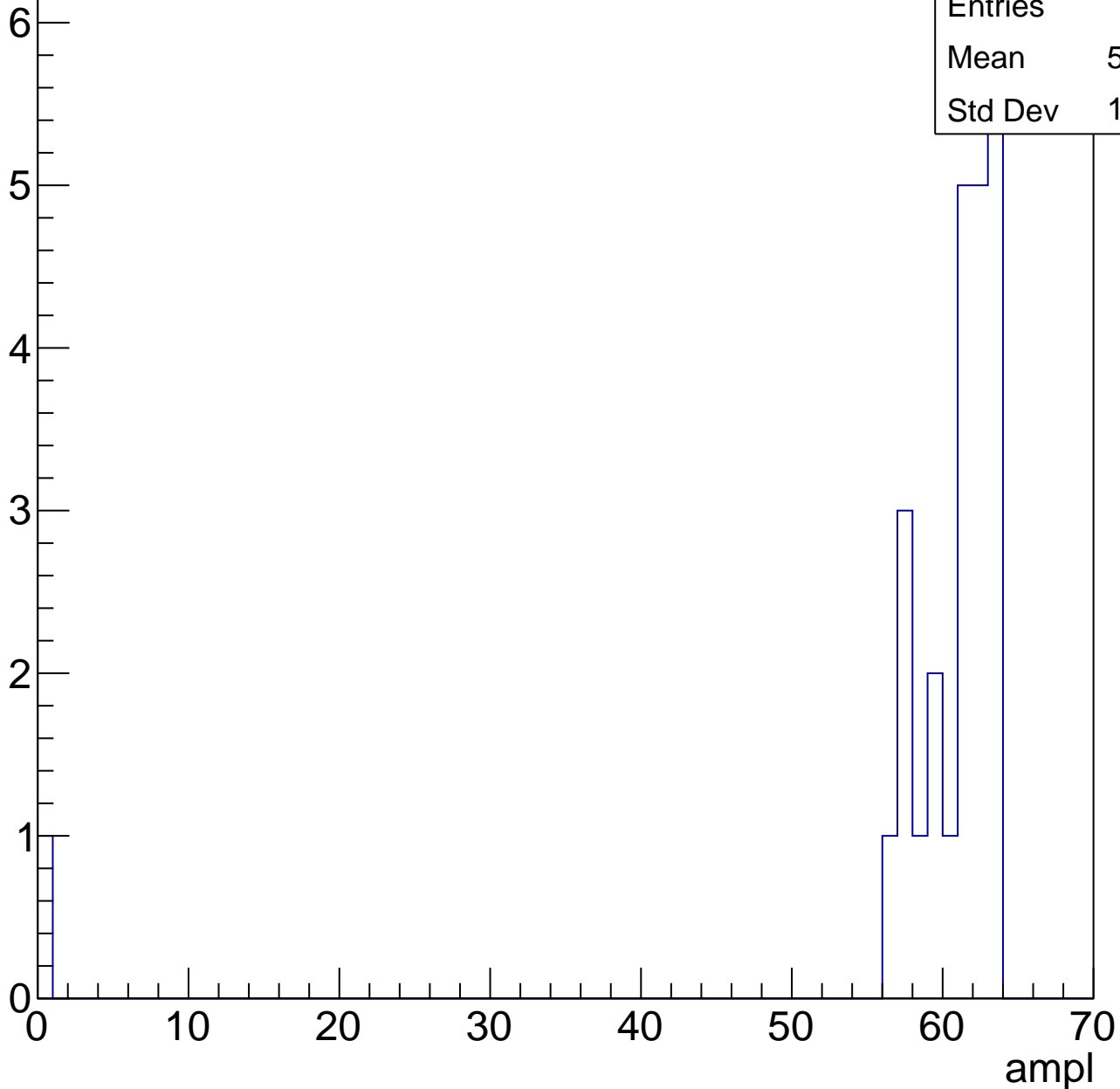


B1L100S, U13-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.24
Std Dev	12.08



B1L100S, U13-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch62, adc0

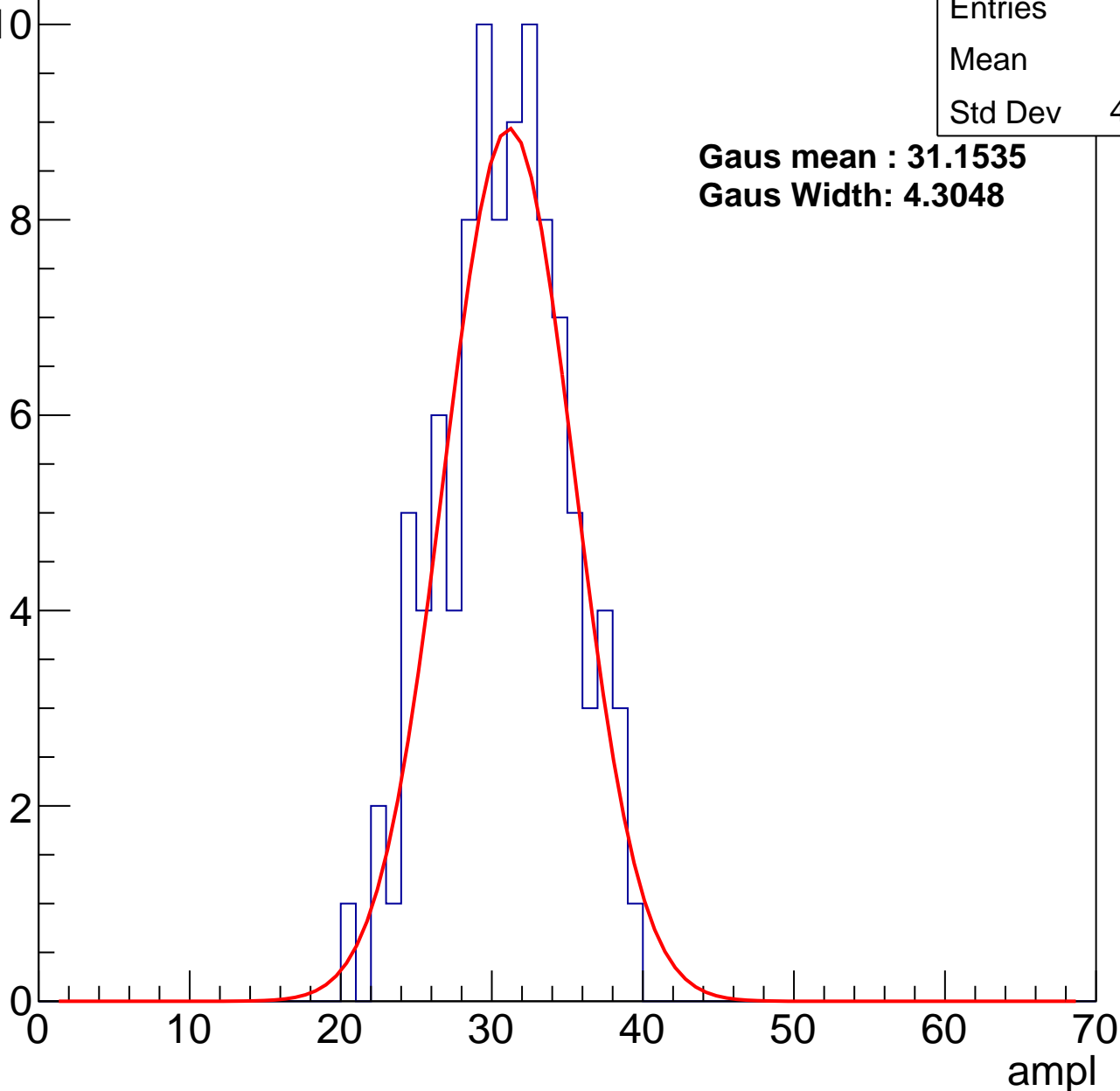
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	99
Mean	30.4
Std Dev	4.082

Gaus mean : 31.1535

Gaus Width: 4.3048



B1L100S, U13-ch62, adc1

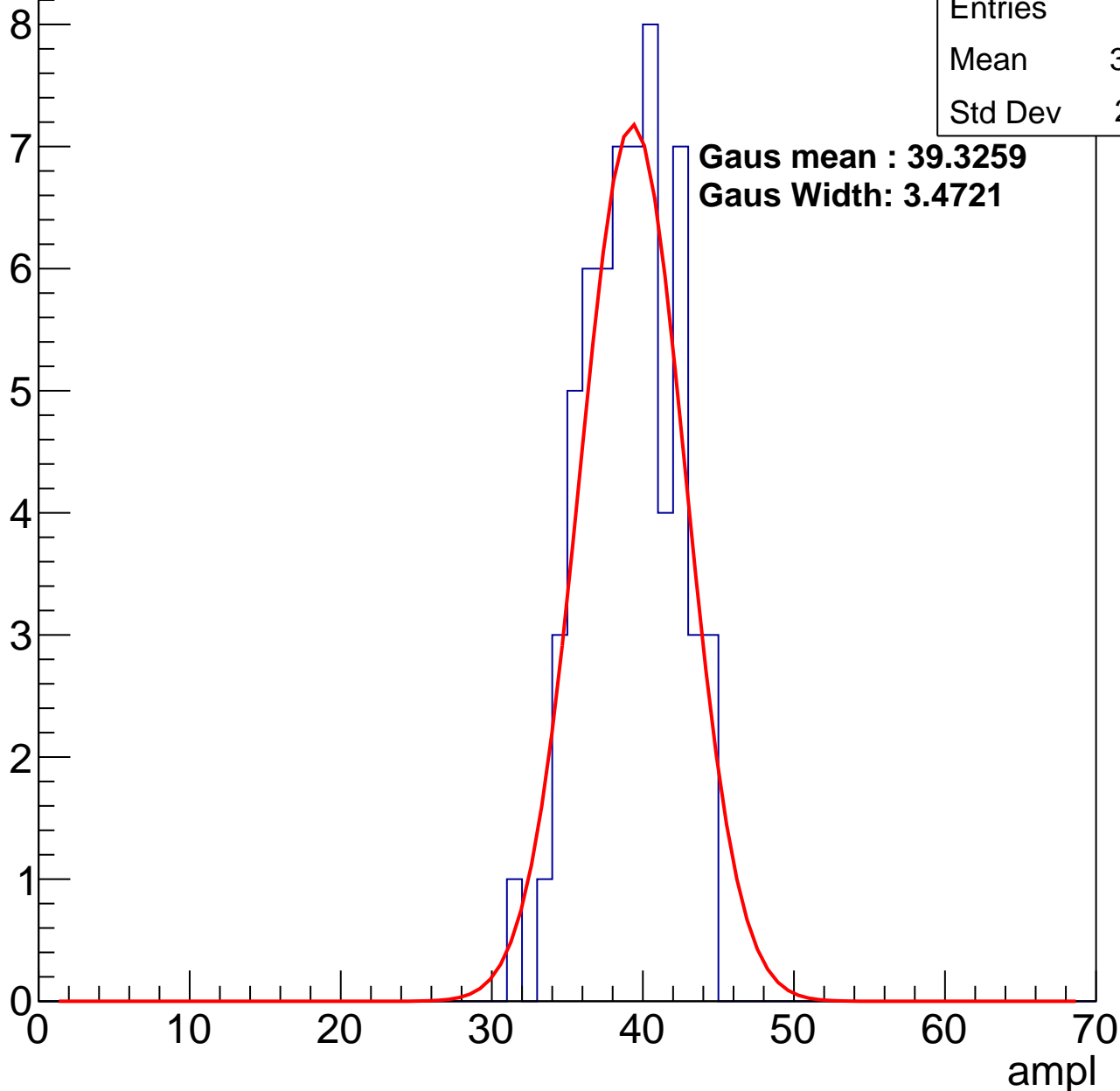
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	38.64
Std Dev	2.981

Gaus mean : 39.3259

Gaus Width: 3.4721



B1L100S, U13-ch62, adc2

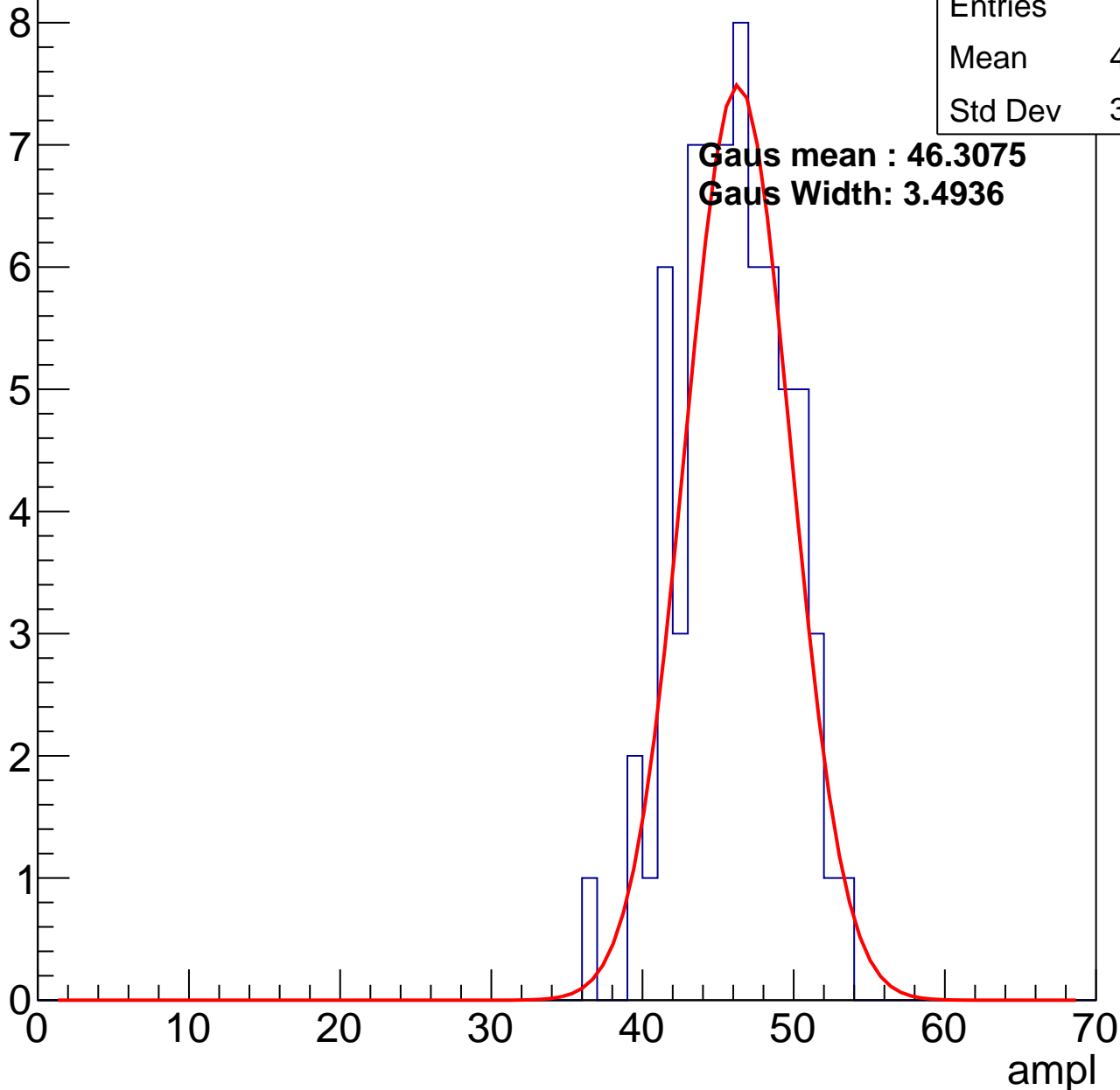
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	45.52
Std Dev	3.467

Gaus mean : 46.3075

Gaus Width: 3.4936

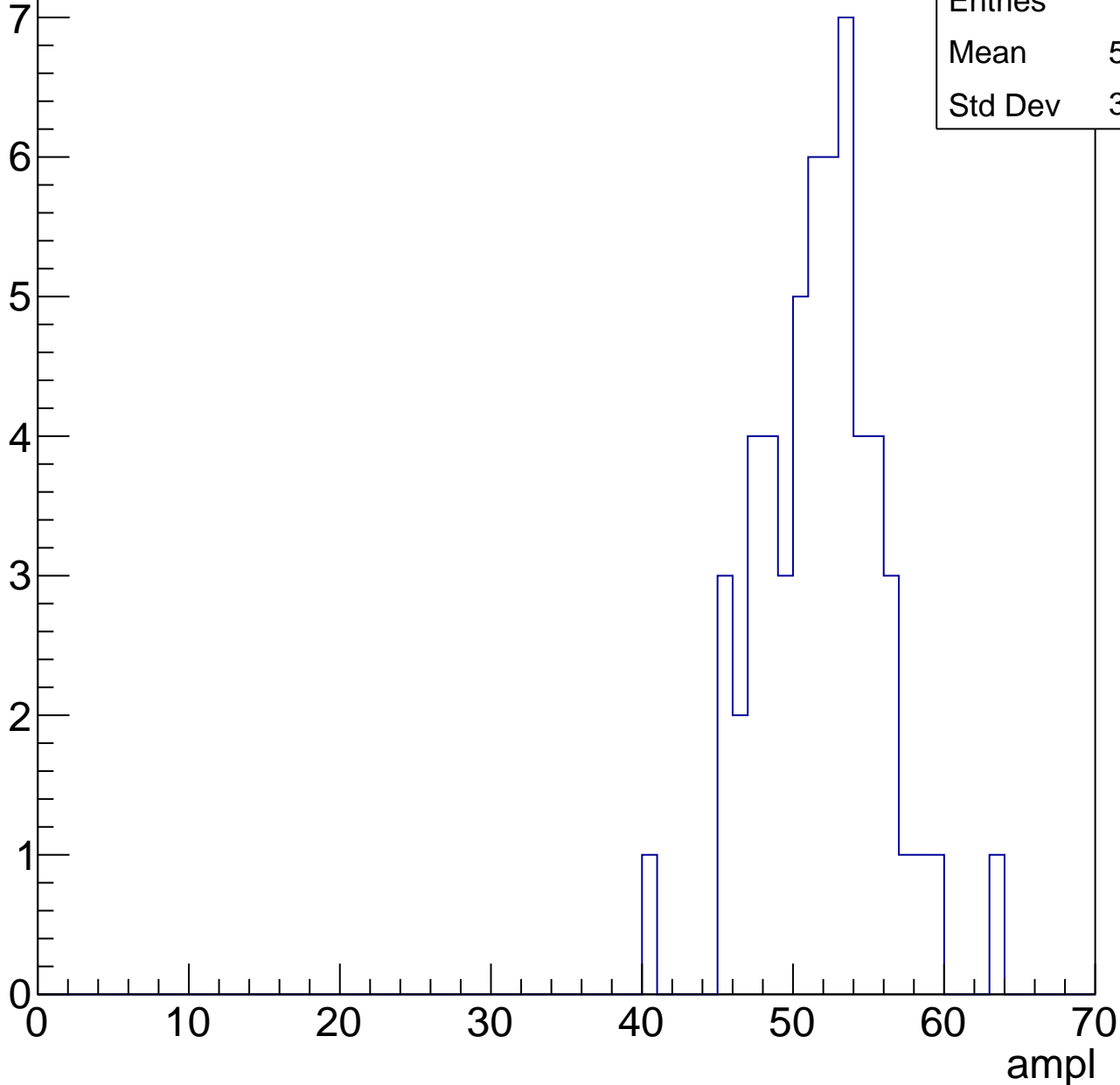


B1L100S, U13-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	51.32
Std Dev	3.992

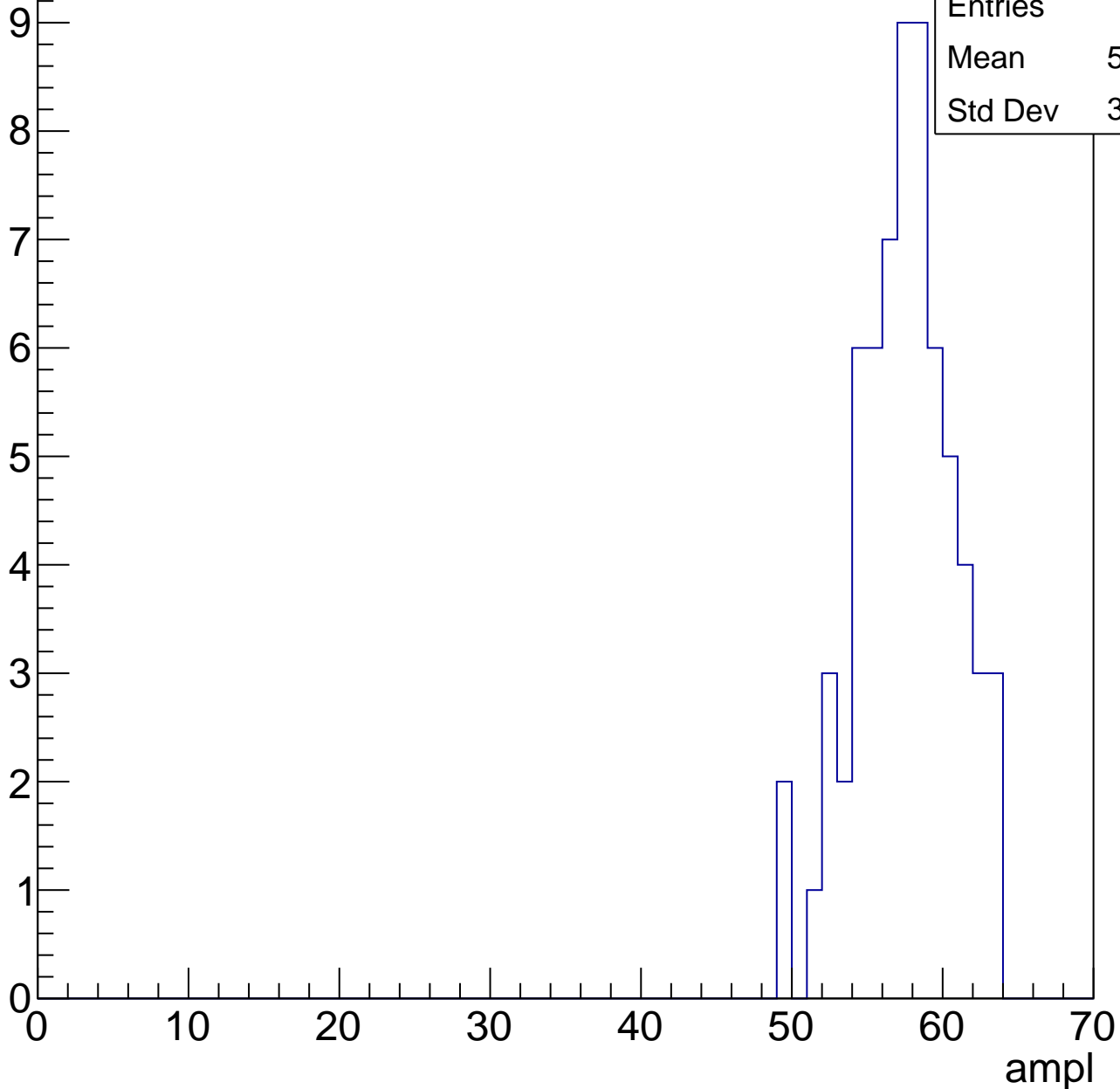


B1L100S, U13-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

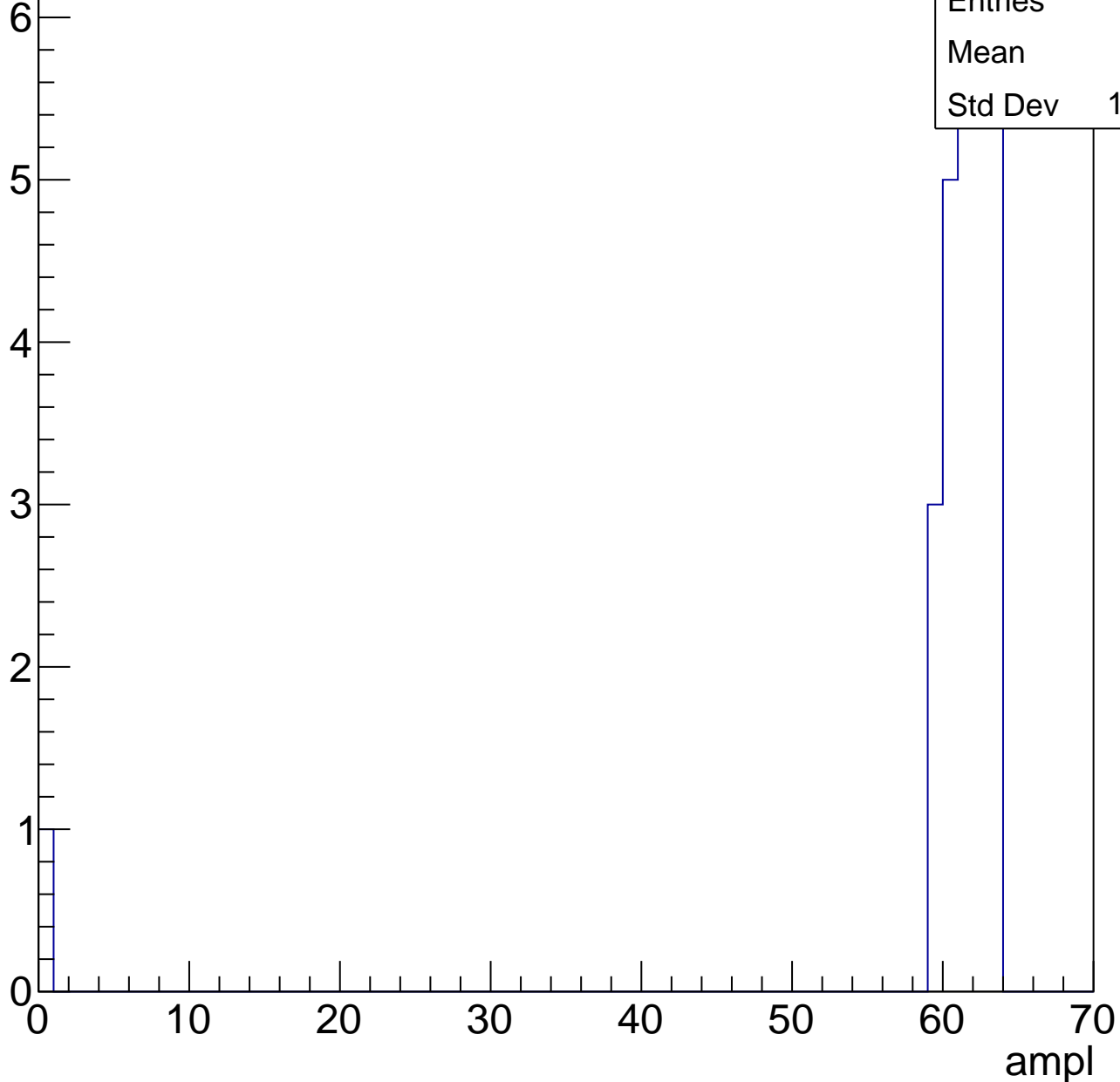
Entries	66
Mean	57.05
Std Dev	3.212



B1L100S, U13-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch63, adc0

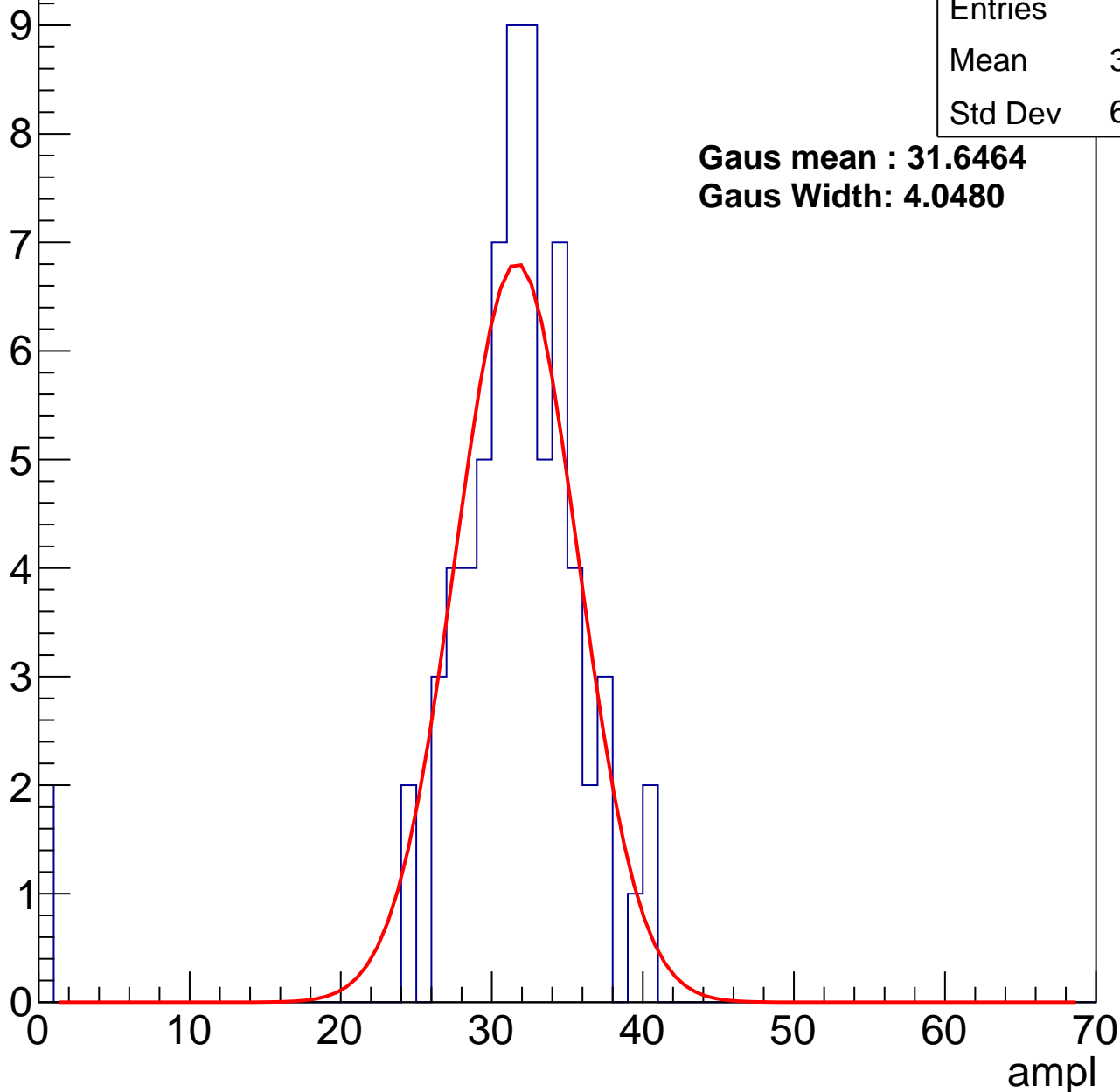
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.62
Std Dev	6.316

Gaus mean : 31.6464

Gaus Width: 4.0480



B1L100S, U13-ch63, adc1

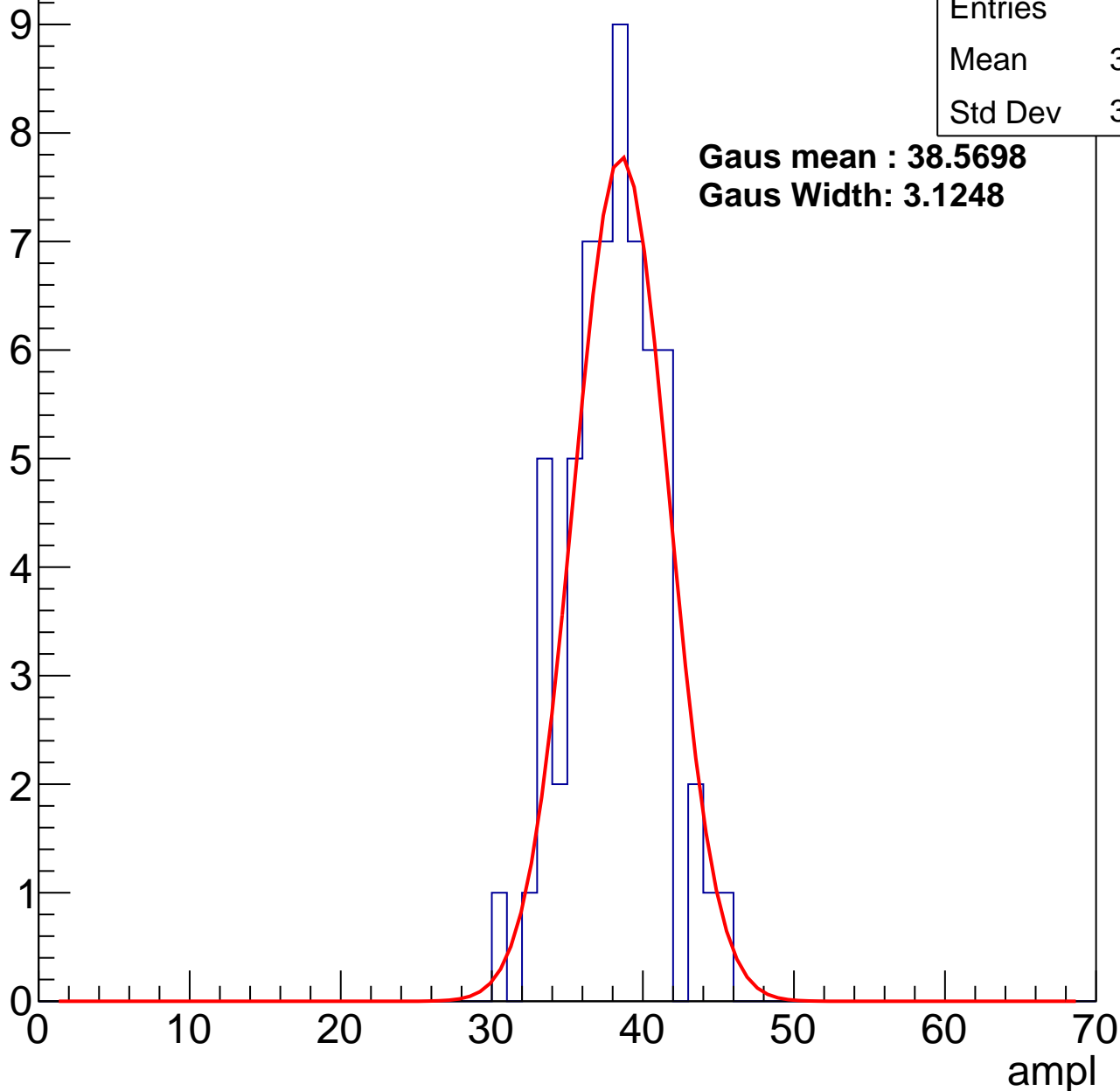
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	37.62
Std Dev	3.023

Gaus mean : 38.5698

Gaus Width: 3.1248



B1L100S, U13-ch63, adc2

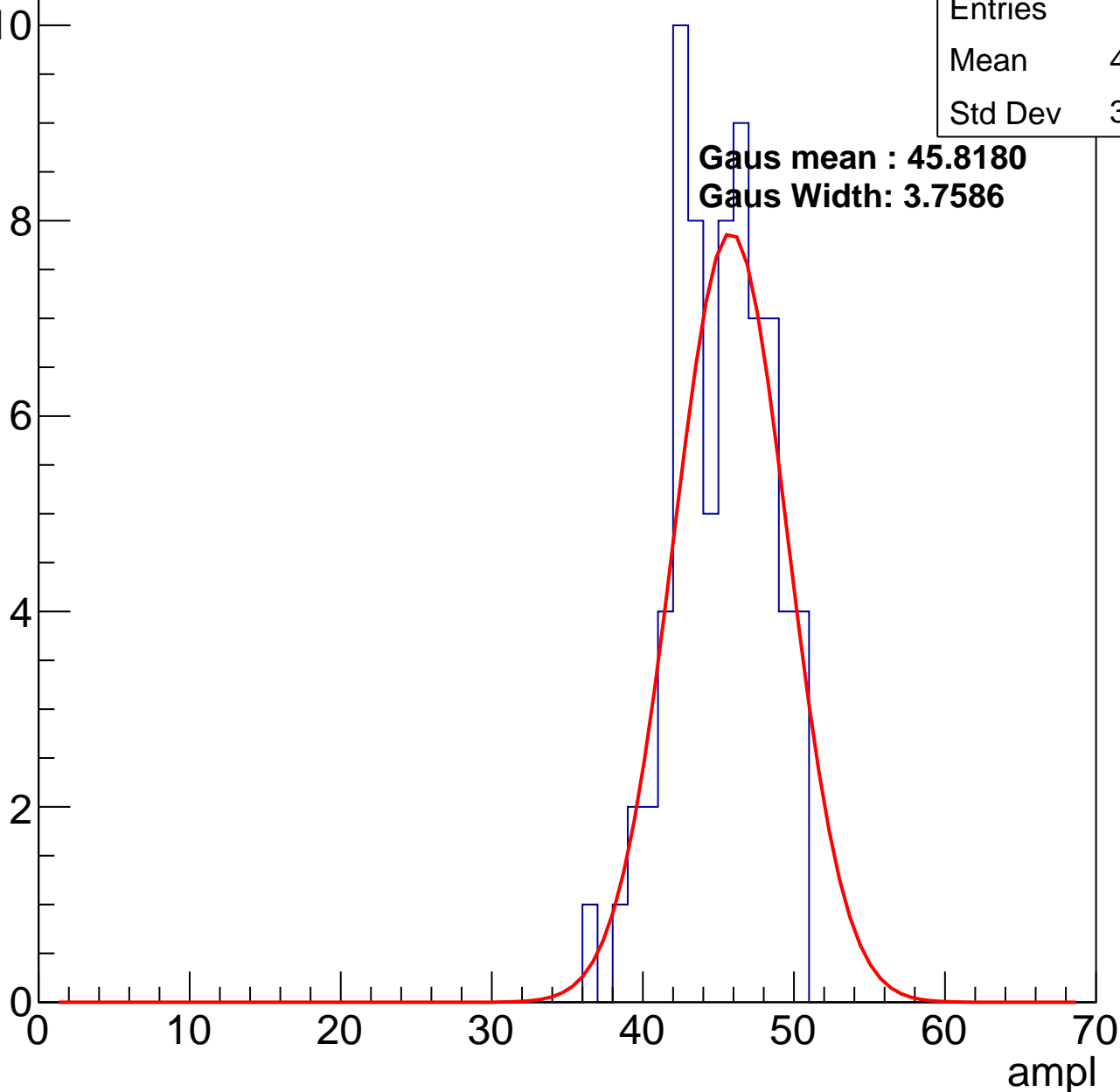
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.65
Std Dev	3.114

Gaus mean : 45.8180

Gaus Width: 3.7586



B1L100S, U13-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

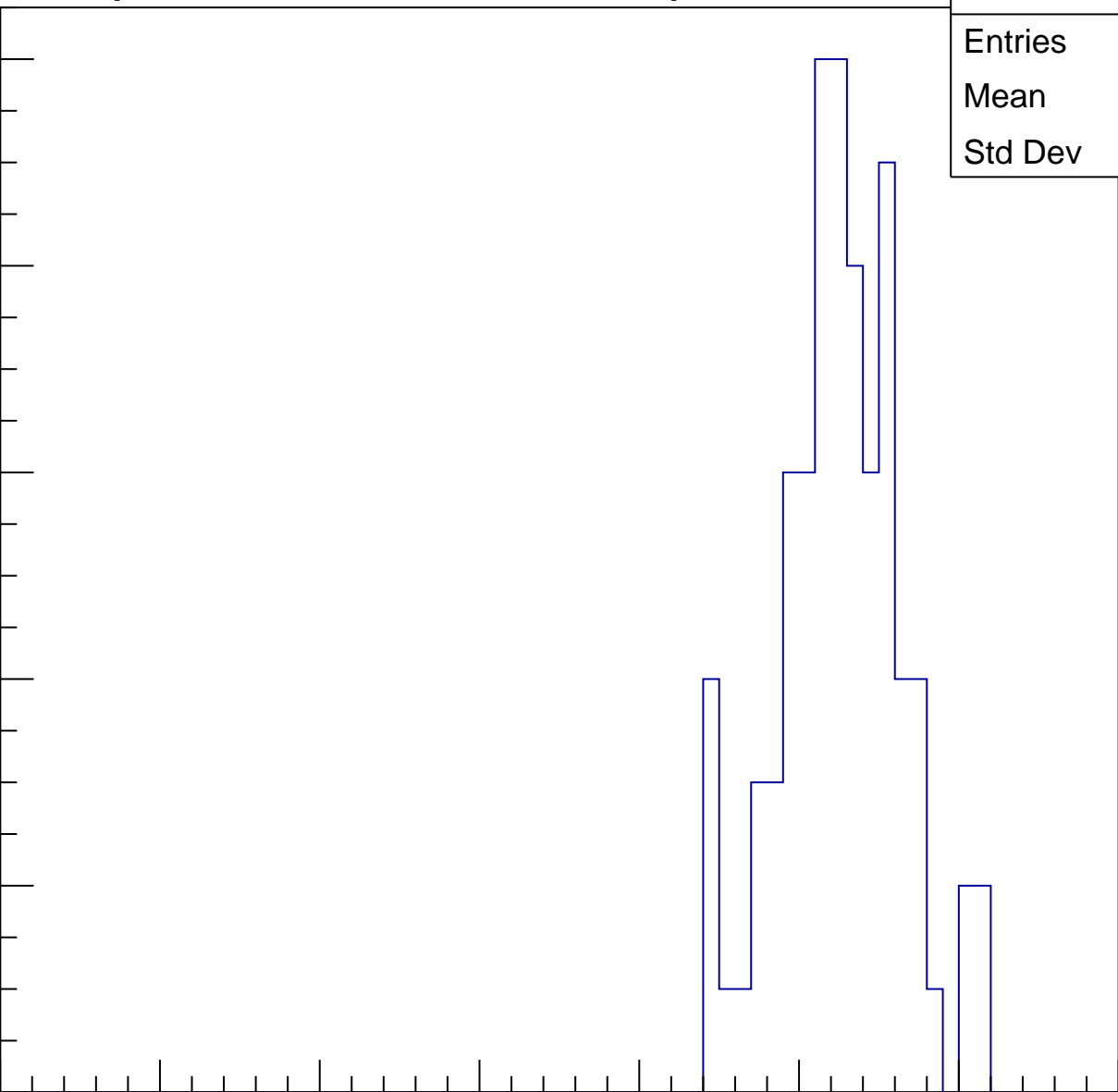
Entries	80
Mean	52.14
Std Dev	3.797

Entry

10
8
6
4
2
0

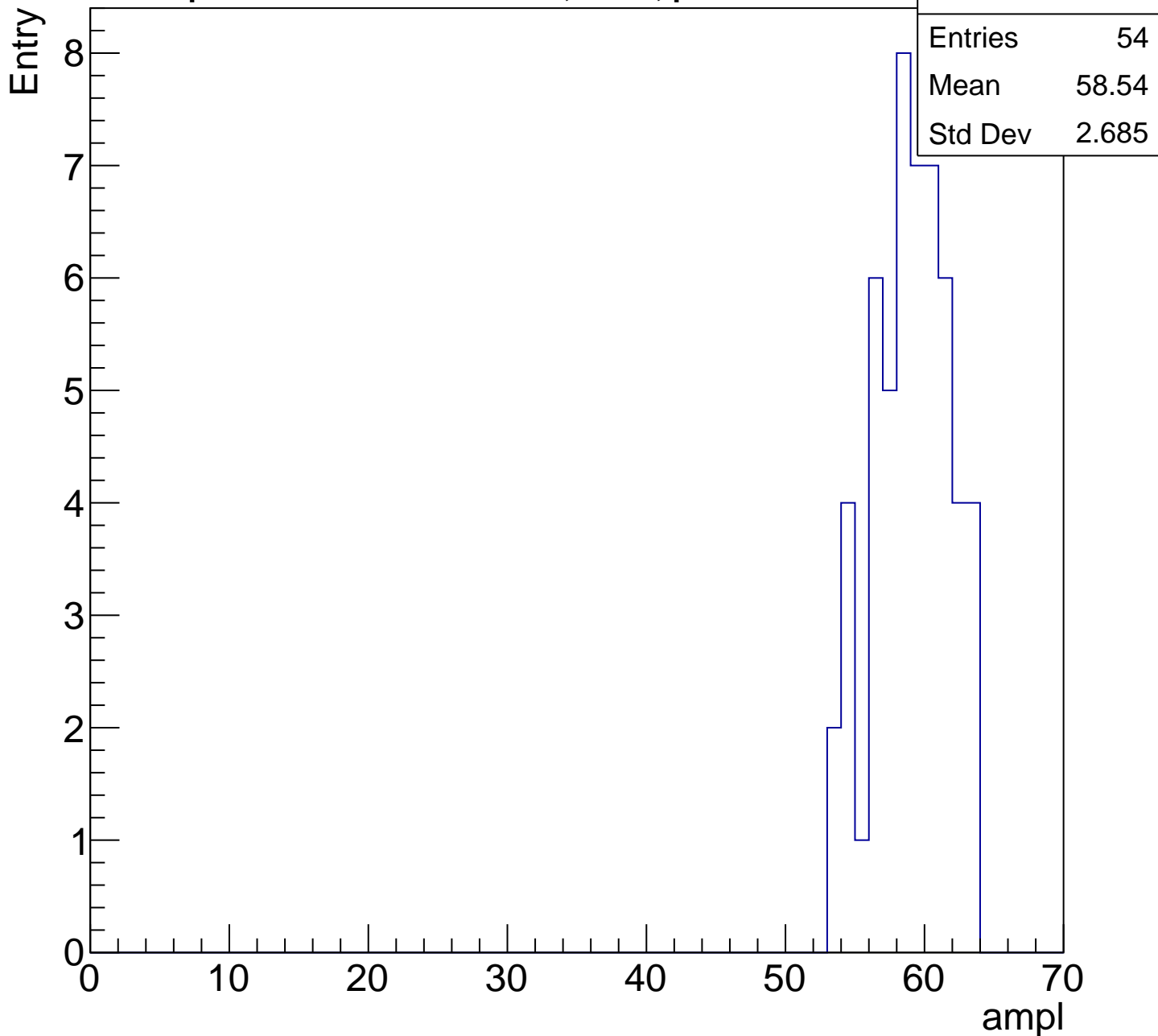
0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch63, adc4

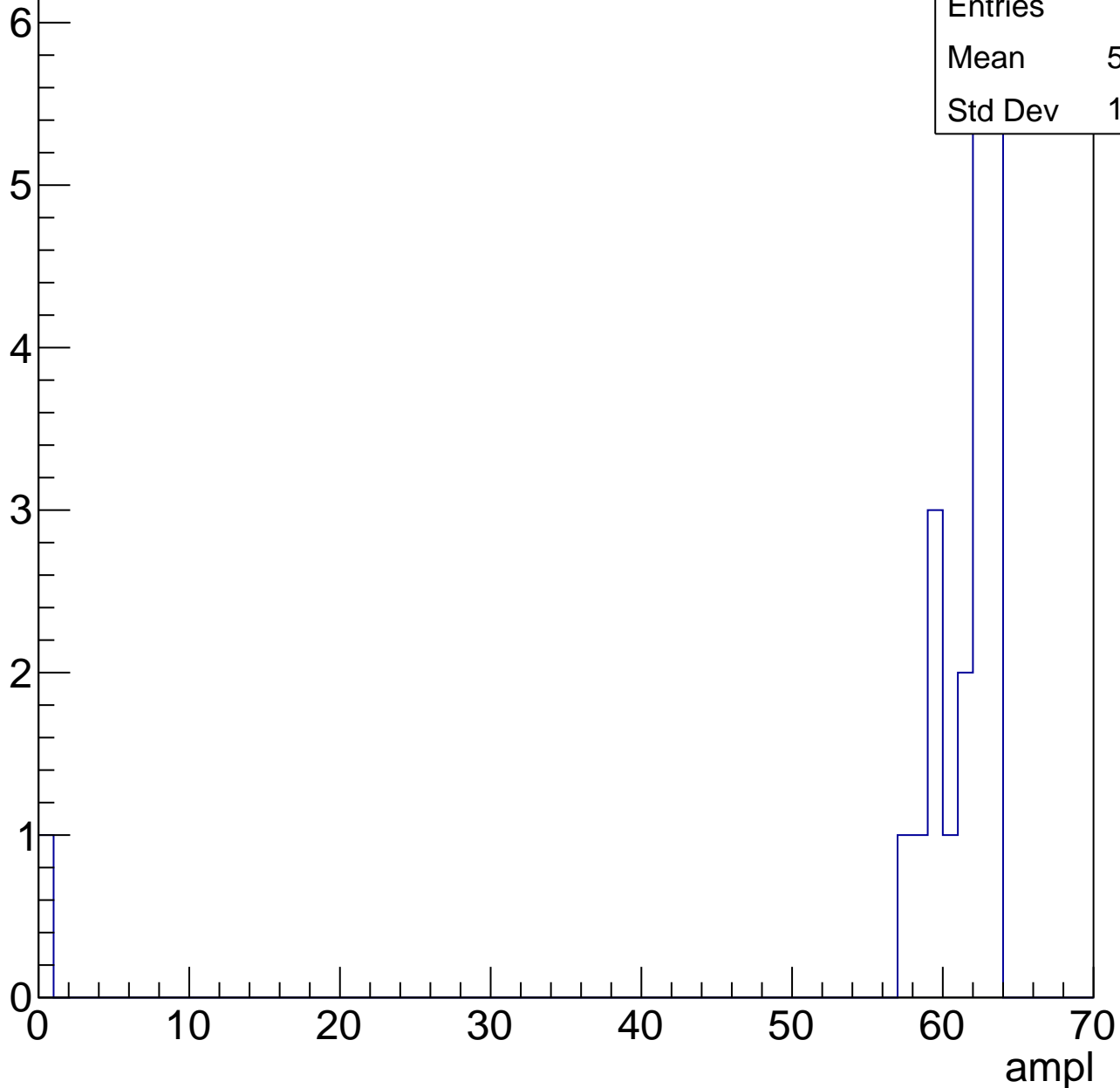
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U13-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch64, adc0

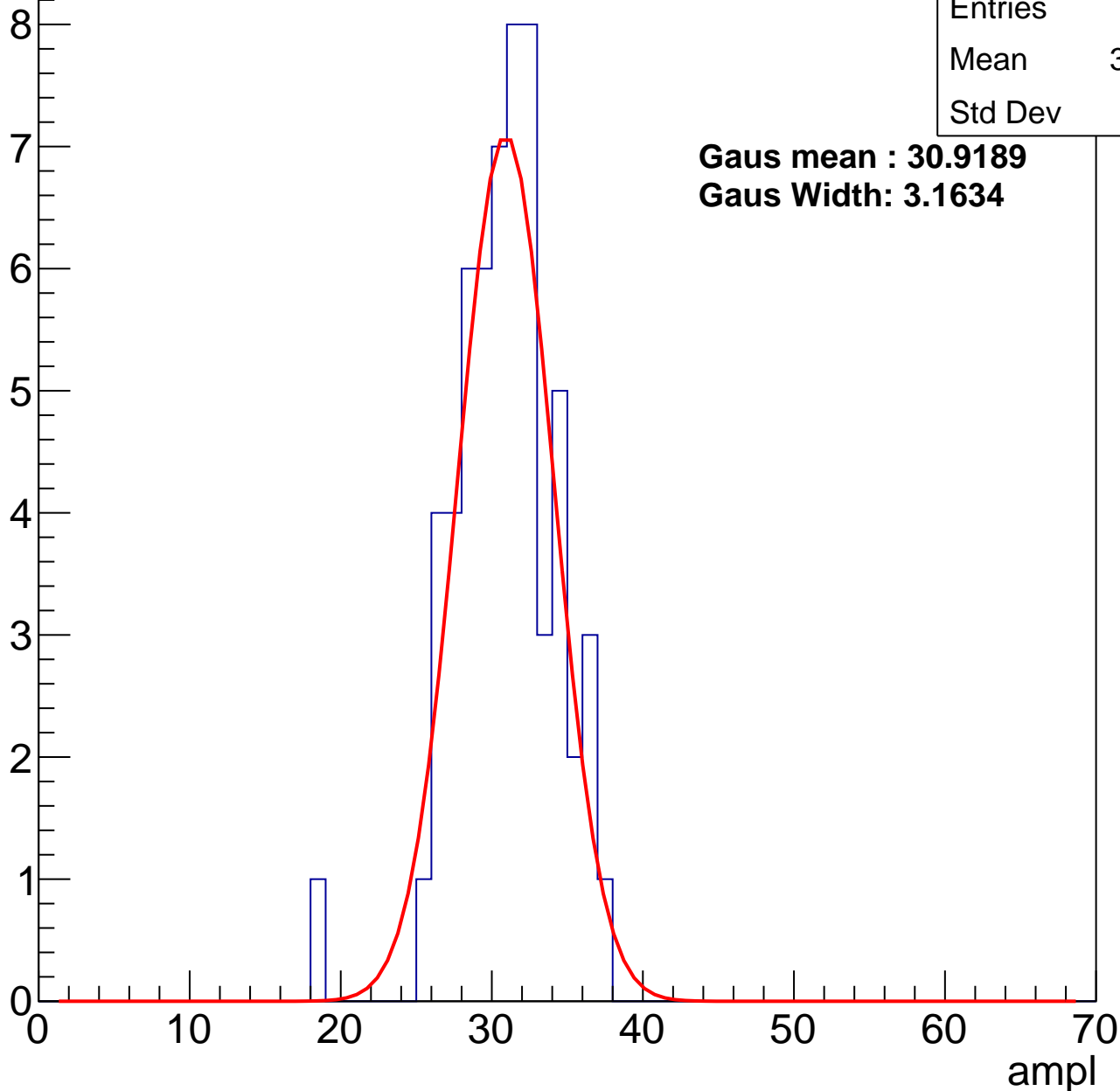
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	30.42
Std Dev	3.3

Gaus mean : 30.9189

Gaus Width: 3.1634



B1L100S, U13-ch64, adc1

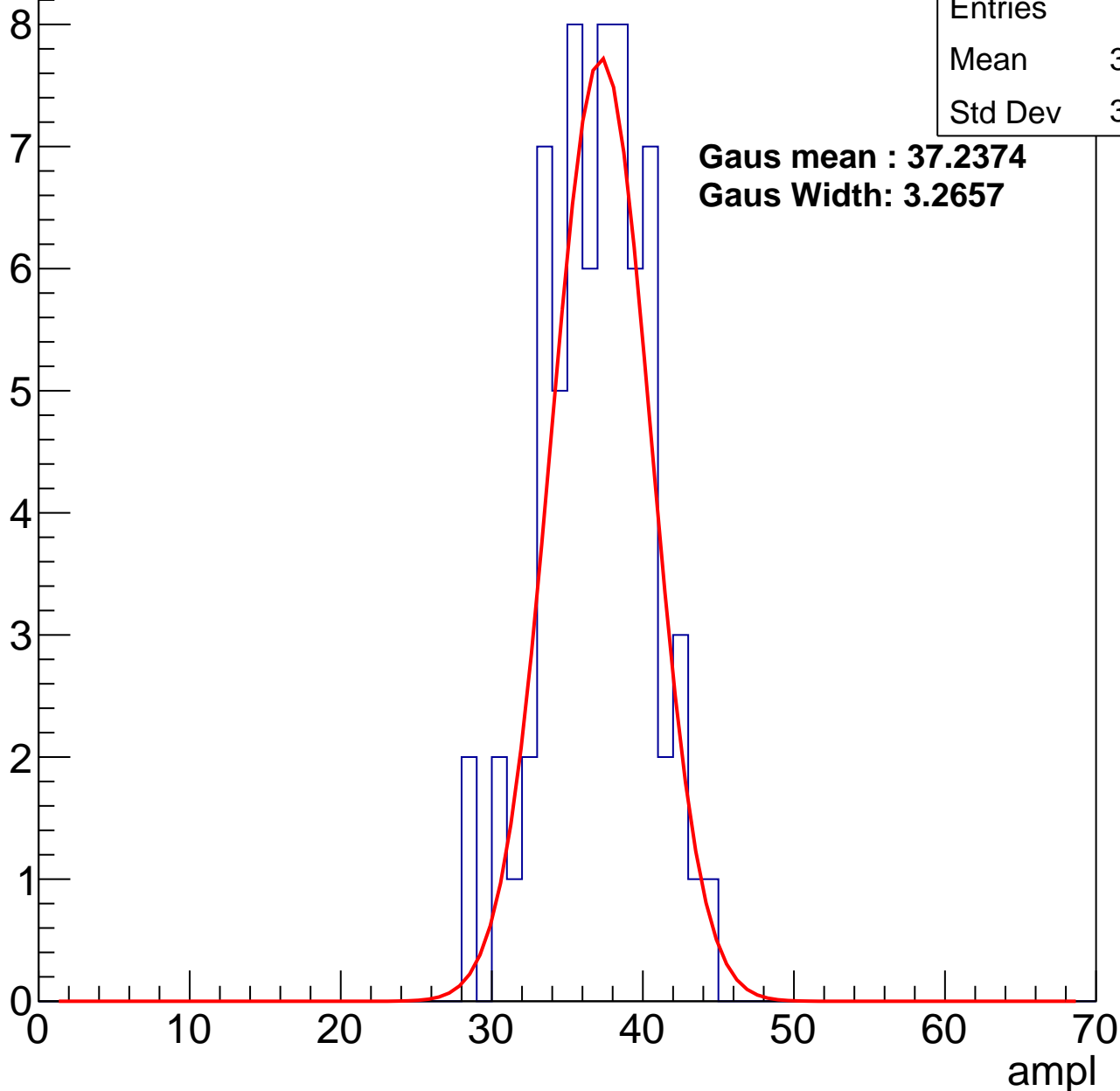
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.48
Std Dev	3.425

Gaus mean : 37.2374

Gaus Width: 3.2657



B1L100S, U13-ch64, adc2

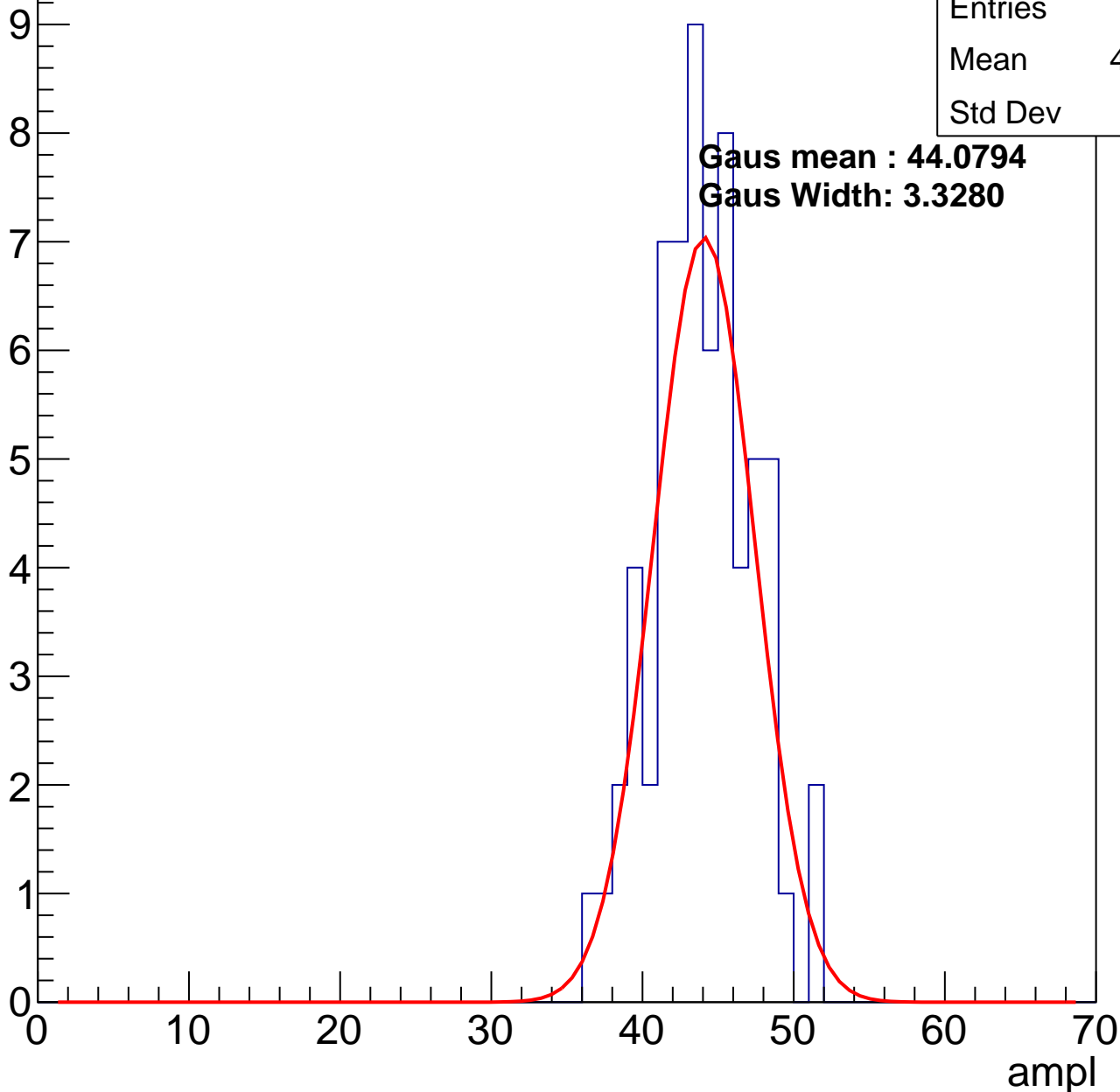
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.55
Std Dev	3.25

Gaus mean : 44.0794

Gaus Width: 3.3280

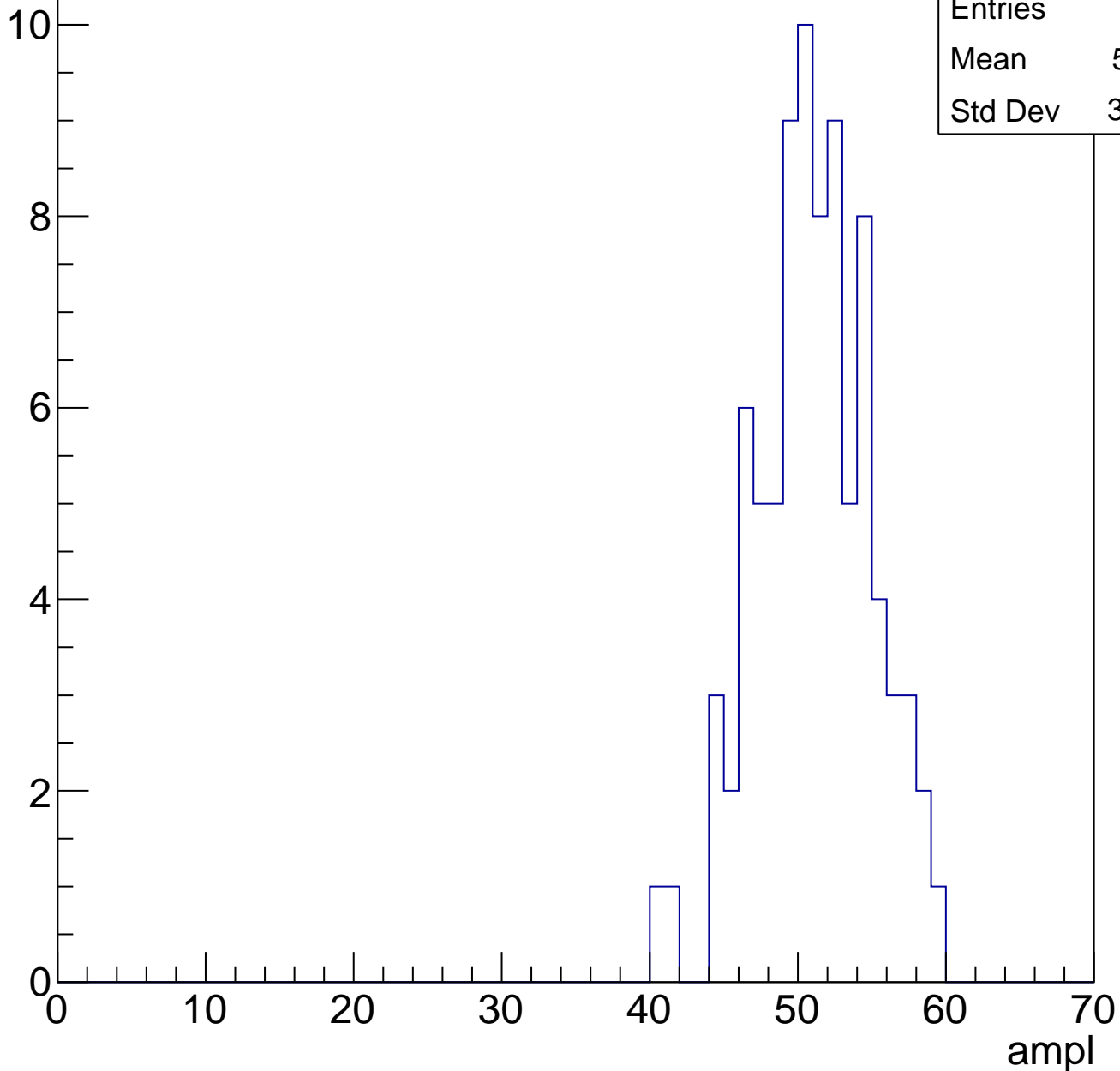


B1L100S, U13-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	50.61
Std Dev	3.847

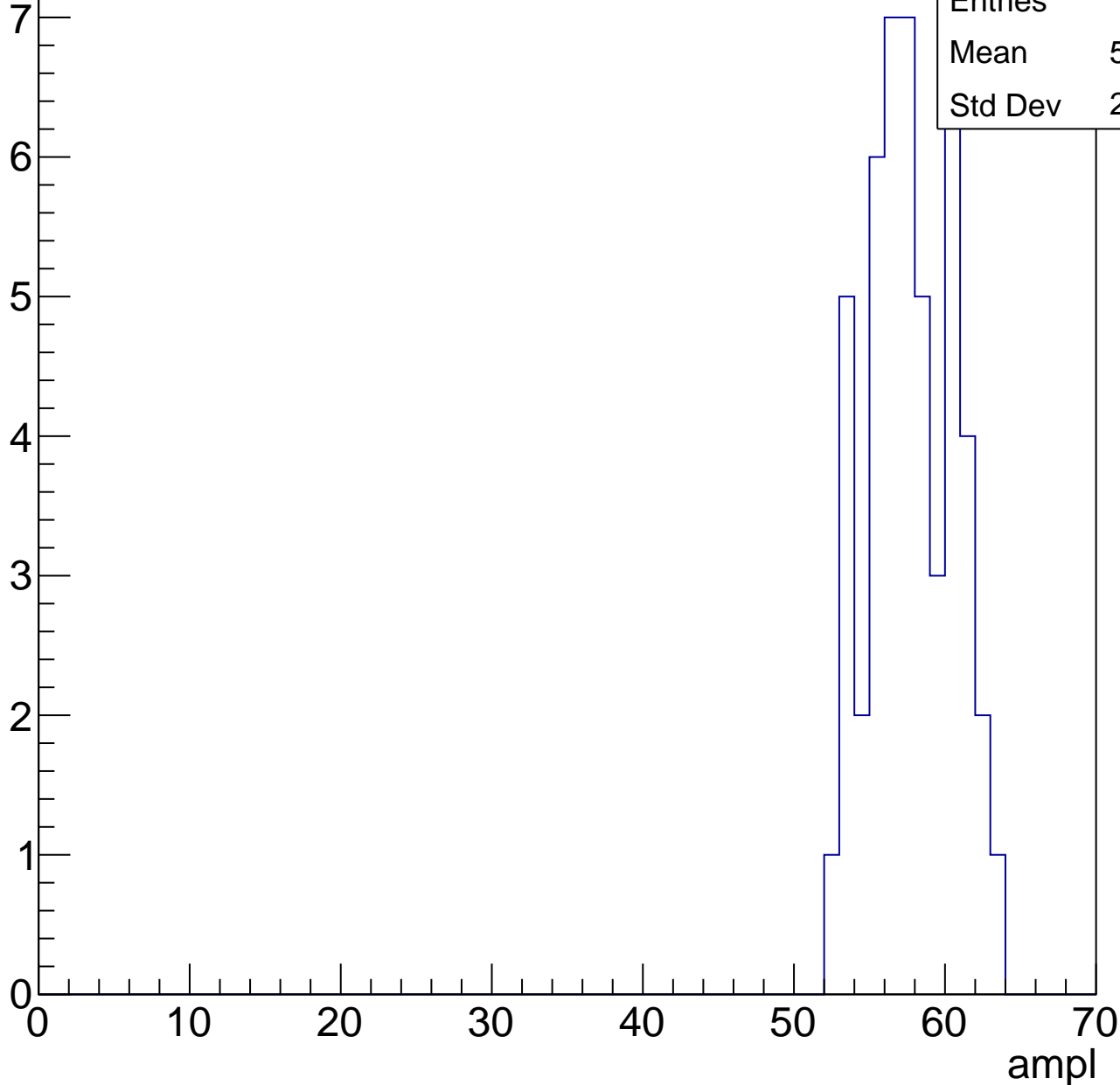
Entry



B1L100S, U13-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



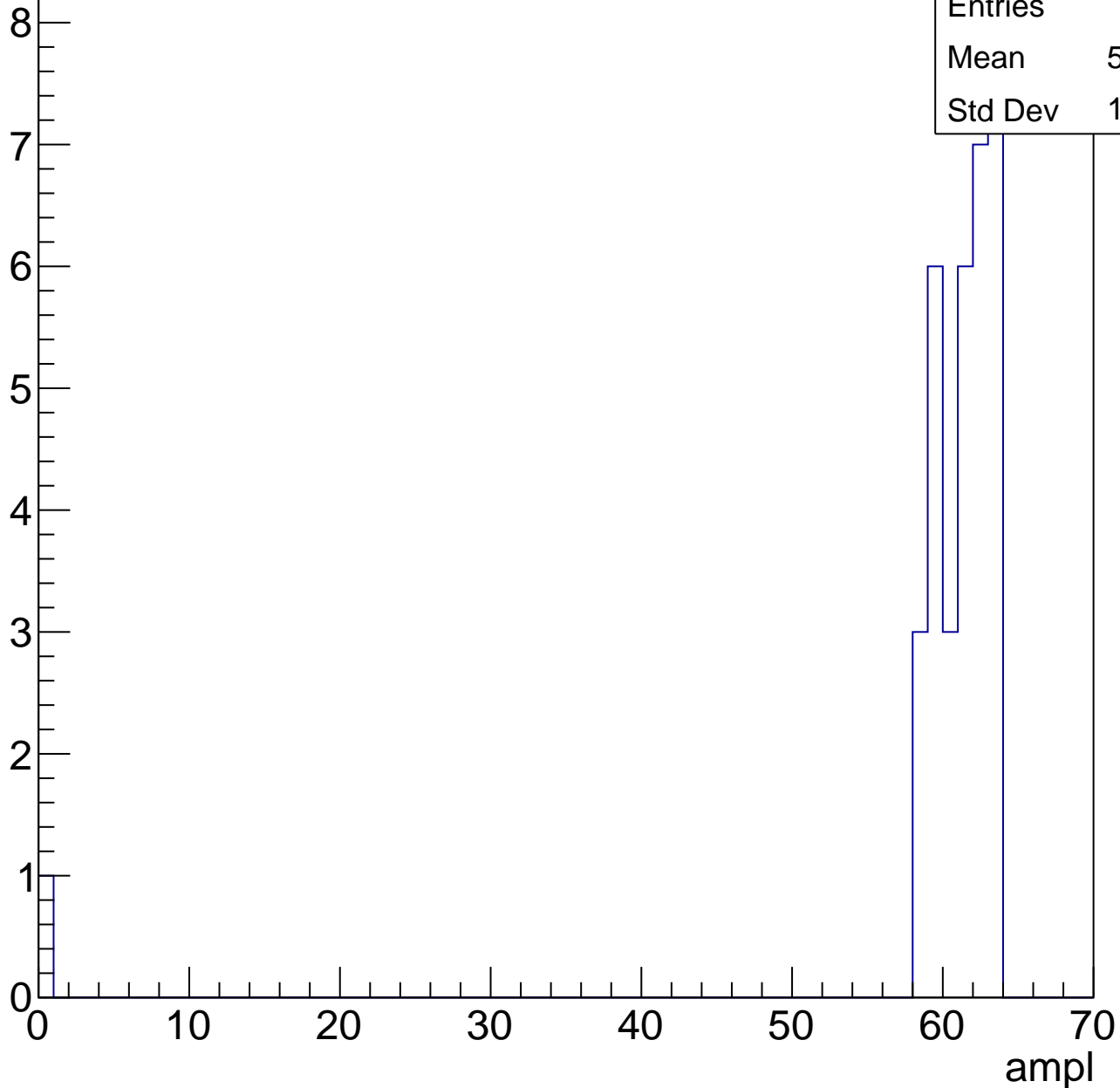
Entries	50
Mean	57.28
Std Dev	2.757

B1L100S, U13-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59.18
Std Dev	10.43



B1L100S, U13-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch65, adc0

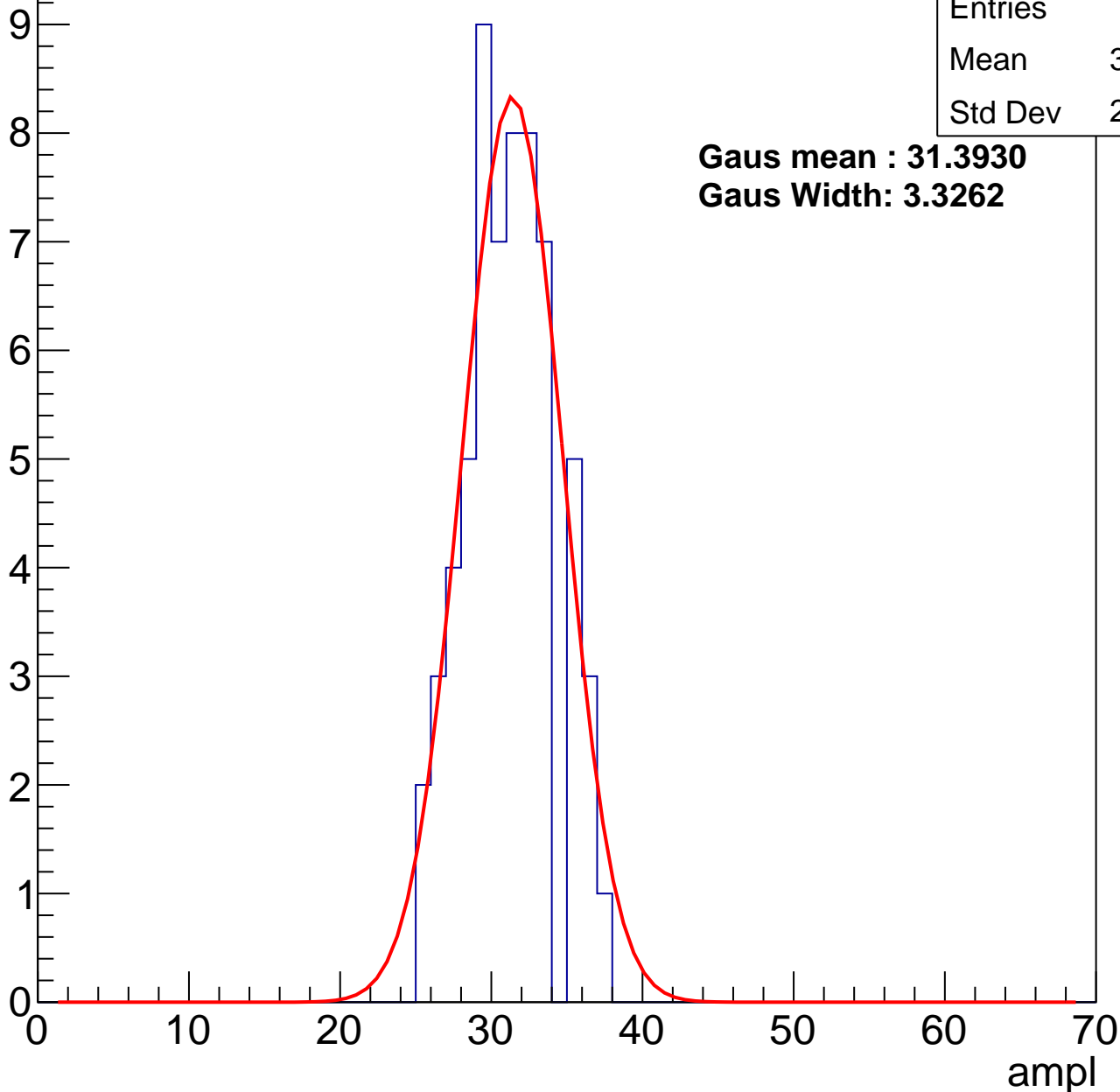
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.68
Std Dev	2.895

Gaus mean : 31.3930

Gaus Width: 3.3262



B1L100S, U13-ch65, adc1

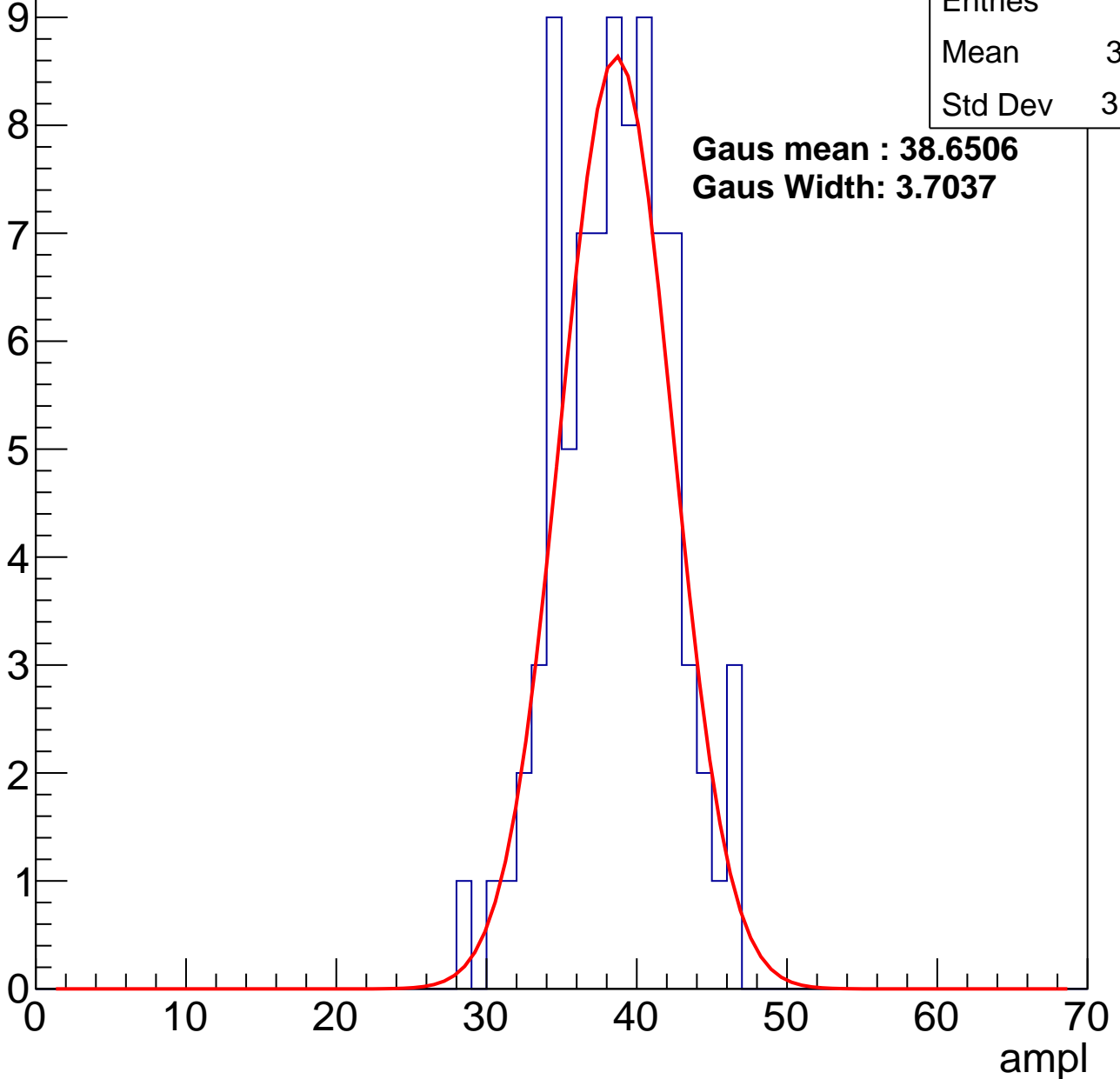
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	38.11
Std Dev	3.742

Gaus mean : 38.6506

Gaus Width: 3.7037



B1L100S, U13-ch65, adc2

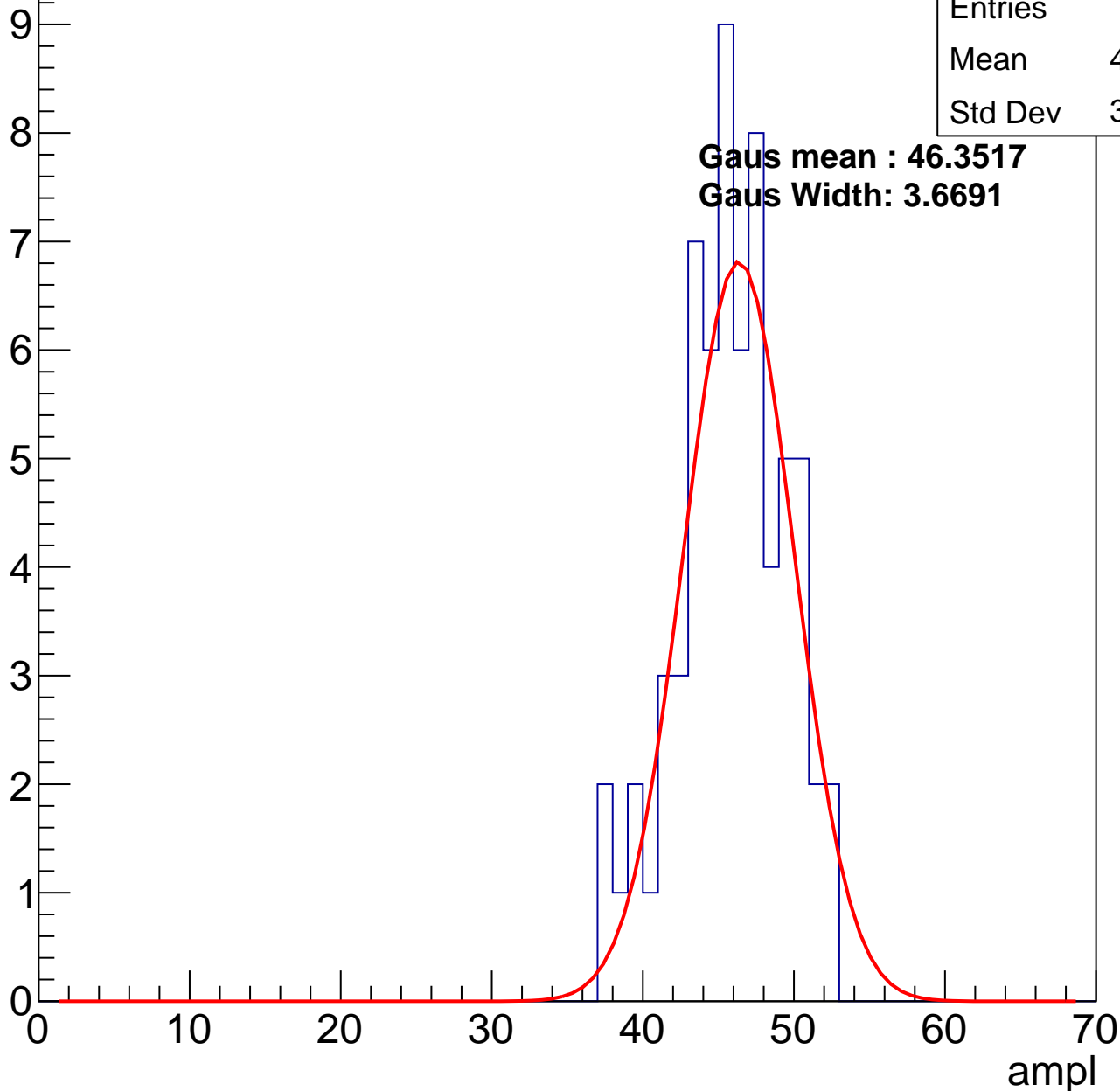
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	45.36
Std Dev	3.532

Gaus mean : 46.3517

Gaus Width: 3.6691

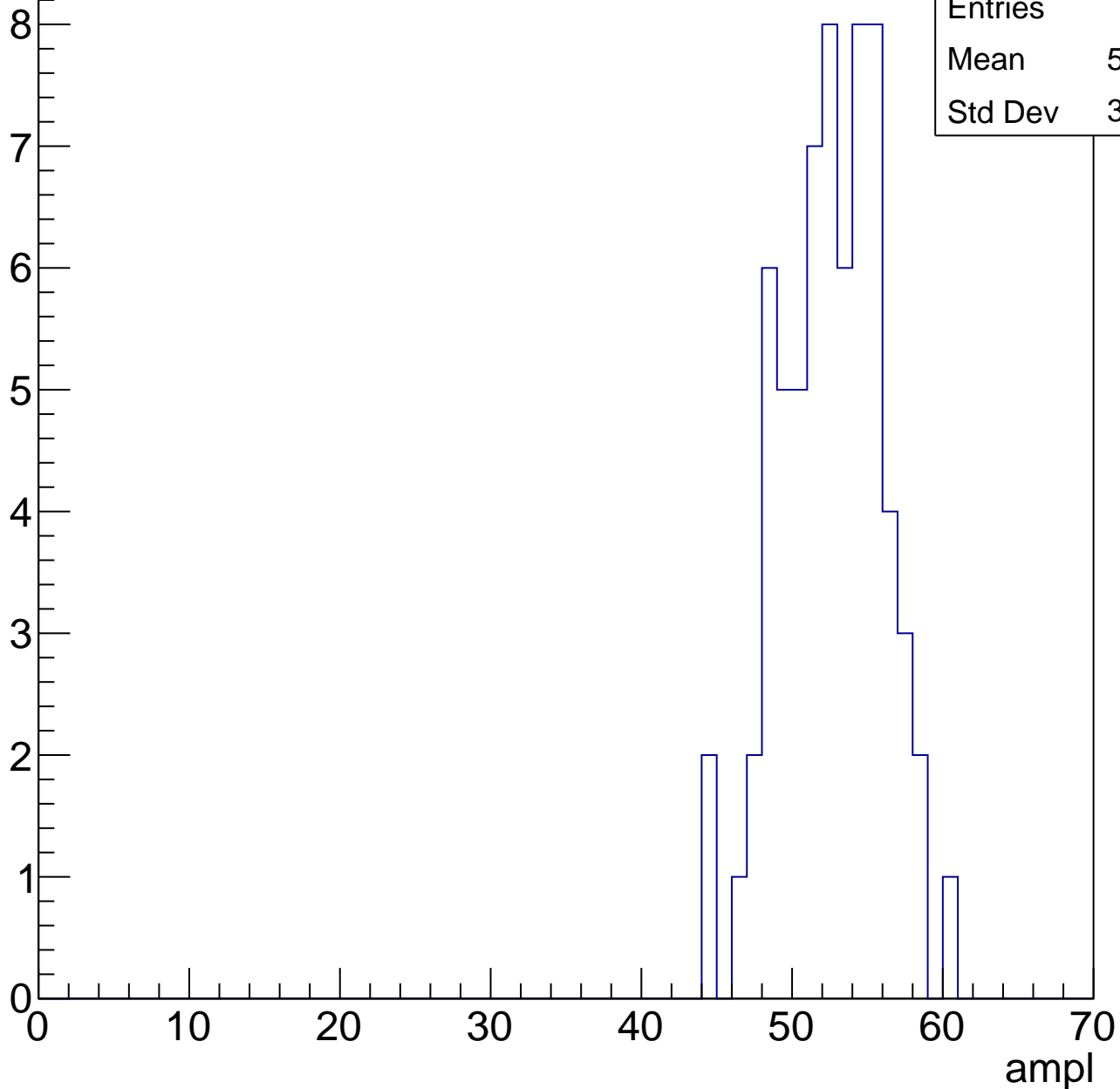


B1L100S, U13-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	52.13
Std Dev	3.356

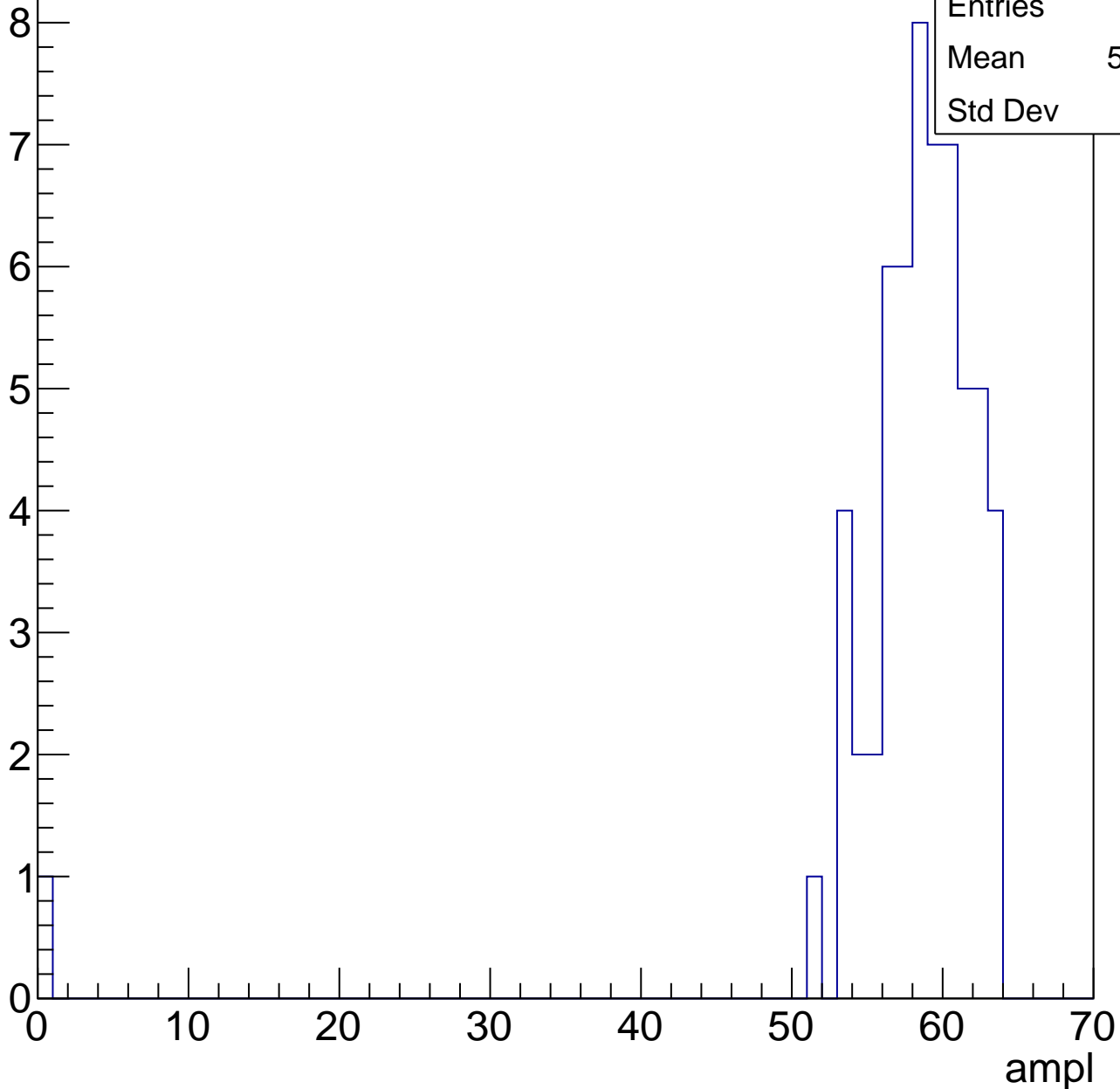


B1L100S, U13-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

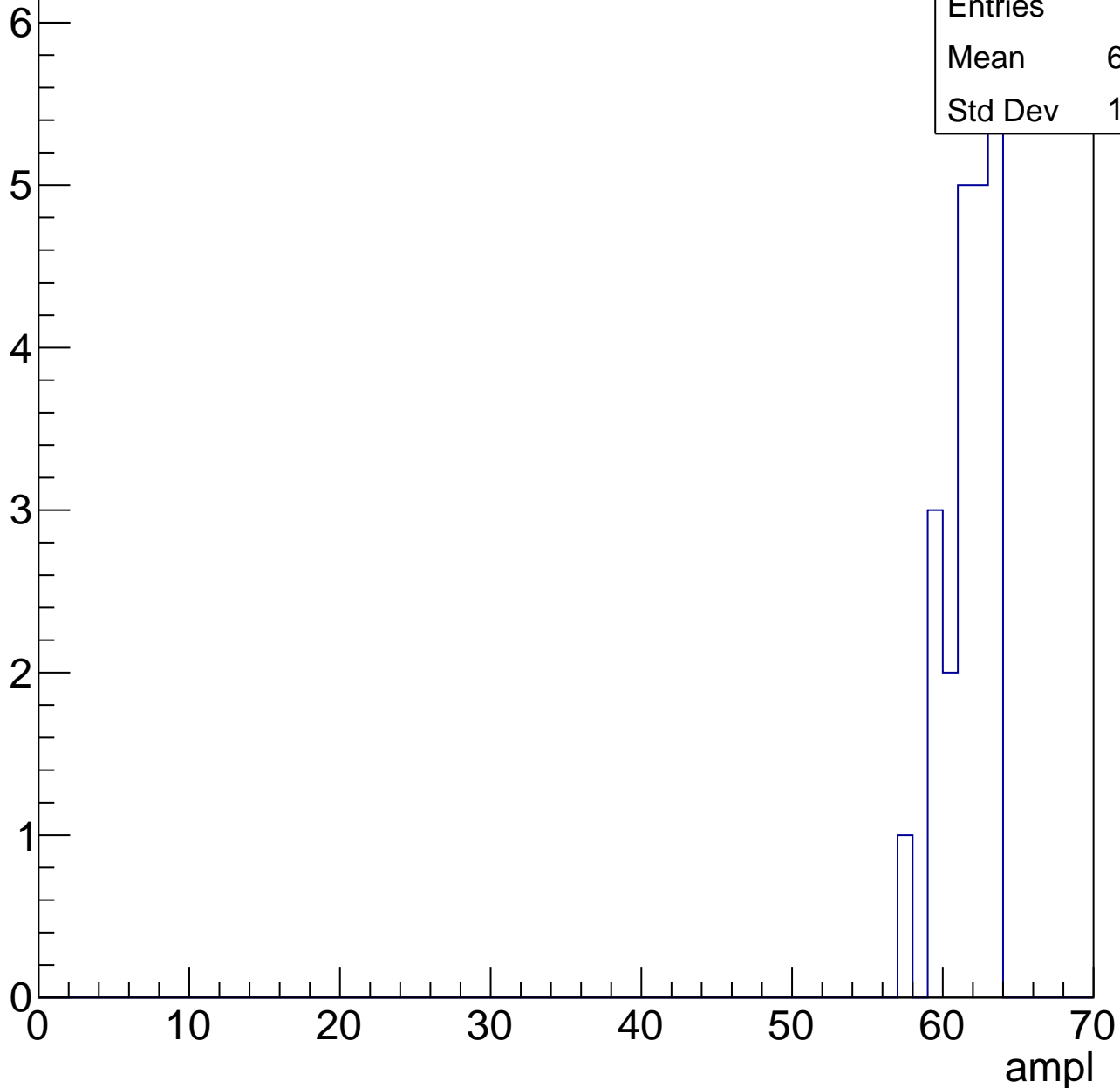
Entries	58
Mean	57.29
Std Dev	8.12



B1L100S, U13-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch66, adc0

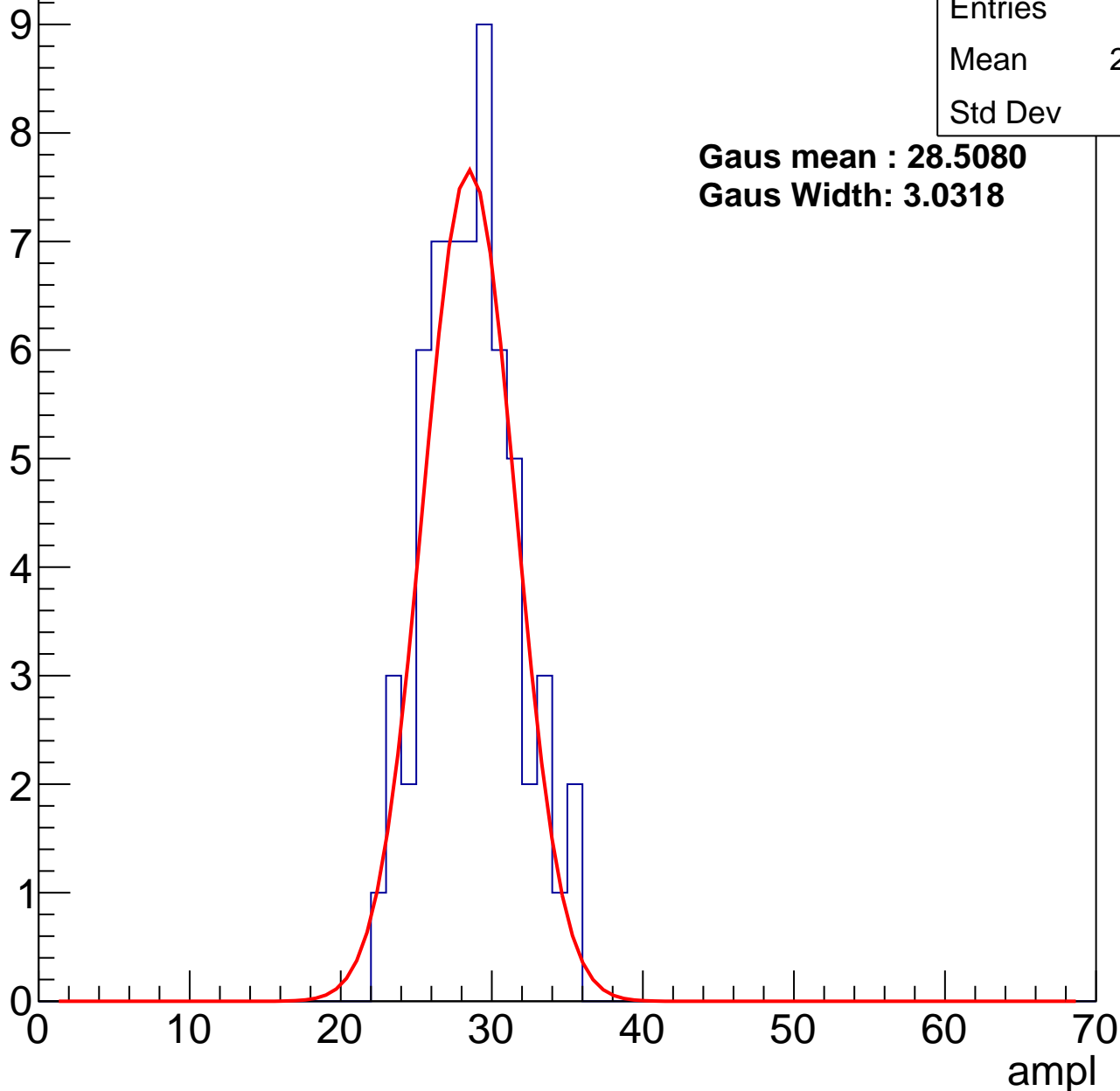
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	28.18
Std Dev	3

Gaus mean : 28.5080

Gaus Width: 3.0318



B1L100S, U13-ch66, adc1

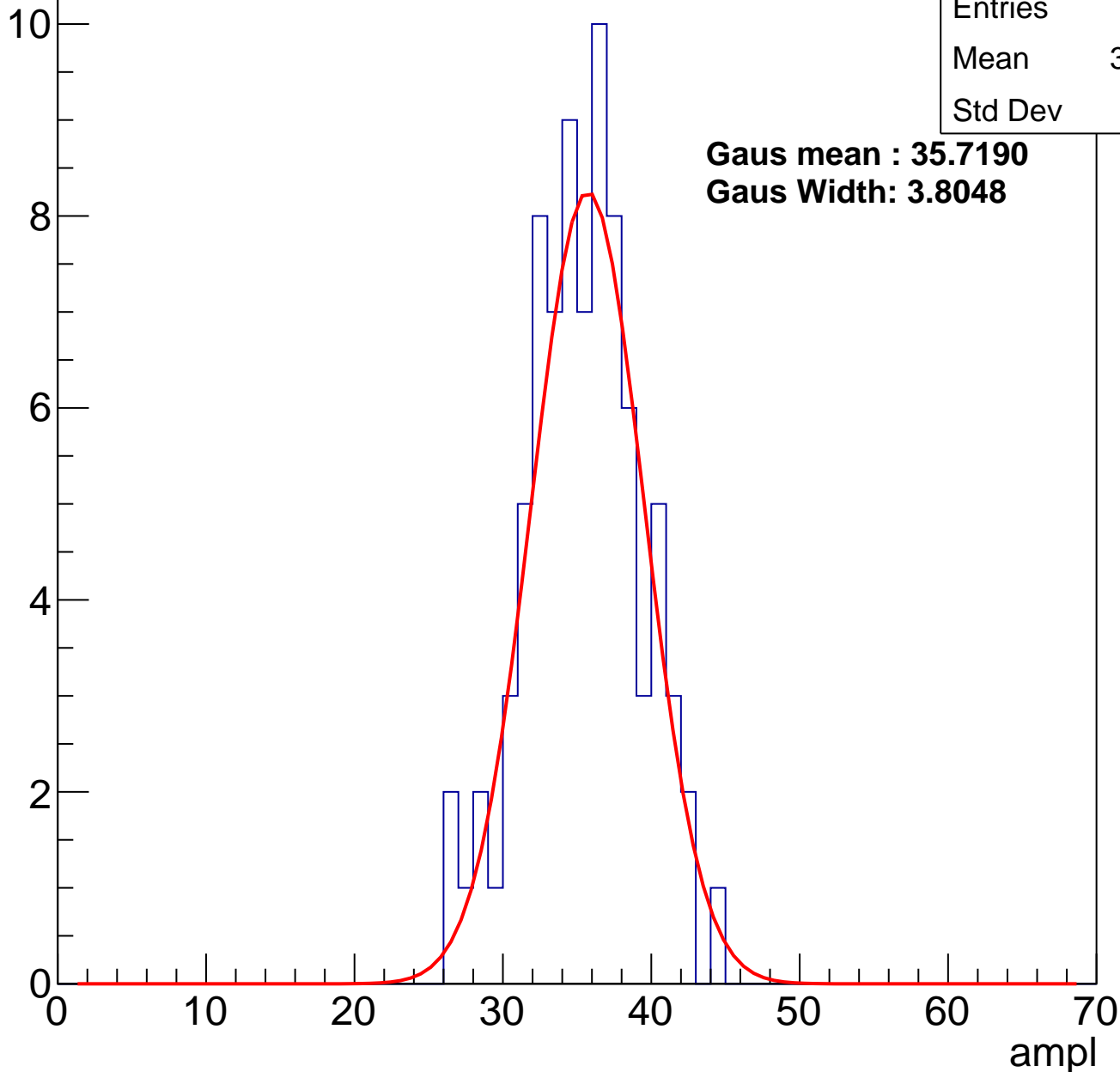
calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	34.93
Std Dev	3.77

Gaus mean : 35.7190

Gaus Width: 3.8048

Entry



B1L100S, U13-ch66, adc2

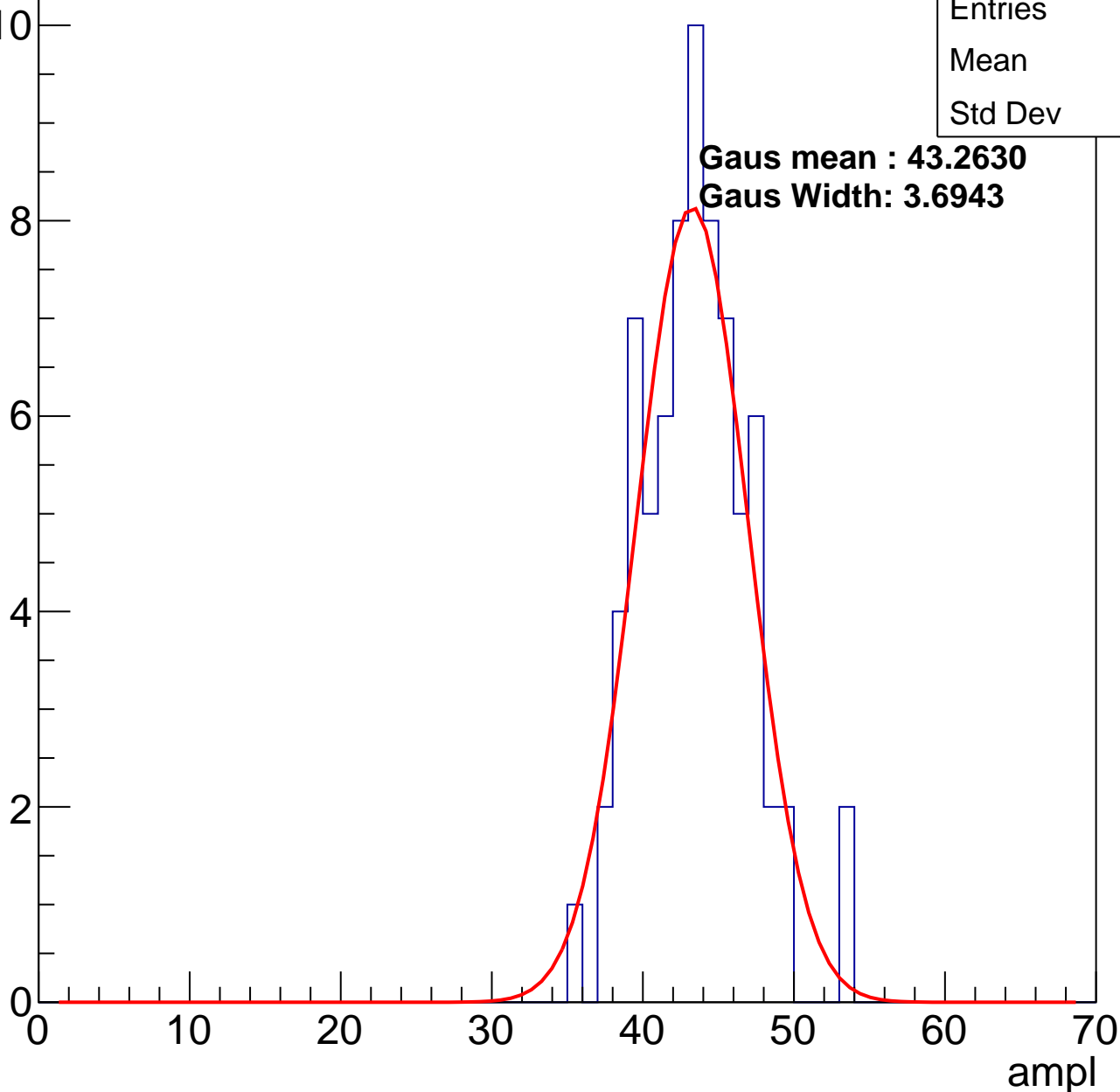
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	43
Std Dev	3.51

Gaus mean : 43.2630

Gaus Width: 3.6943

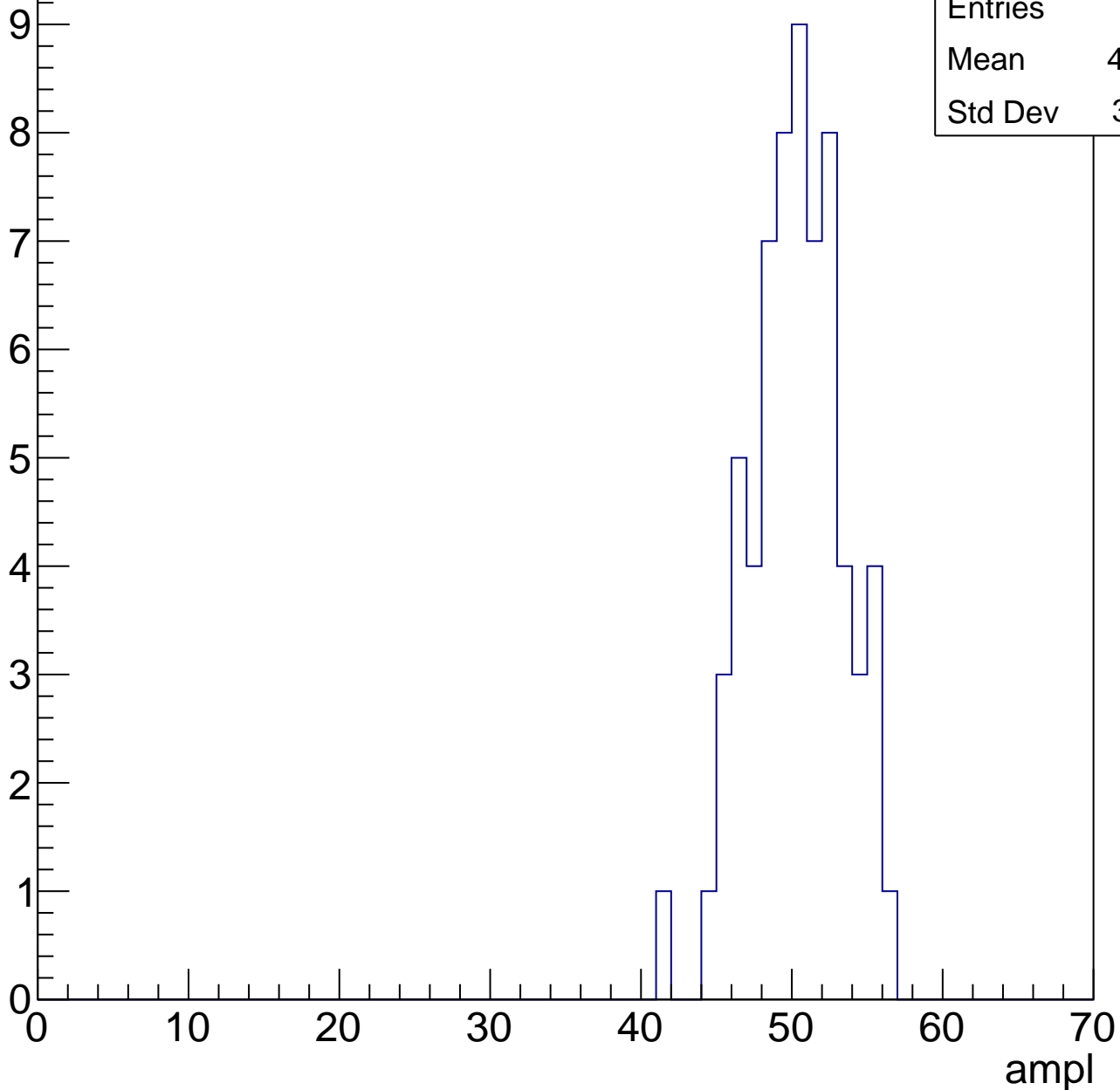


B1L100S, U13-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.83
Std Dev	3.041

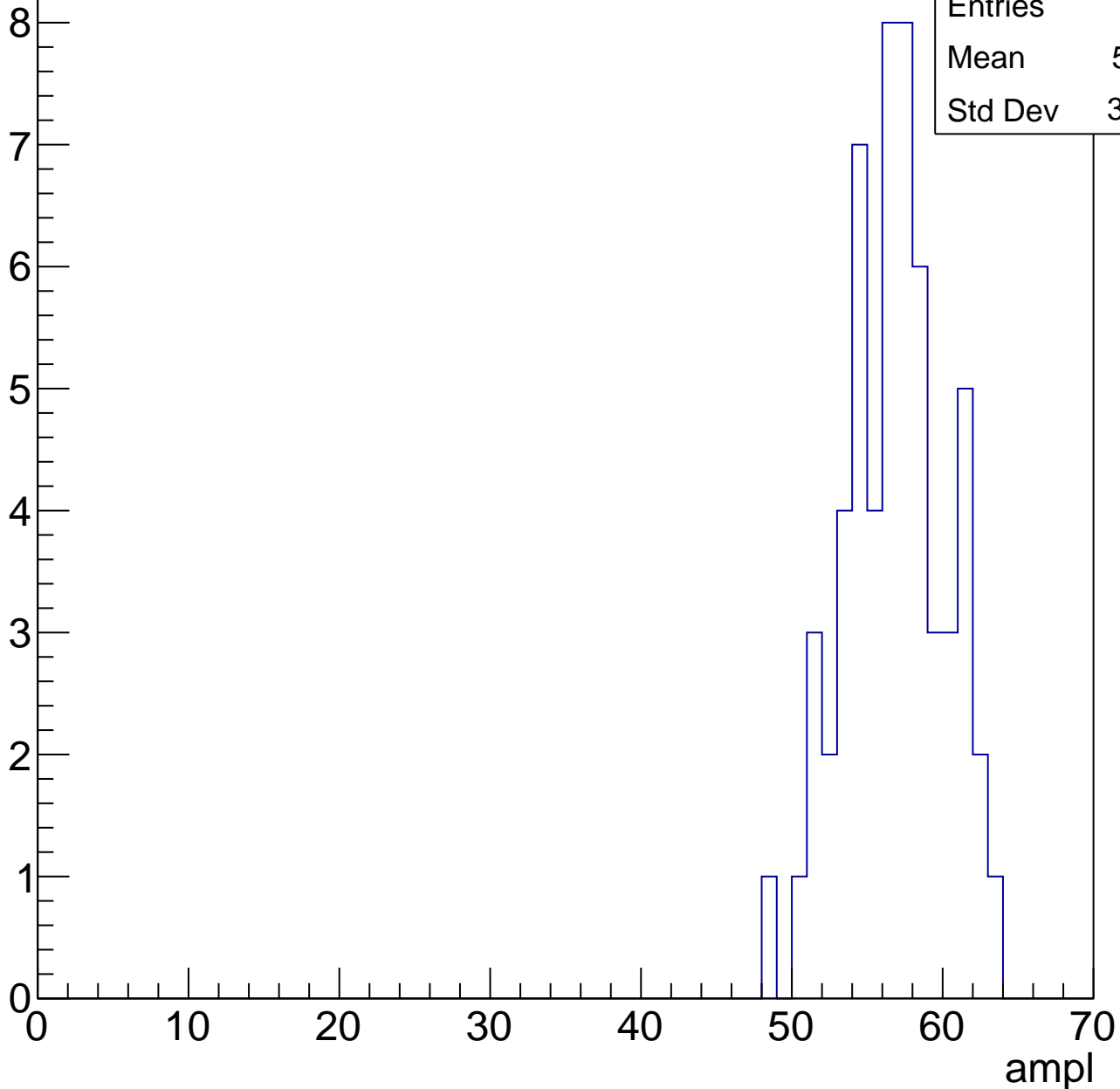


B1L100S, U13-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.31
Std Dev	3.276

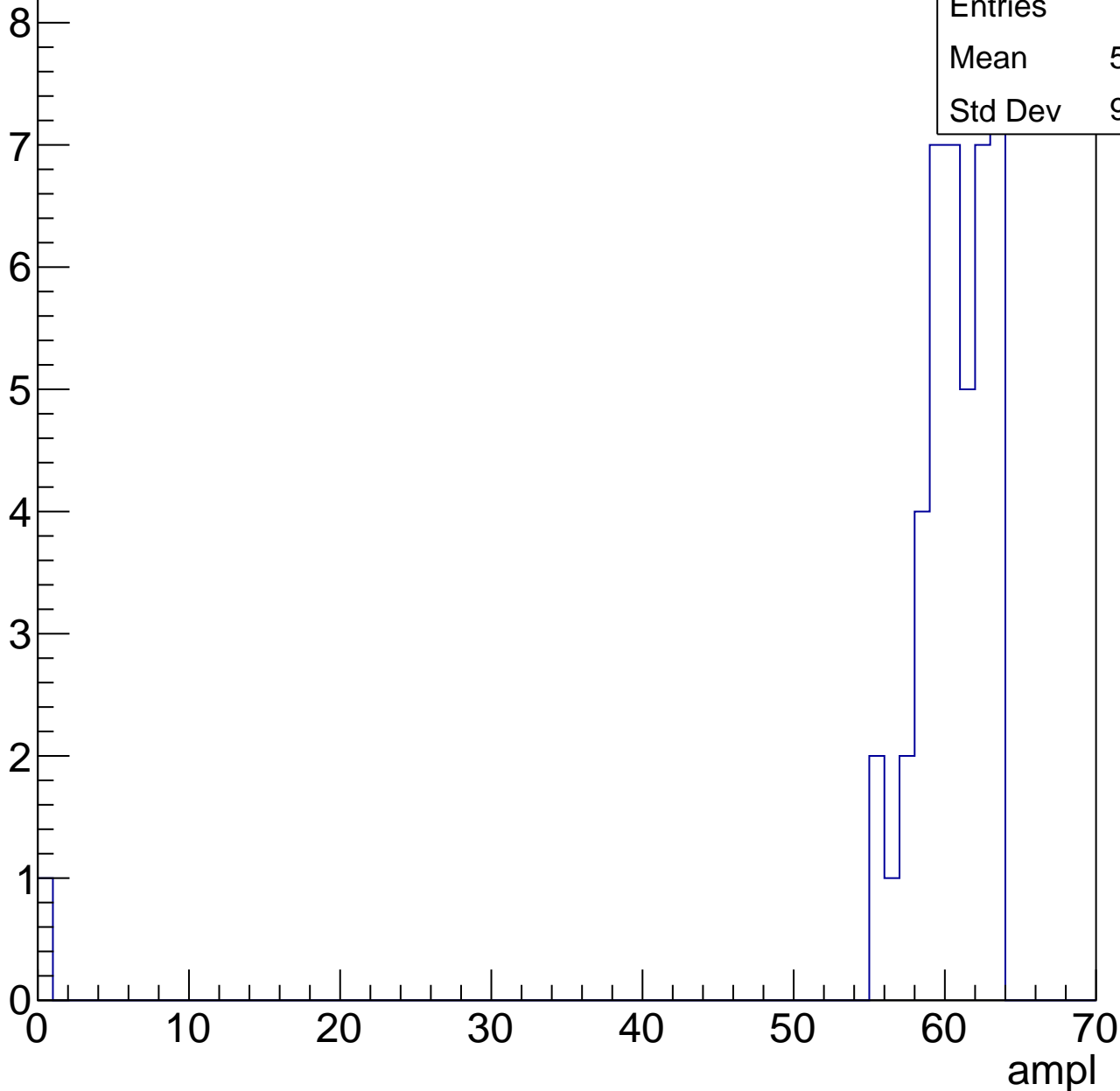


B1L100S, U13-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.82
Std Dev	9.233



B1L100S, U13-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

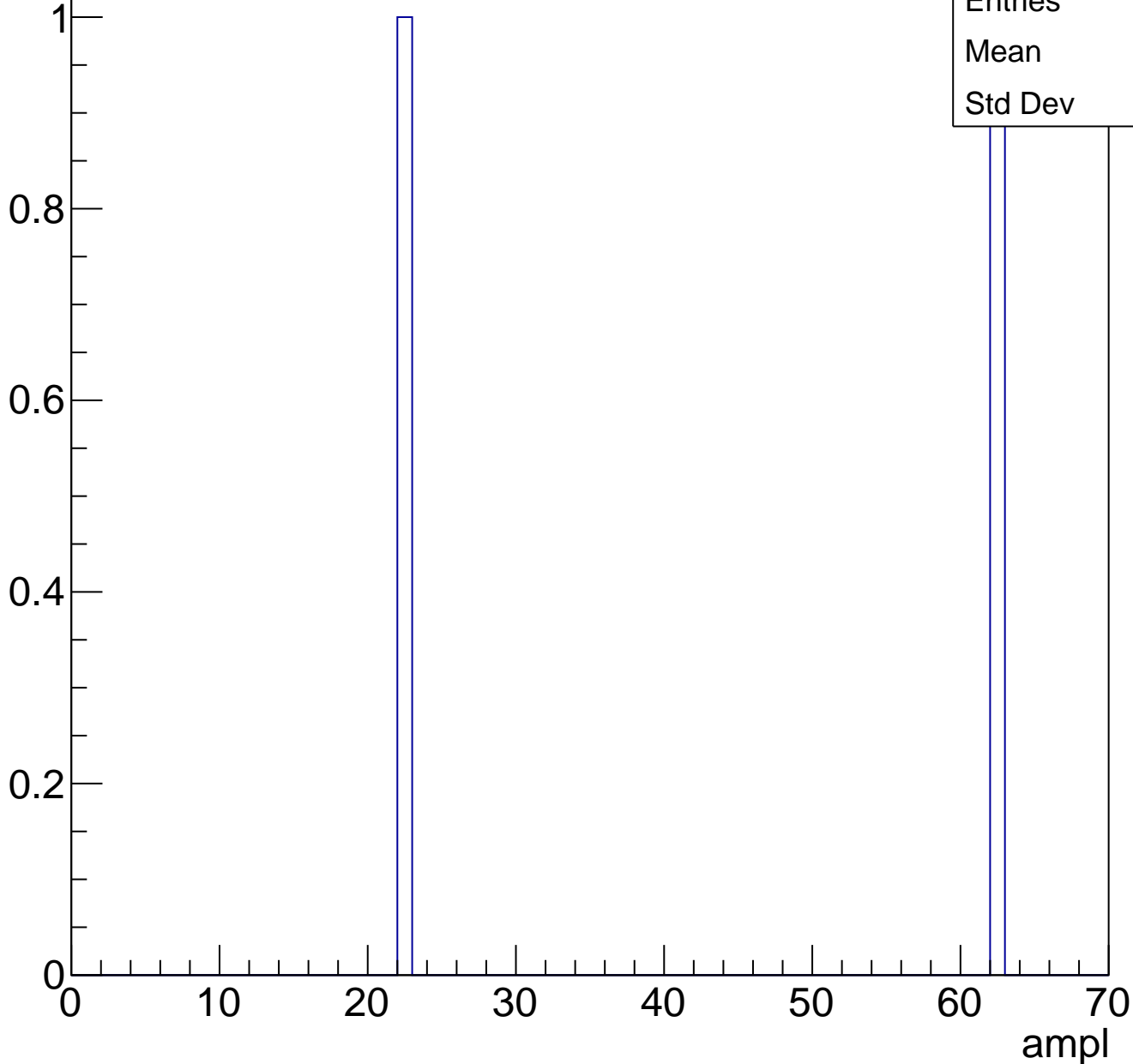
Entry



B1L100S, U13-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch67, adc0

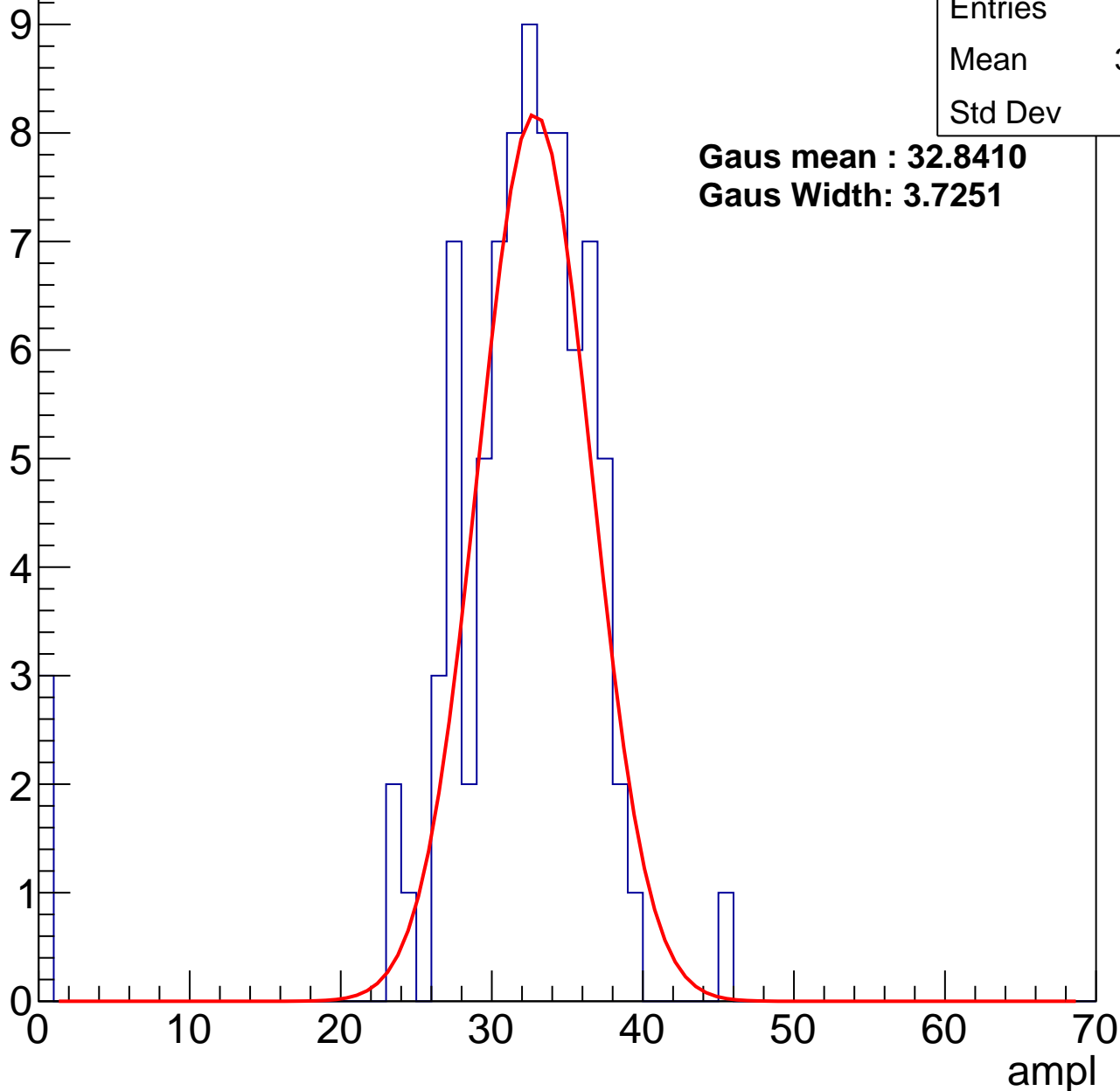
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.91
Std Dev	7.04

Gaus mean : 32.8410

Gaus Width: 3.7251



B1L100S, U13-ch67, adc1

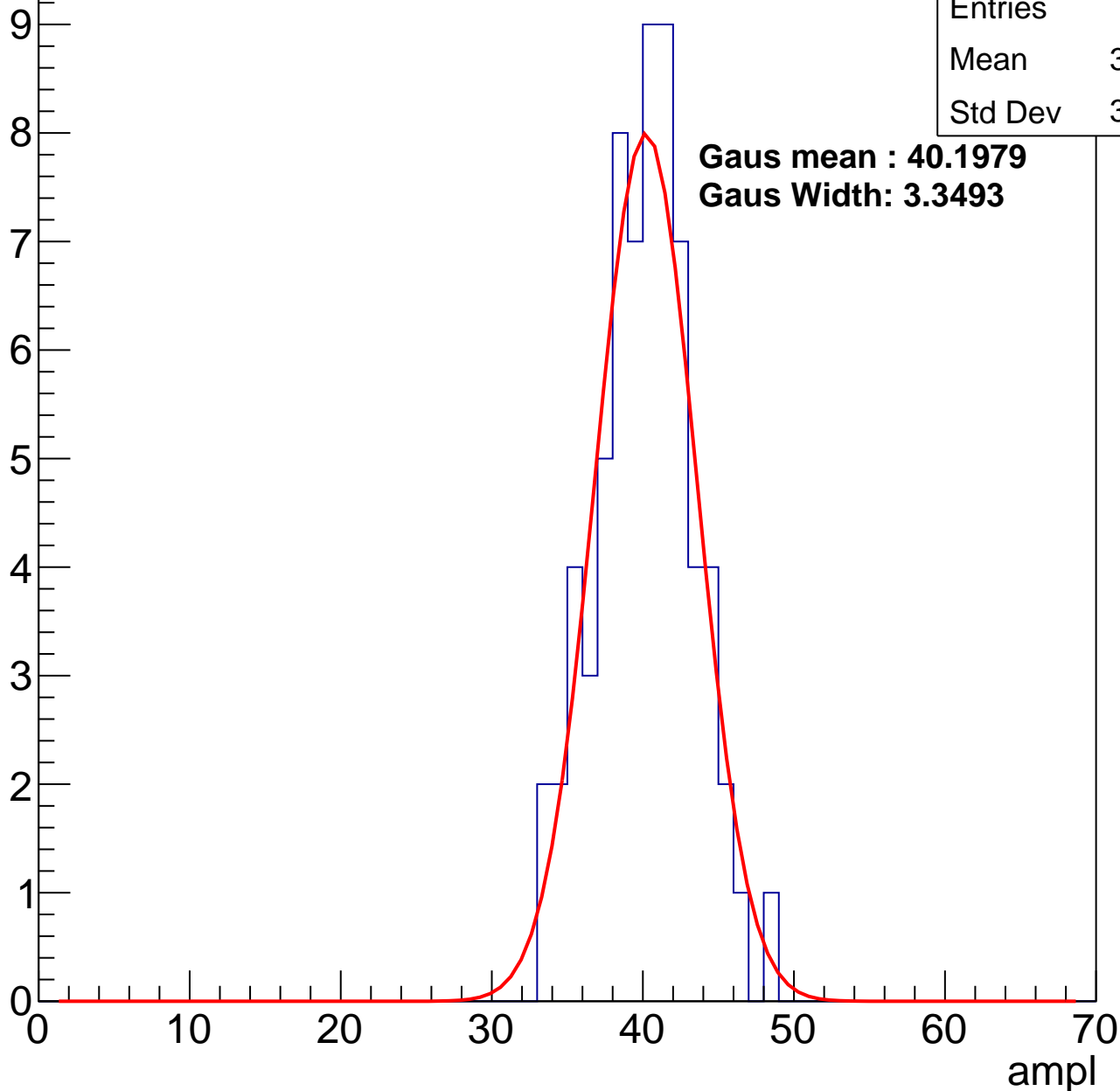
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	39.69
Std Dev	3.168

Gaus mean : 40.1979

Gaus Width: 3.3493



B1L100S, U13-ch67, adc2

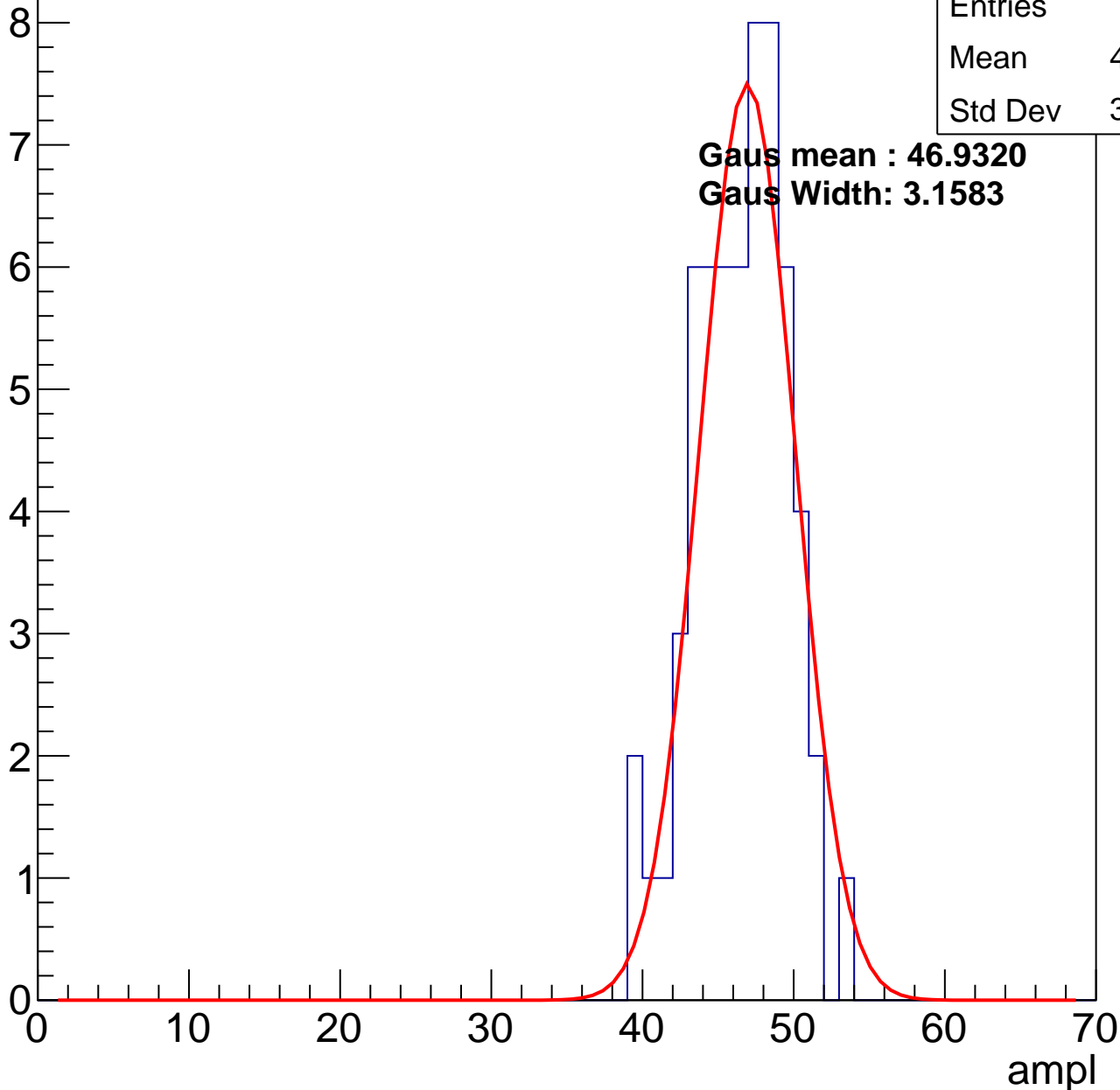
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	46.03
Std Dev	3.022

Gaus mean : 46.9320

Gaus Width: 3.1583

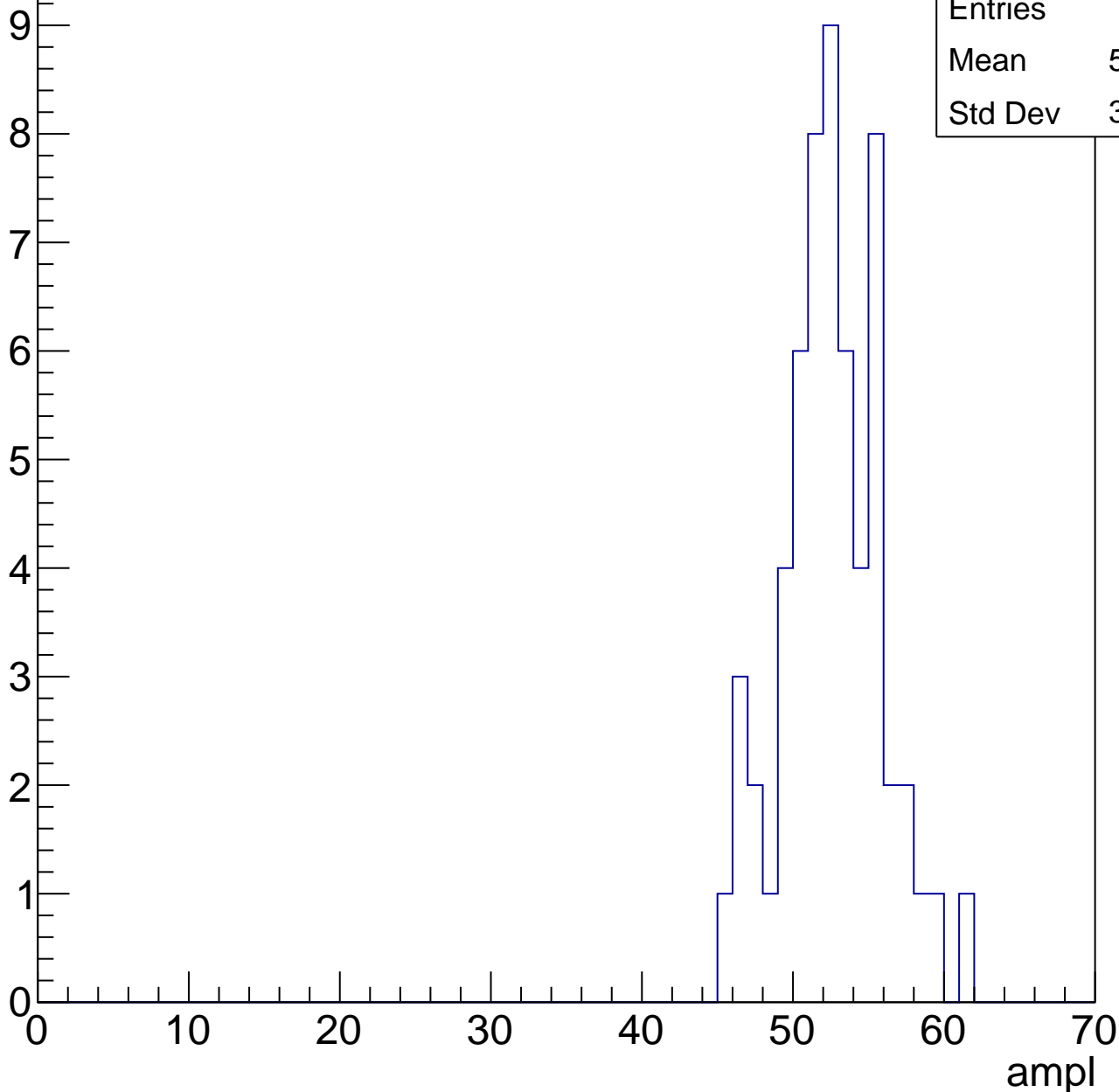


B1L100S, U13-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	52.12
Std Dev	3.273

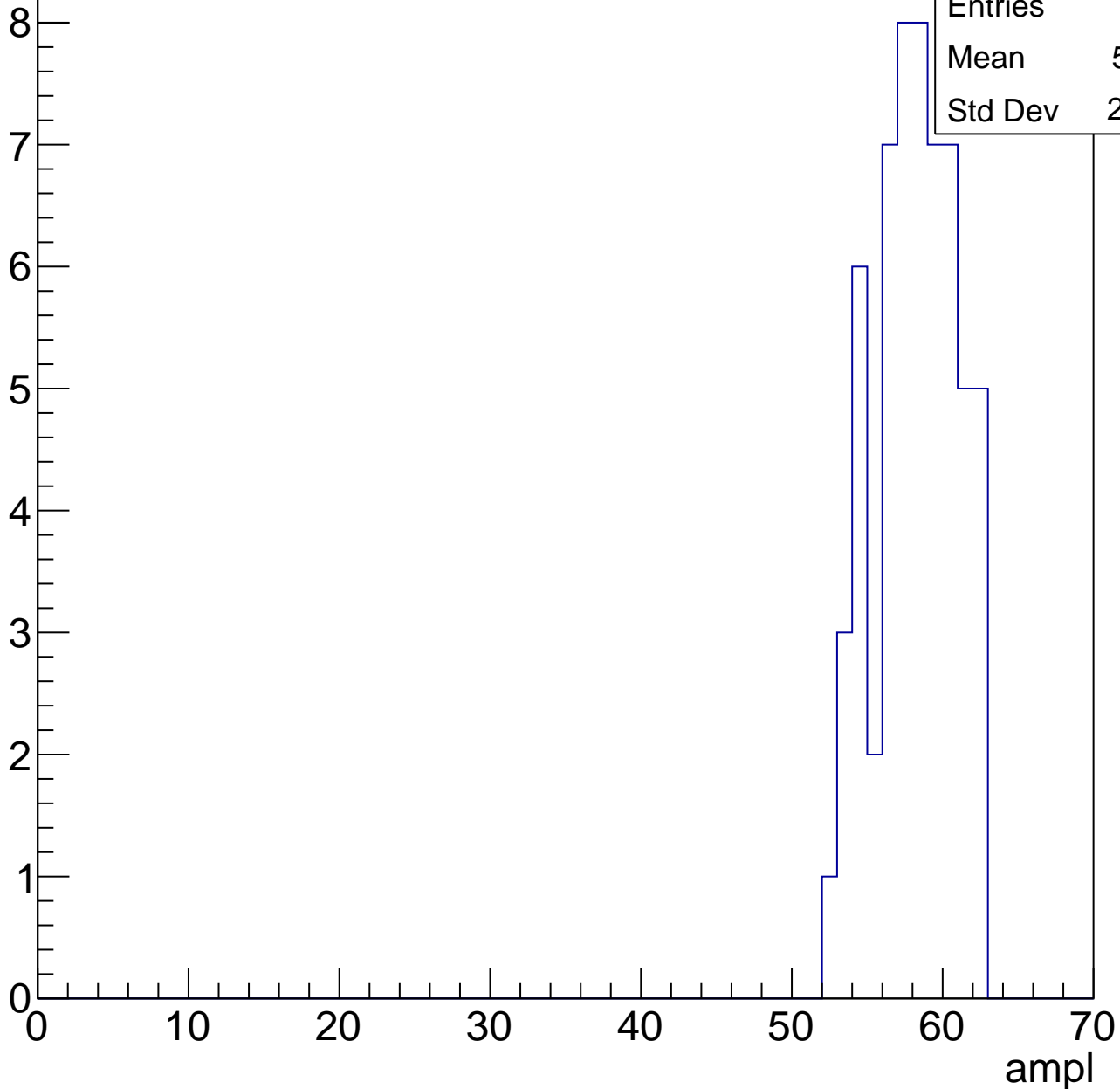


B1L100S, U13-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	57.71
Std Dev	2.656



B1L100S, U13-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	27
Mean	59.11
Std Dev	11.73

Entry

10

8

6

4

2

0

0

10

20

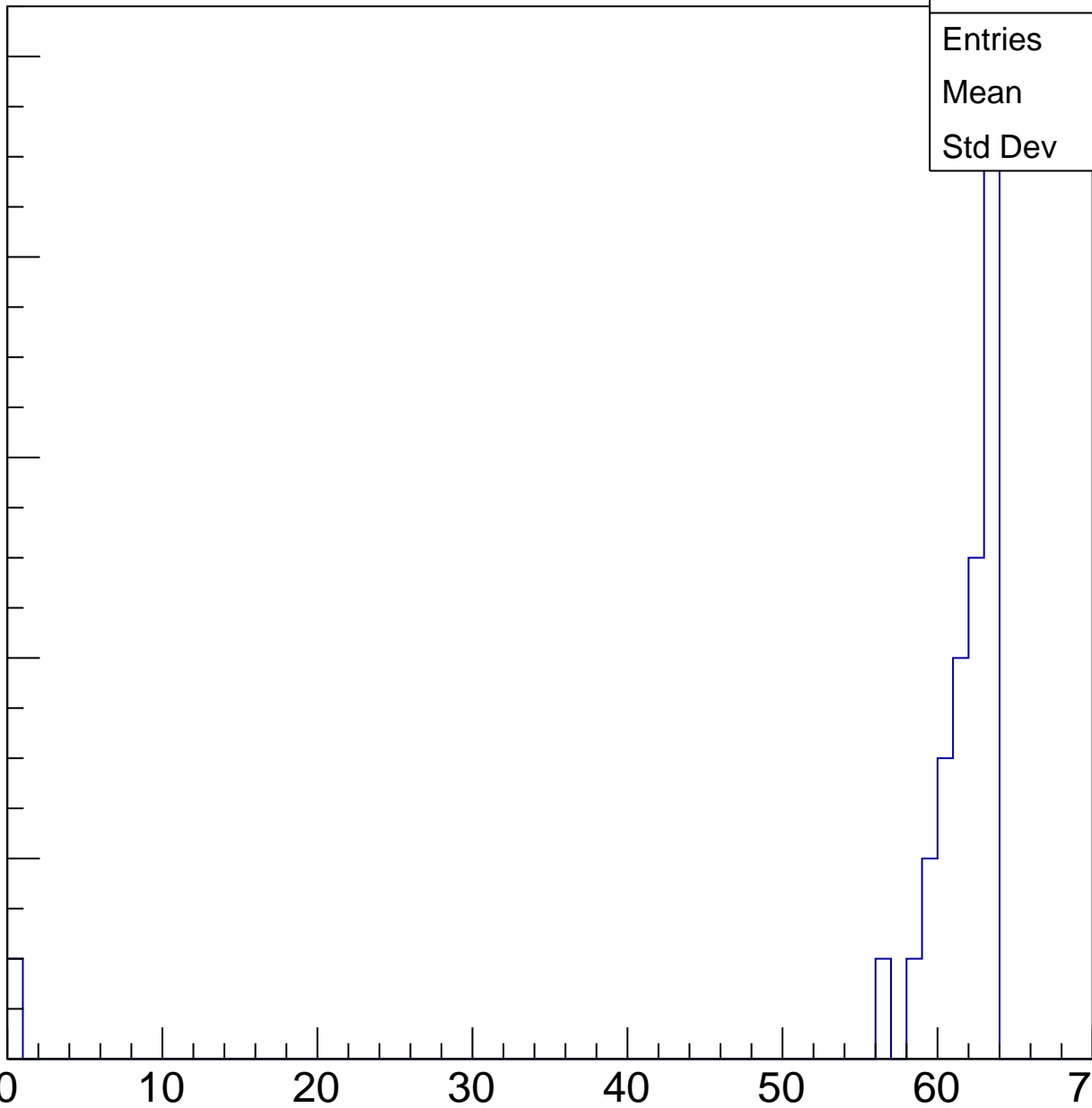
30

40

50

60

ampl



B1L100S, U13-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch68, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	29.56
Std Dev	3.742

Gaus mean : 30.2031

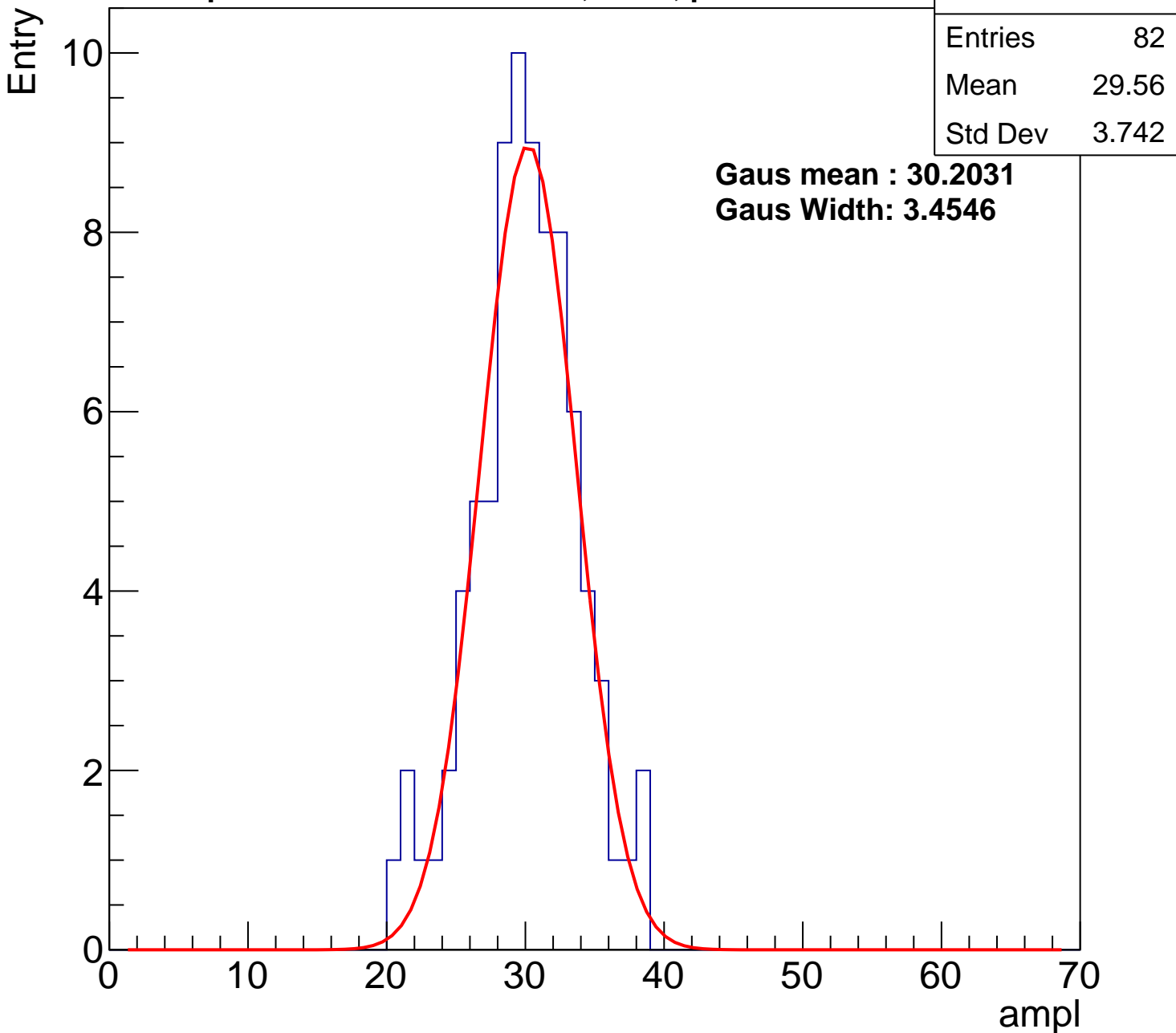
Gaus Width: 3.4546

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch68, adc1

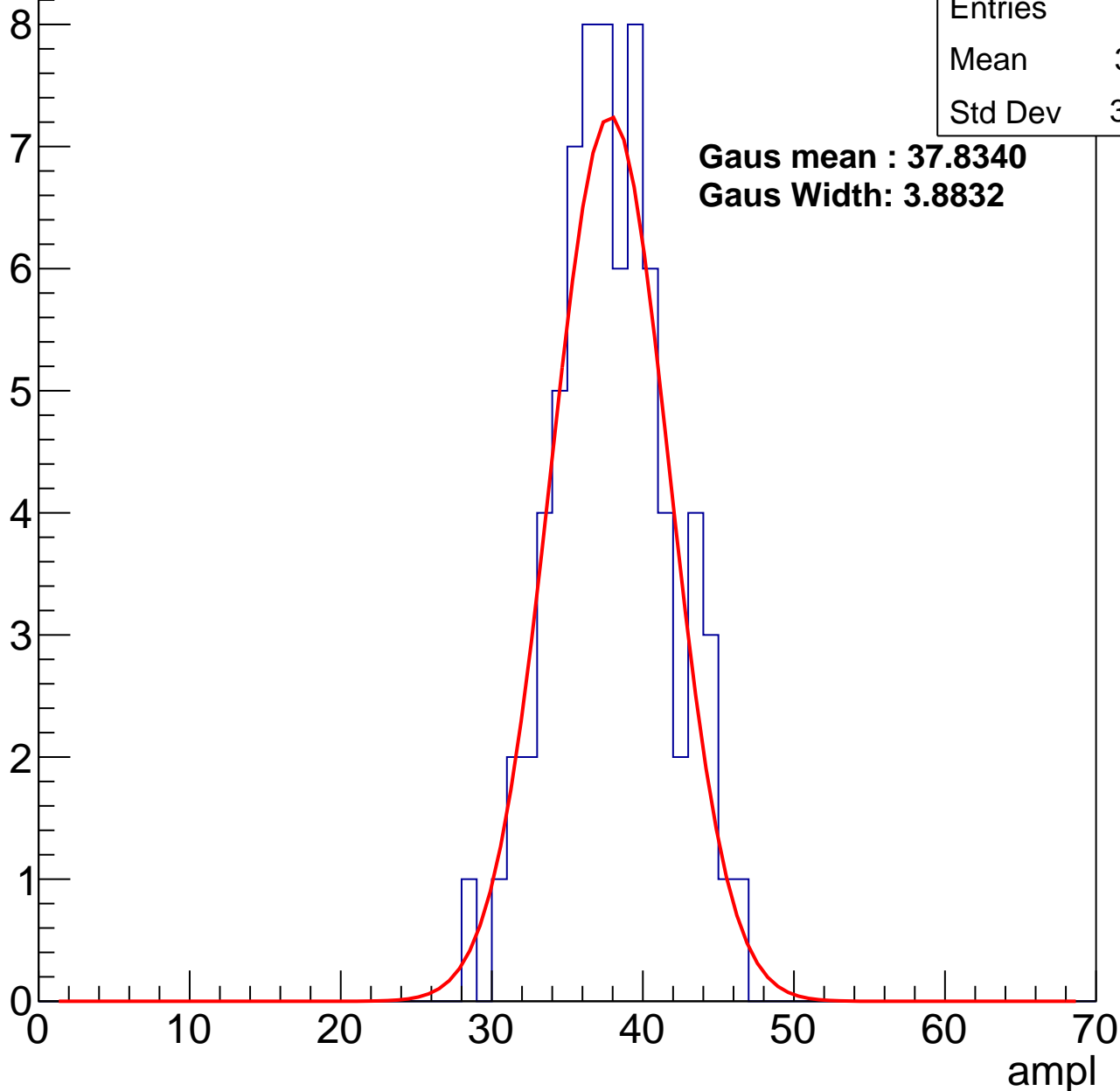
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.51
Std Dev	3.738

Gaus mean : 37.8340

Gaus Width: 3.8832



B1L100S, U13-ch68, adc2

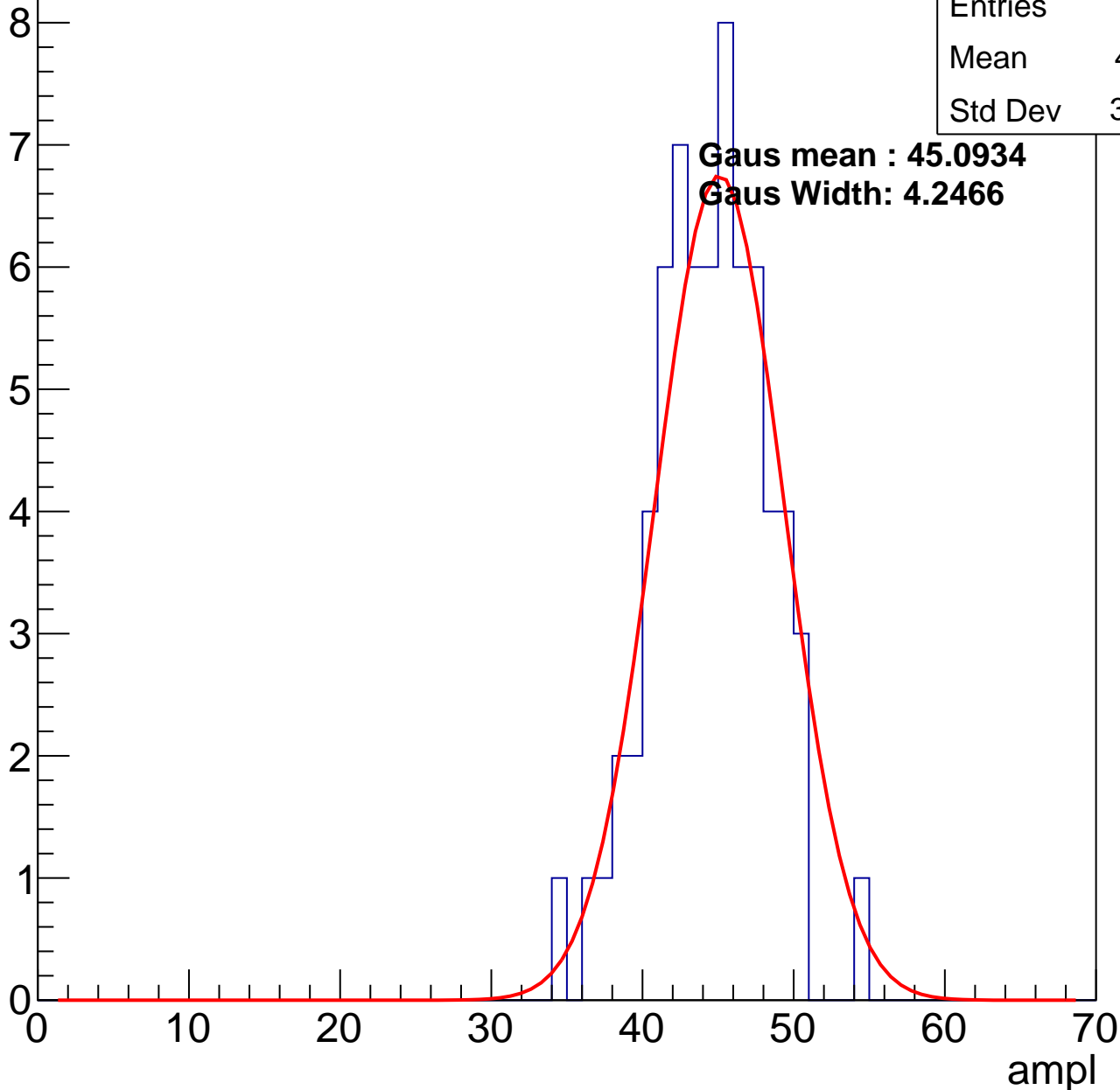
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.01
Std Dev	3.728

Gaus mean : 45.0934

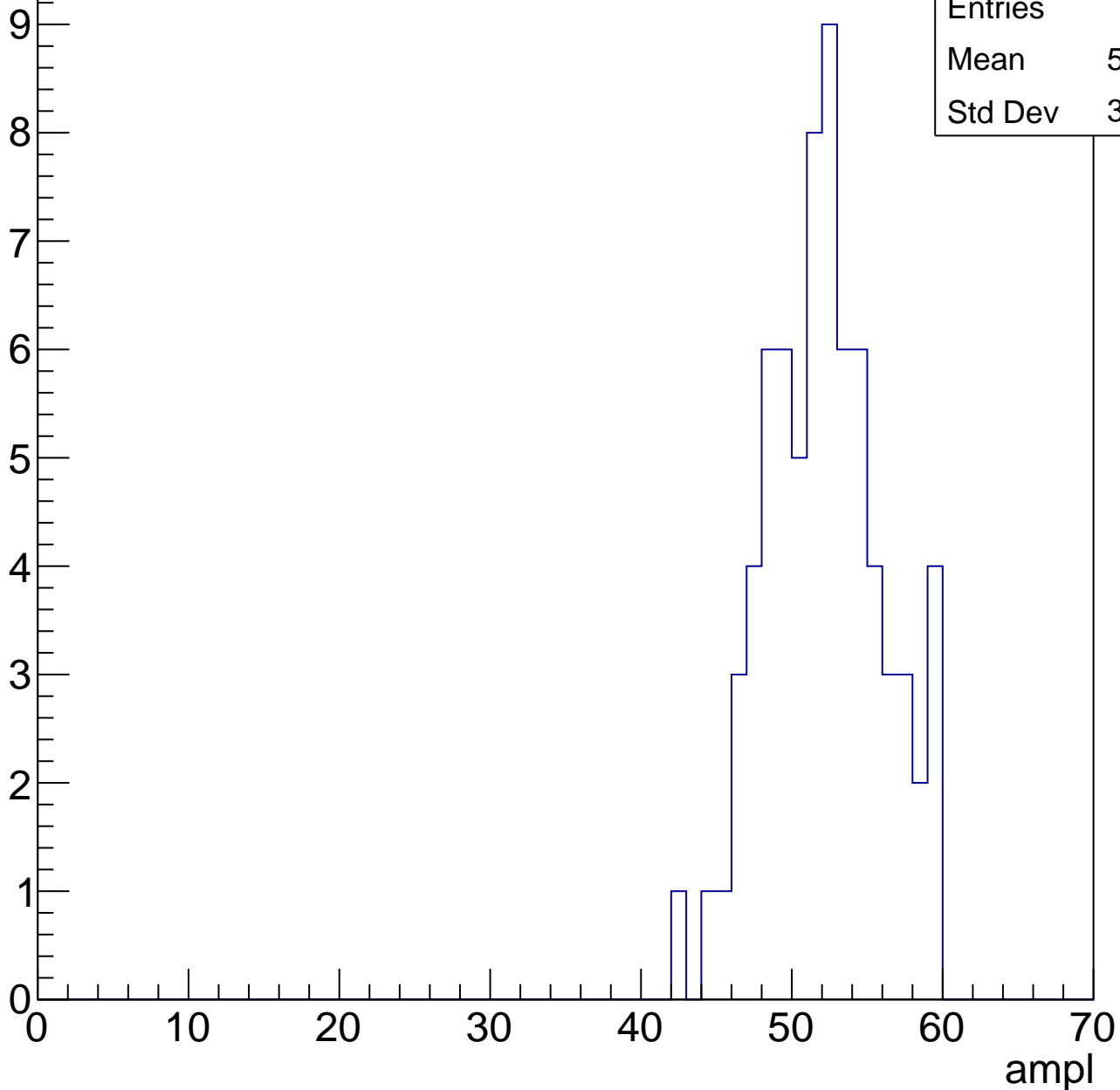
Gaus Width: 4.2466



B1L100S, U13-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

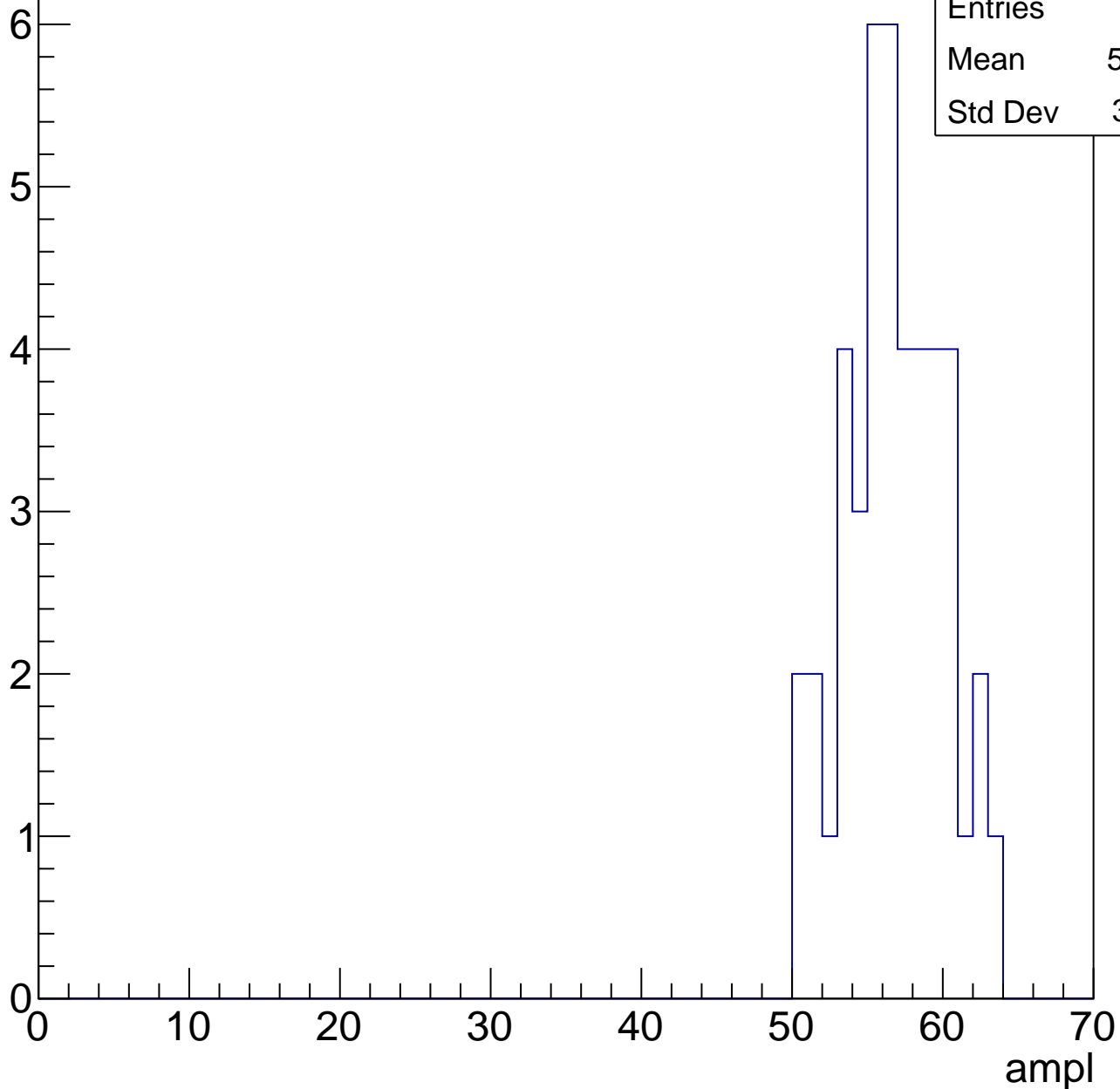


B1L100S, U13-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	56.32
Std Dev	3.211



B1L100S, U13-ch68, adc5

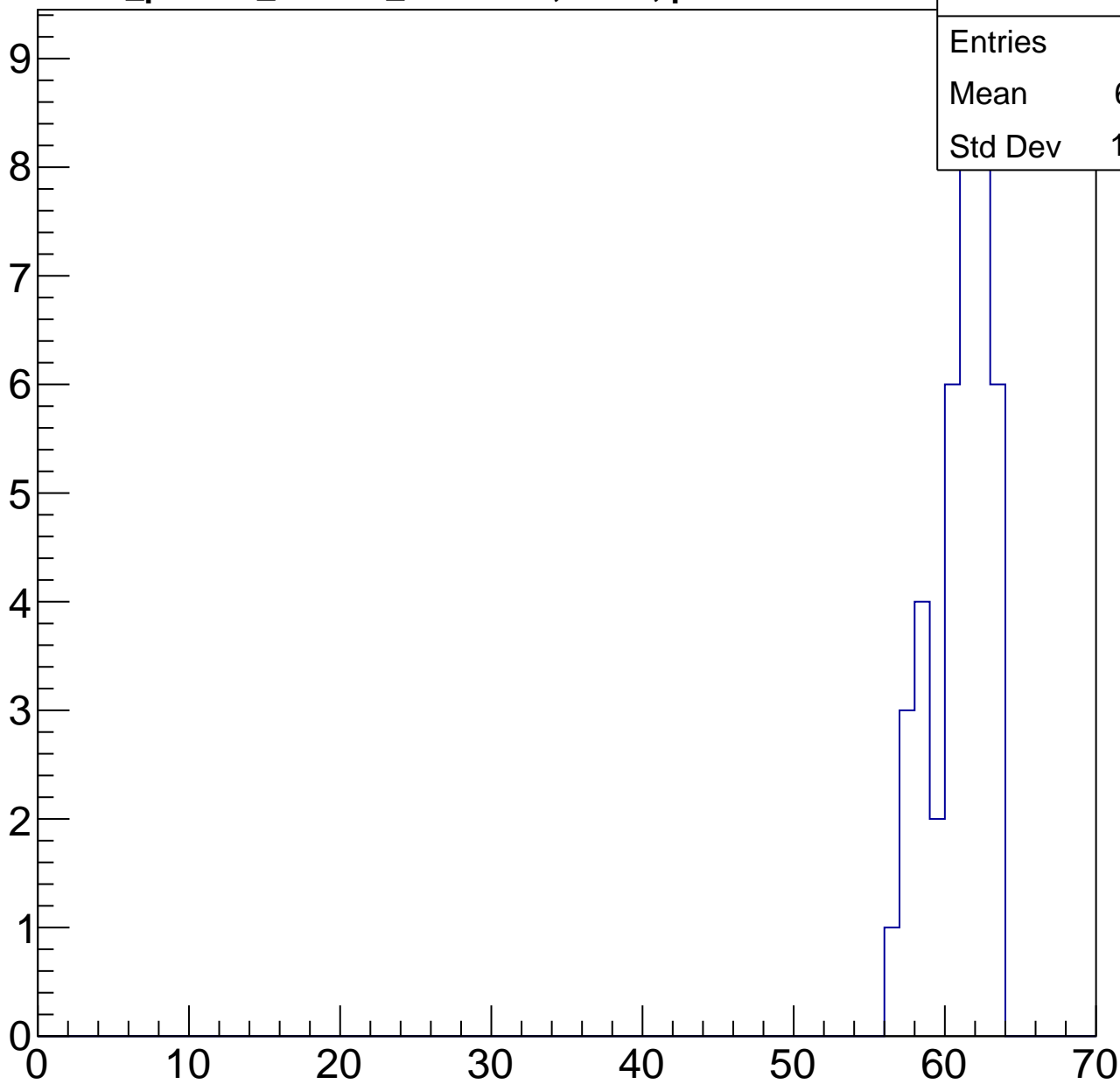
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.51
Std Dev	1.933

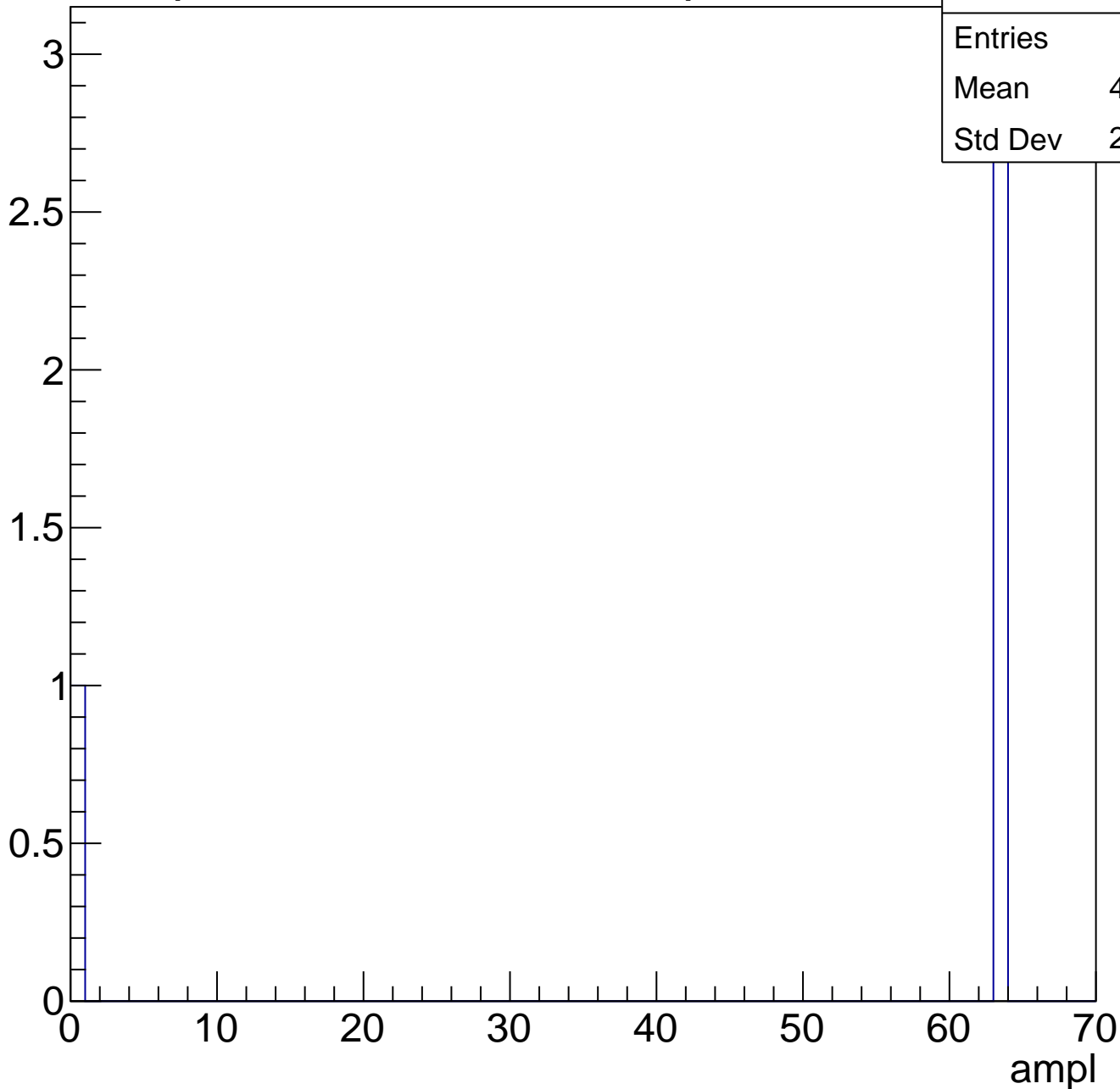
ampl



B1L100S, U13-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

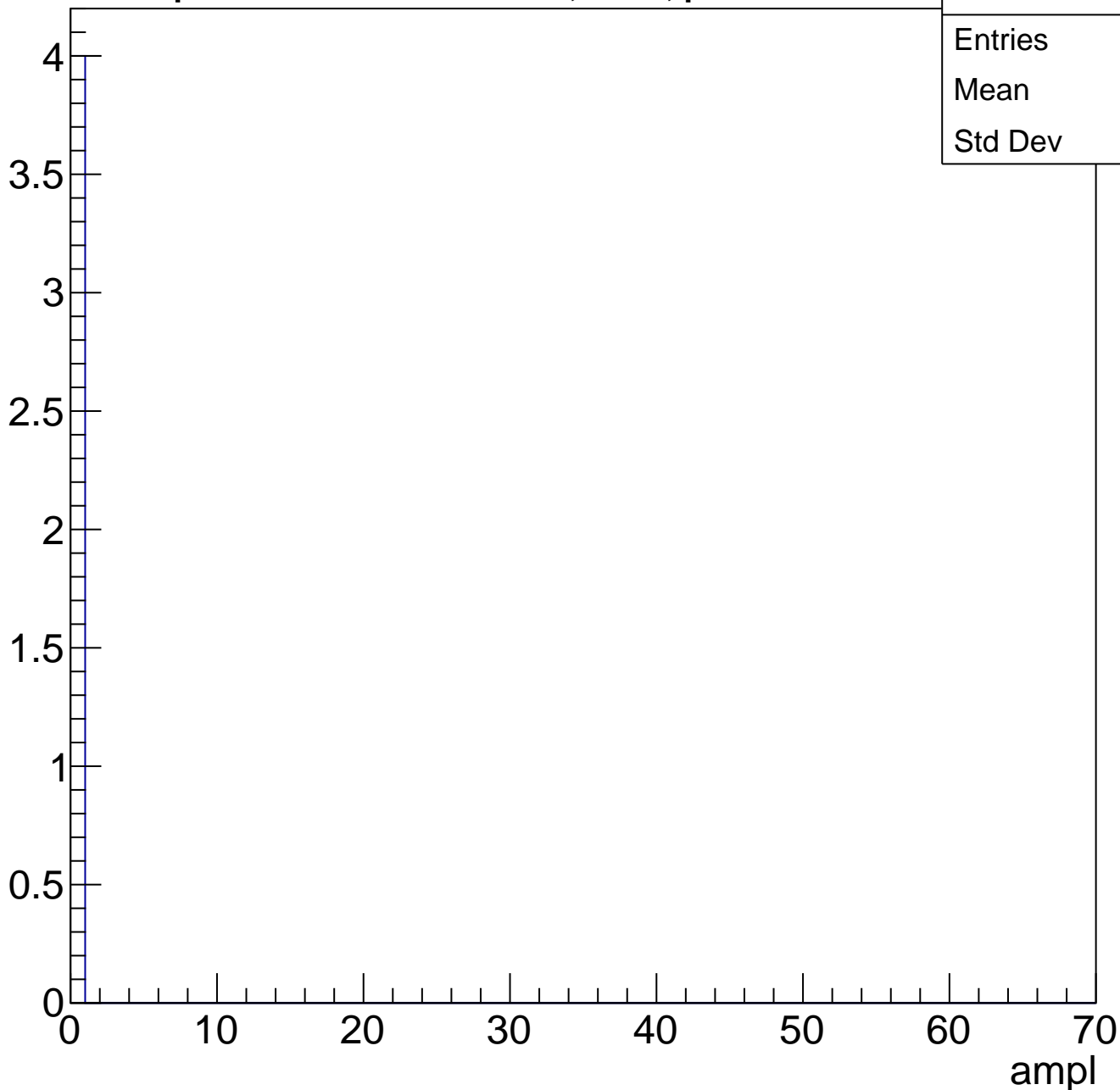
Entry



B1L100S, U13-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L100S, U13-ch69, adc0

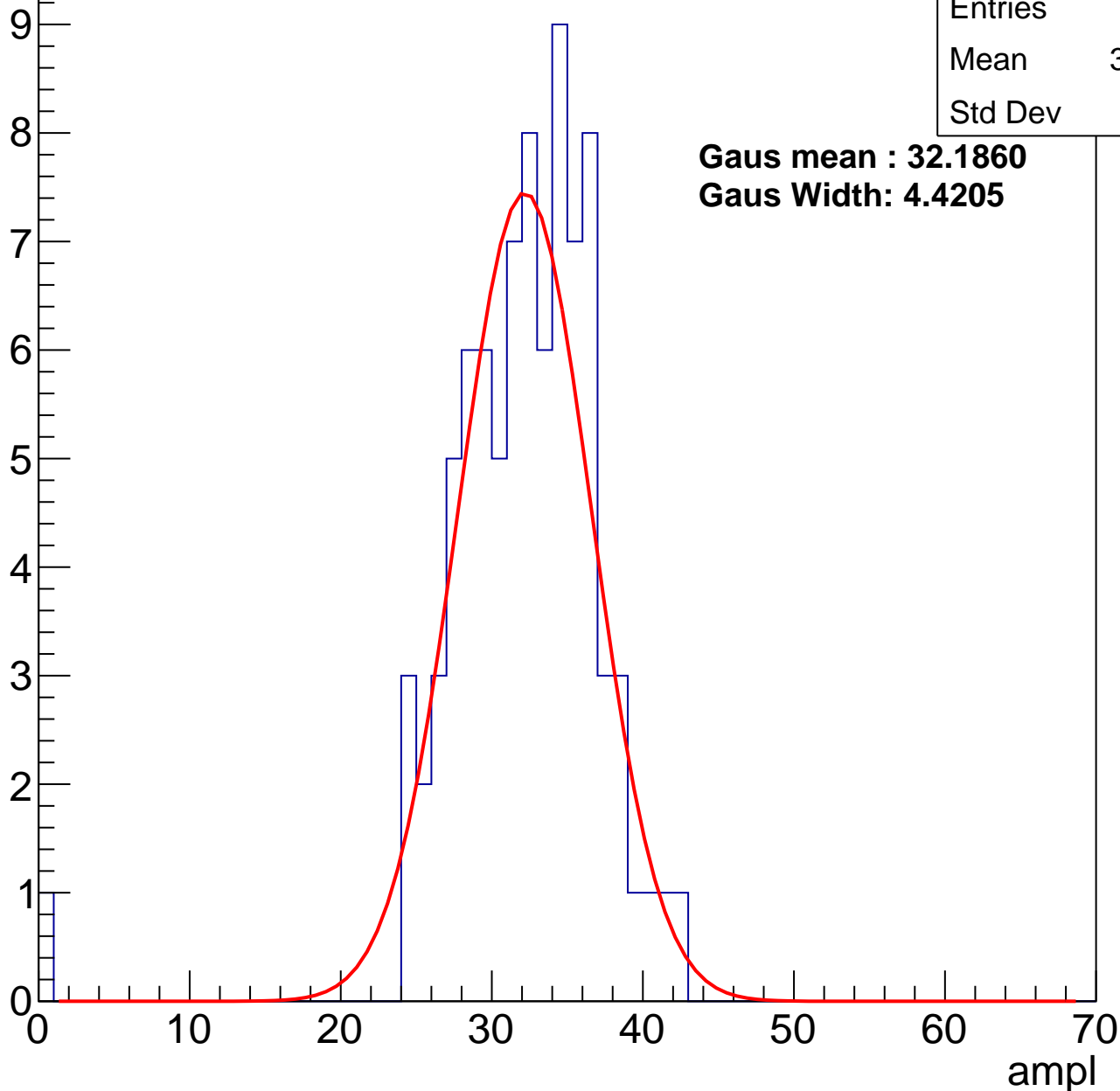
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	31.67
Std Dev	5.31

Gaus mean : 32.1860

Gaus Width: 4.4205



B1L100S, U13-ch69, adc1

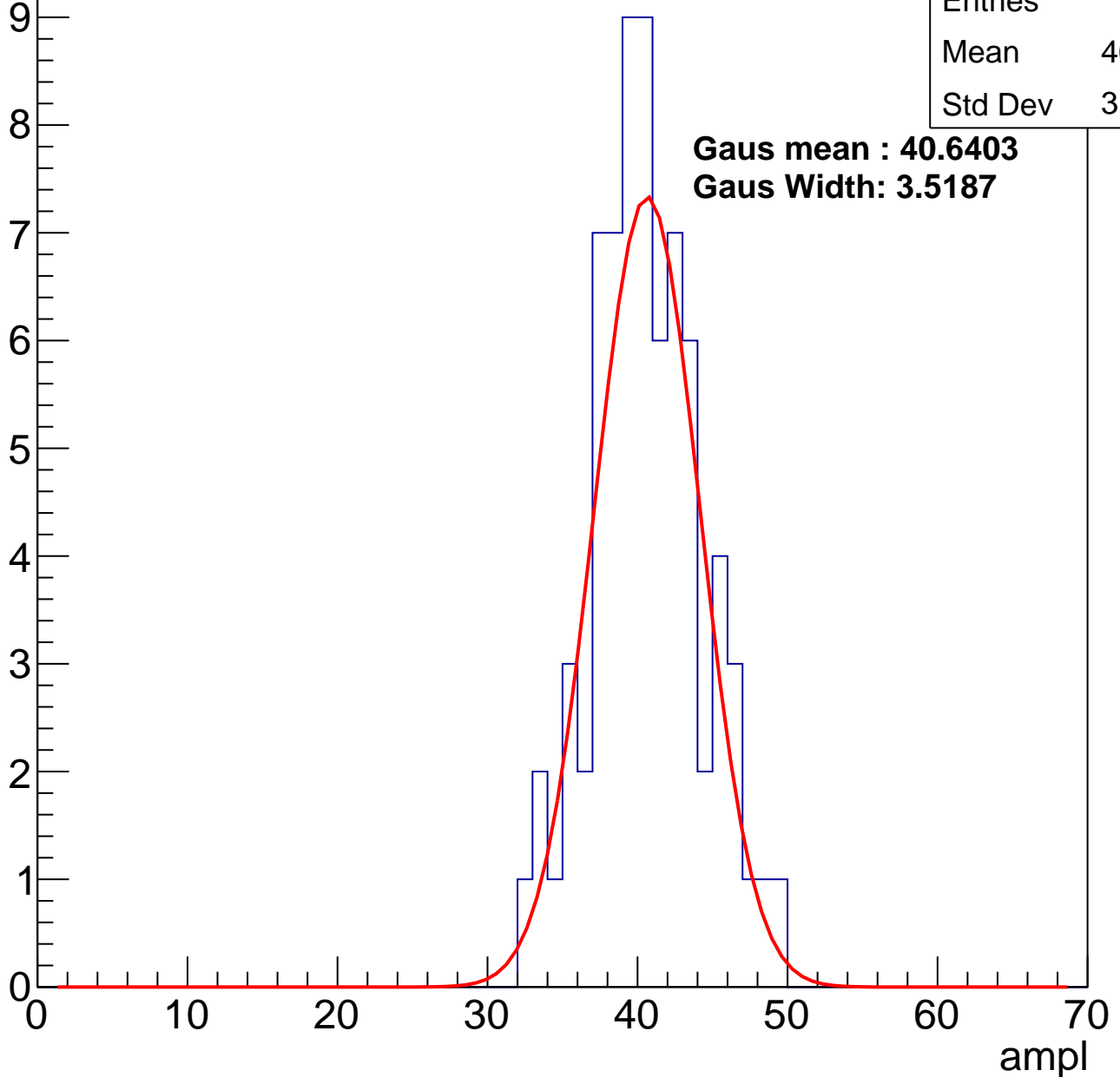
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	40.18
Std Dev	3.584

Gaus mean : 40.6403

Gaus Width: 3.5187



B1L100S, U13-ch69, adc2

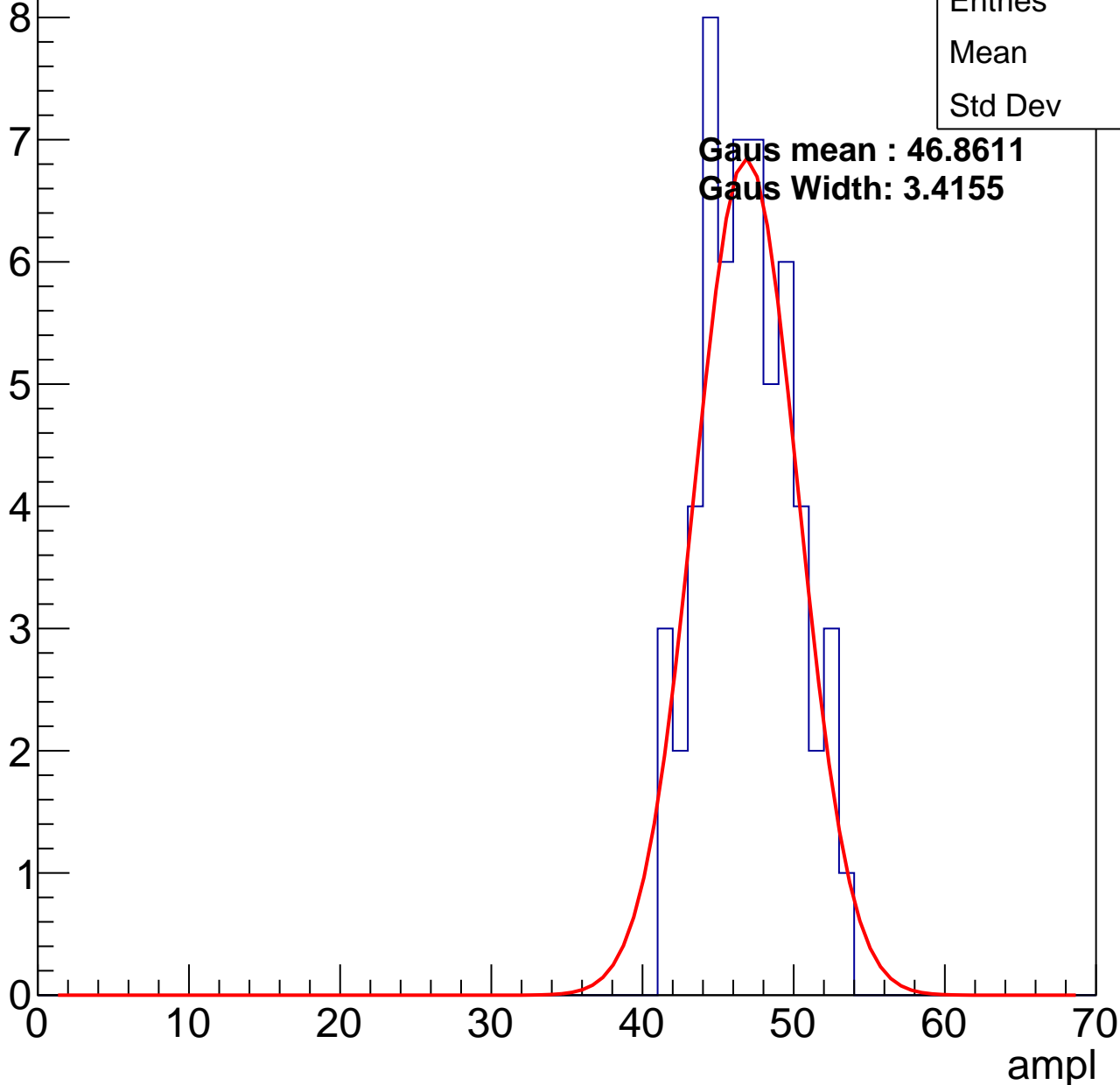
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	46.5
Std Dev	2.99

Gaus mean : 46.8611

Gaus Width: 3.4155

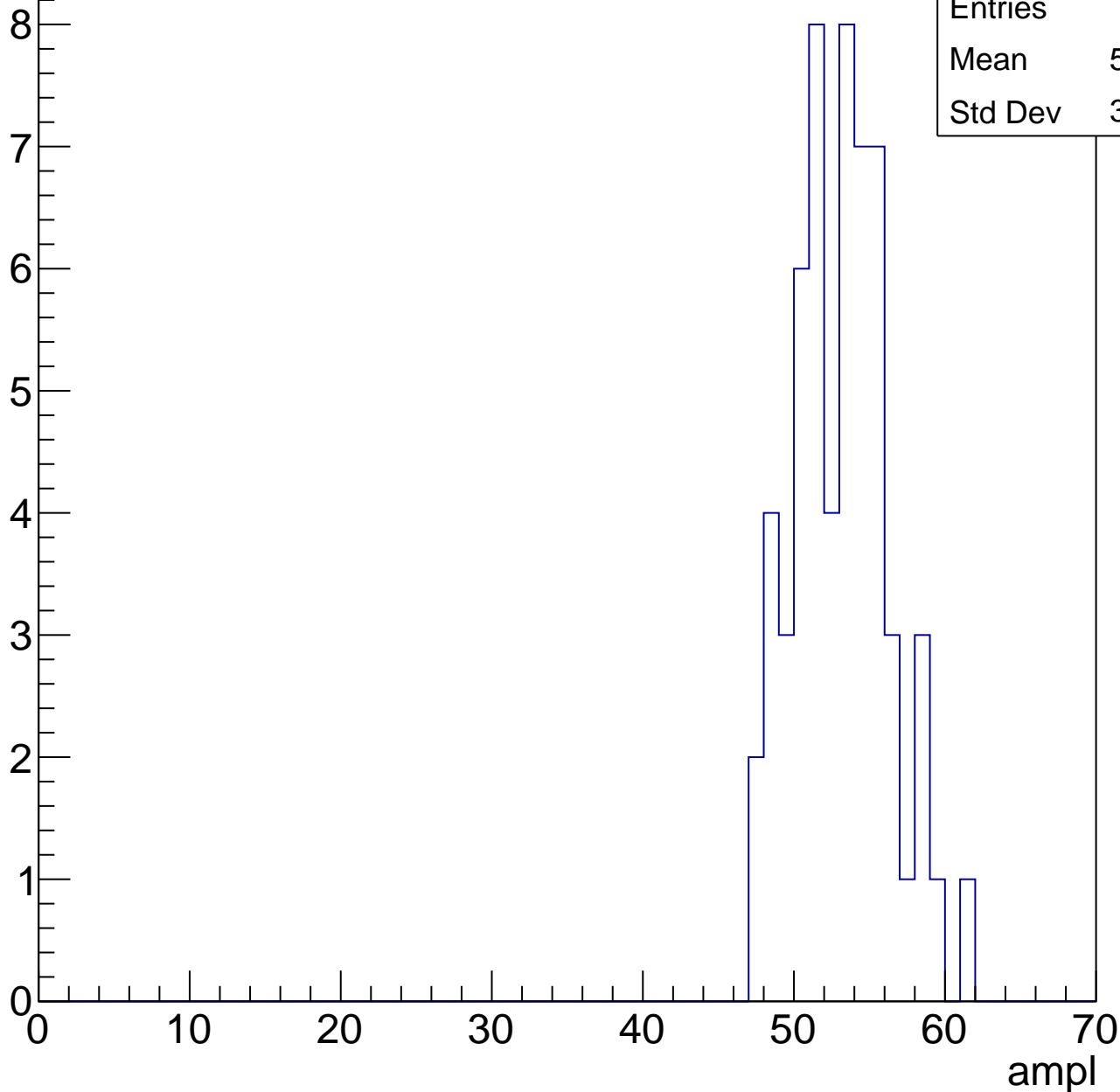


B1L100S, U13-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

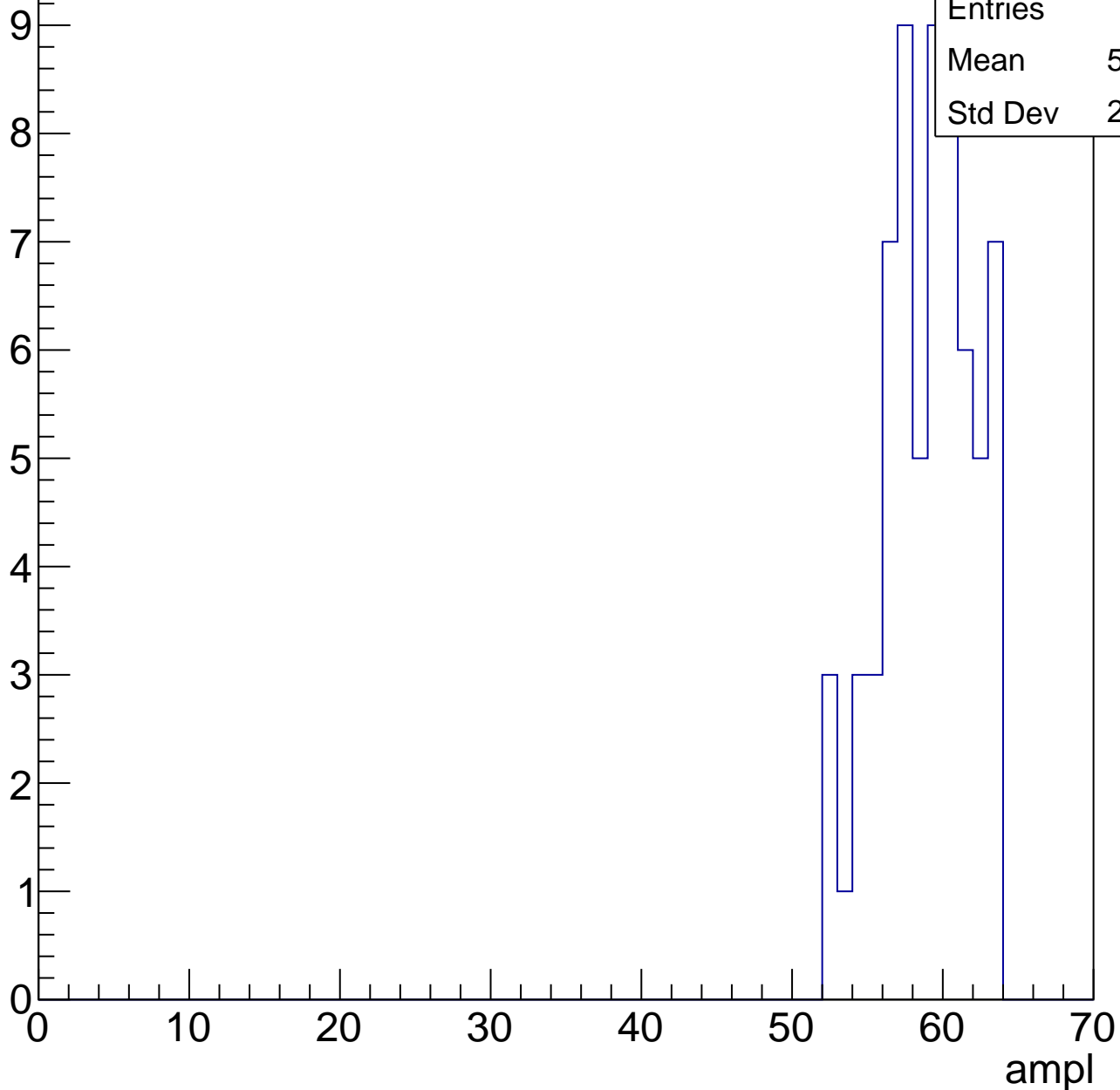
Entries	58
Mean	52.67
Std Dev	3.098



B1L100S, U13-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

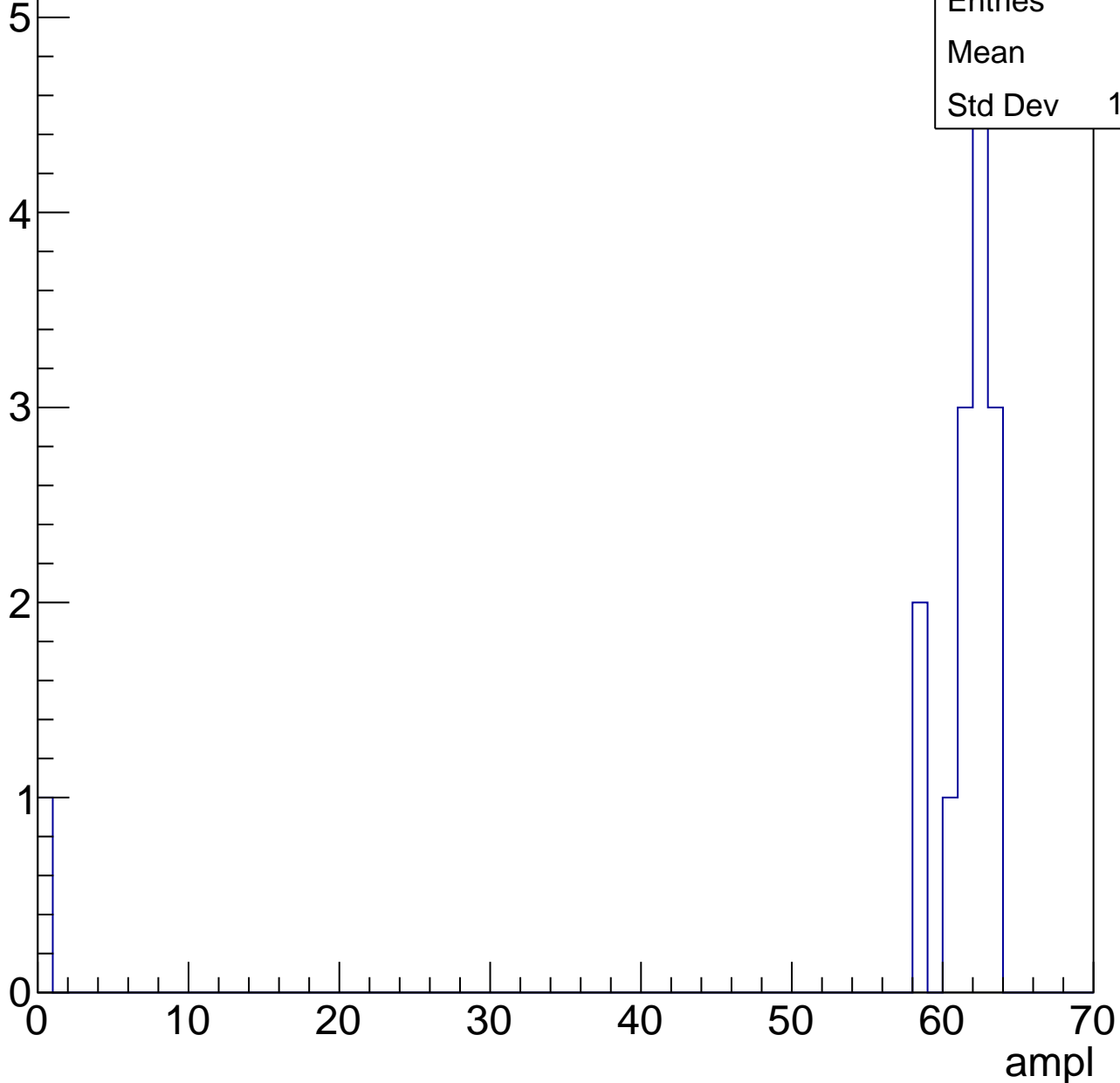


B1L100S, U13-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	57.2
Std Dev	15.36



B1L100S, U13-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch70, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	30.21
Std Dev	3.436

Gaus mean : 31.0980

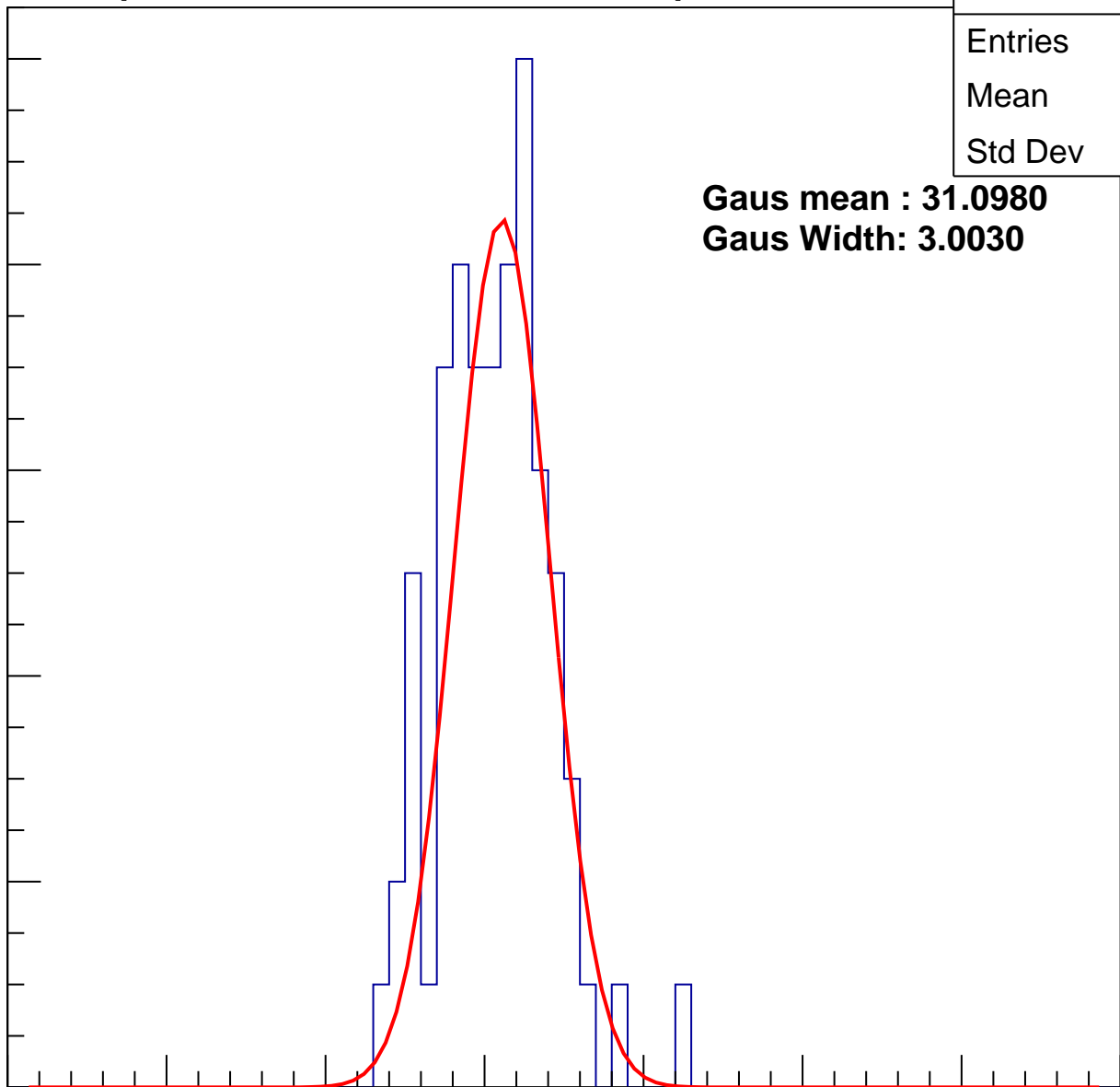
Gaus Width: 3.0030

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	36.7
Std Dev	3.252

Gaus mean : 37.1601

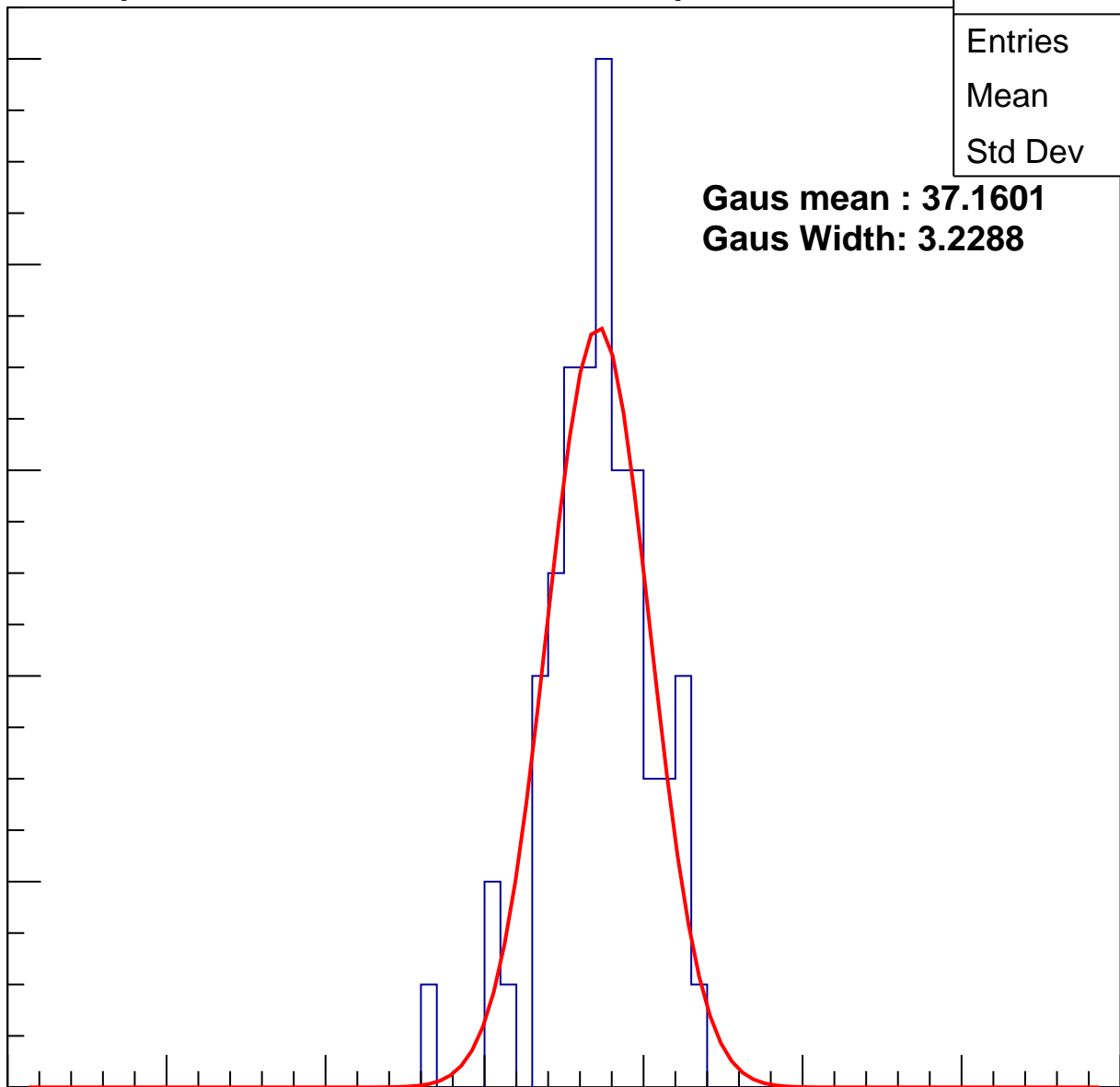
Gaus Width: 3.2288

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

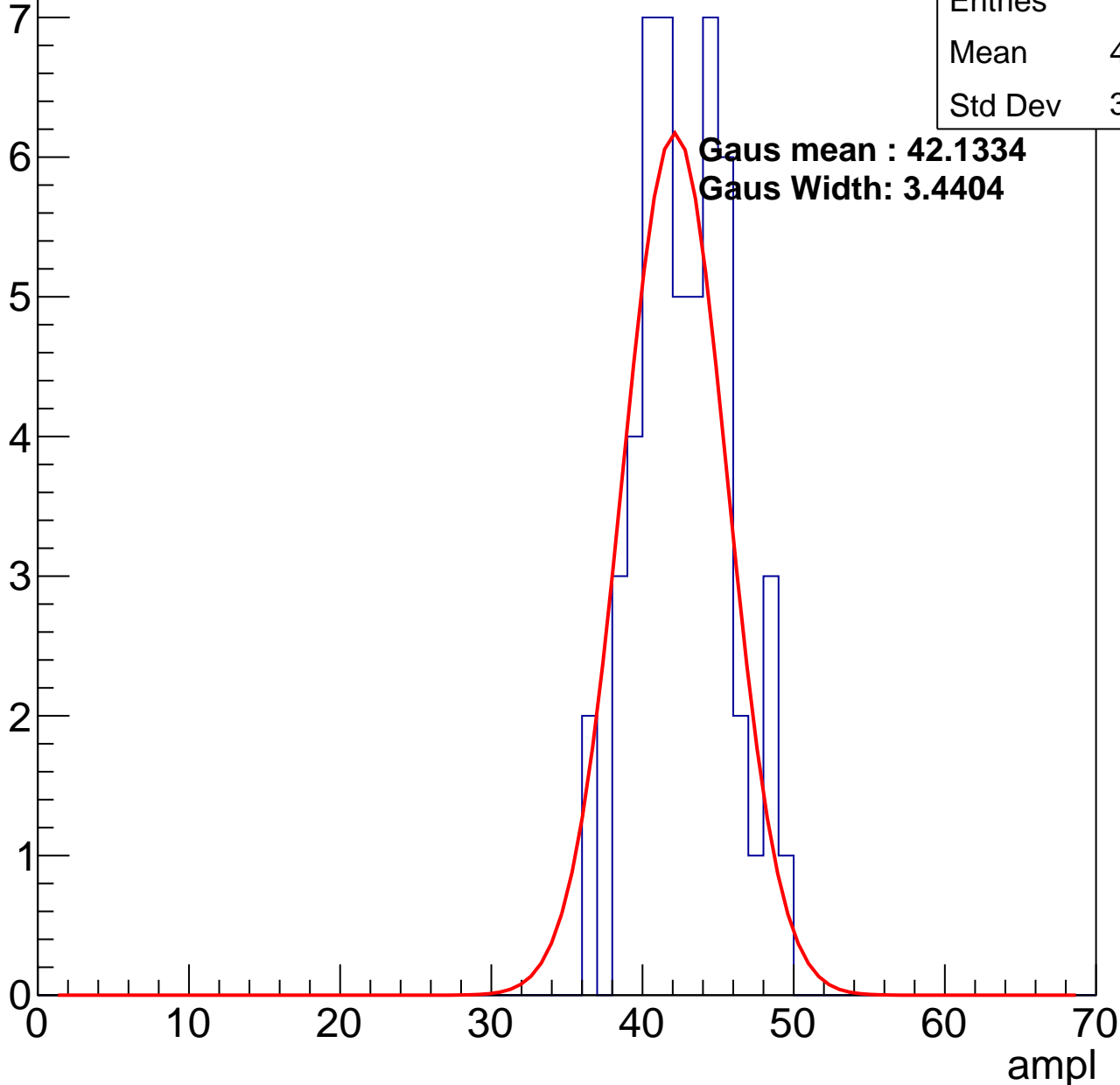


B1L100S, U13-ch70, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	42.34
Std Dev	3.028

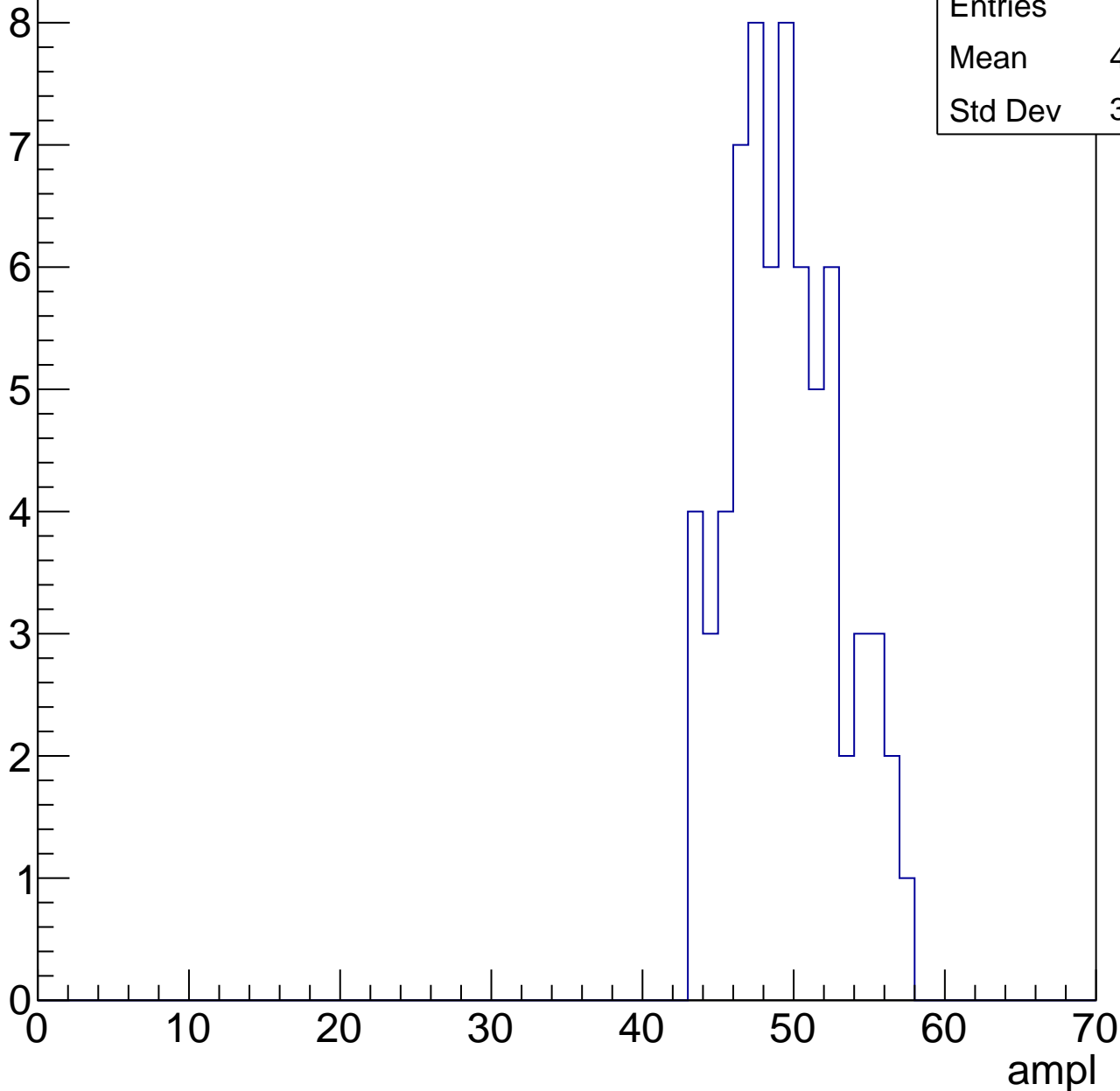


B1L100S, U13-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	48.99
Std Dev	3.517

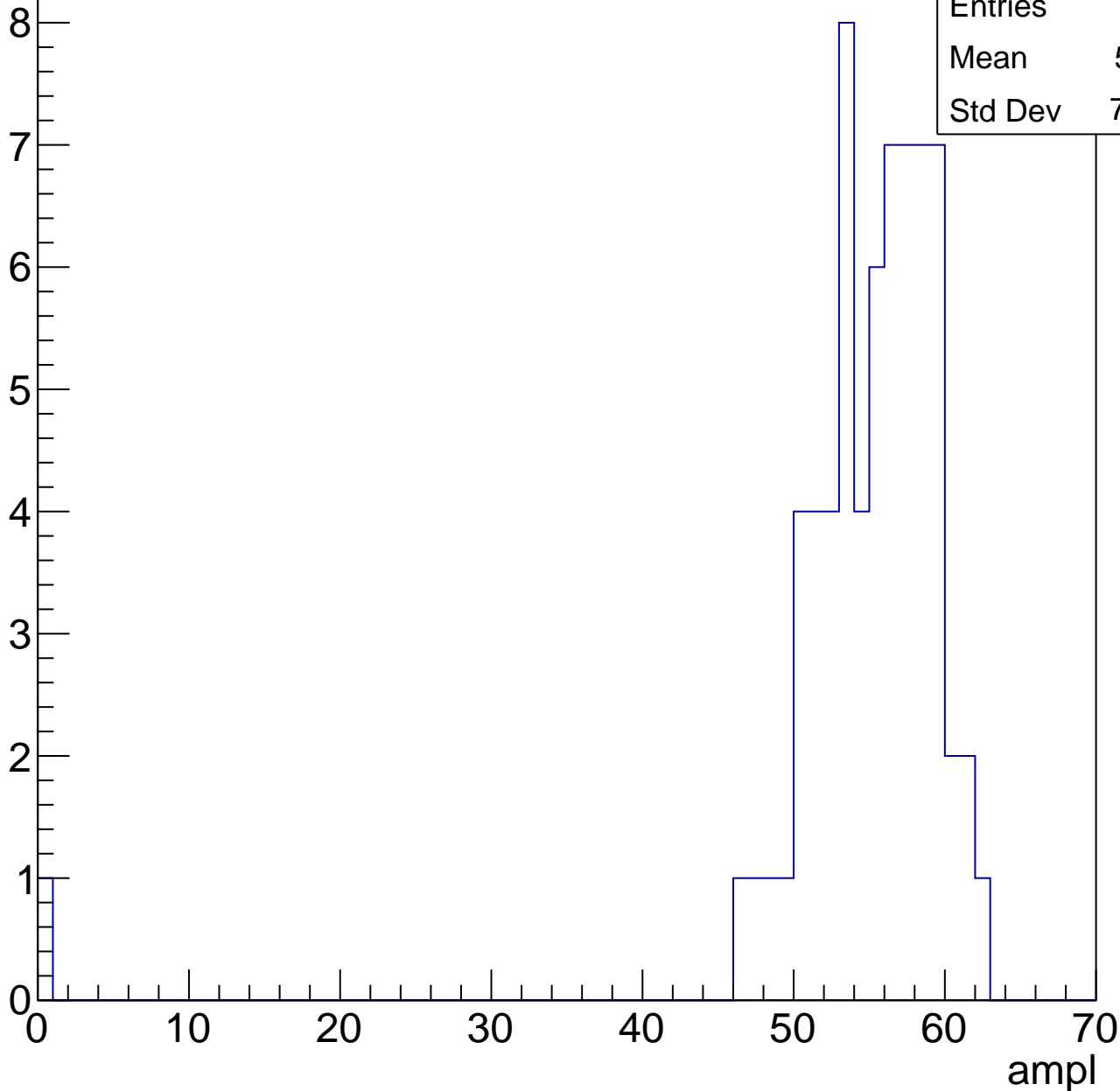


B1L100S, U13-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	54.21
Std Dev	7.504



B1L100S, U13-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.66
Std Dev	2.893

ampl

0

1

2

3

4

5

6

7

8

10

20

30

40

50

60

70

B1L100S, U13-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch71, adc0

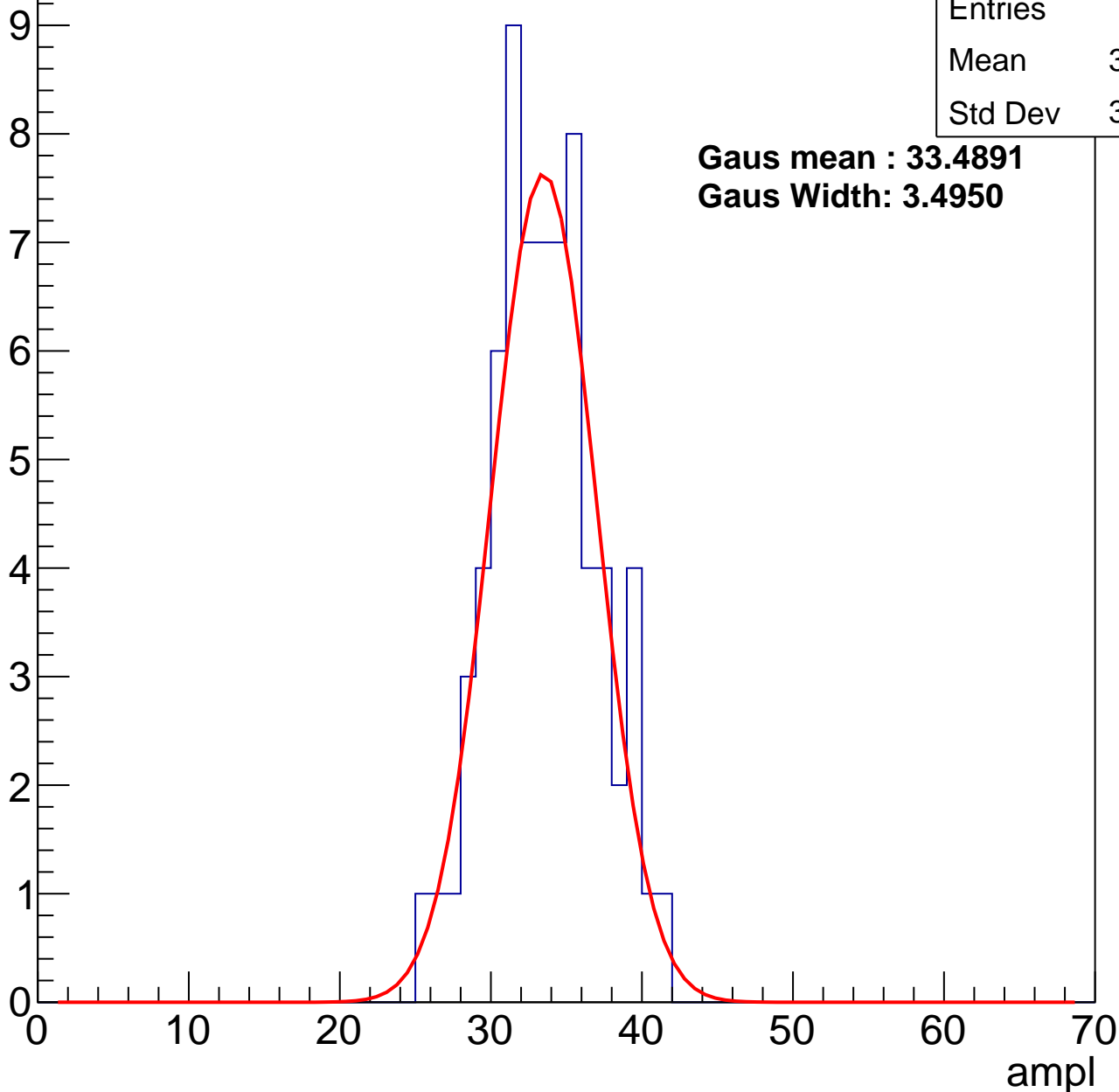
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	33.07
Std Dev	3.445

Gaus mean : 33.4891

Gaus Width: 3.4950



B1L100S, U13-ch71, adc1

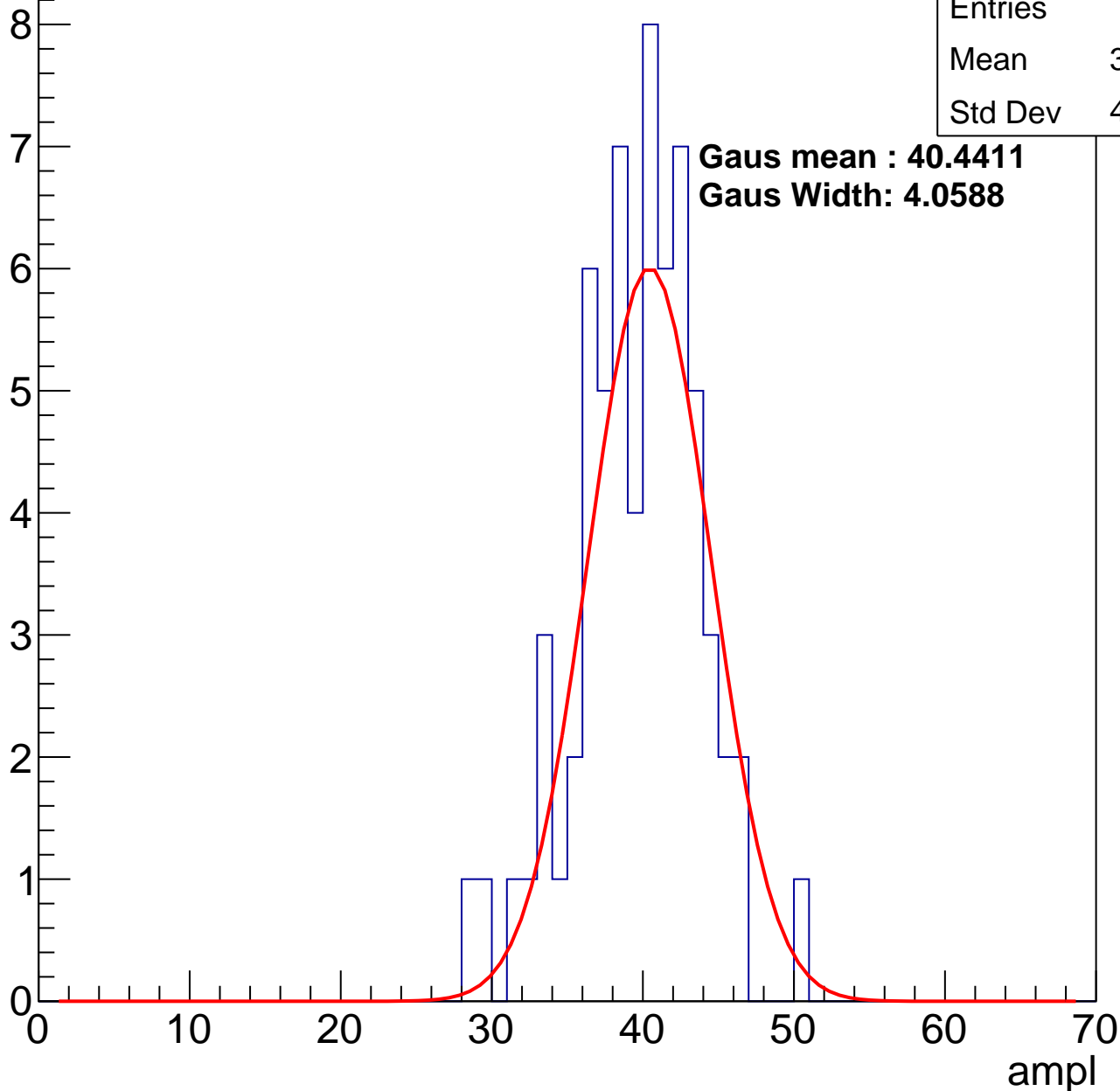
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	39.17
Std Dev	4.118

Gaus mean : 40.4411

Gaus Width: 4.0588



B1L100S, U13-ch71, adc2

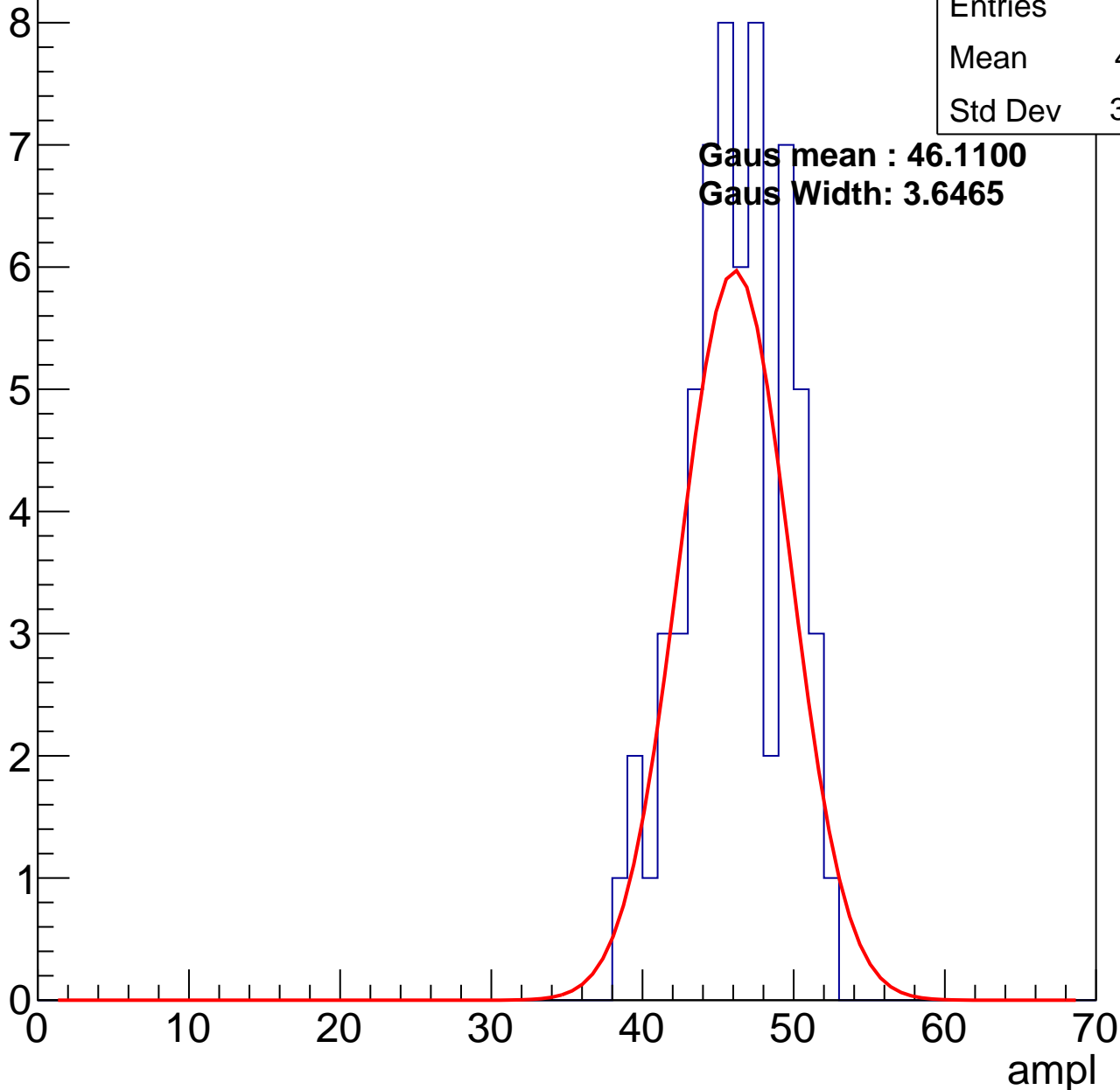
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	45.71
Std Dev	3.279

Gaus mean : 46.1100

Gaus Width: 3.6465

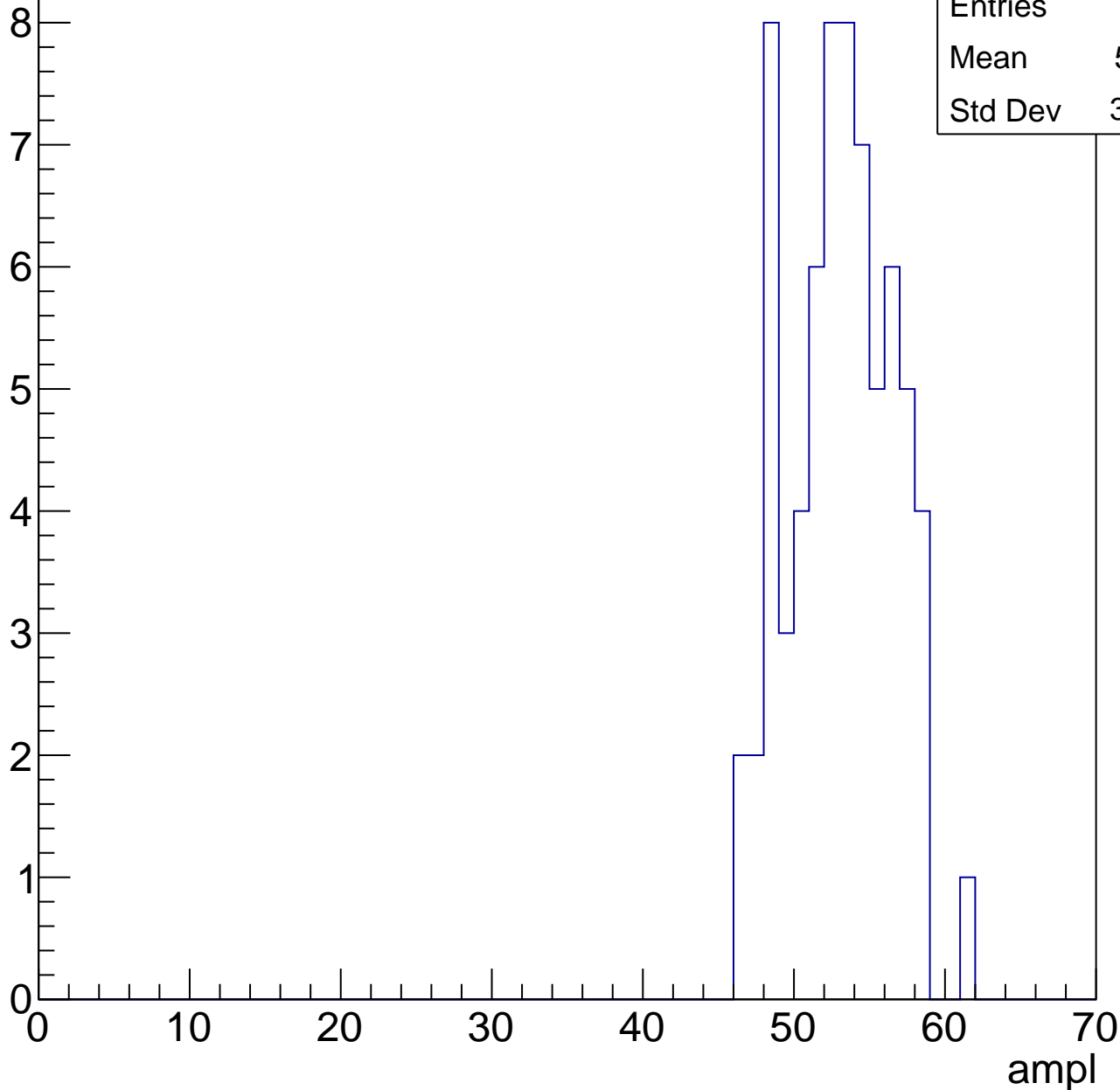


B1L100S, U13-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	52.61
Std Dev	3.406

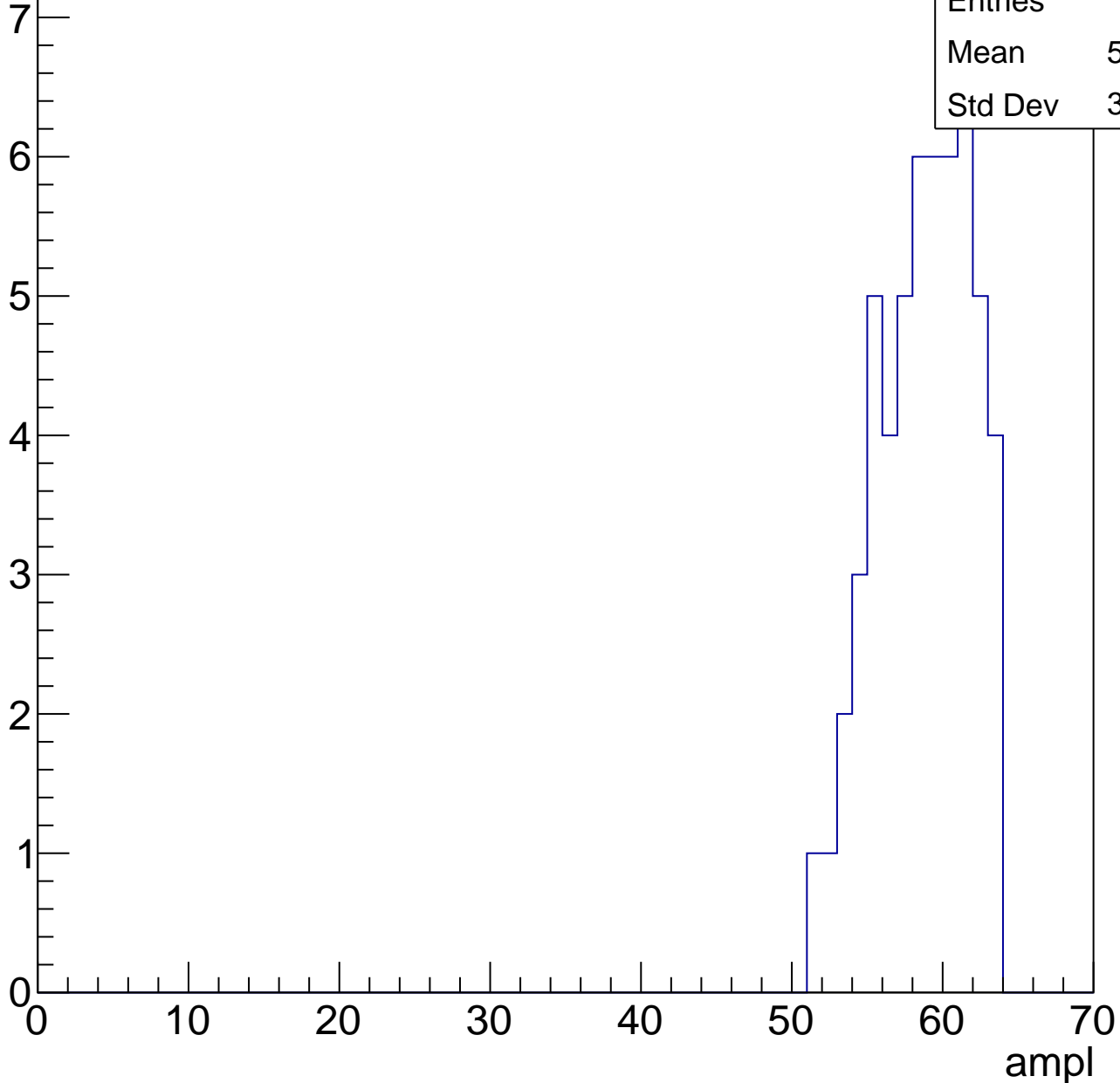


B1L100S, U13-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	58.29
Std Dev	3.067

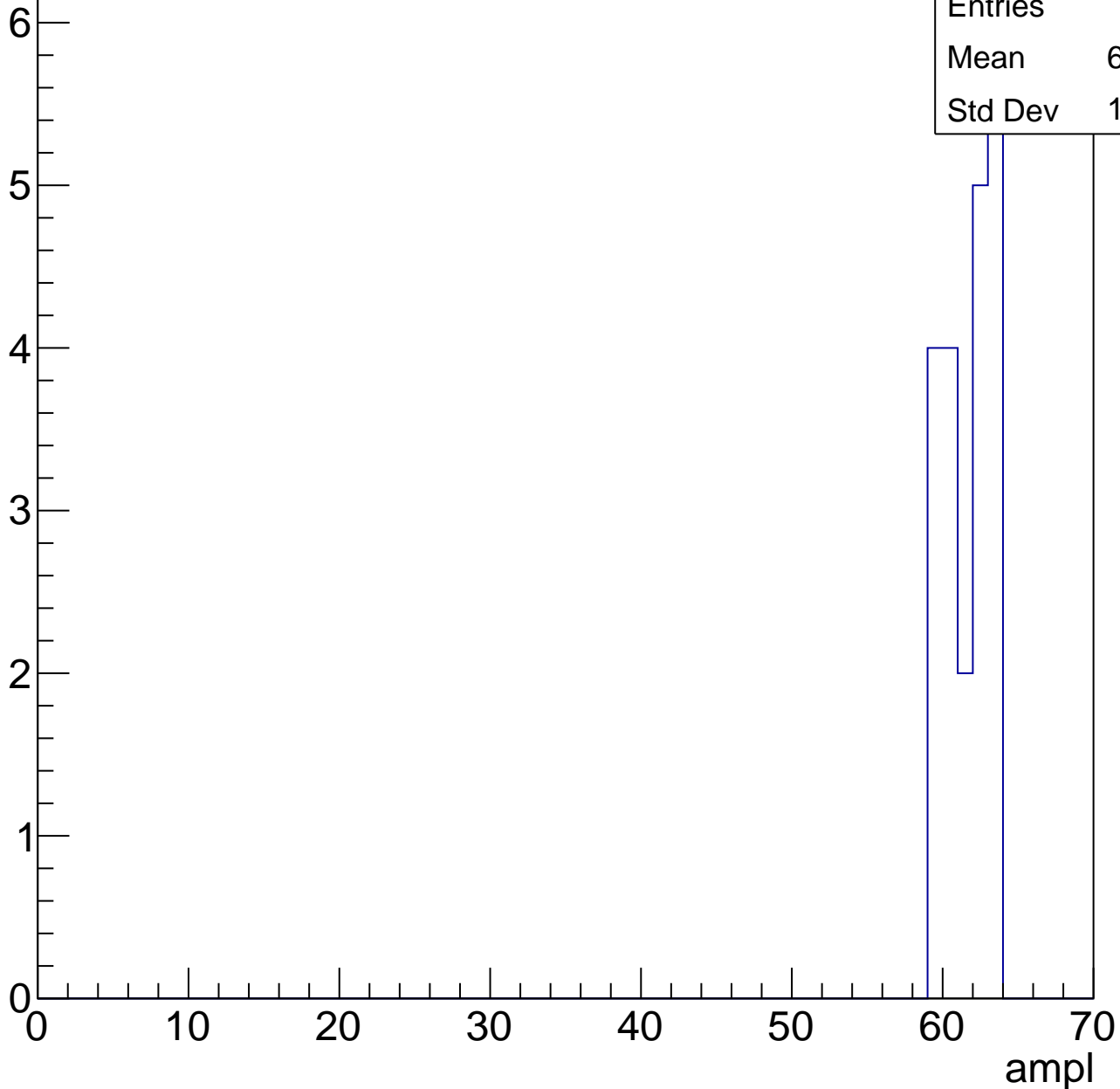


B1L100S, U13-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	61.24
Std Dev	1.509



B1L100S, U13-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L100S, U13-ch72, adc0

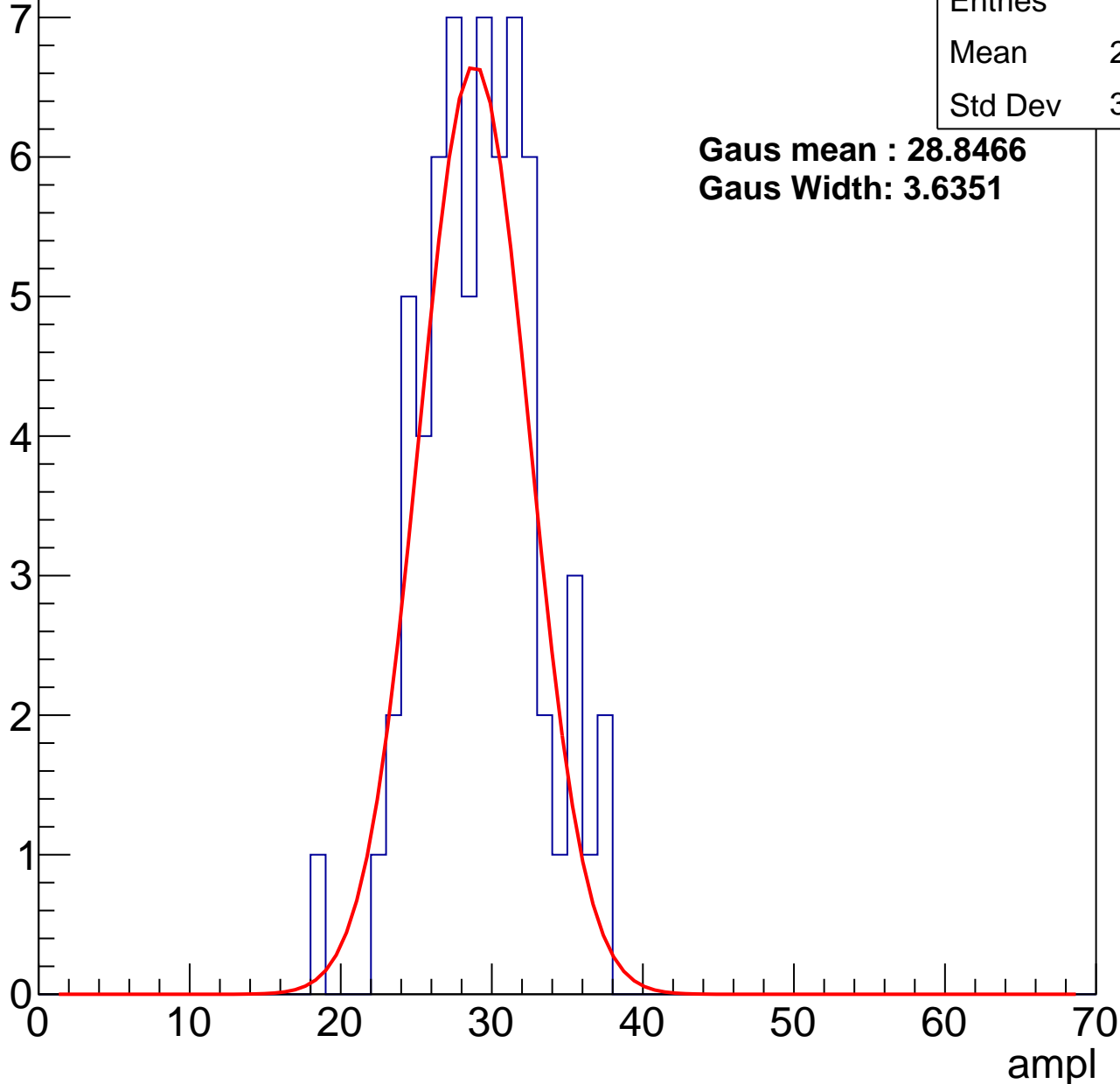
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.76
Std Dev	3.782

Gaus mean : 28.8466

Gaus Width: 3.6351



B1L100S, U13-ch72, adc1

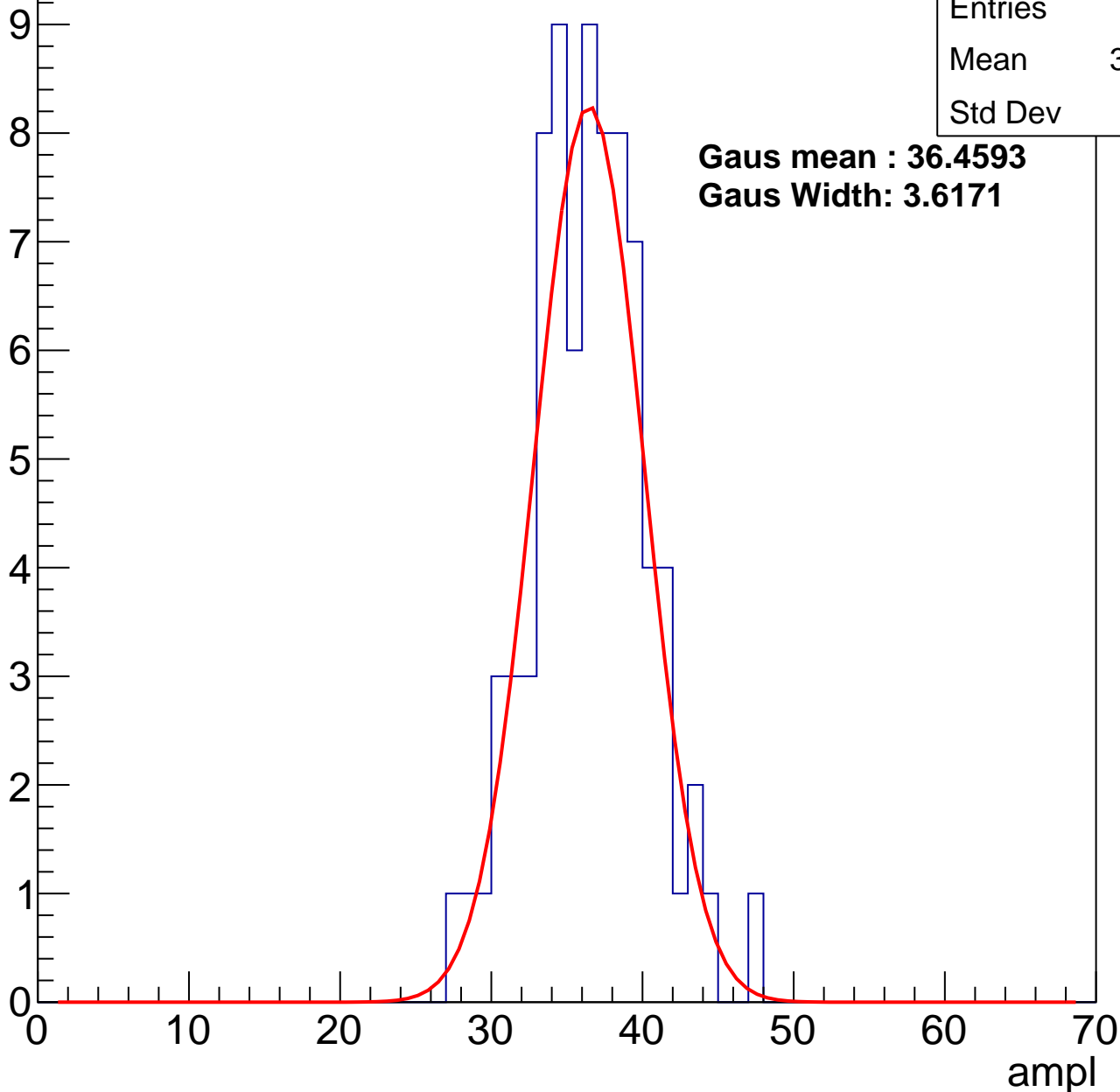
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	36.04
Std Dev	3.75

Gaus mean : 36.4593

Gaus Width: 3.6171



B1L100S, U13-ch72, adc2

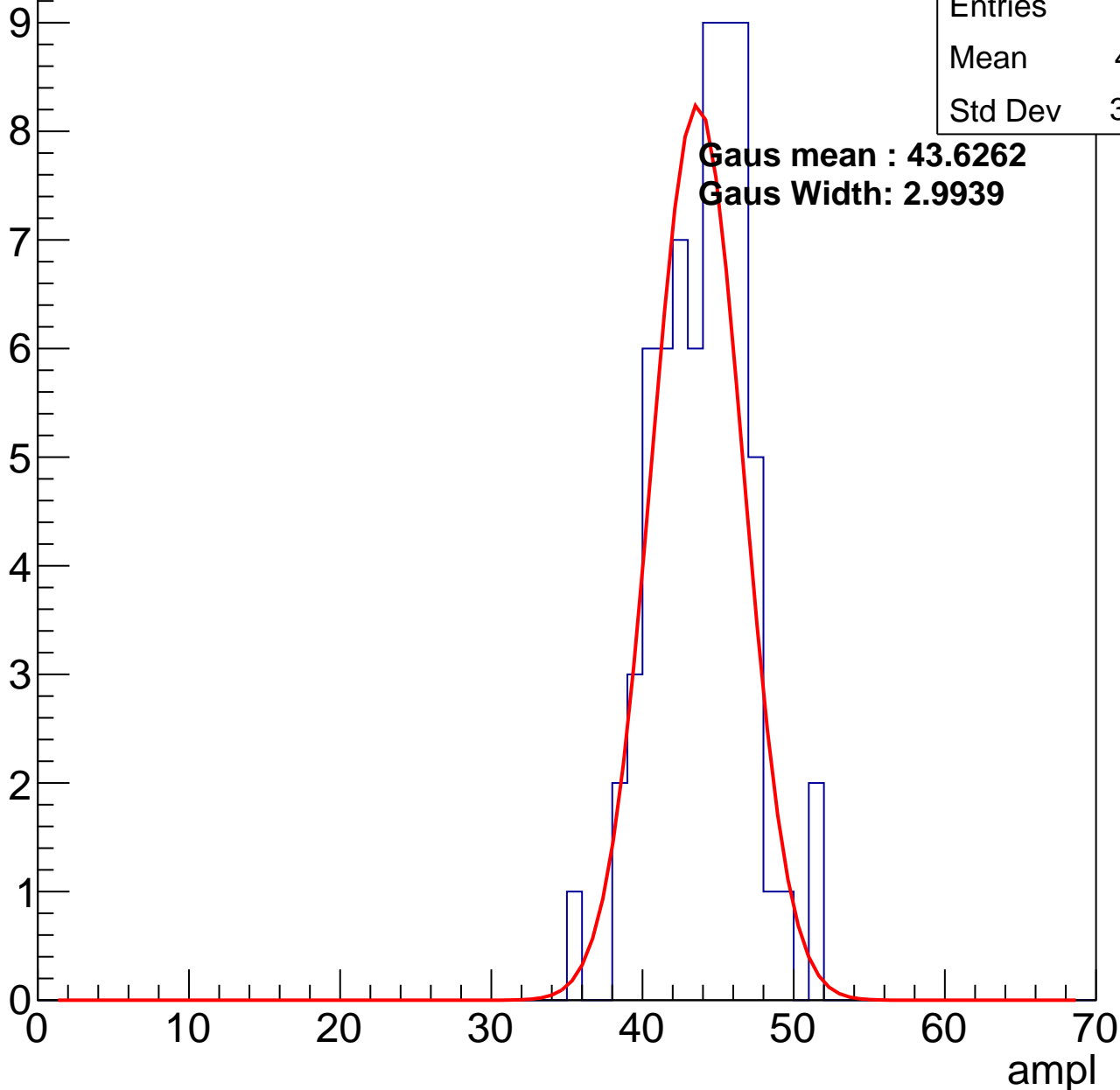
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.51
Std Dev	3.054

Gaus mean : 43.6262

Gaus Width: 2.9939



B1L100S, U13-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	50.84
Std Dev	3.716

Entry

10

8

6

4

2

0

0

10

20

30

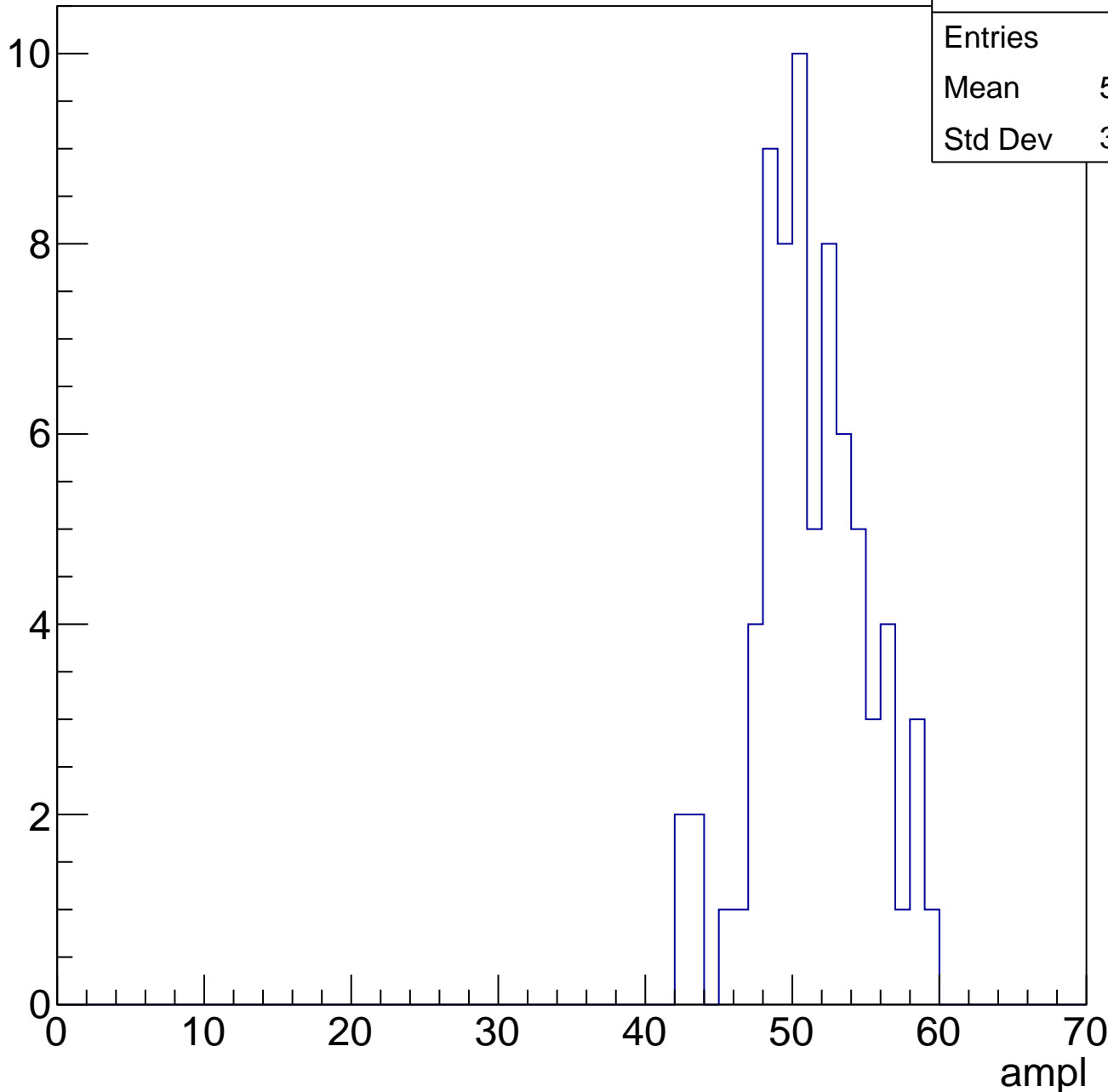
40

50

60

ampl

70

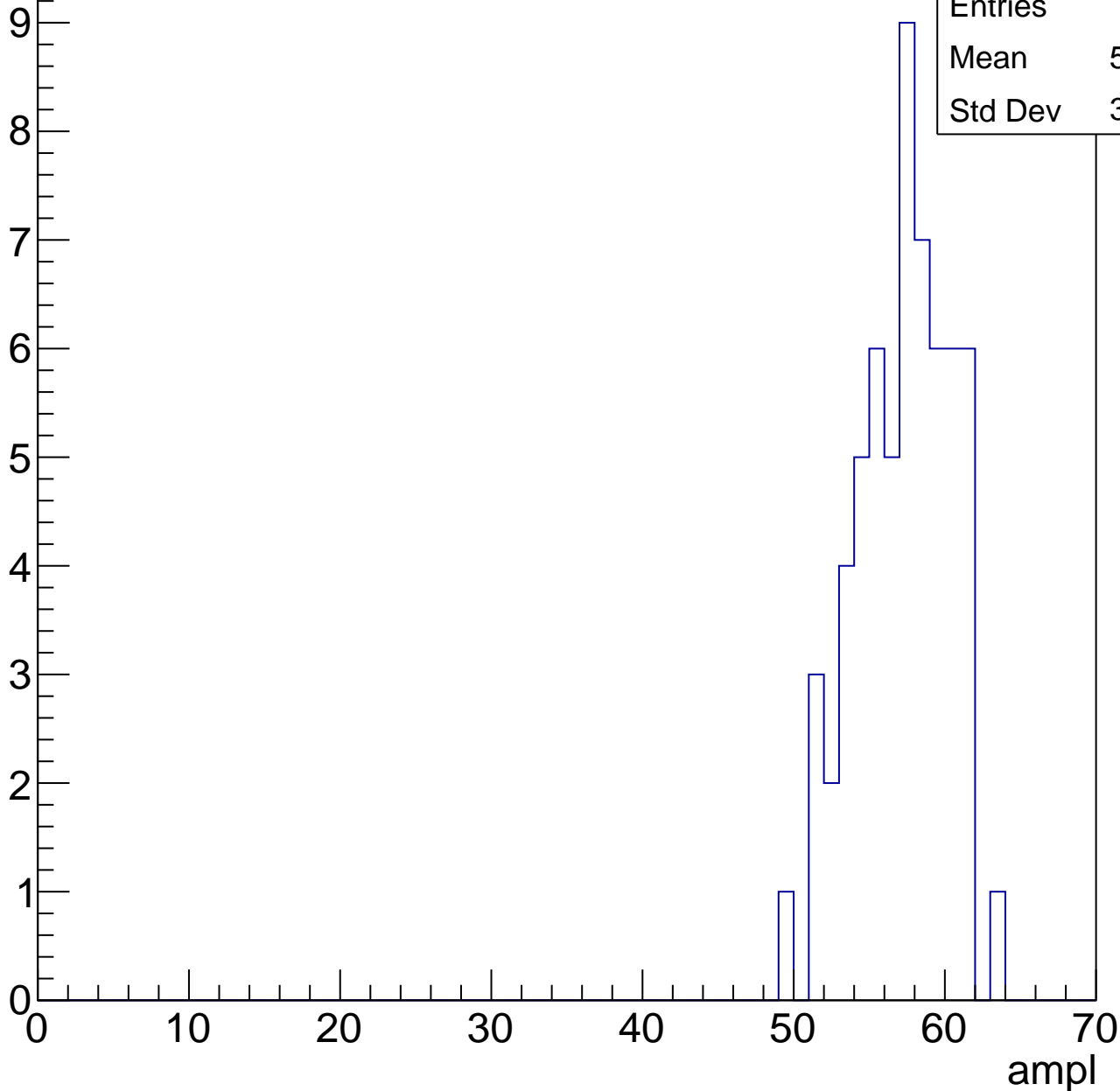


B1L100S, U13-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.72
Std Dev	3.063

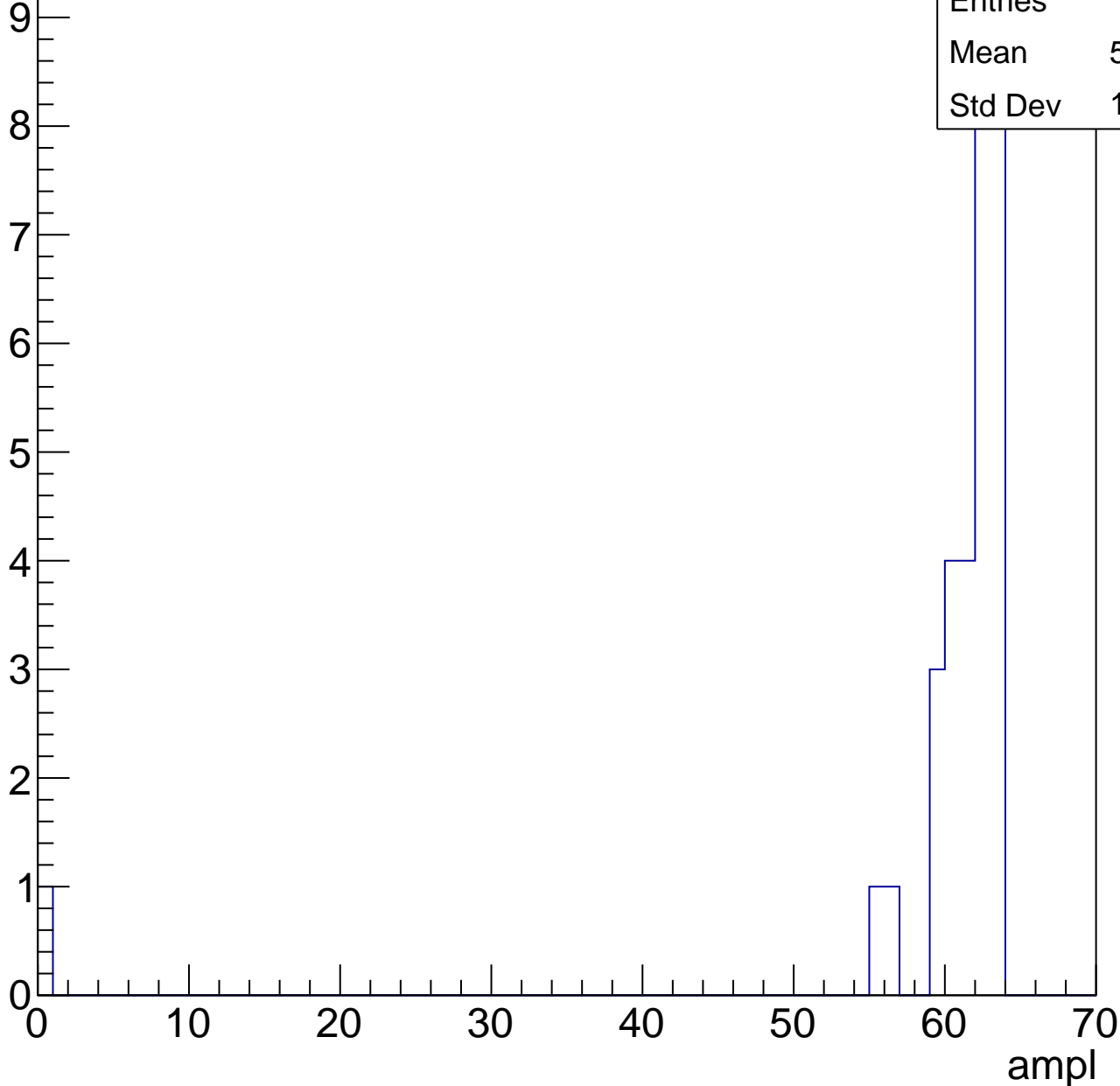


B1L100S, U13-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	59.16
Std Dev	10.98



B1L100S, U13-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch73, adc0

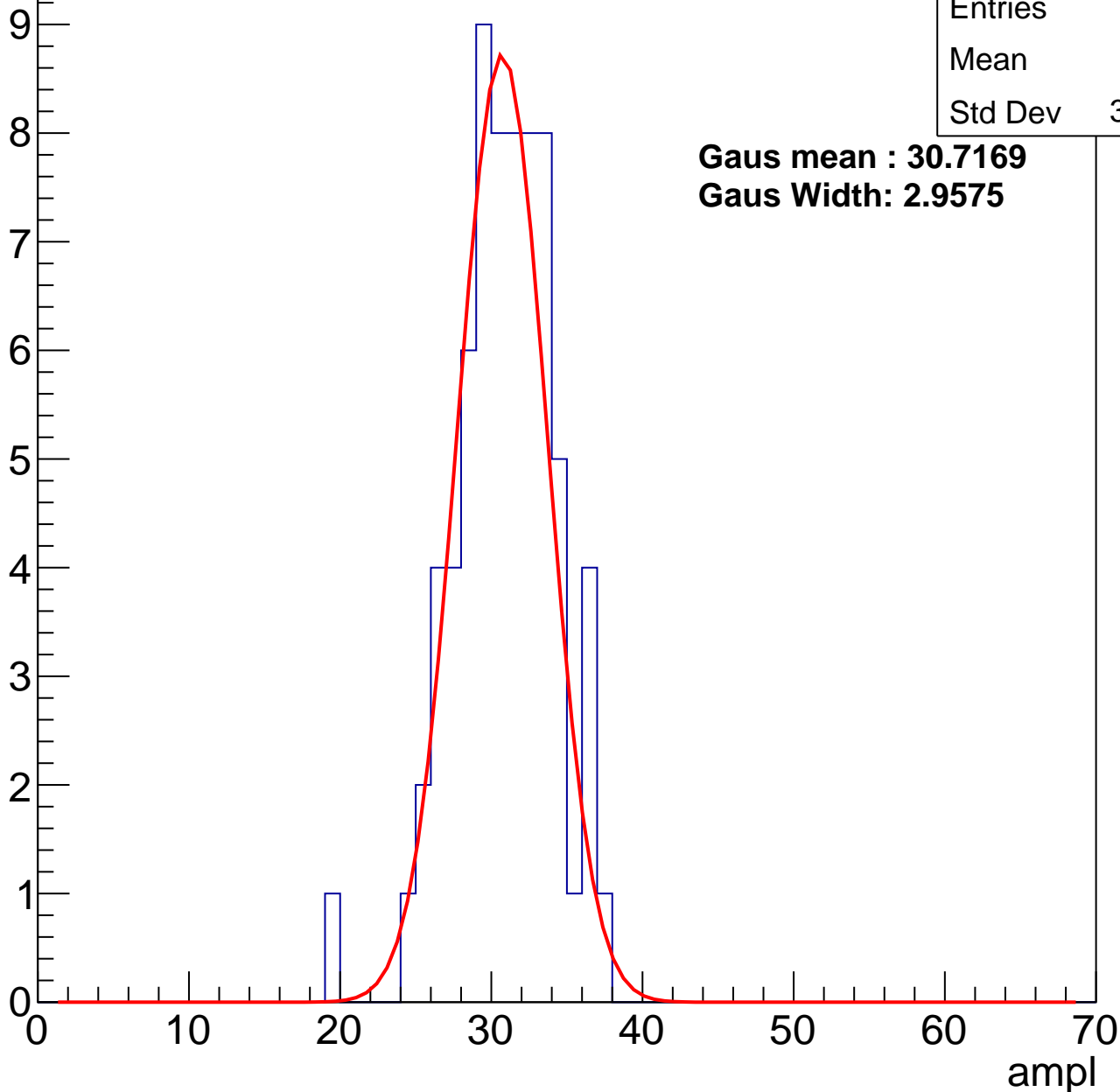
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.4
Std Dev	3.249

Gaus mean : 30.7169

Gaus Width: 2.9575



B1L100S, U13-ch73, adc1

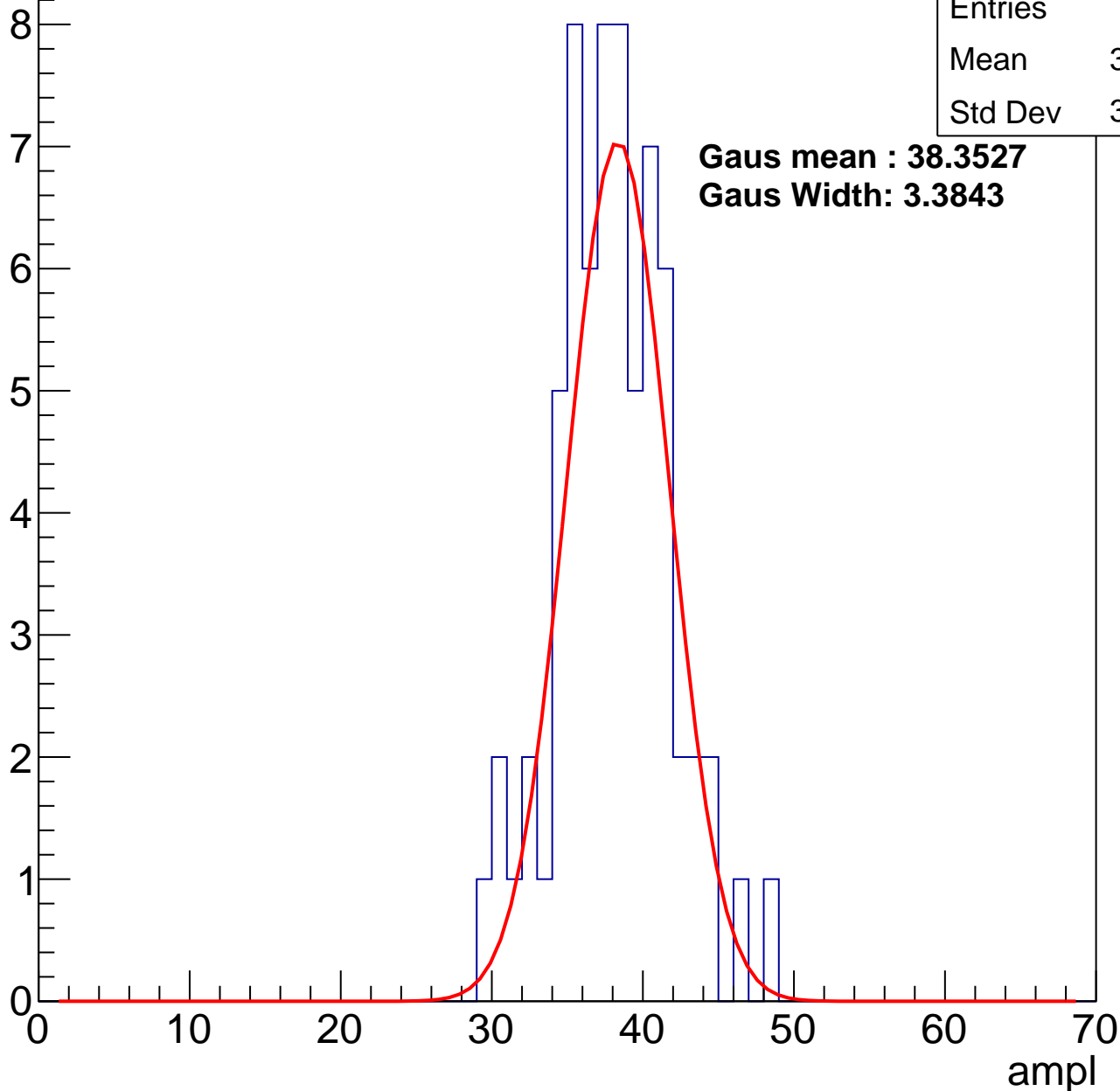
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.59
Std Dev	3.703

Gaus mean : 38.3527

Gaus Width: 3.3843



B1L100S, U13-ch73, adc2

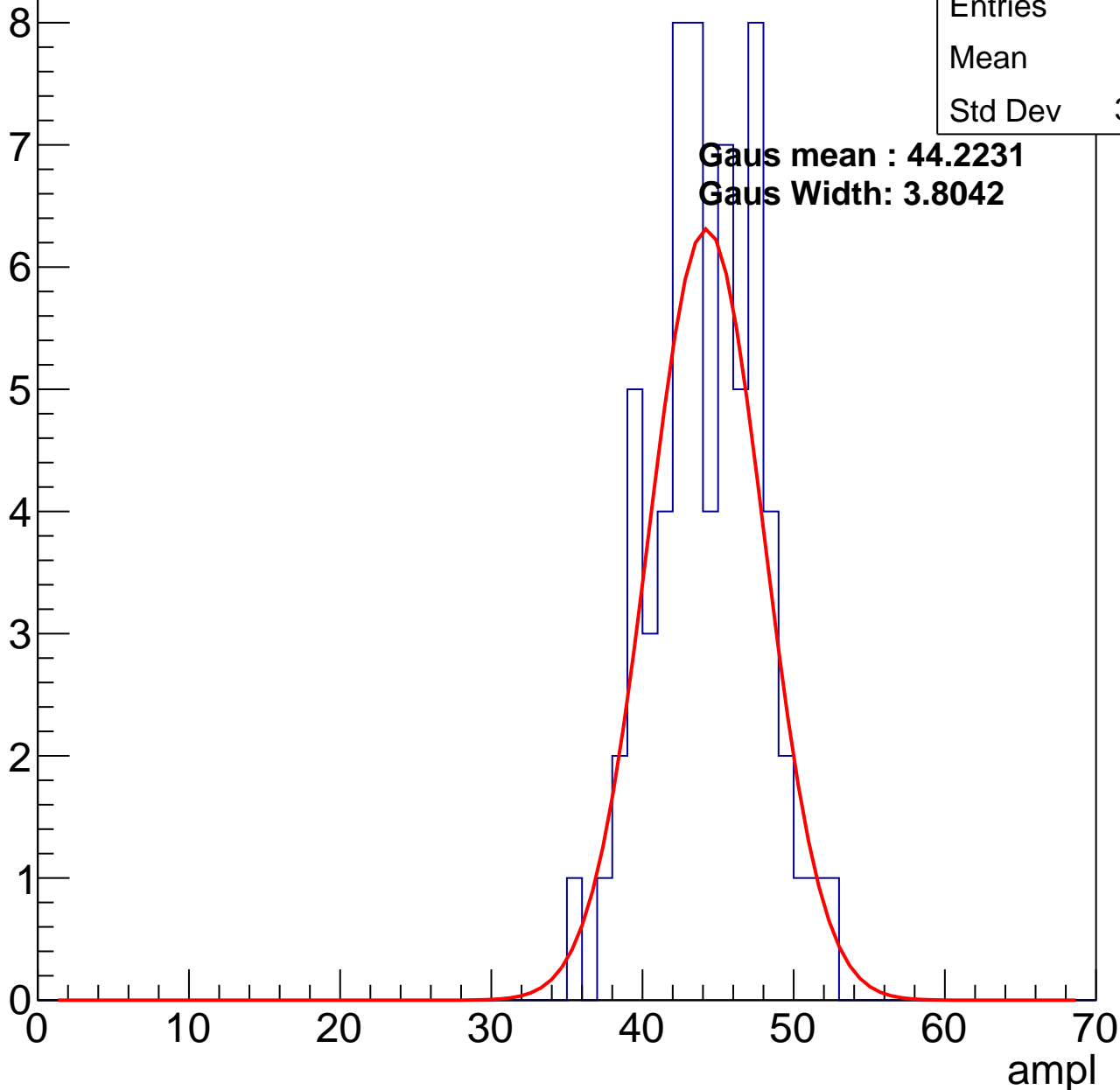
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.8
Std Dev	3.531

Gaus mean : 44.2231

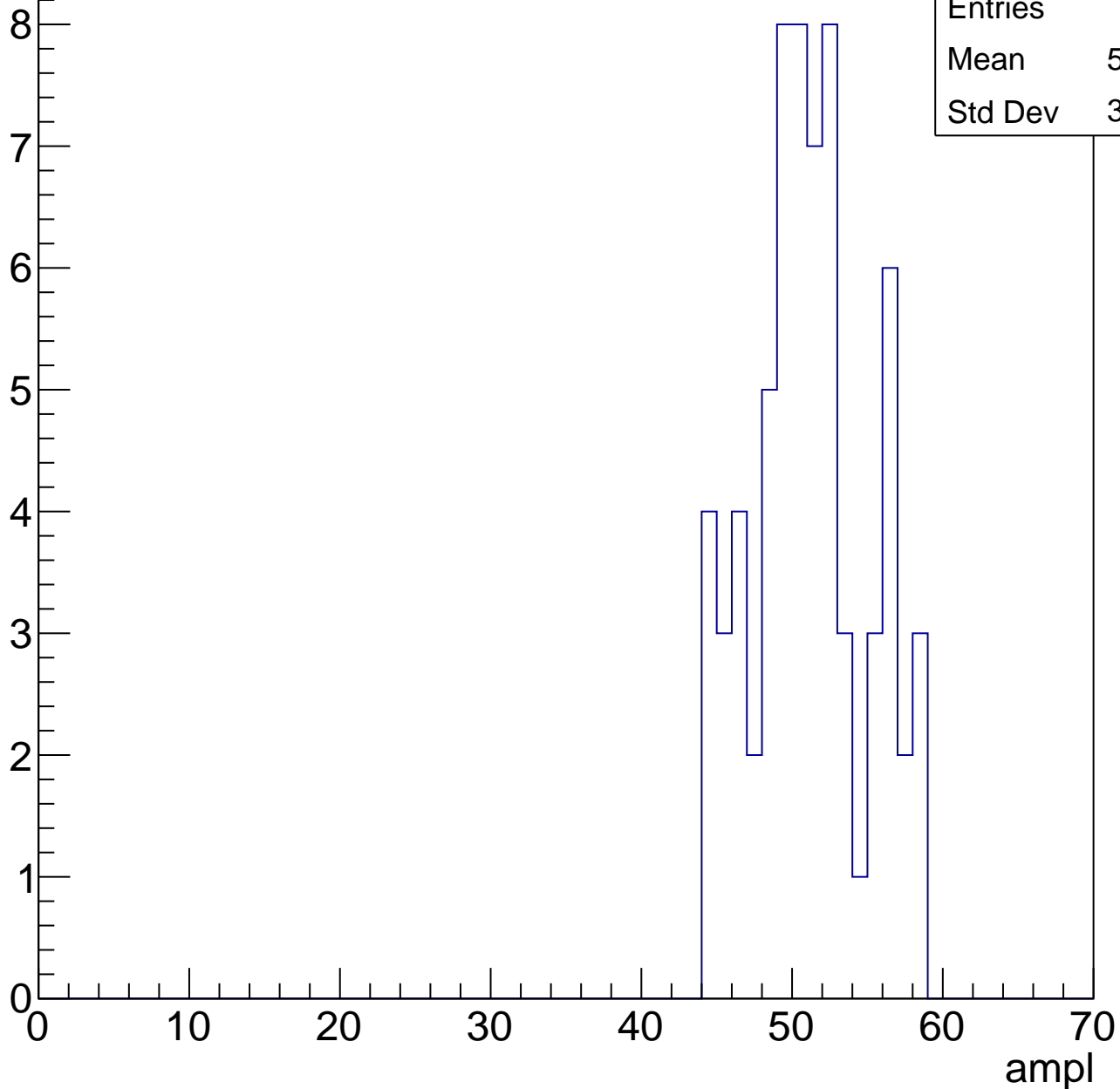
Gaus Width: 3.8042



B1L100S, U13-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

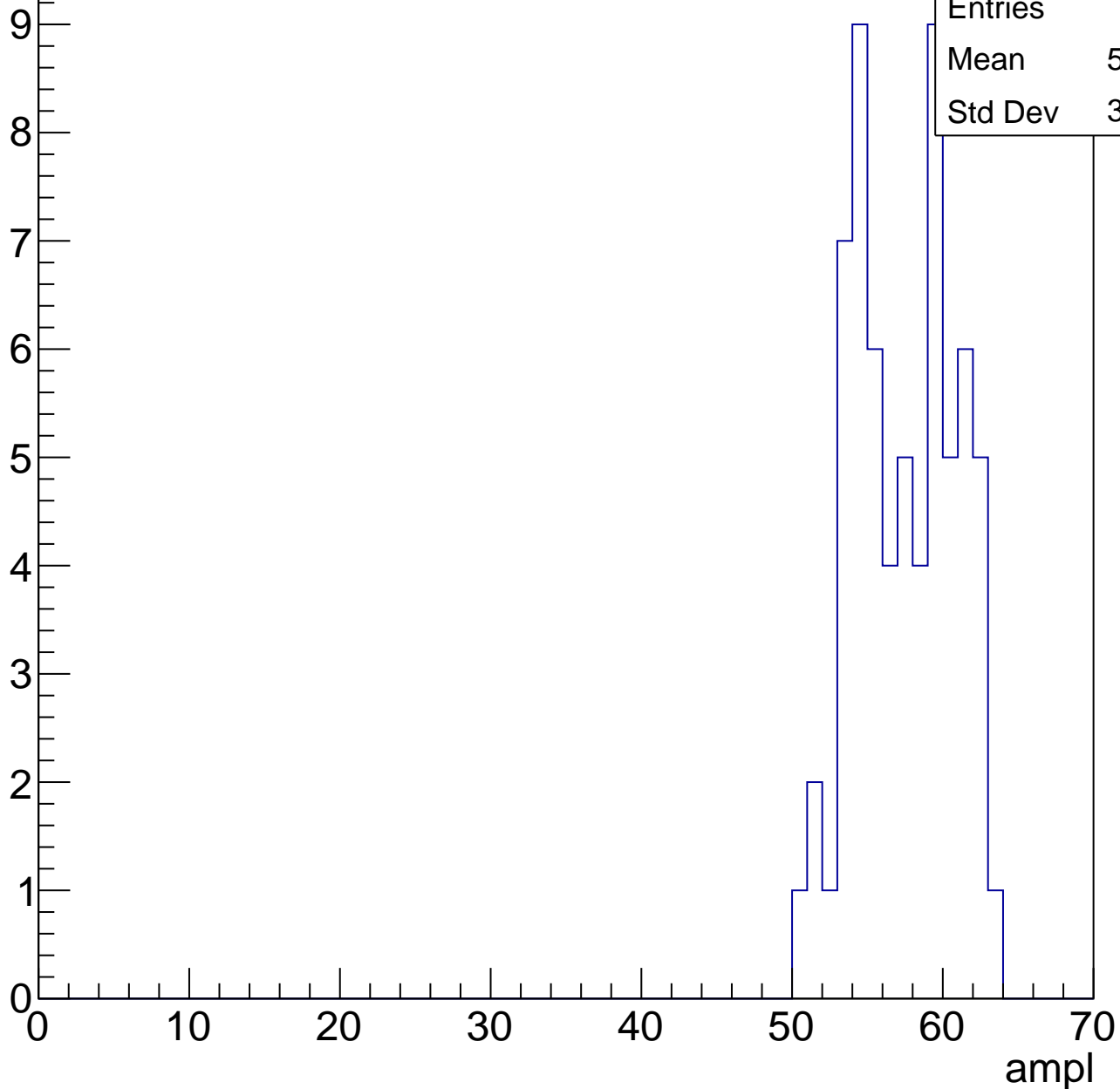


Entries	67
Mean	50.69
Std Dev	3.786

B1L100S, U13-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

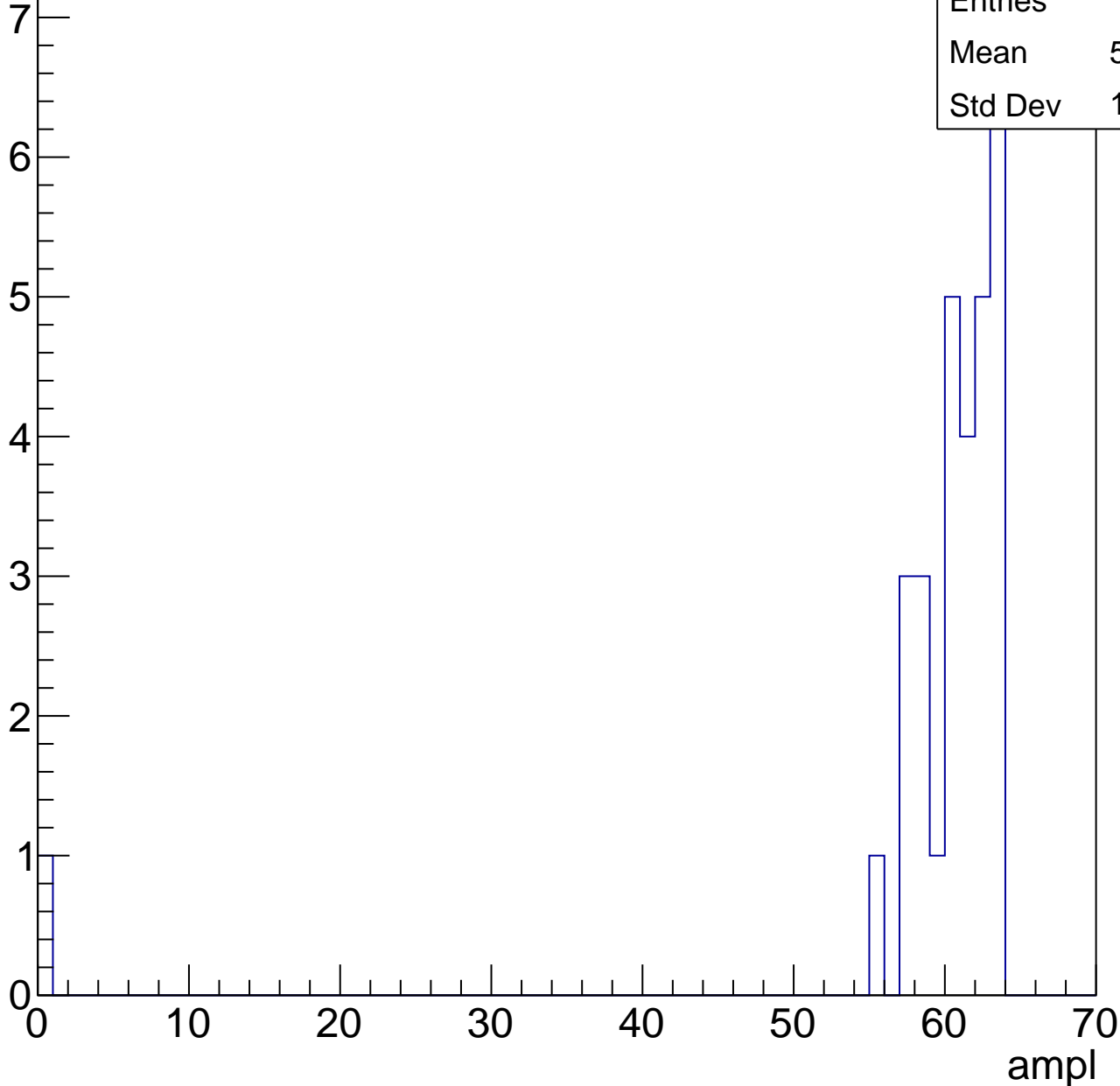


B1L100S, U13-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.47
Std Dev	11.08



B1L100S, U13-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U13-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch74, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	31.28
Std Dev	3.672

Gaus mean : 32.0012

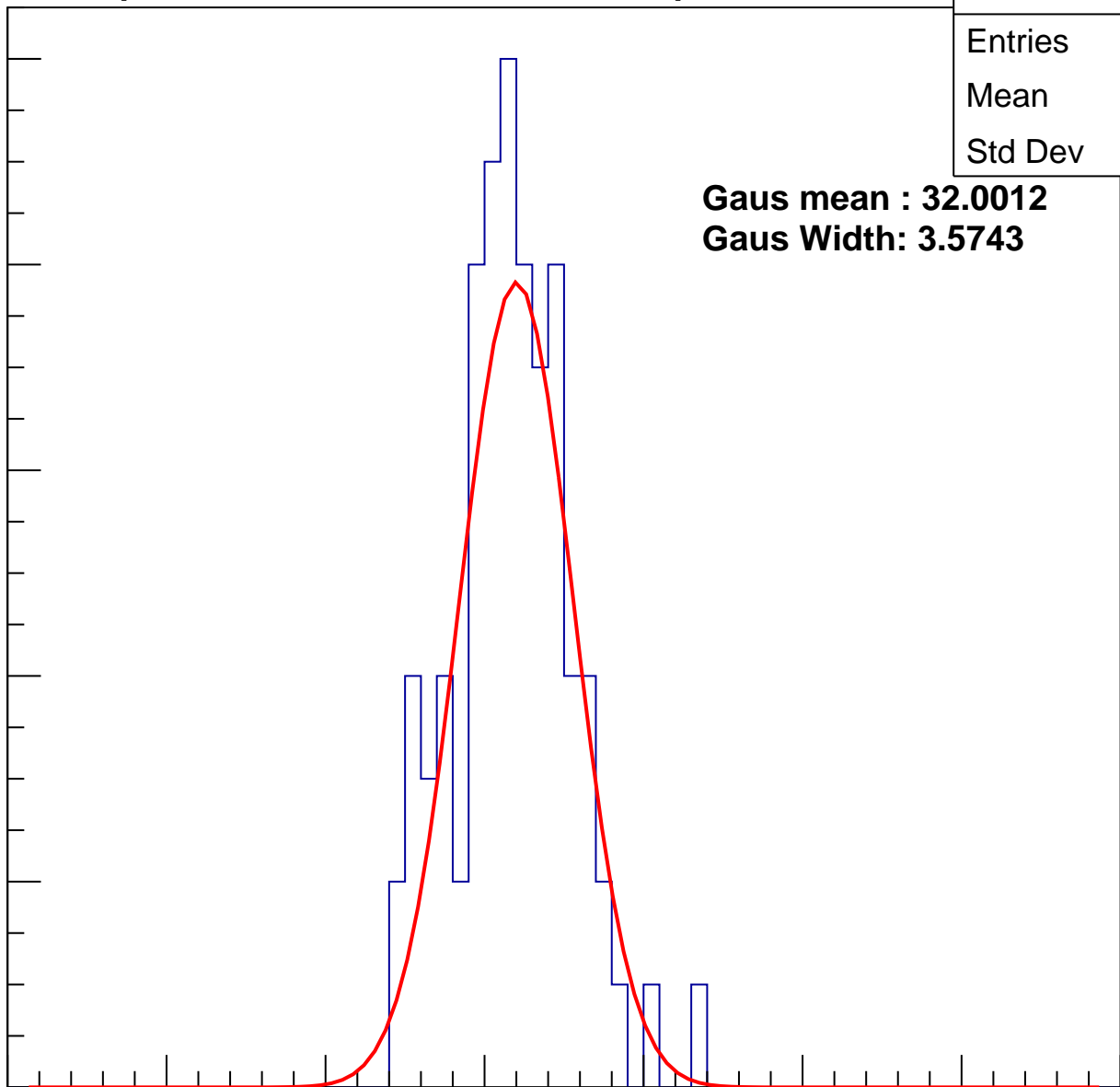
Gaus Width: 3.5743

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch74, adc1

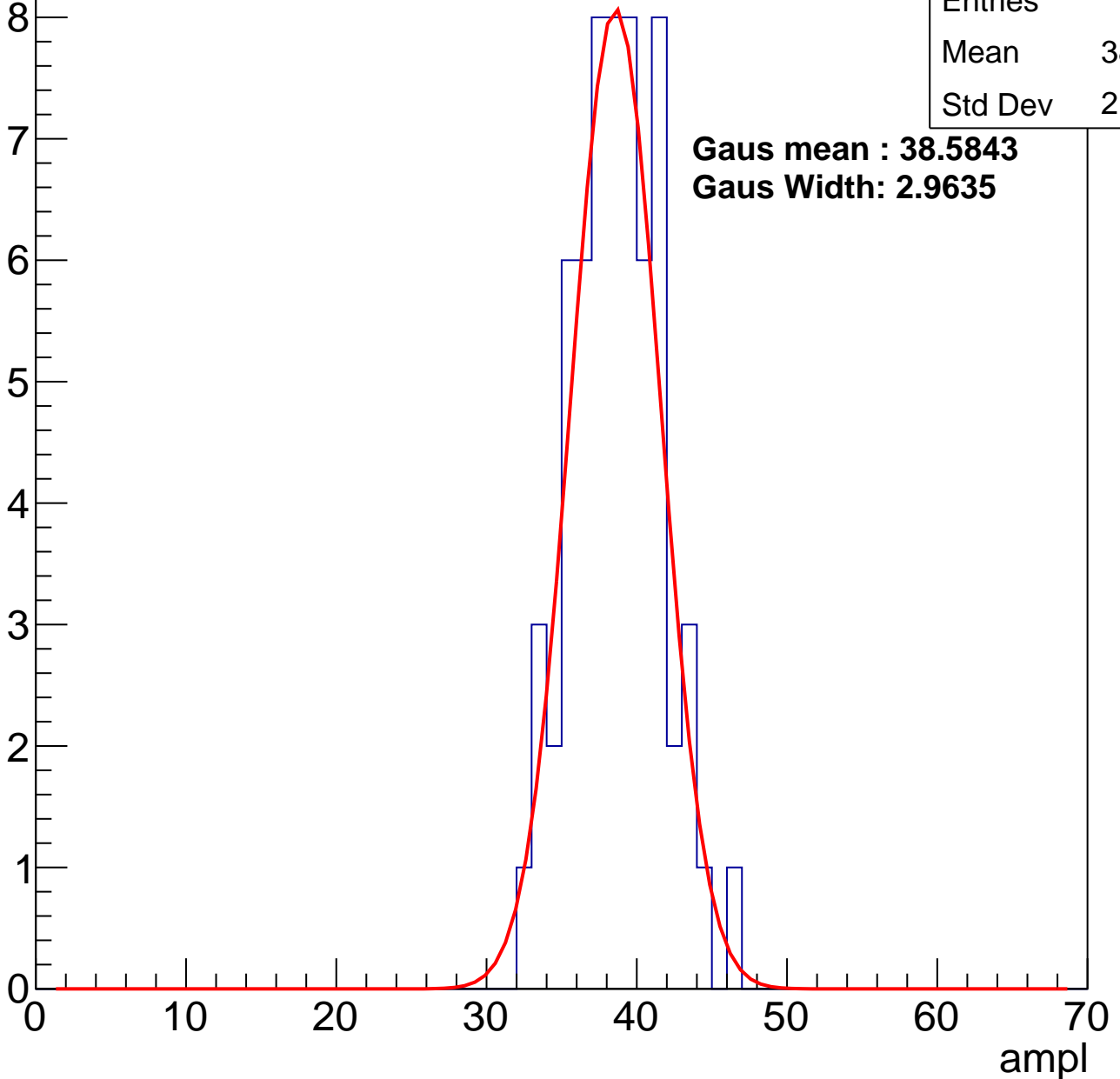
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	38.22
Std Dev	2.919

Gaus mean : 38.5843

Gaus Width: 2.9635



B1L100S, U13-ch74, adc2

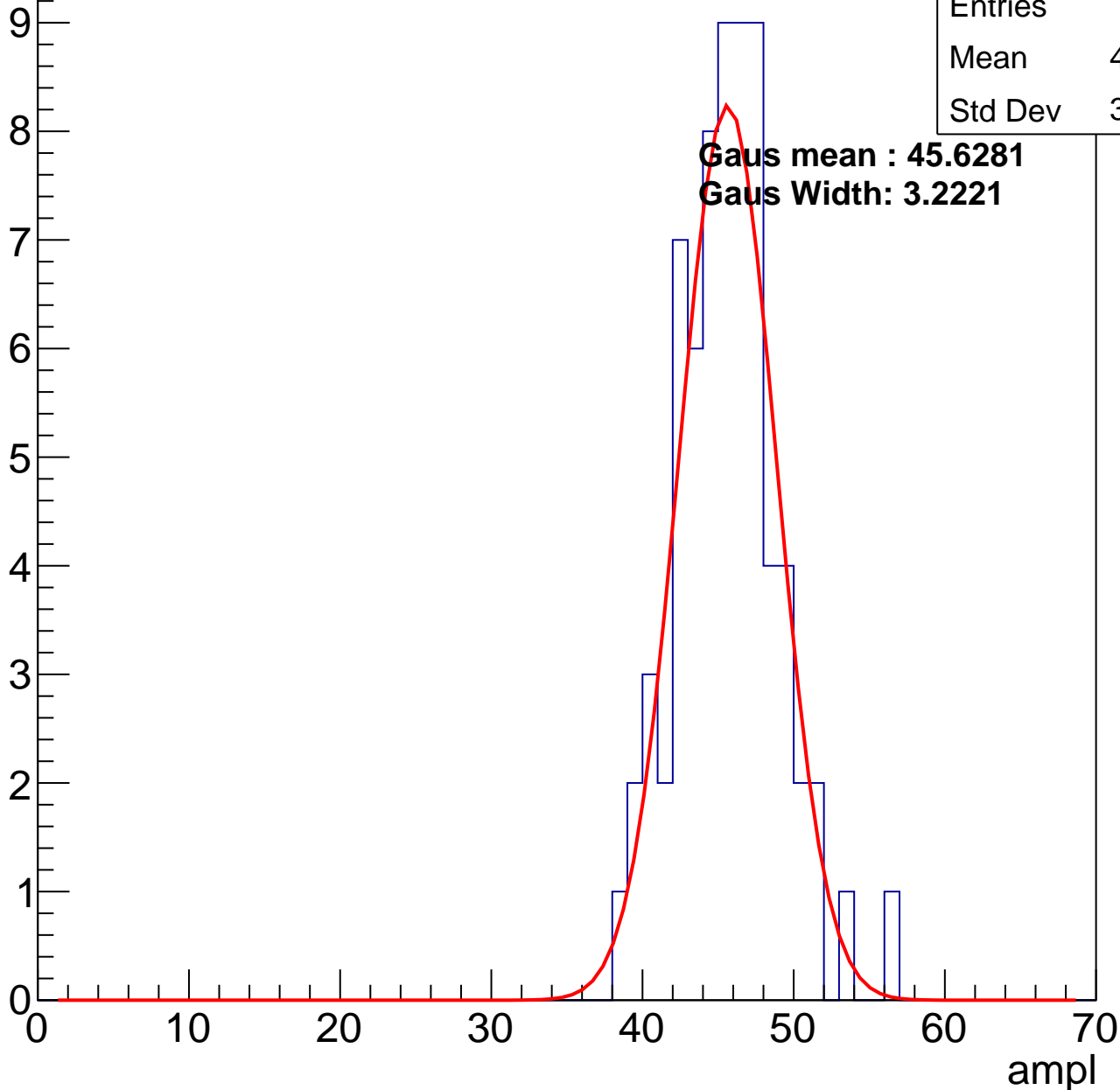
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.19
Std Dev	3.322

Gaus mean : 45.6281

Gaus Width: 3.2221

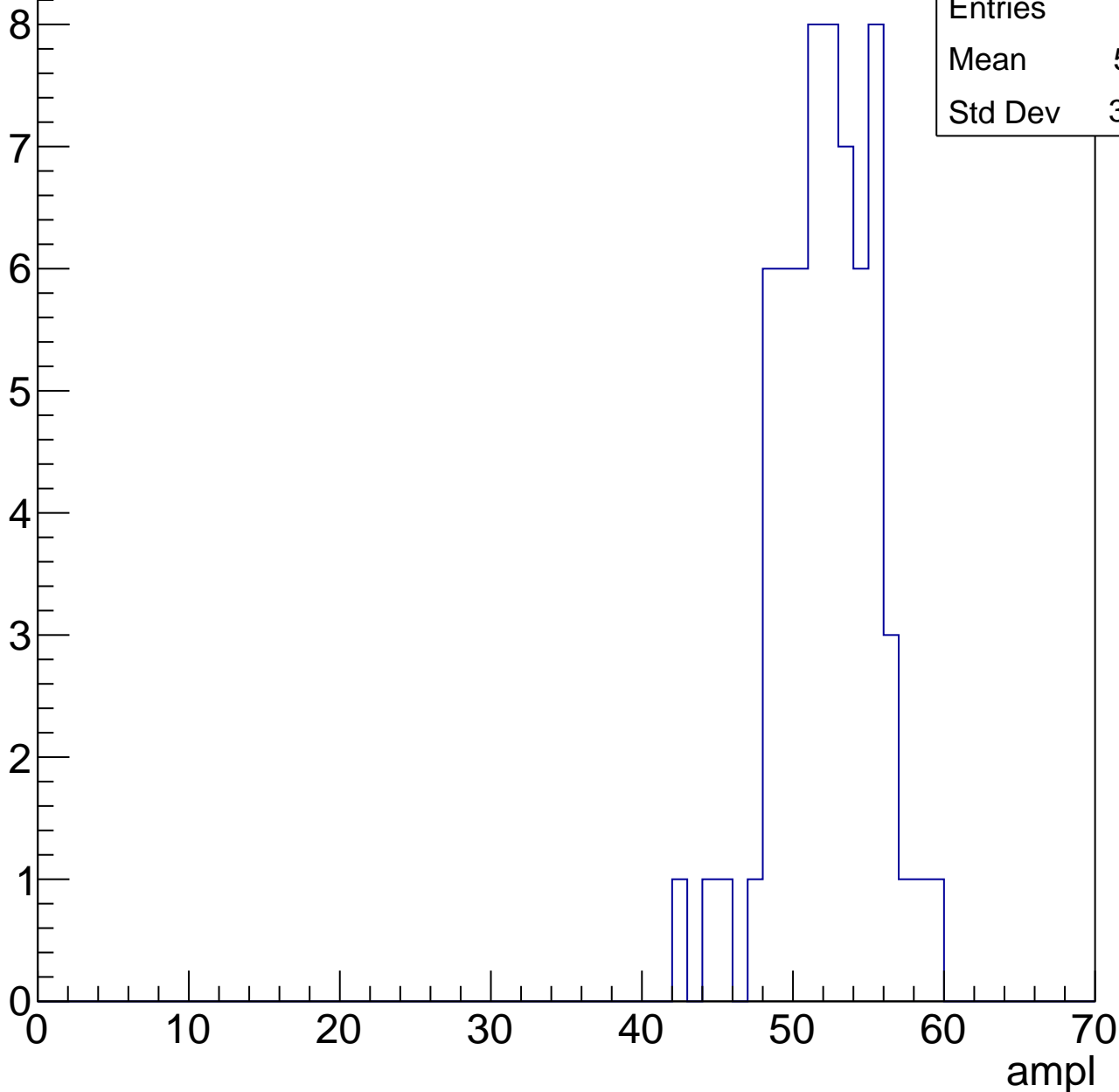


B1L100S, U13-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

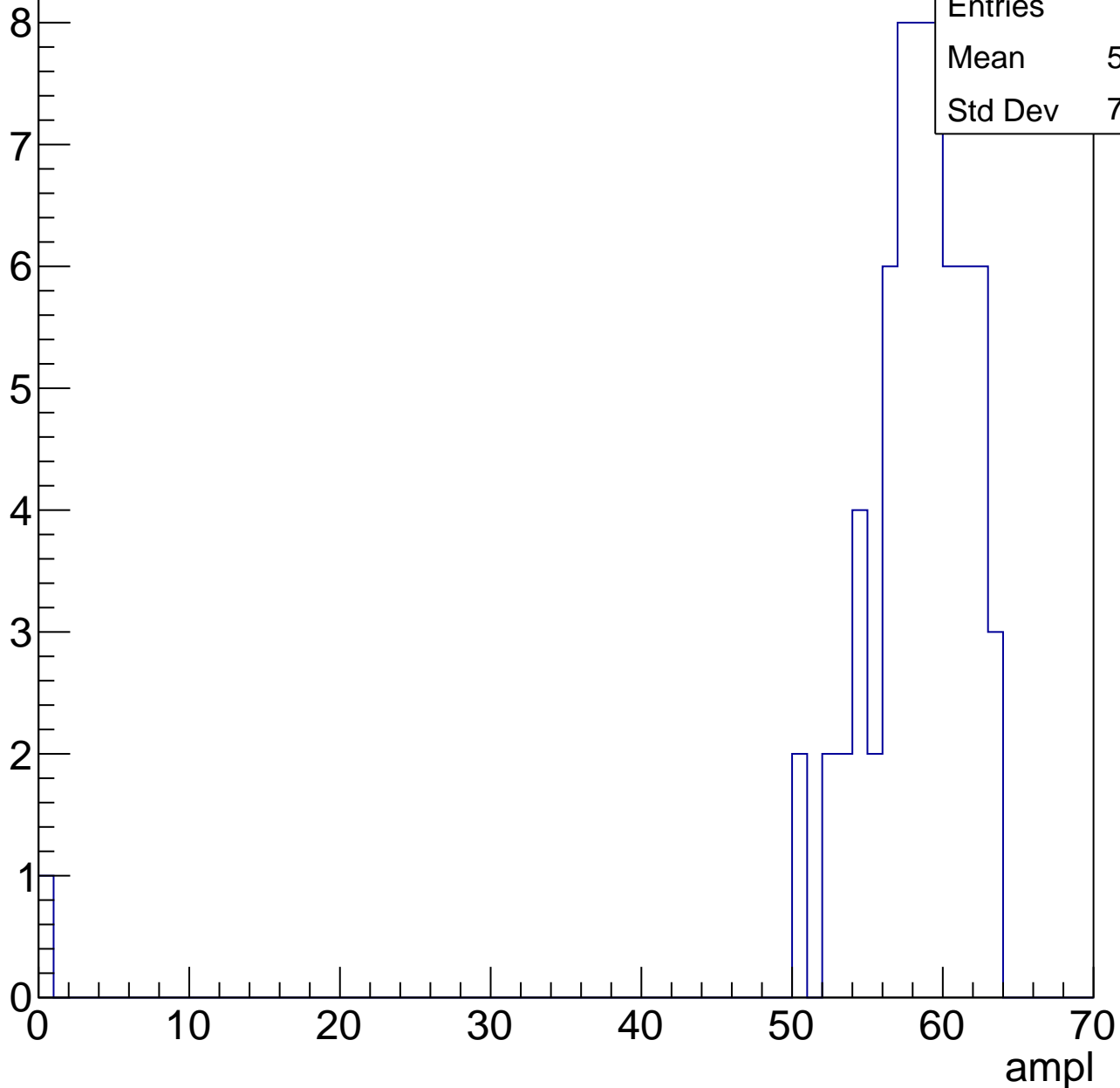
Entries	65
Mean	51.71
Std Dev	3.224



B1L100S, U13-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

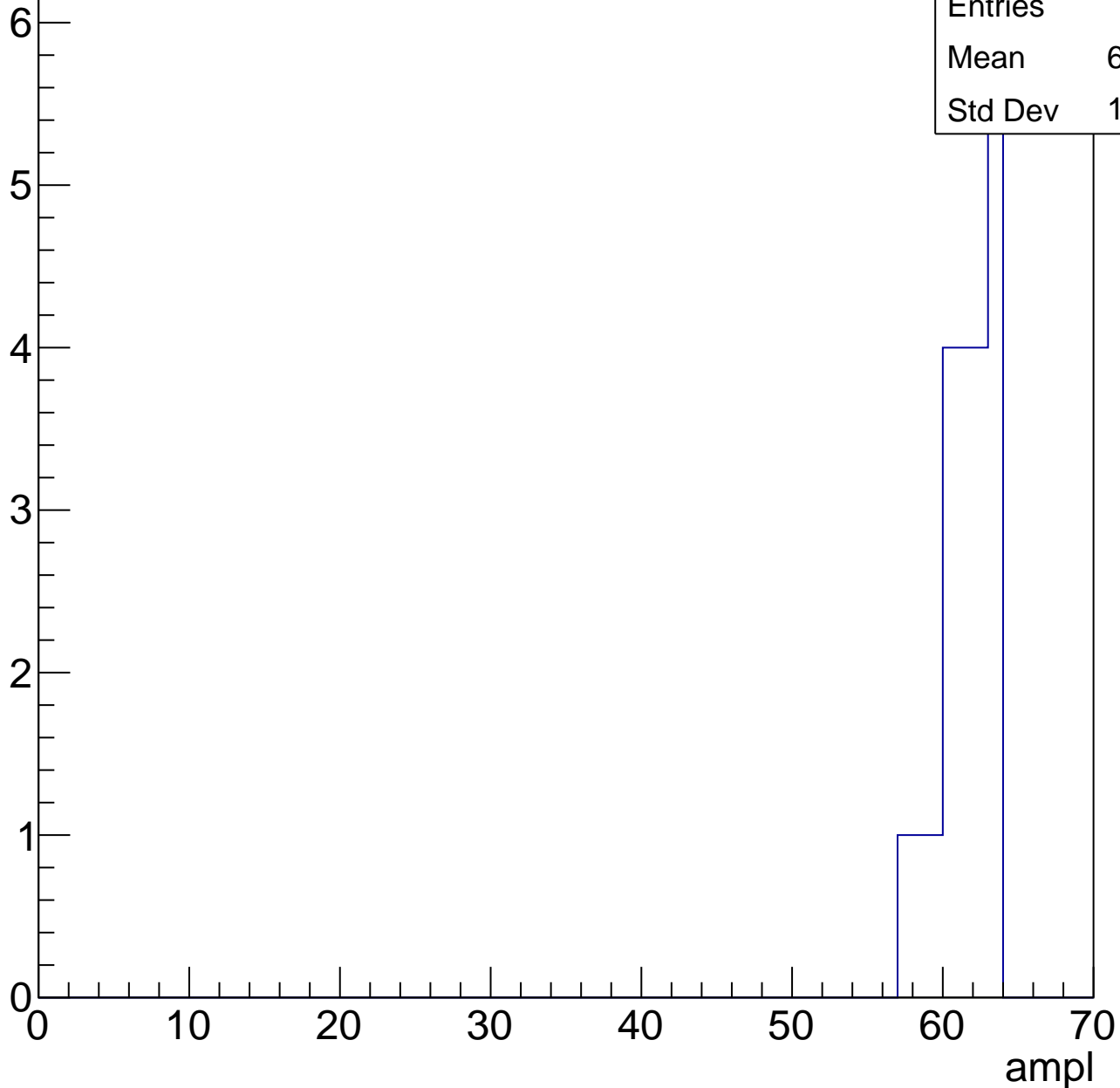
Entry



B1L100S, U13-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch75, adc0

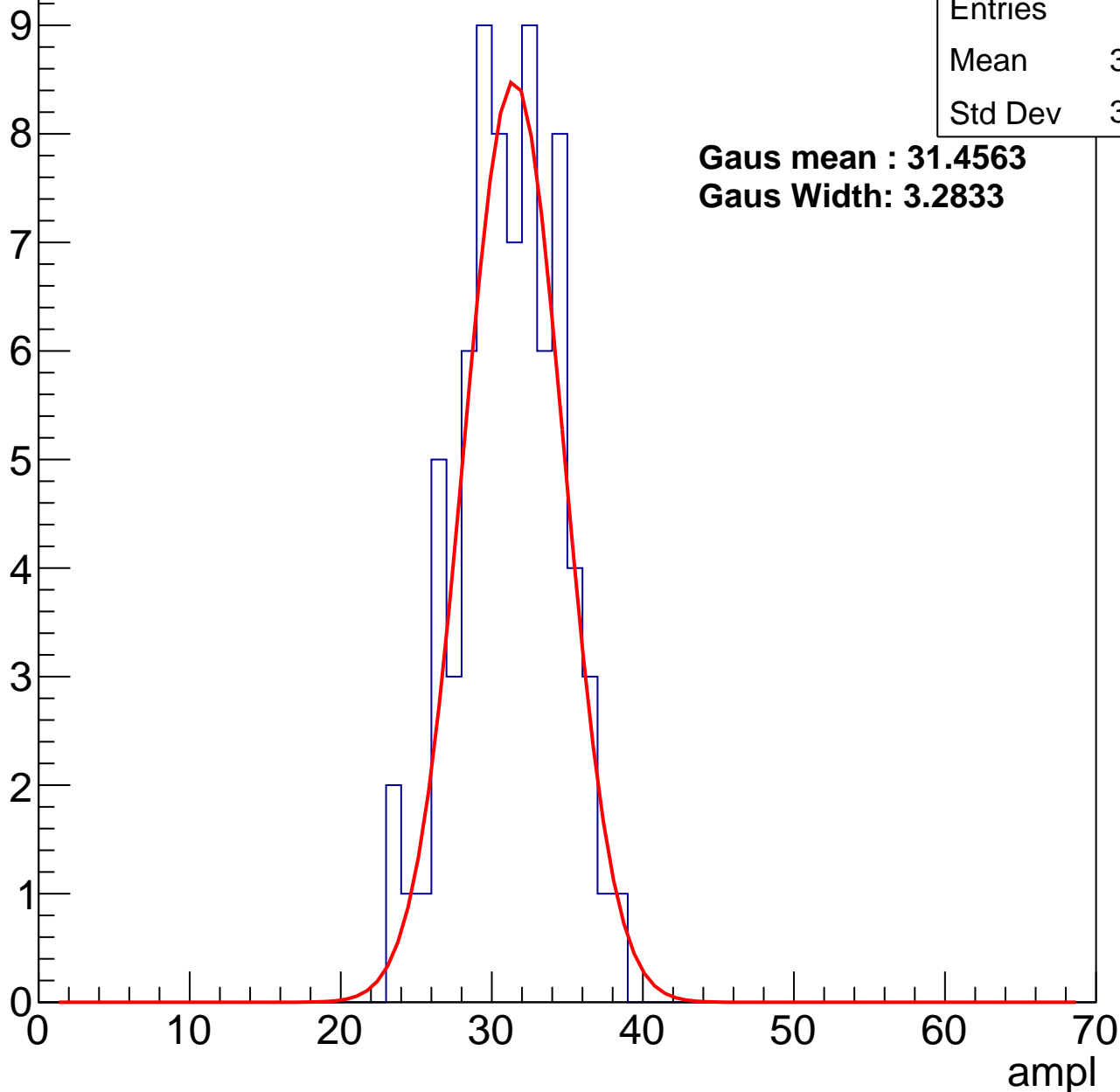
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.72
Std Dev	3.302

Gaus mean : 31.4563

Gaus Width: 3.2833



B1L100S, U13-ch75, adc1

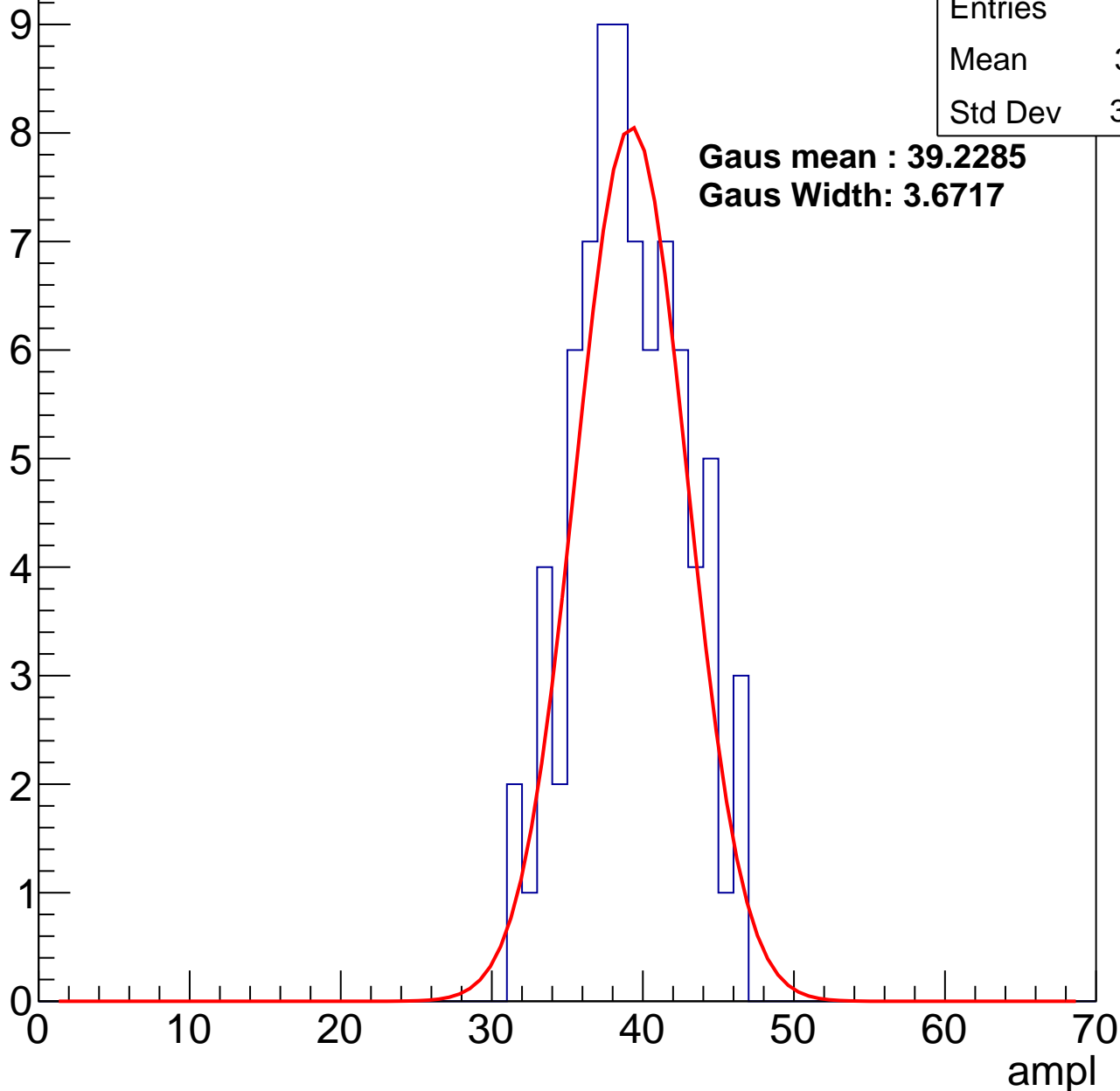
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	38.71
Std Dev	3.608

Gaus mean : 39.2285

Gaus Width: 3.6717



B1L100S, U13-ch75, adc2

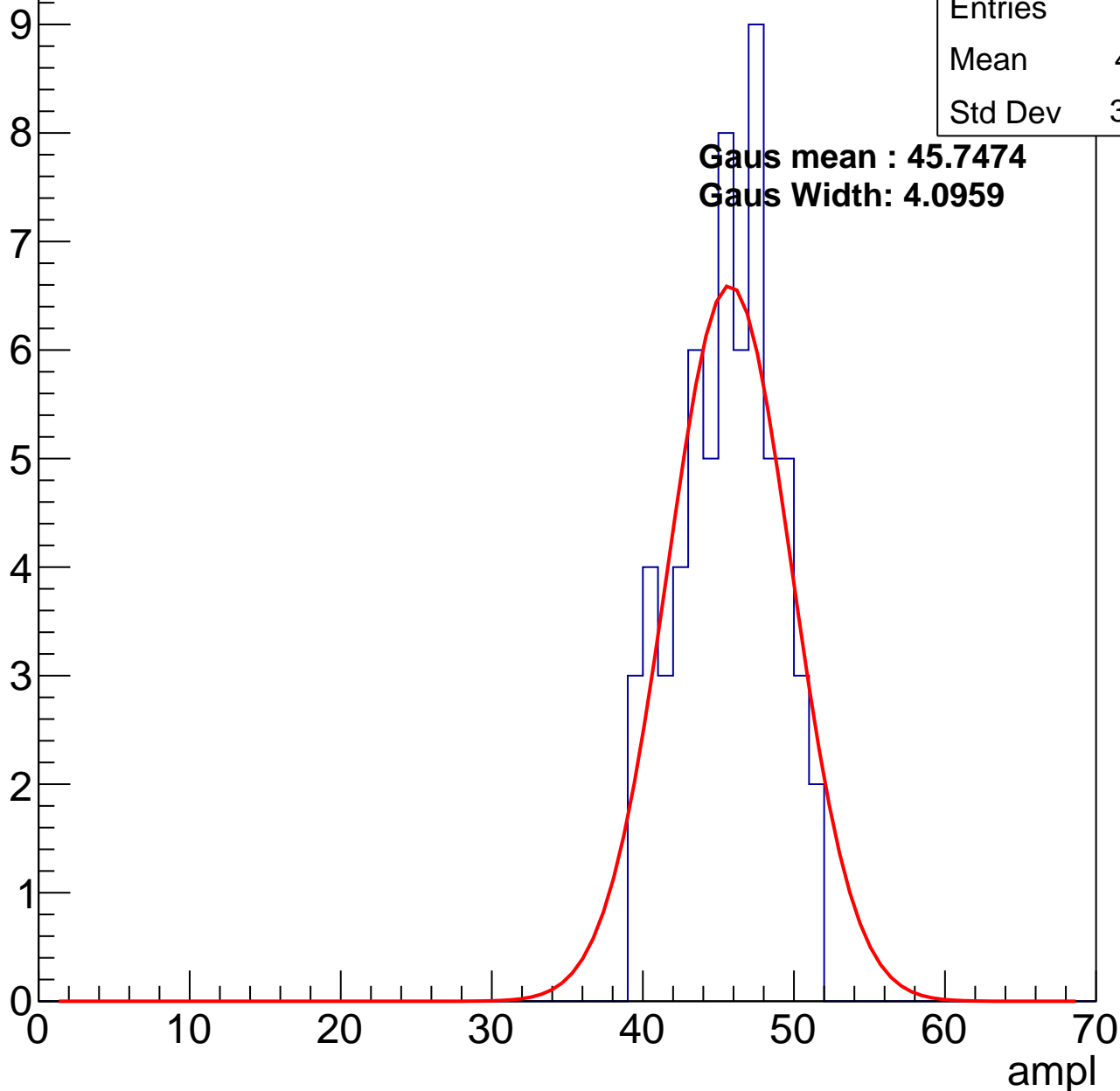
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	45.11
Std Dev	3.173

Gaus mean : 45.7474

Gaus Width: 4.0959

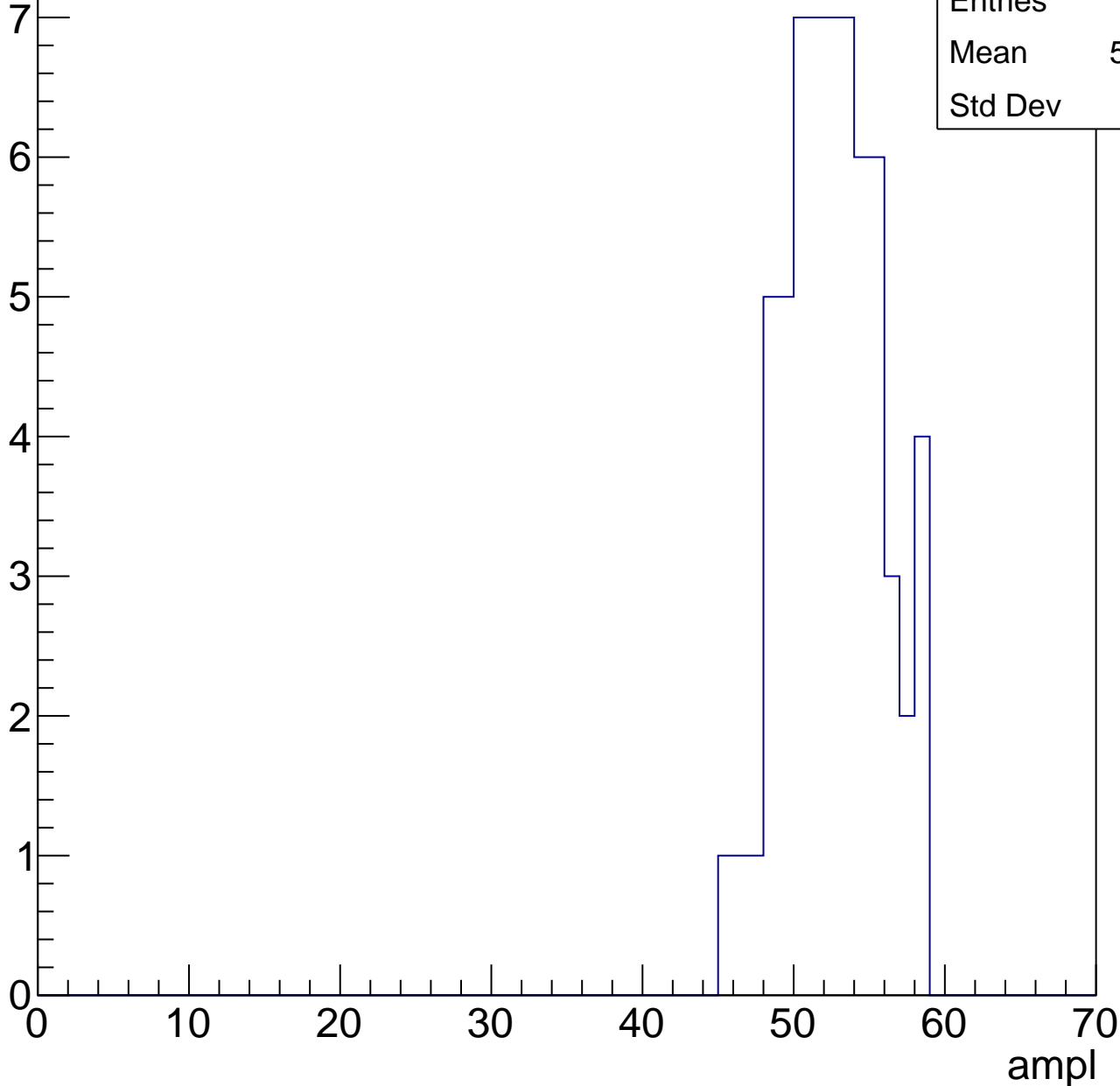


B1L100S, U13-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	52.15
Std Dev	3.1

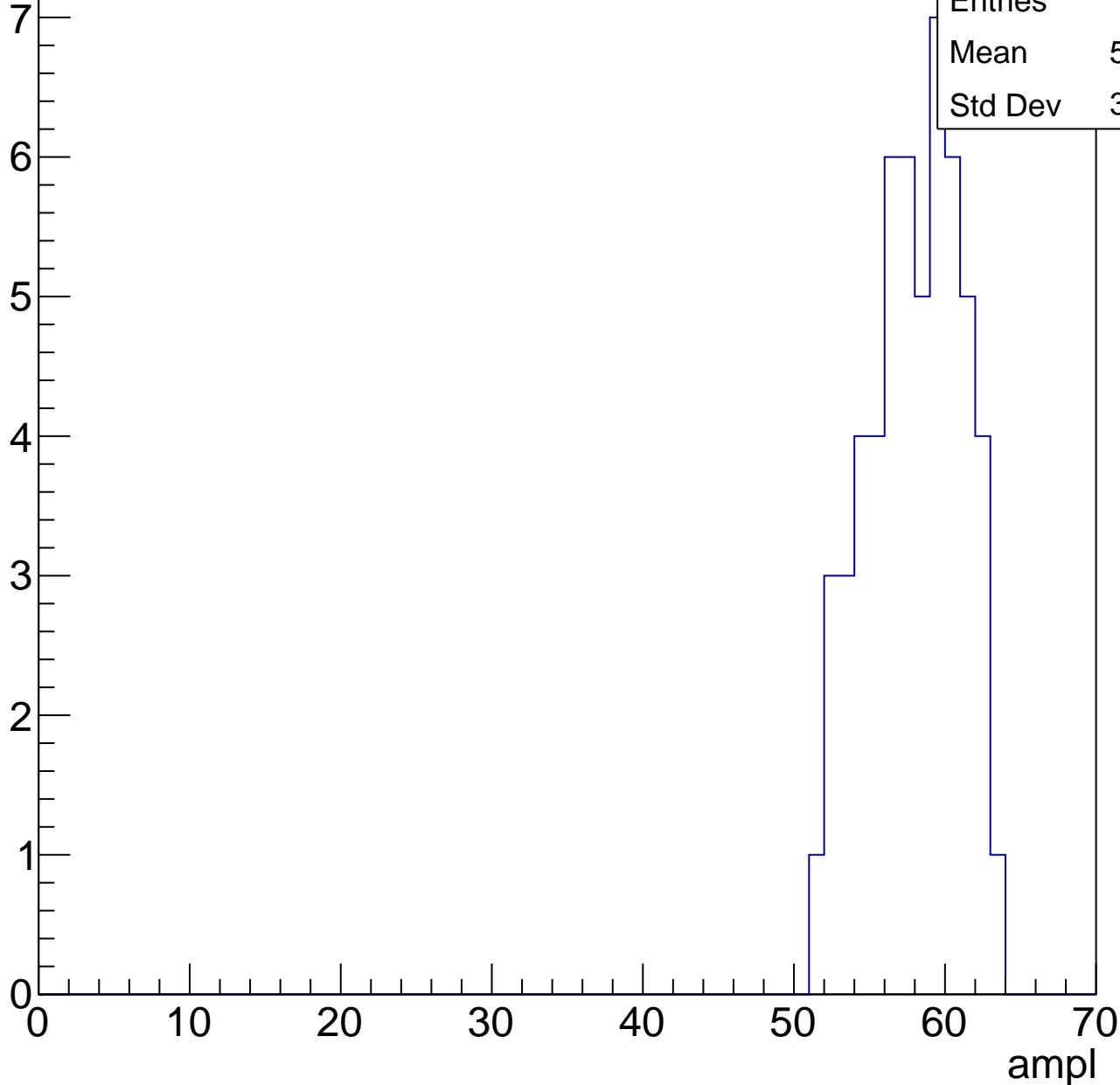


B1L100S, U13-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.44
Std Dev	3.044

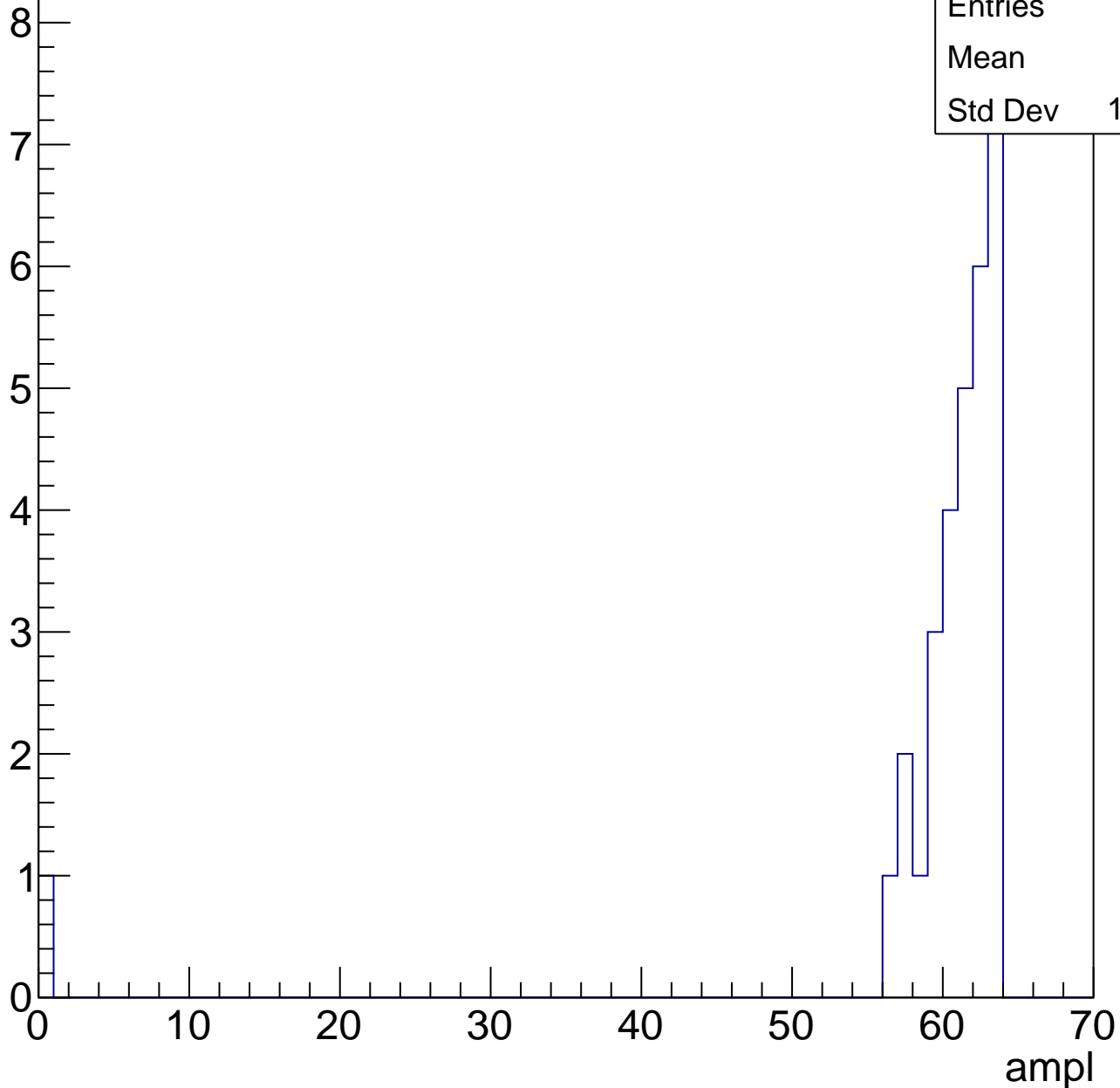


B1L100S, U13-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.9
Std Dev	10.93



B1L100S, U13-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch76, adc0

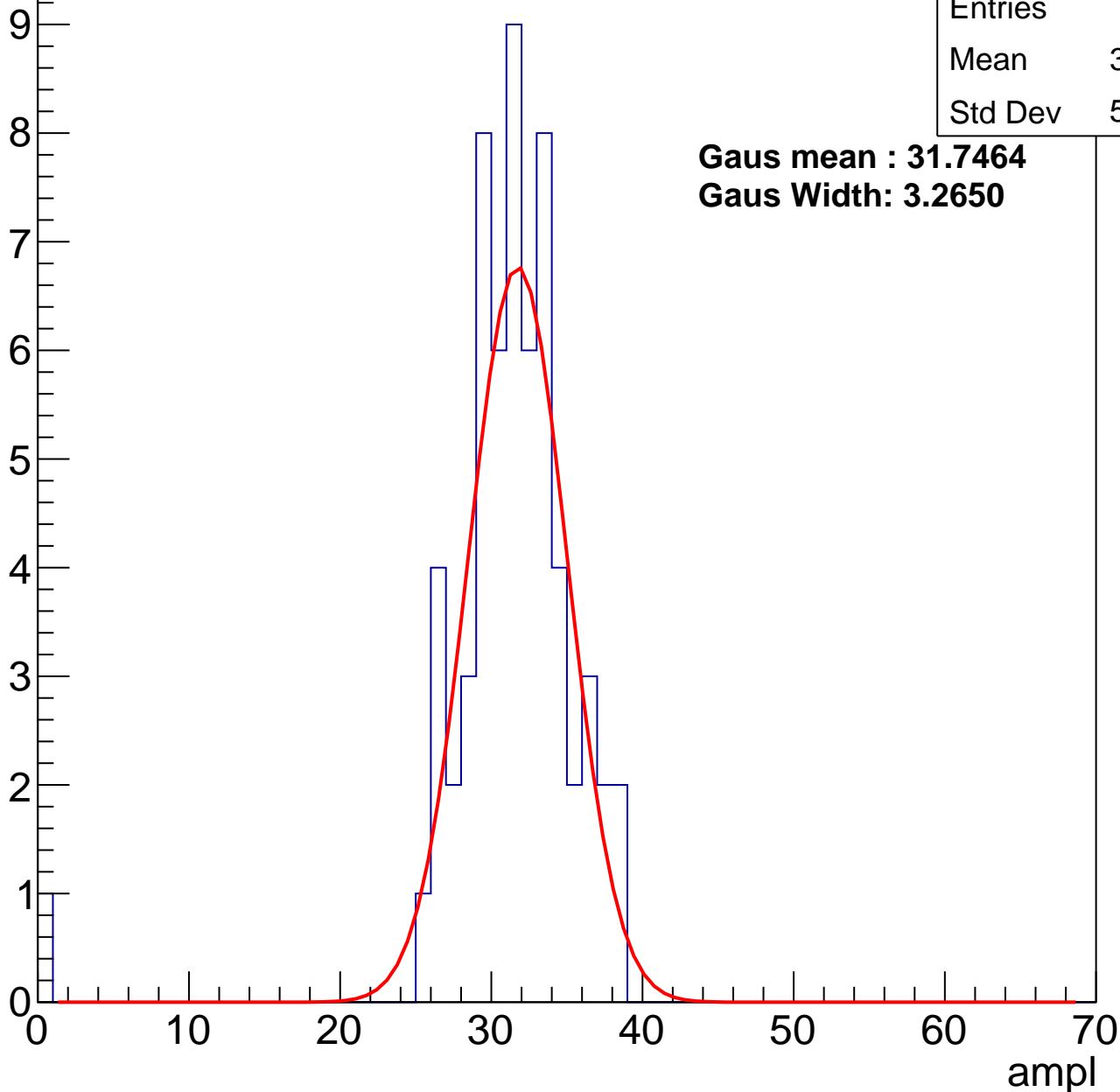
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.79
Std Dev	5.028

Gaus mean : 31.7464

Gaus Width: 3.2650



B1L100S, U13-ch76, adc1

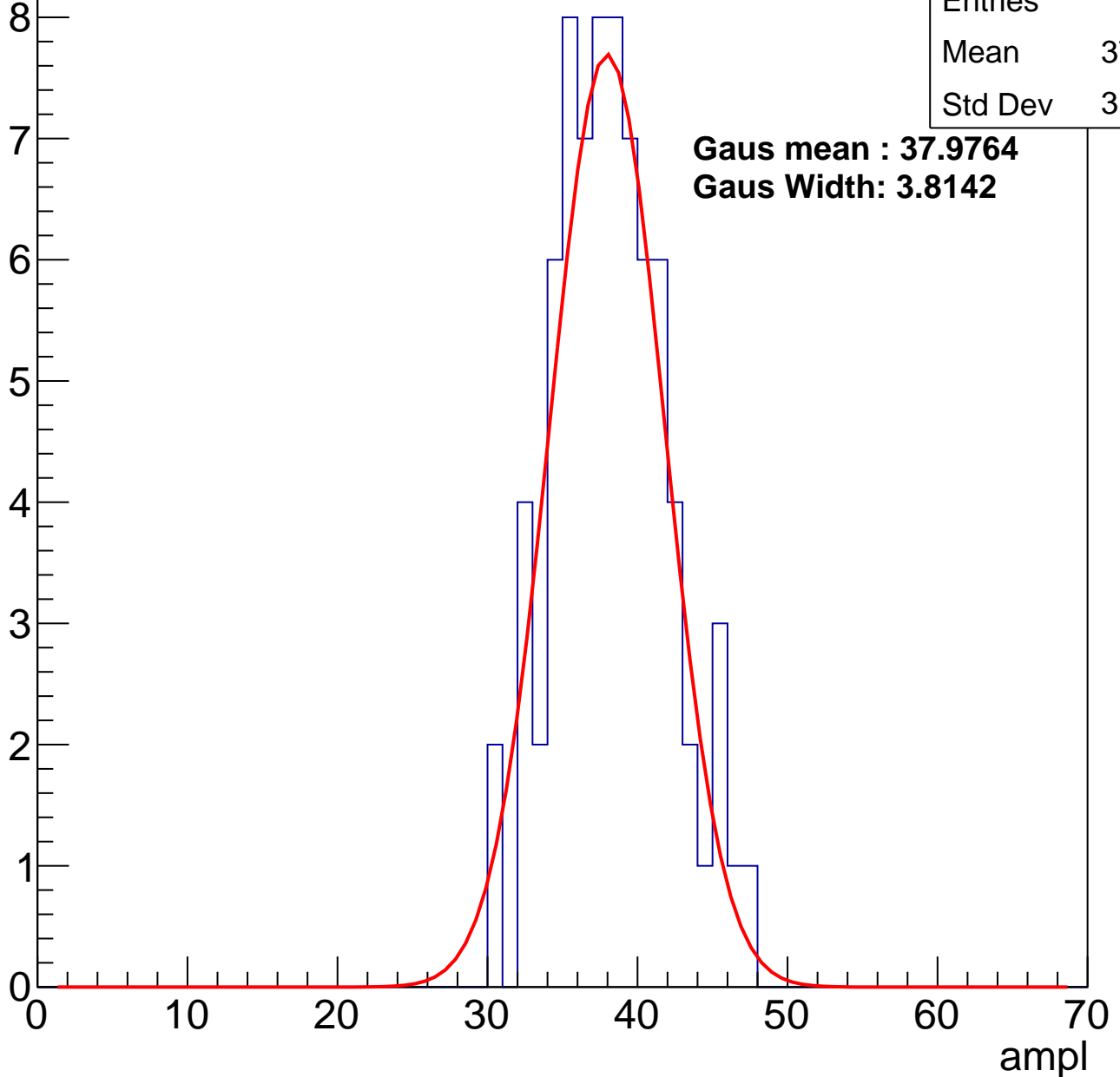
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	37.83
Std Dev	3.718

Gaus mean : 37.9764

Gaus Width: 3.8142



B1L100S, U13-ch76, adc2

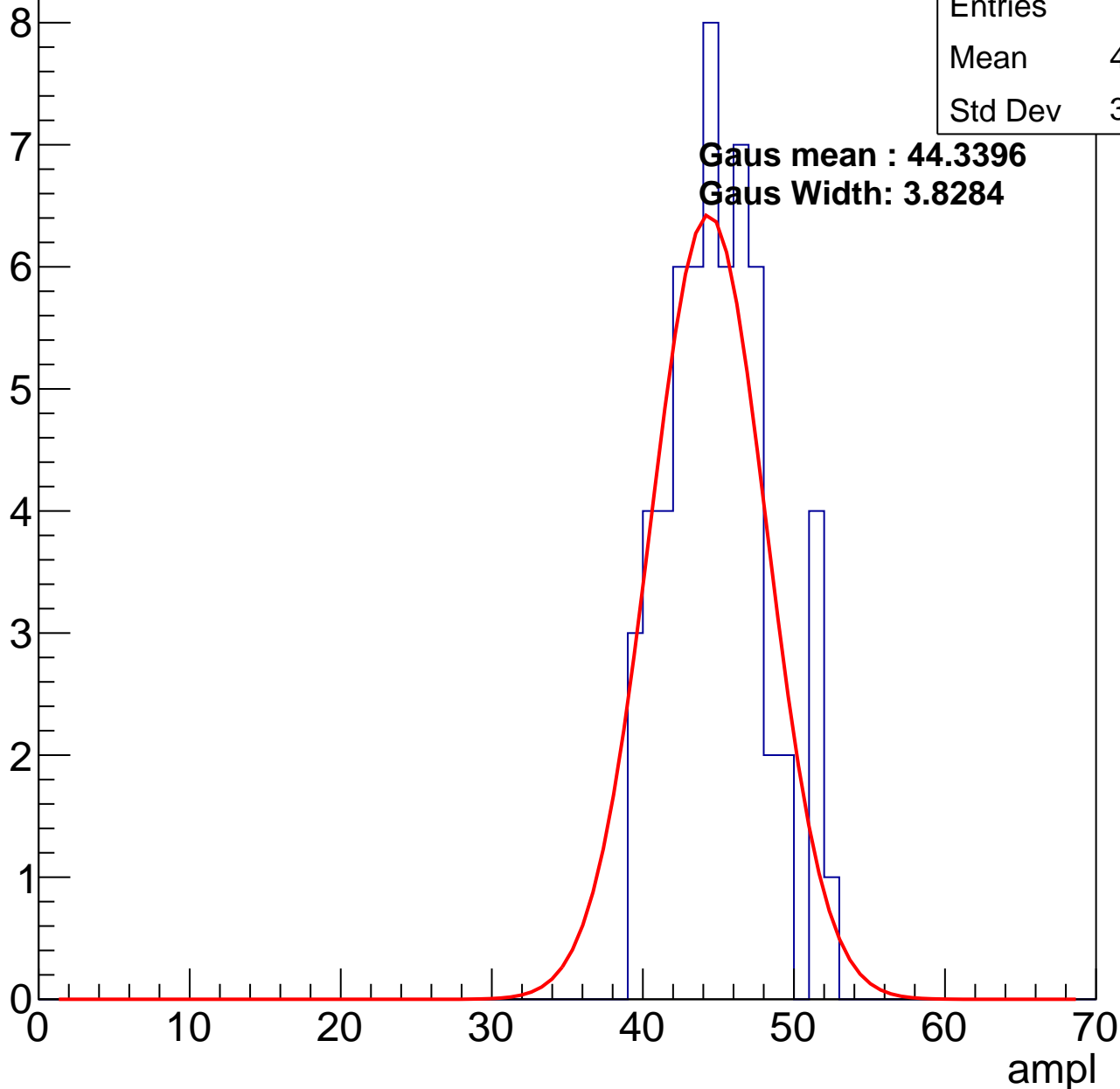
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.53
Std Dev	3.238

Gaus mean : 44.3396

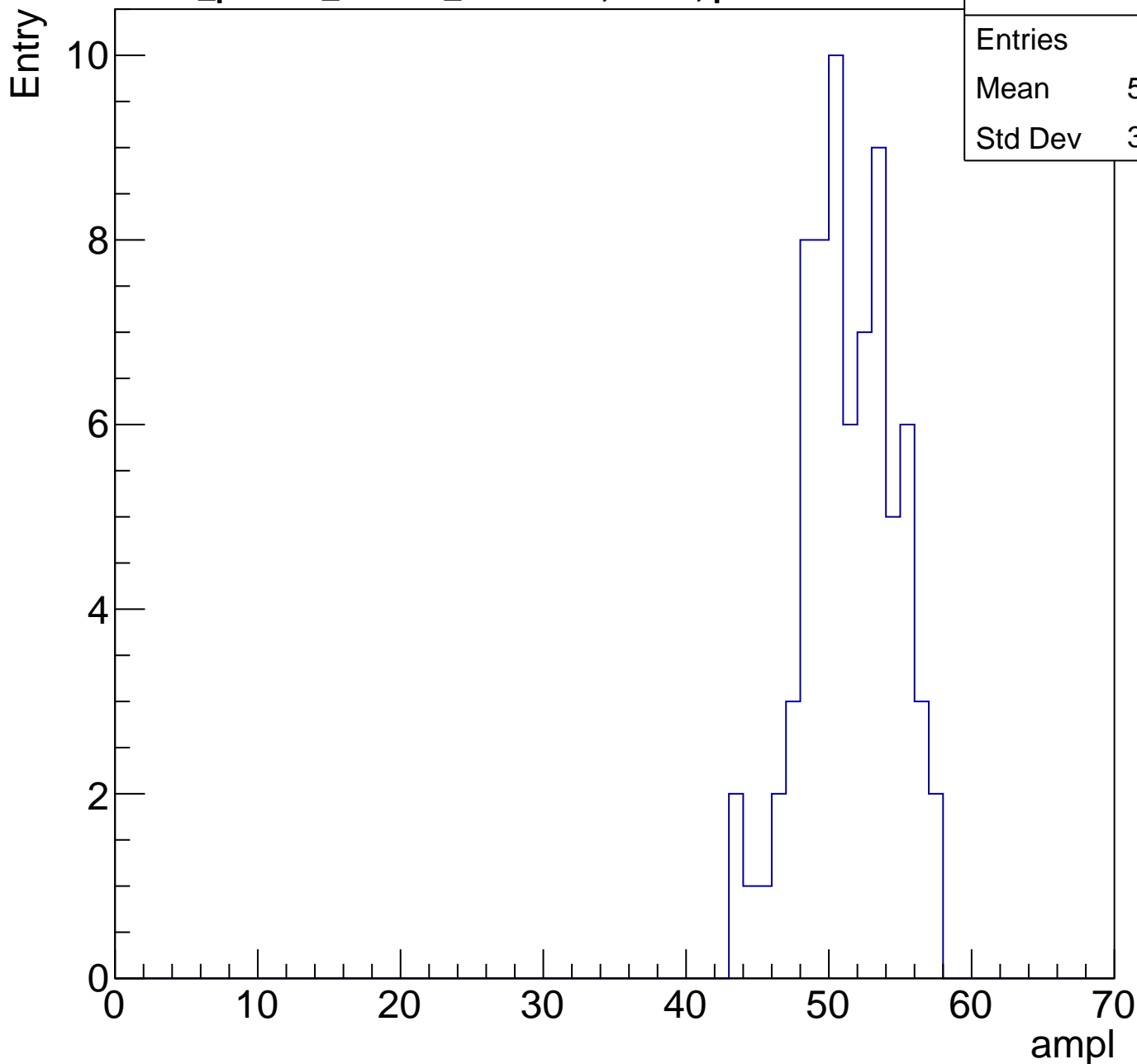
Gaus Width: 3.8284



B1L100S, U13-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	50.86
Std Dev	3.215

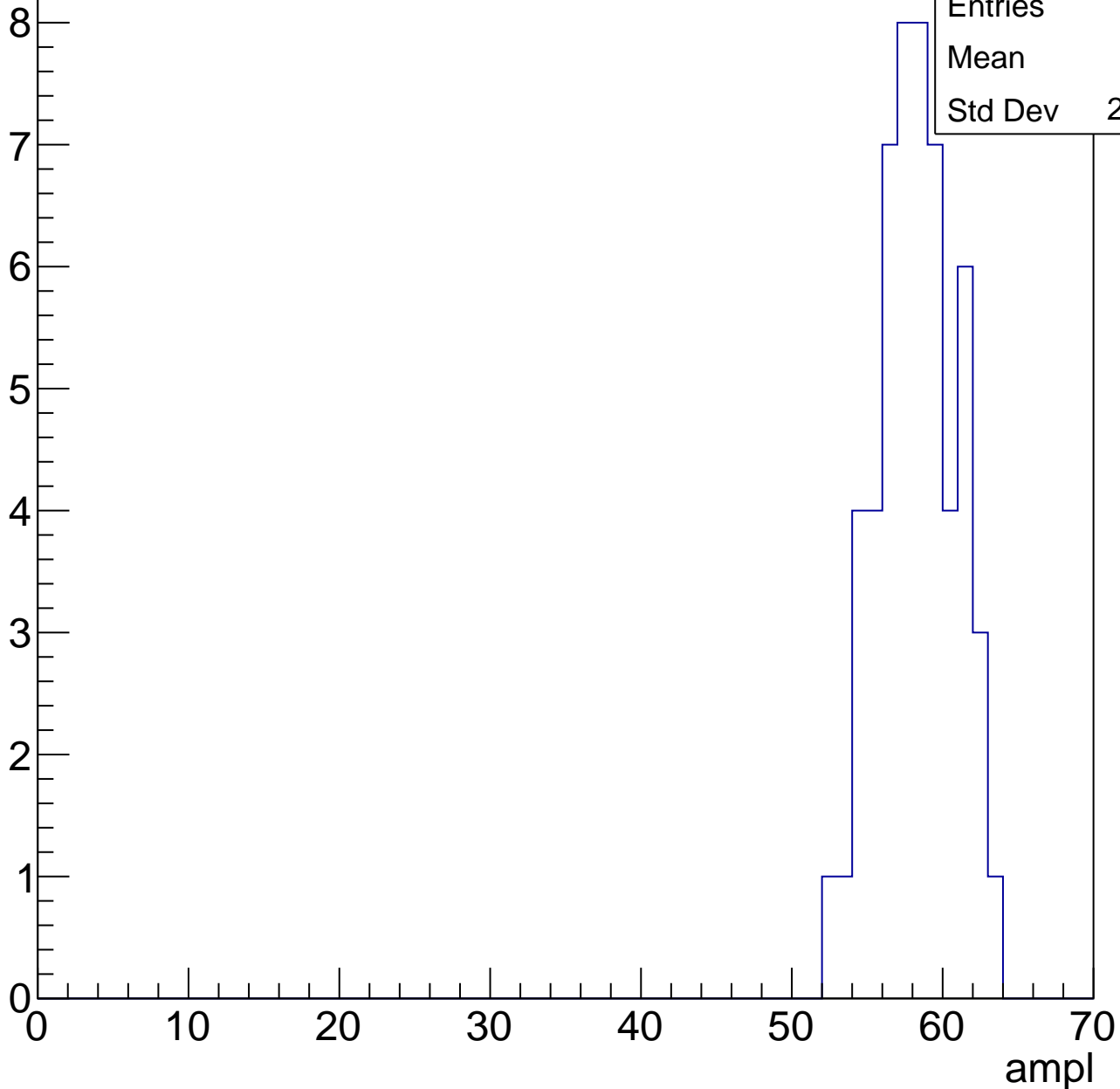


B1L100S, U13-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.8
Std Dev	2.527

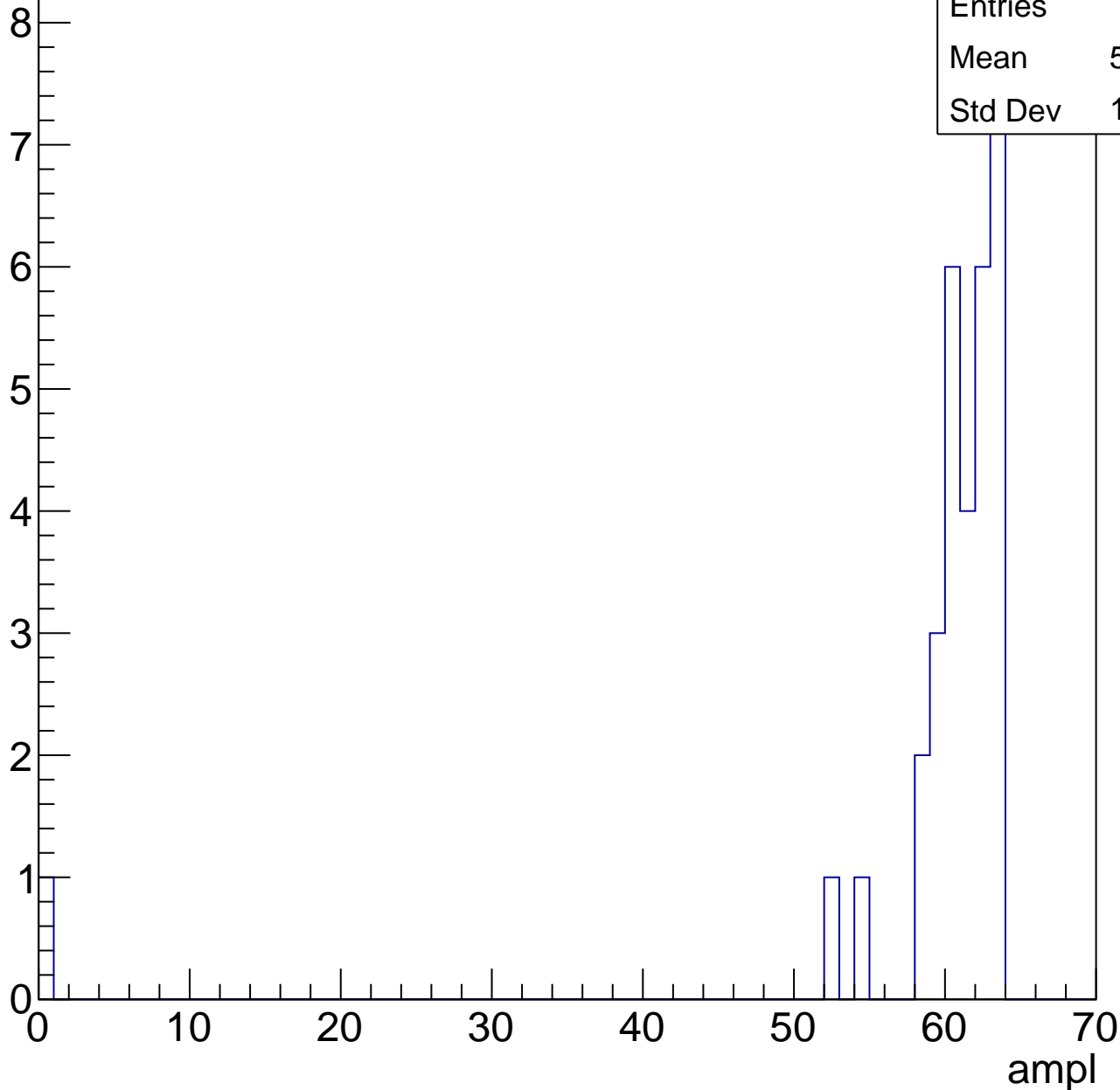


B1L100S, U13-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.72
Std Dev	10.84



B1L100S, U13-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch77, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	29.08
Std Dev	3.467

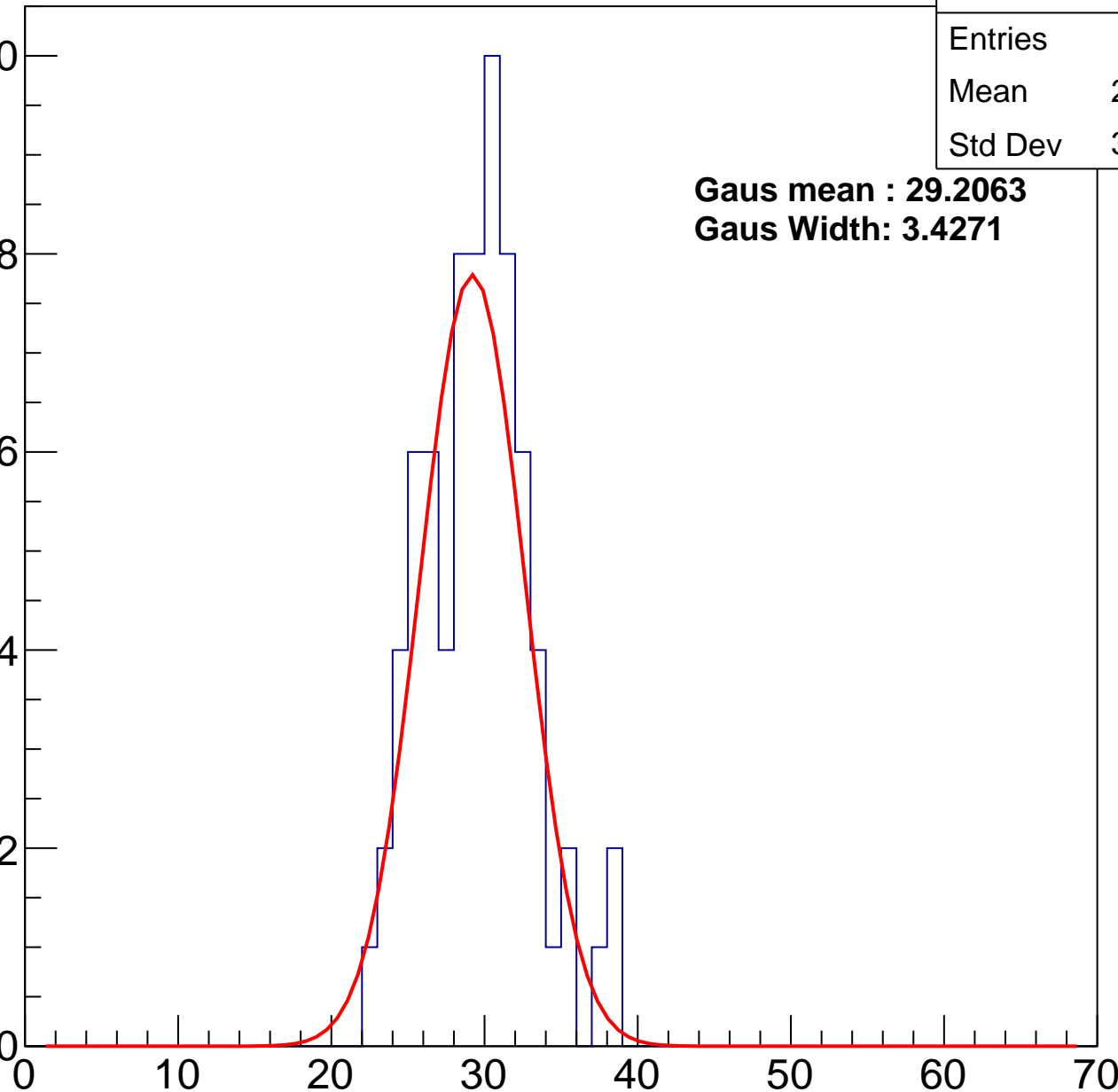
Gaus mean : 29.2063

Gaus Width: 3.4271

Entry

10
8
6
4
2
0

ampl



B1L100S, U13-ch77, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	37.21
Std Dev	3.74

Gaus mean : 37.4719

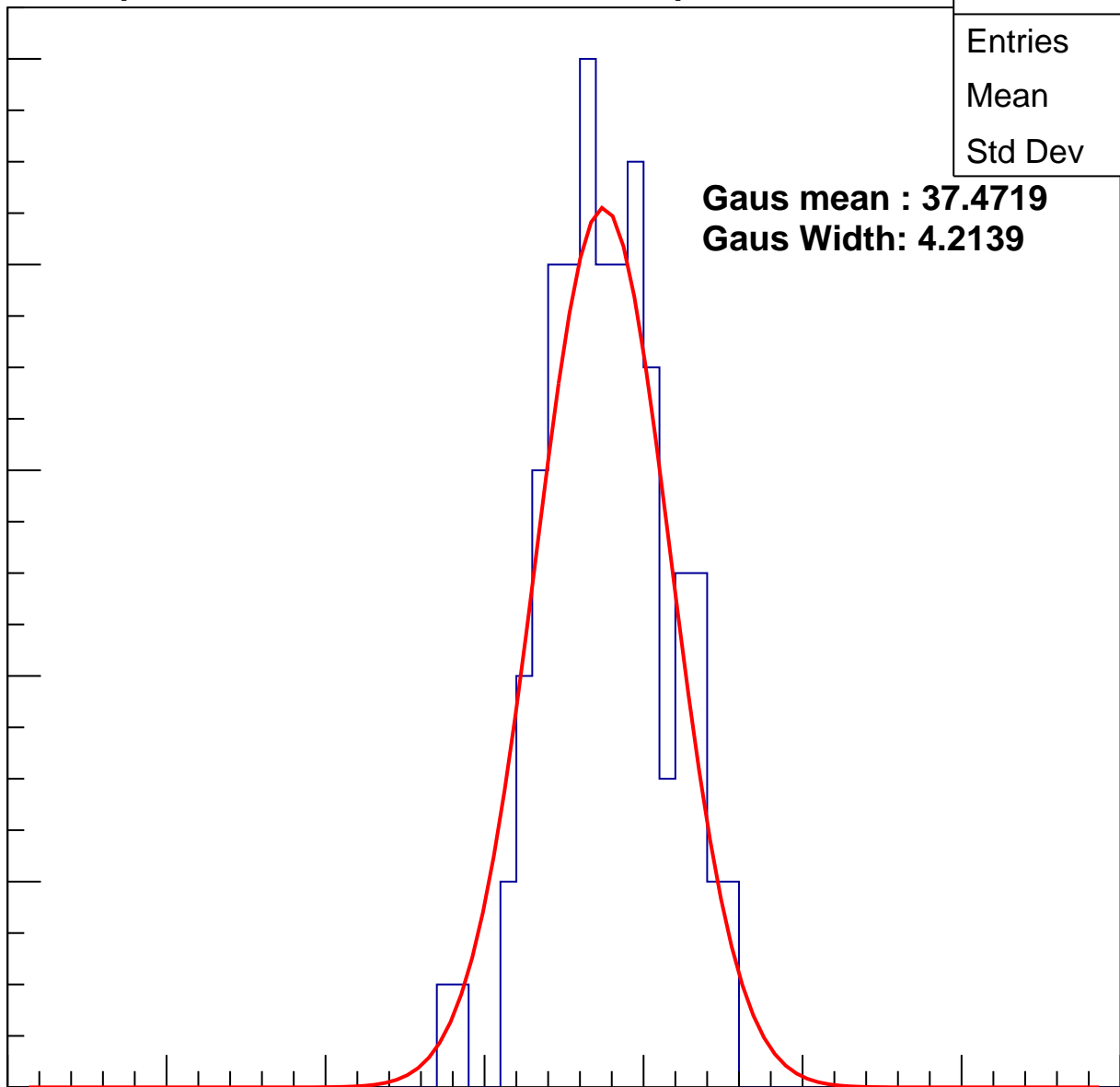
Gaus Width: 4.2139

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch77, adc2

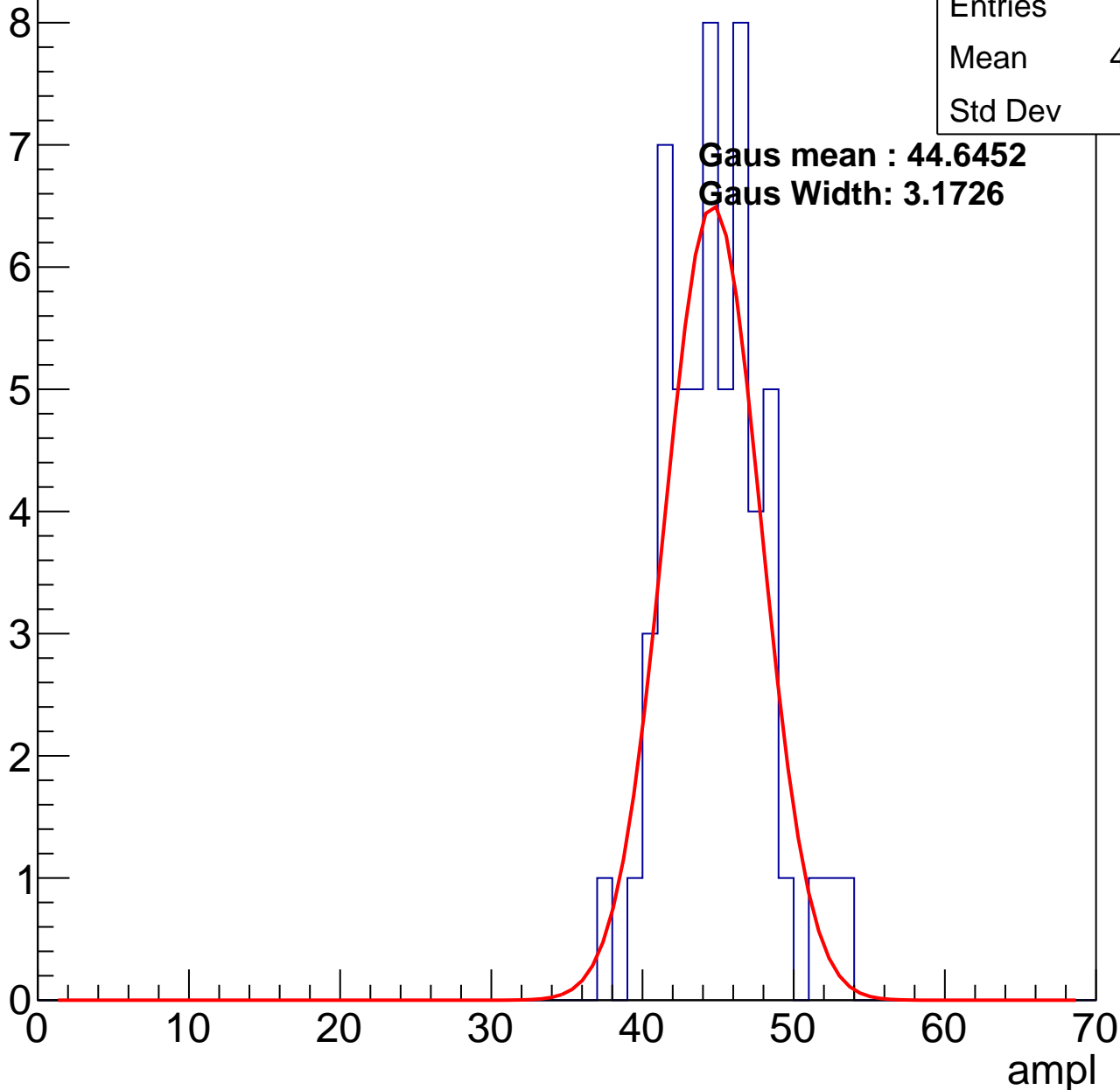
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.39
Std Dev	3.2

Gaus mean : 44.6452

Gaus Width: 3.1726

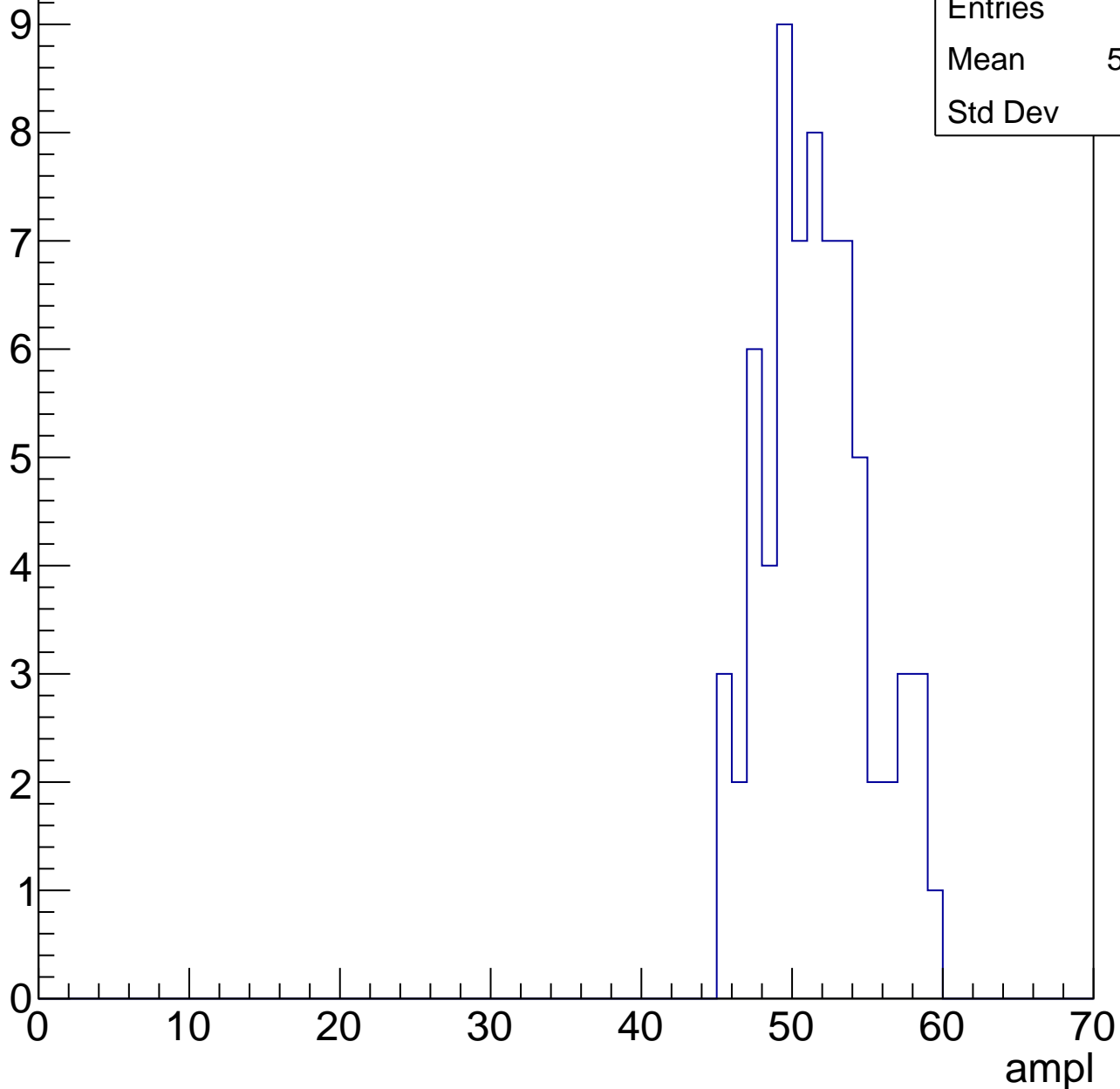


B1L100S, U13-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	51.17
Std Dev	3.43

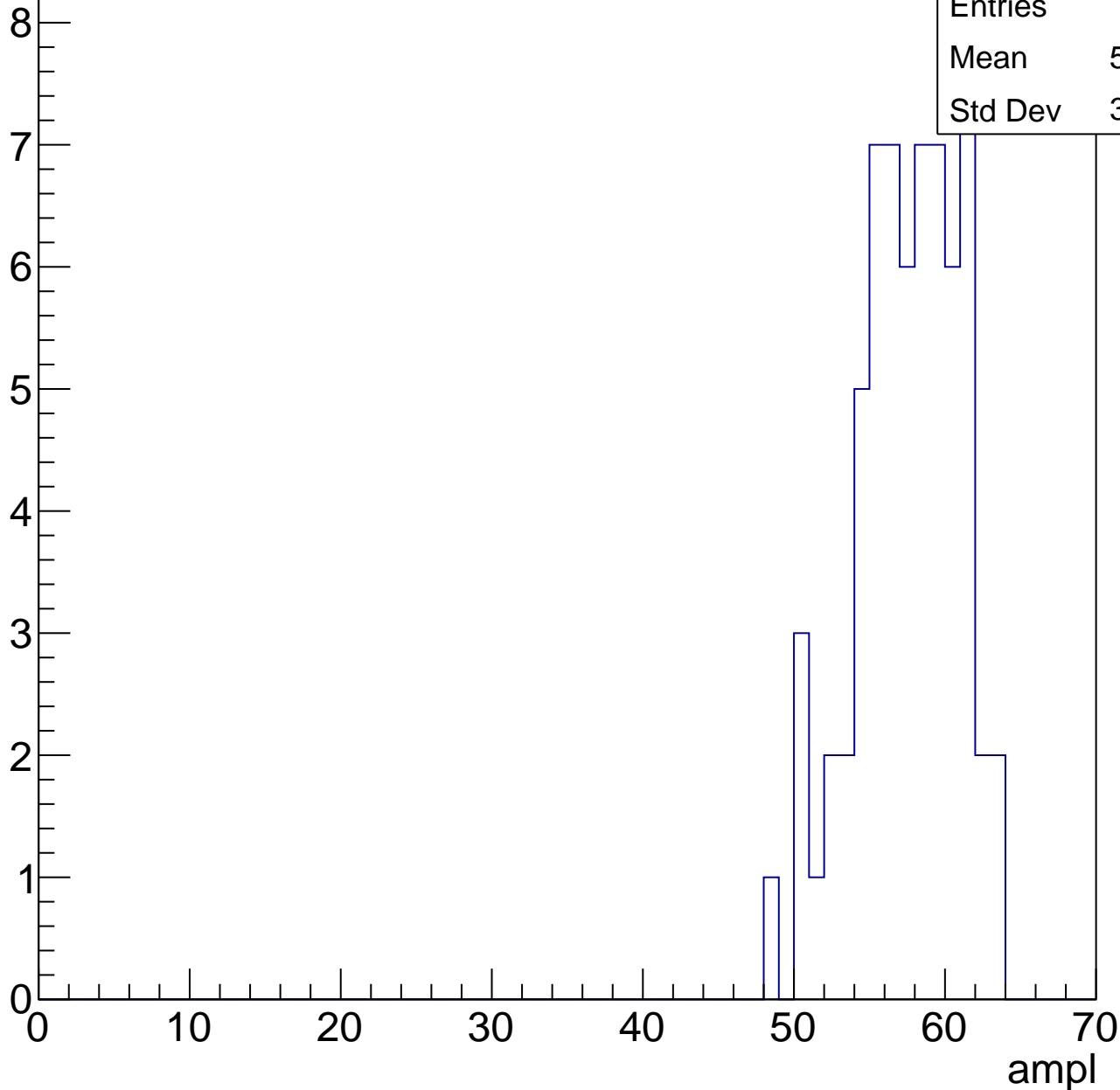


B1L100S, U13-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.05
Std Dev	3.404

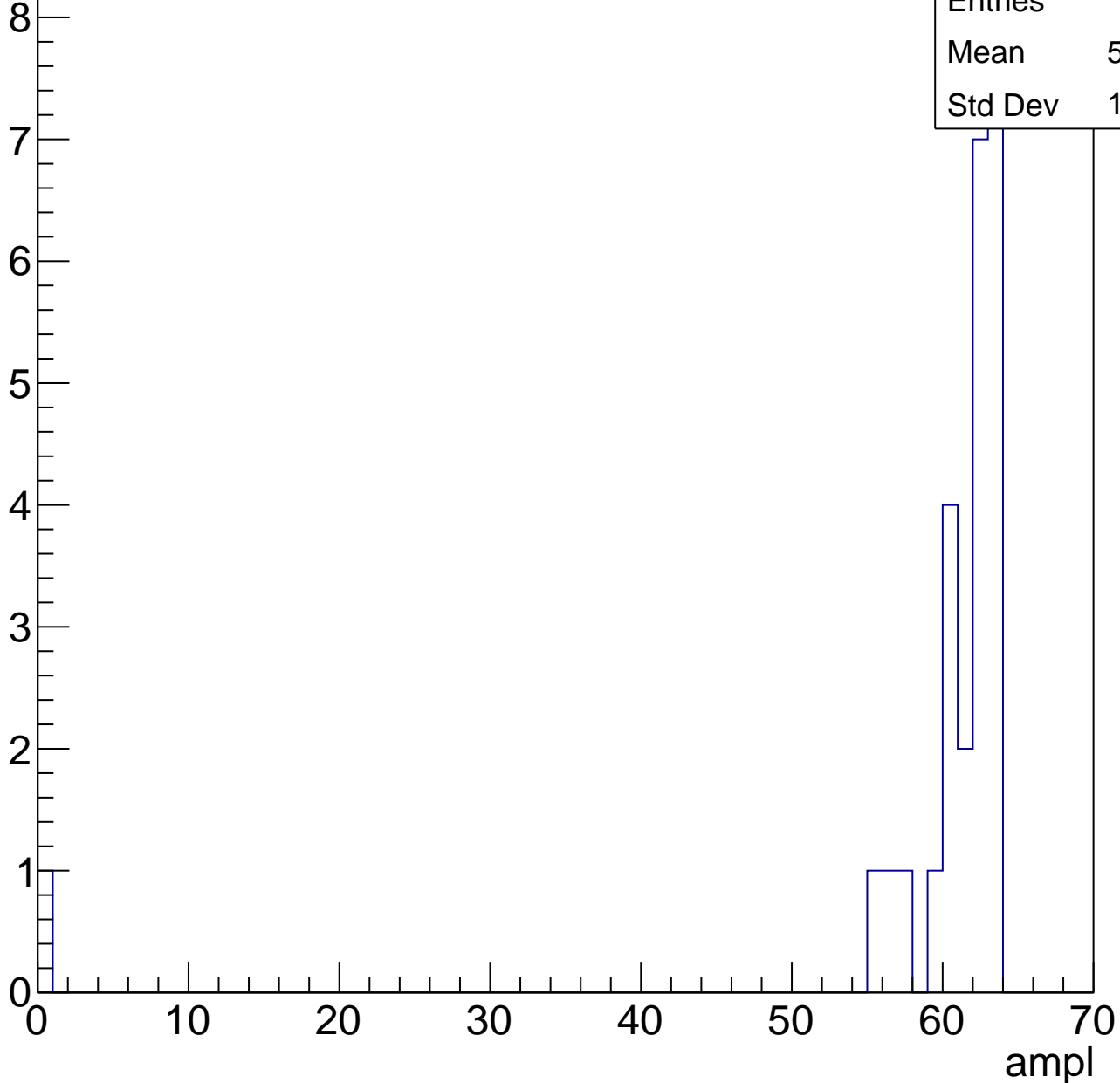


B1L100S, U13-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

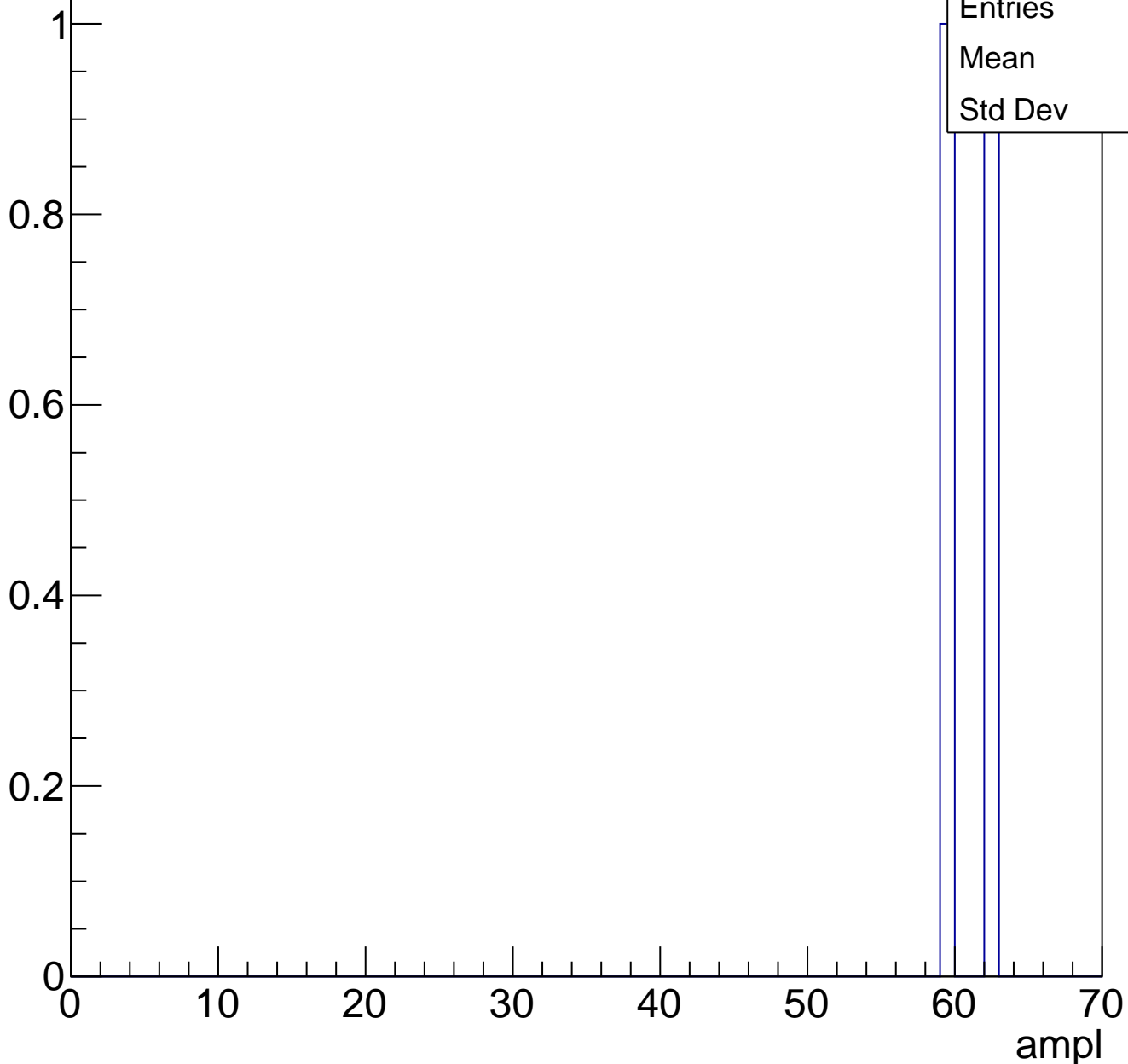
Entries	26
Mean	58.73
Std Dev	11.95



B1L100S, U13-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U13-ch78, adc0

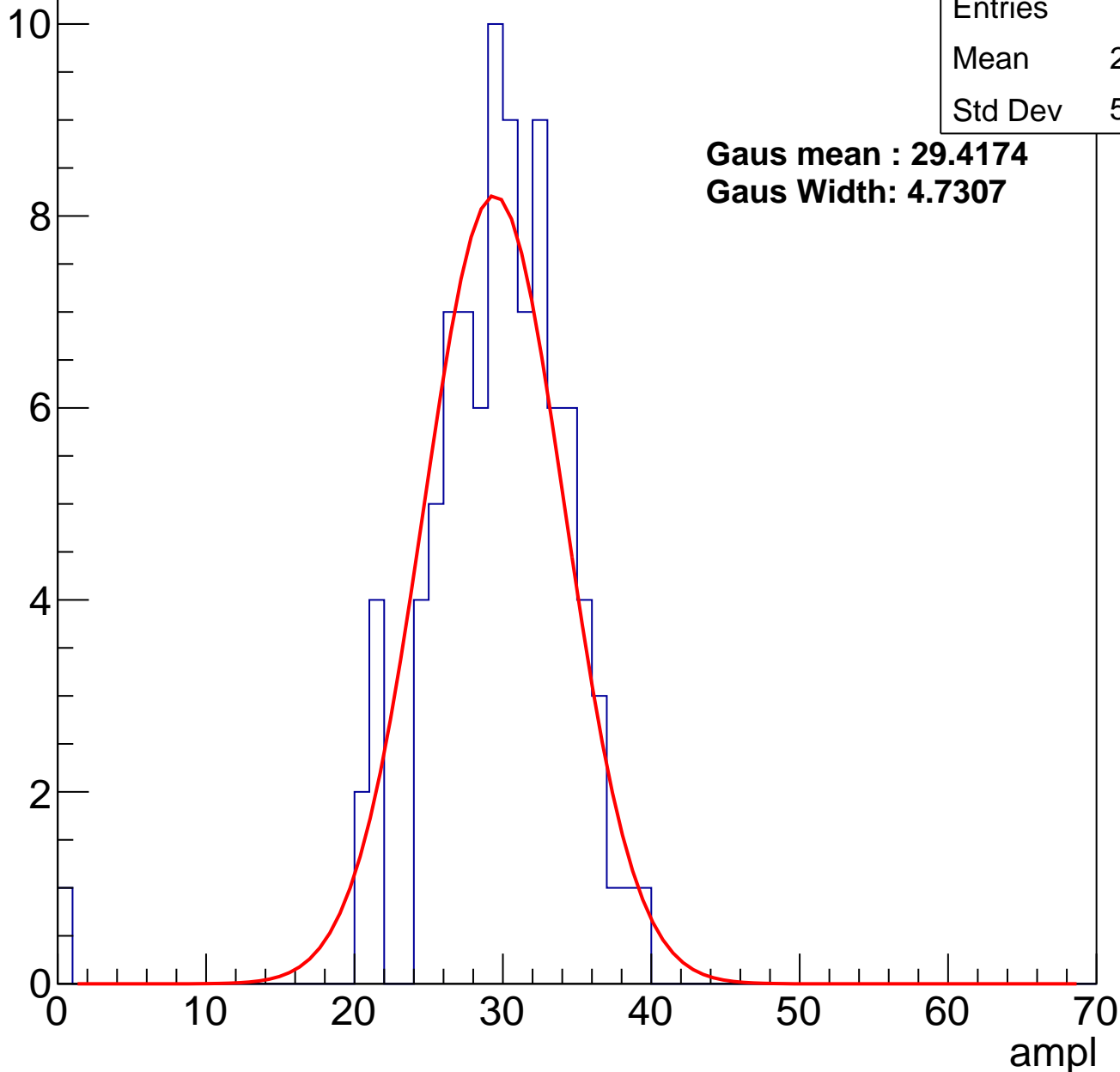
calib_packv5_042523_0143.root, FC#4, port A2

Entries	93
Mean	29.17
Std Dev	5.115

Gaus mean : 29.4174

Gaus Width: 4.7307

Entry



B1L100S, U13-ch78, adc1

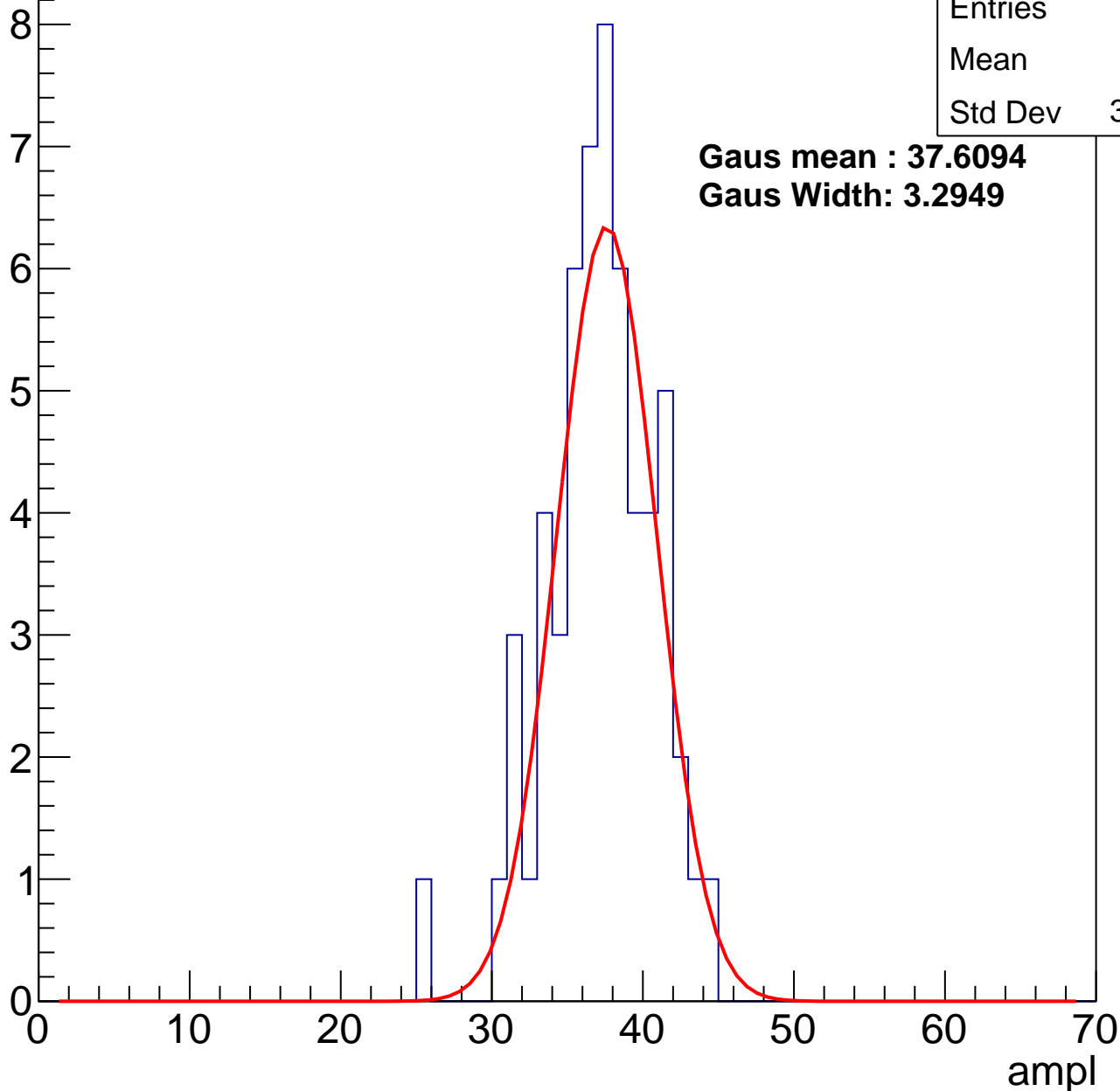
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	36.7
Std Dev	3.544

Gaus mean : 37.6094

Gaus Width: 3.2949



B1L100S, U13-ch78, adc2

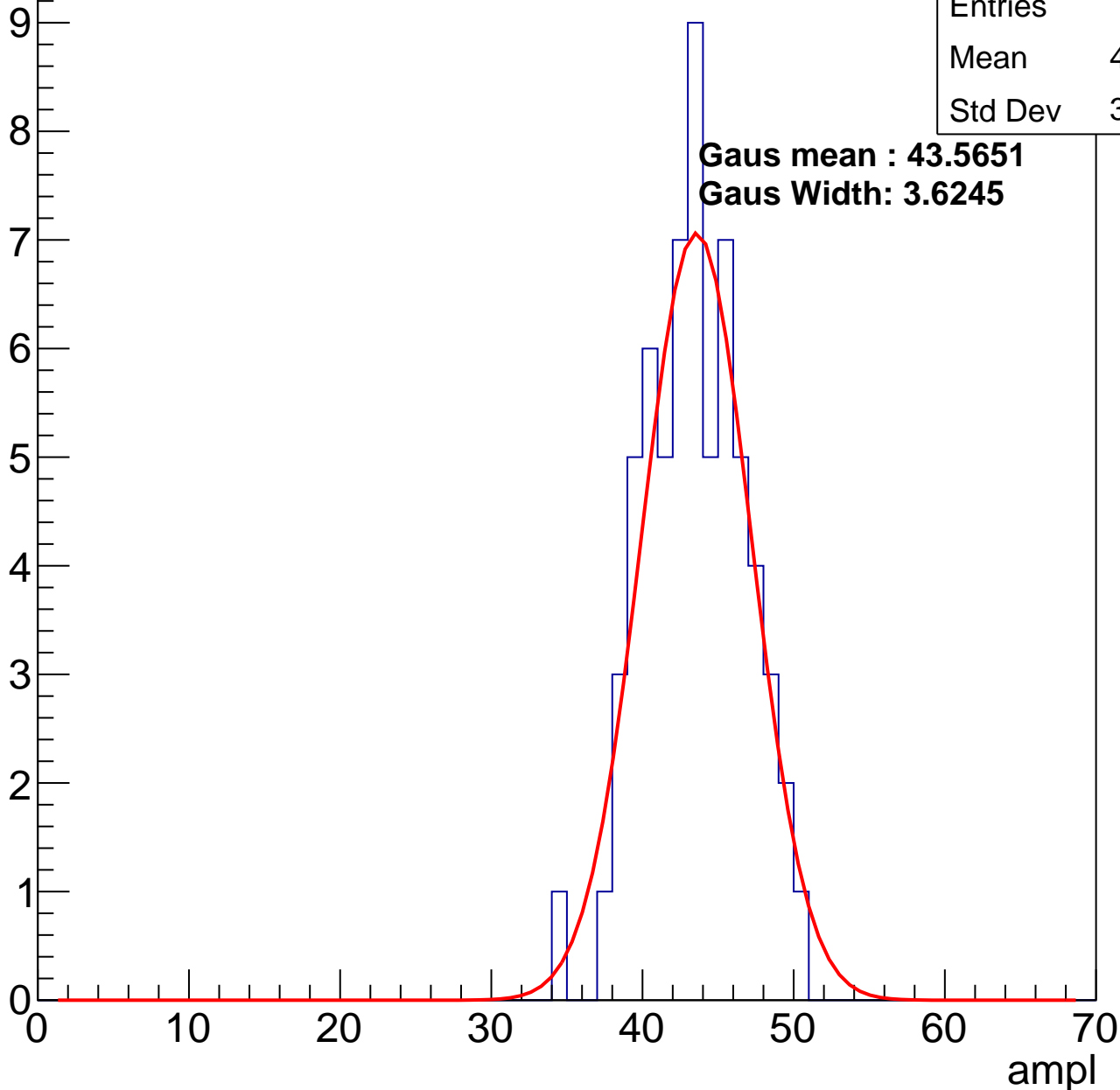
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.98
Std Dev	3.286

Gaus mean : 43.5651

Gaus Width: 3.6245



B1L100S, U13-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	49.95
Std Dev	3.324

Entry

10

8

6

4

2

0

0

10

20

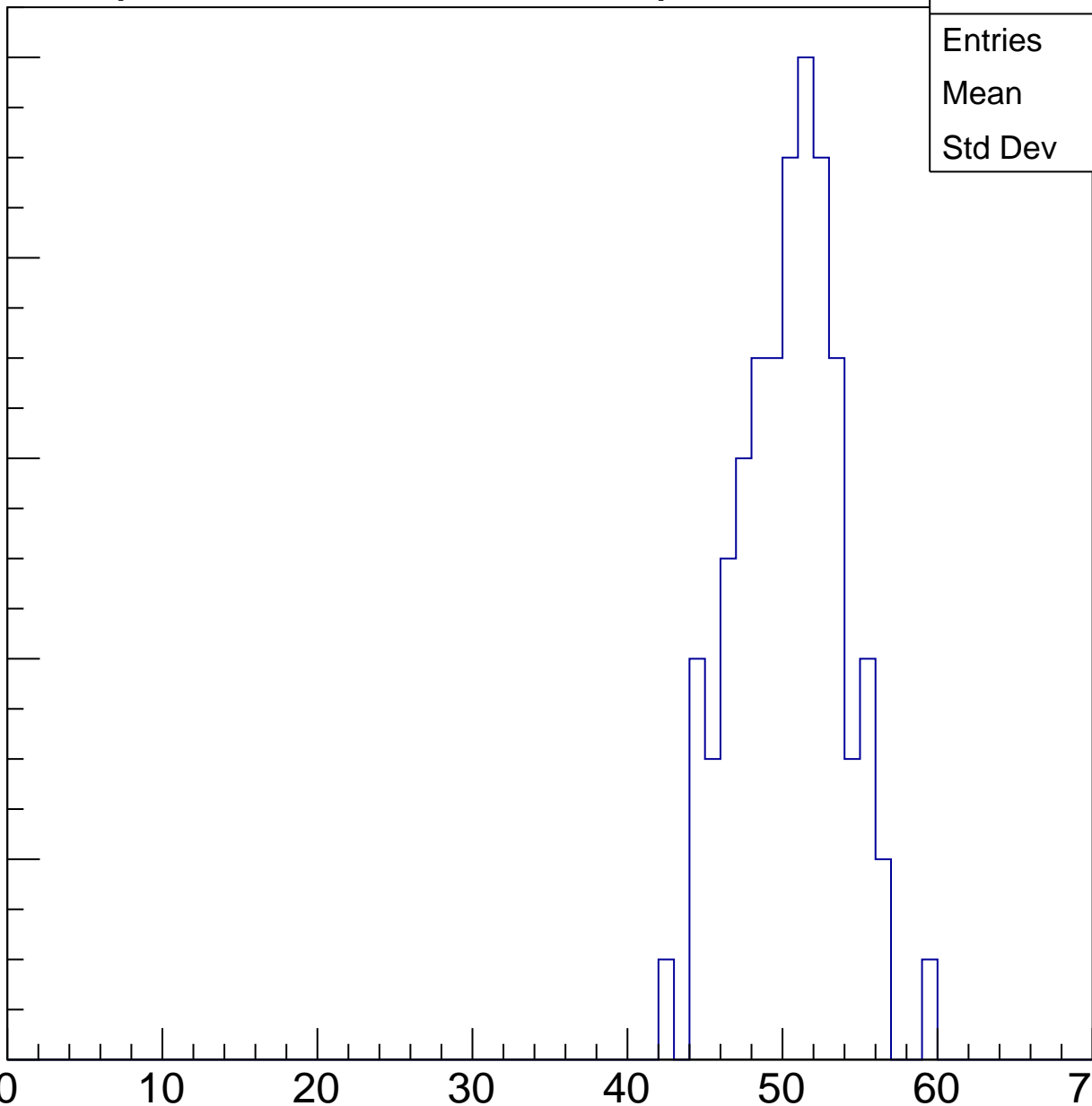
30

40

50

60

ampl

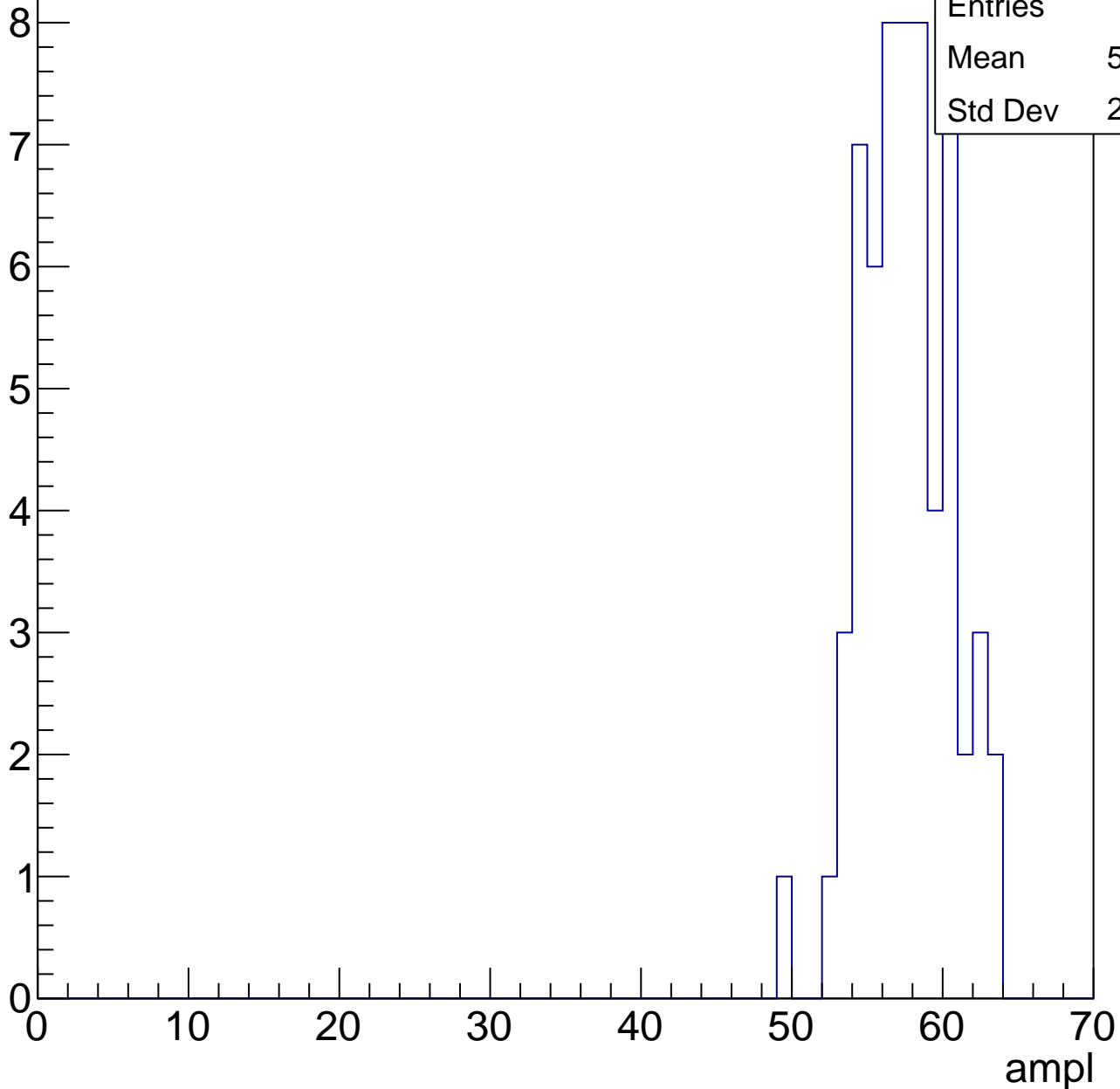


B1L100S, U13-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

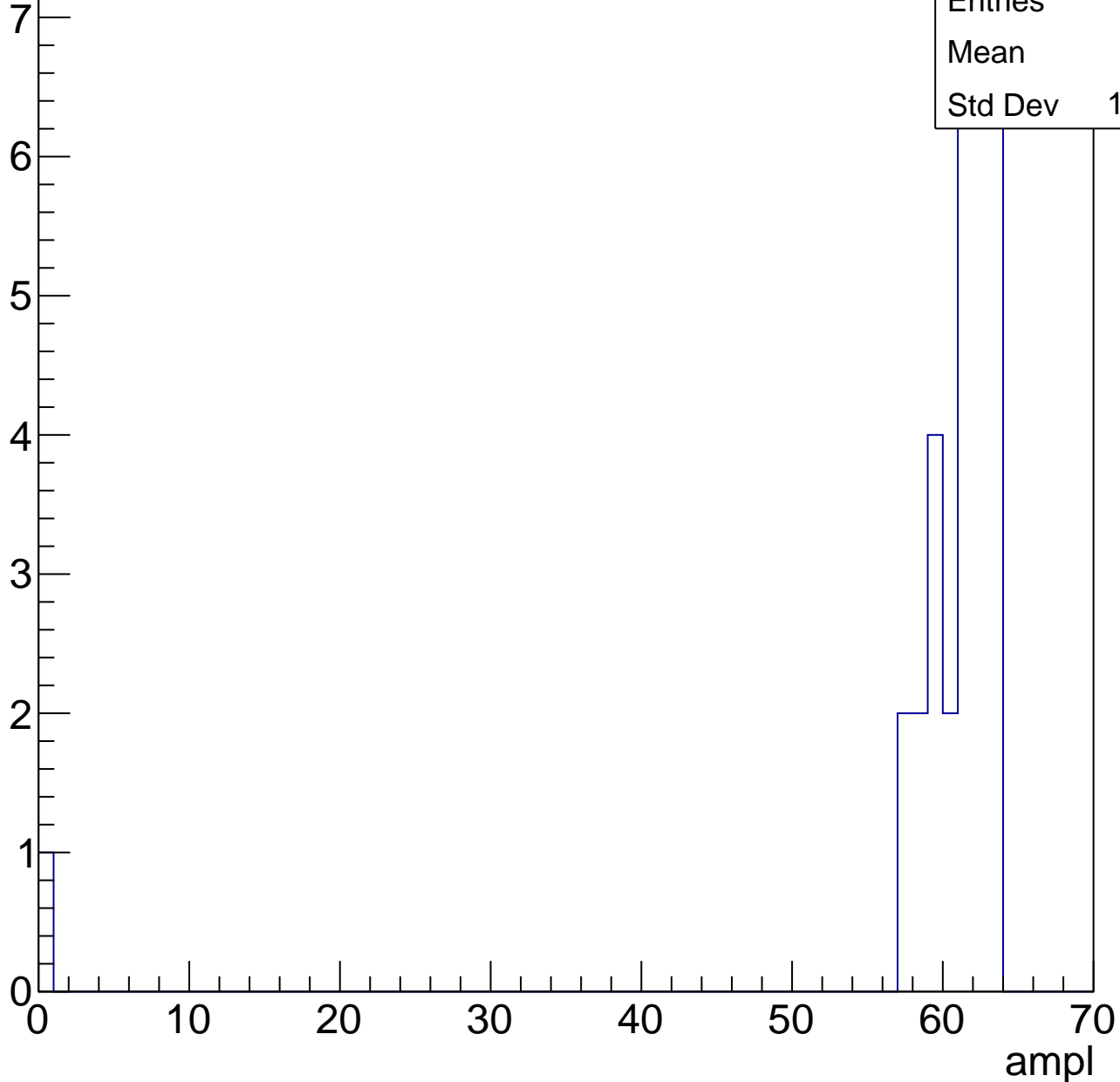
Entries	61
Mean	57.15
Std Dev	2.879



B1L100S, U13-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

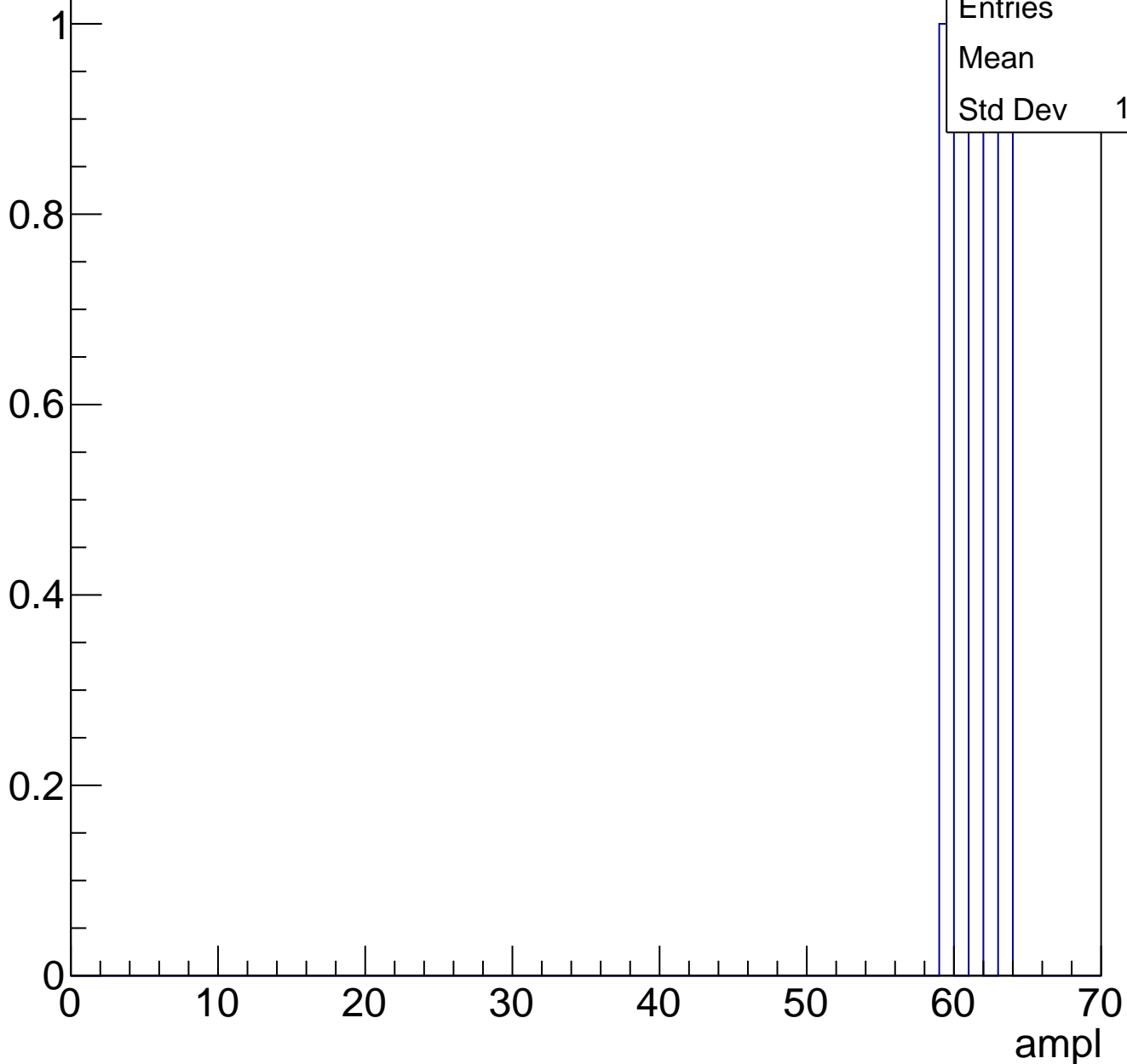
Entry



B1L100S, U13-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch79, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	31.52
Std Dev	3.47

Gaus mean : 31.8827

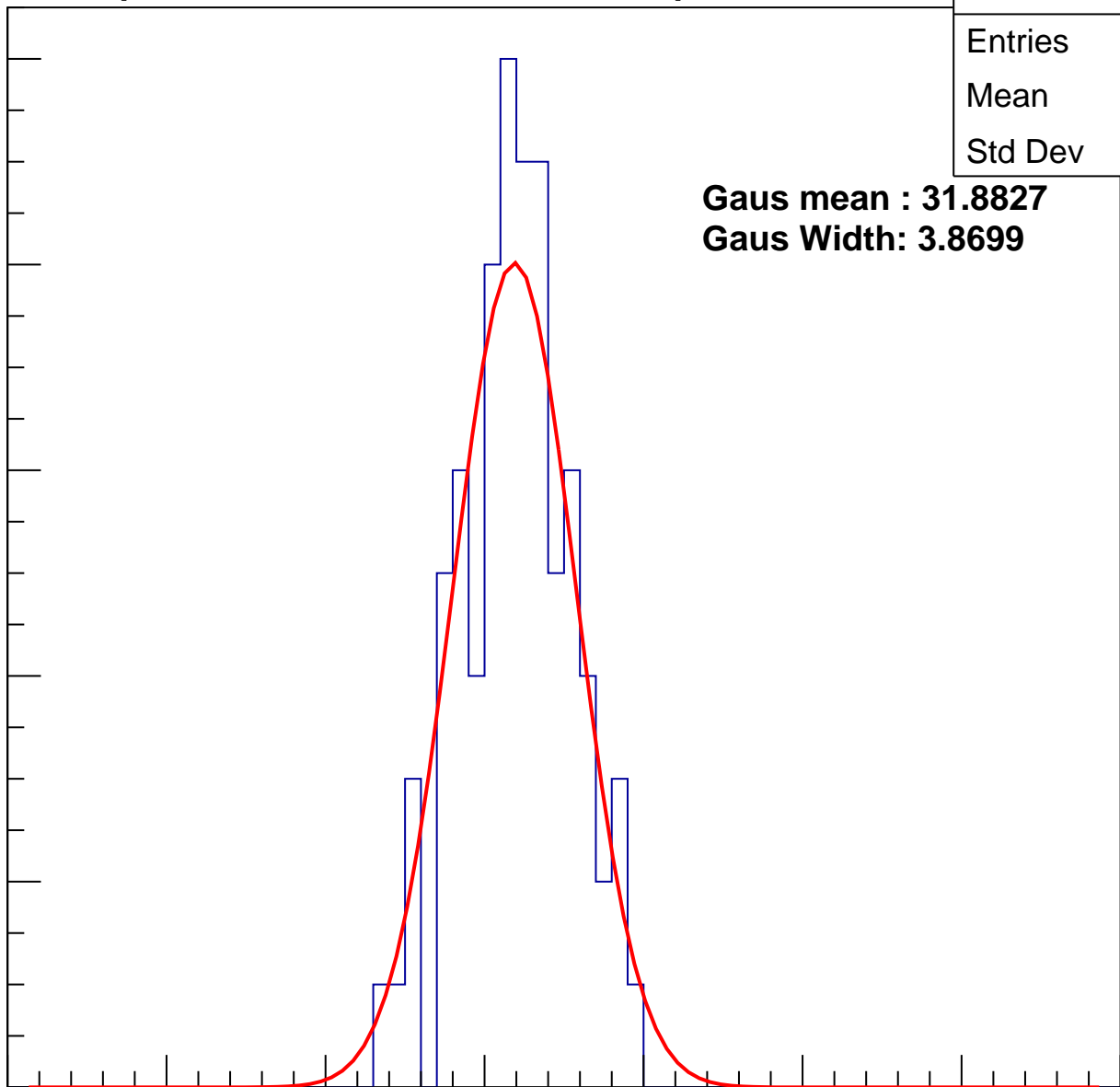
Gaus Width: 3.8699

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



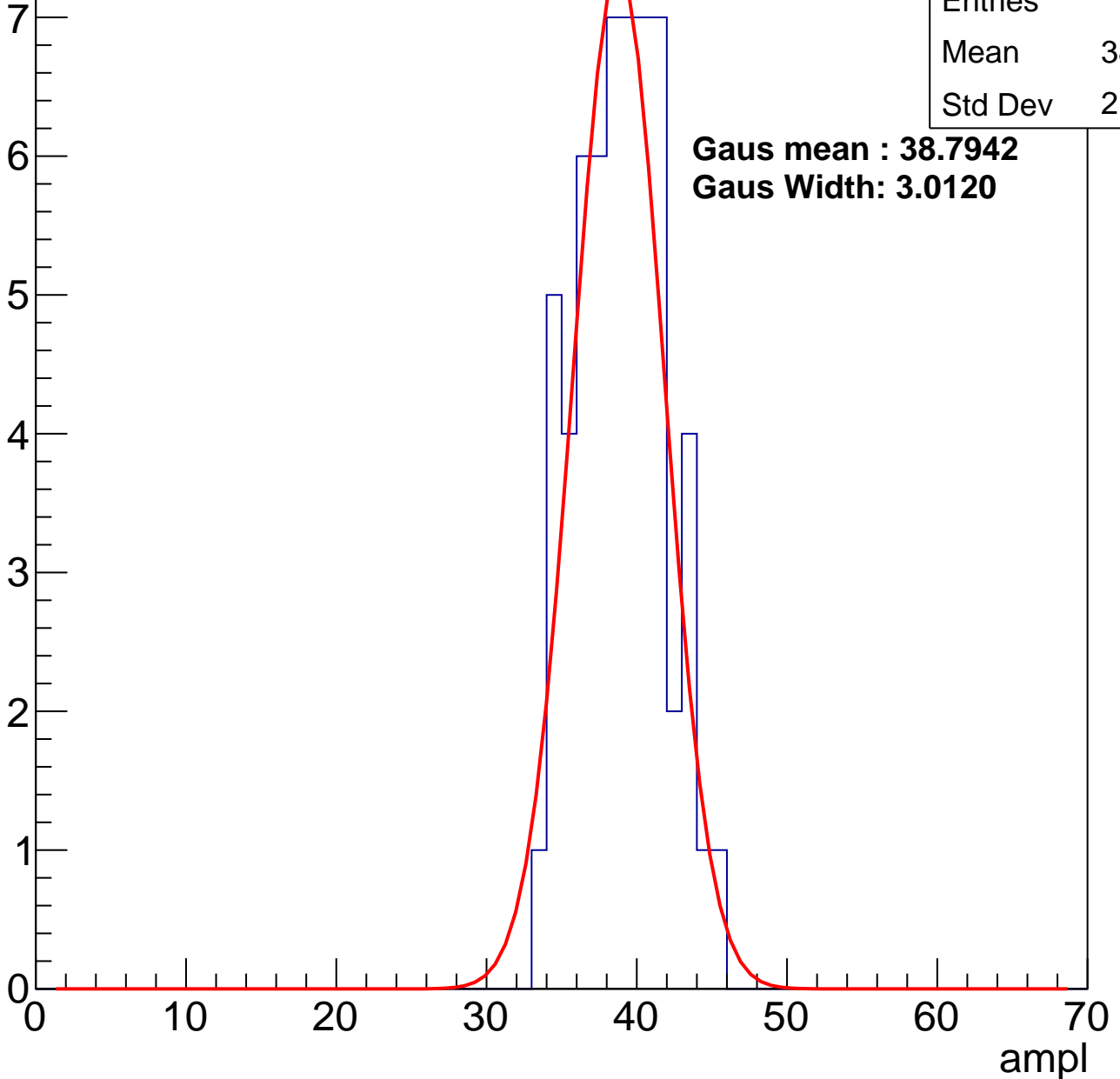
B1L100S, U13-ch79, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.48
Std Dev	2.854

Gaus mean : 38.7942
Gaus Width: 3.0120

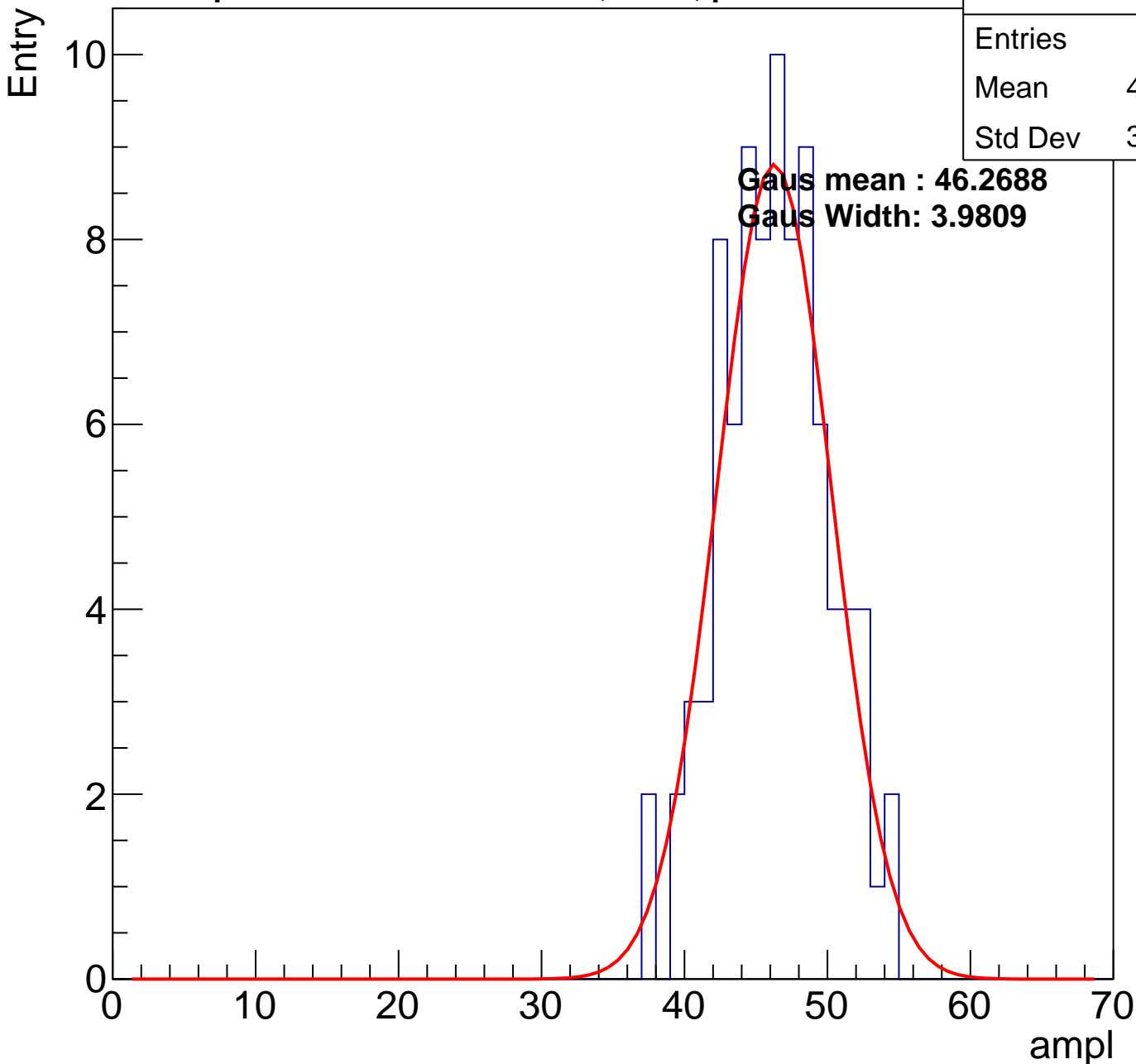


B1L100S, U13-ch79, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	45.84
Std Dev	3.747

Gaus mean : 46.2688
Gaus Width: 3.9809

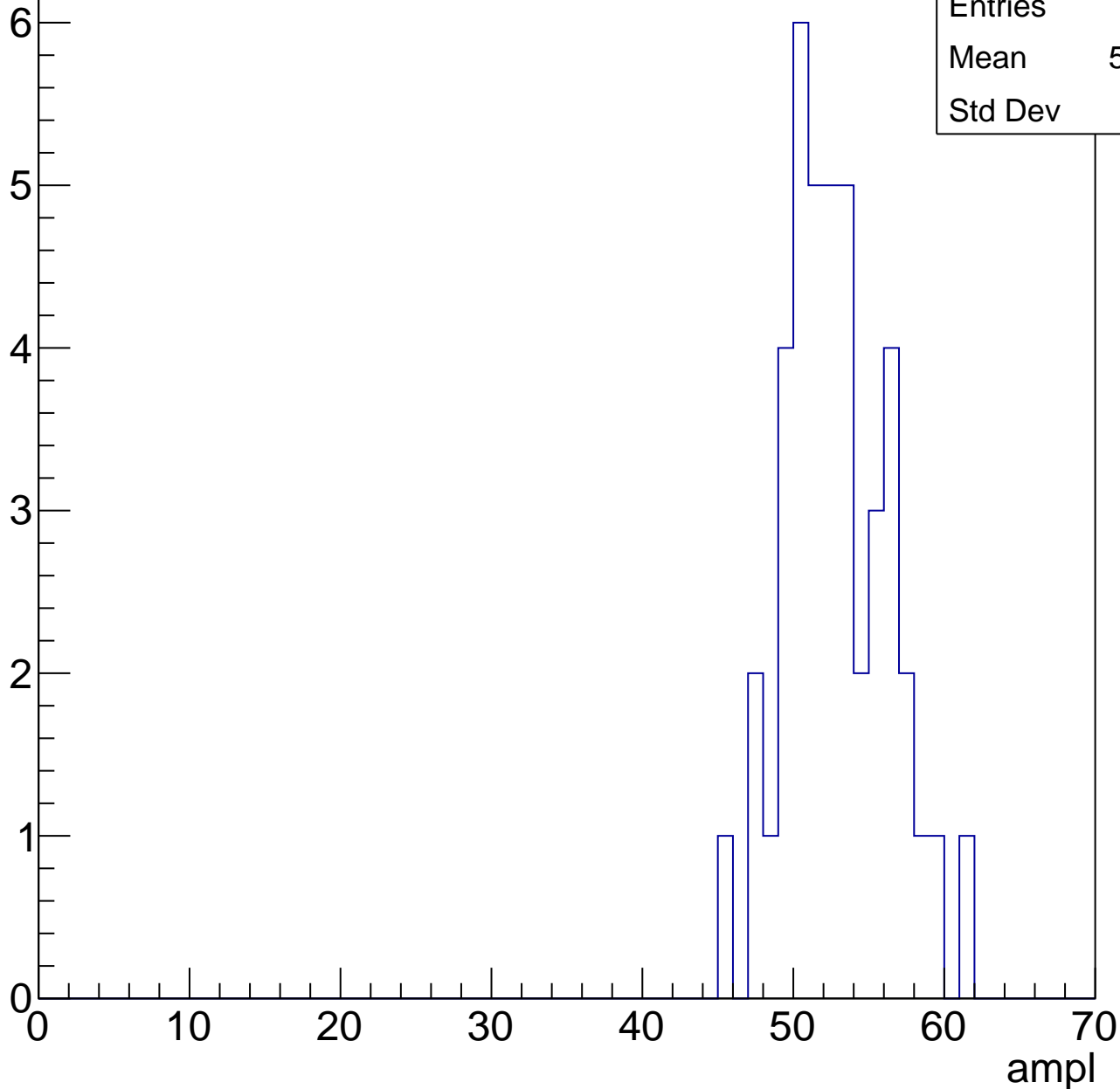


B1L100S, U13-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

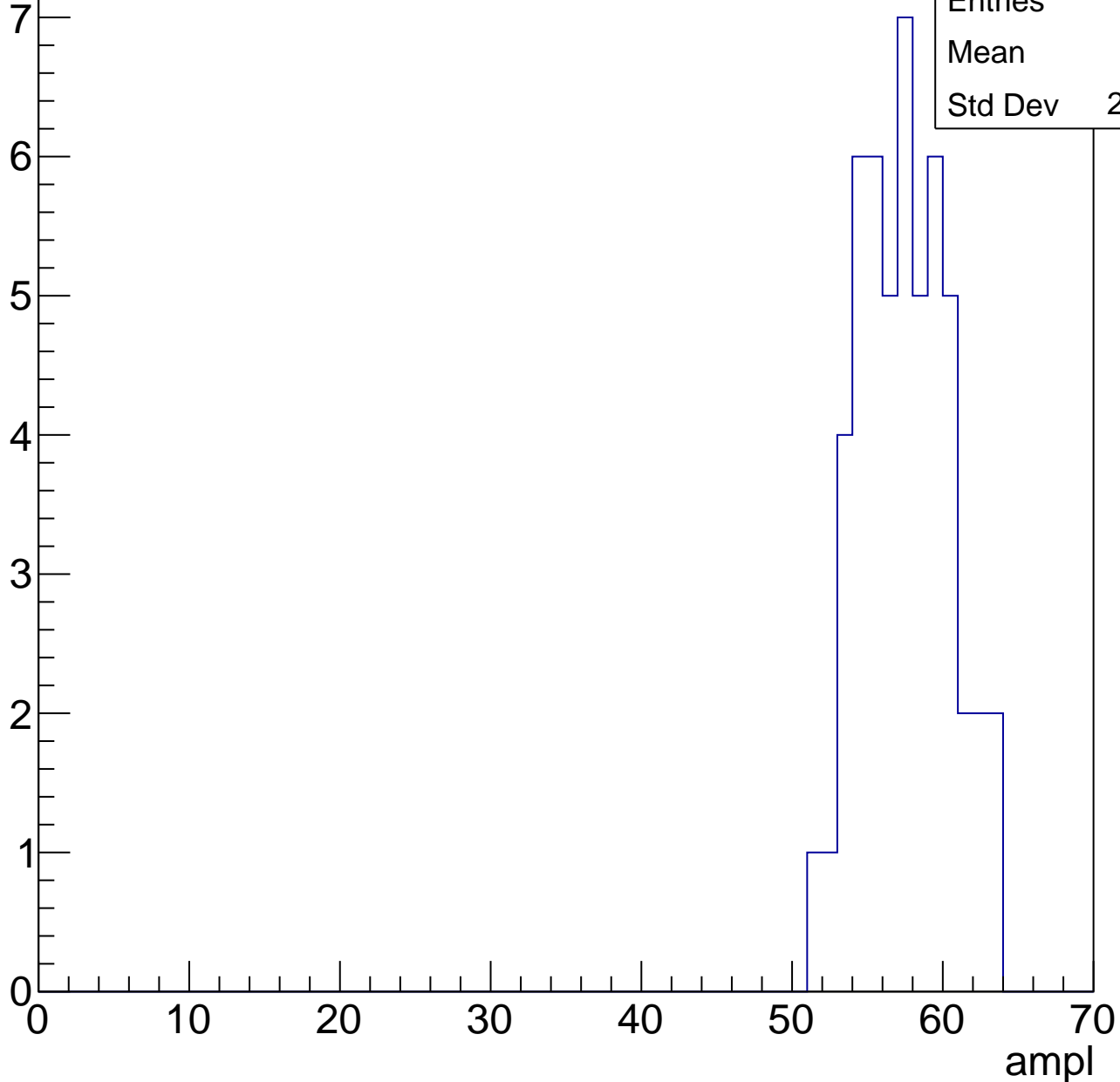
Entries	43
Mean	52.37
Std Dev	3.39



B1L100S, U13-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

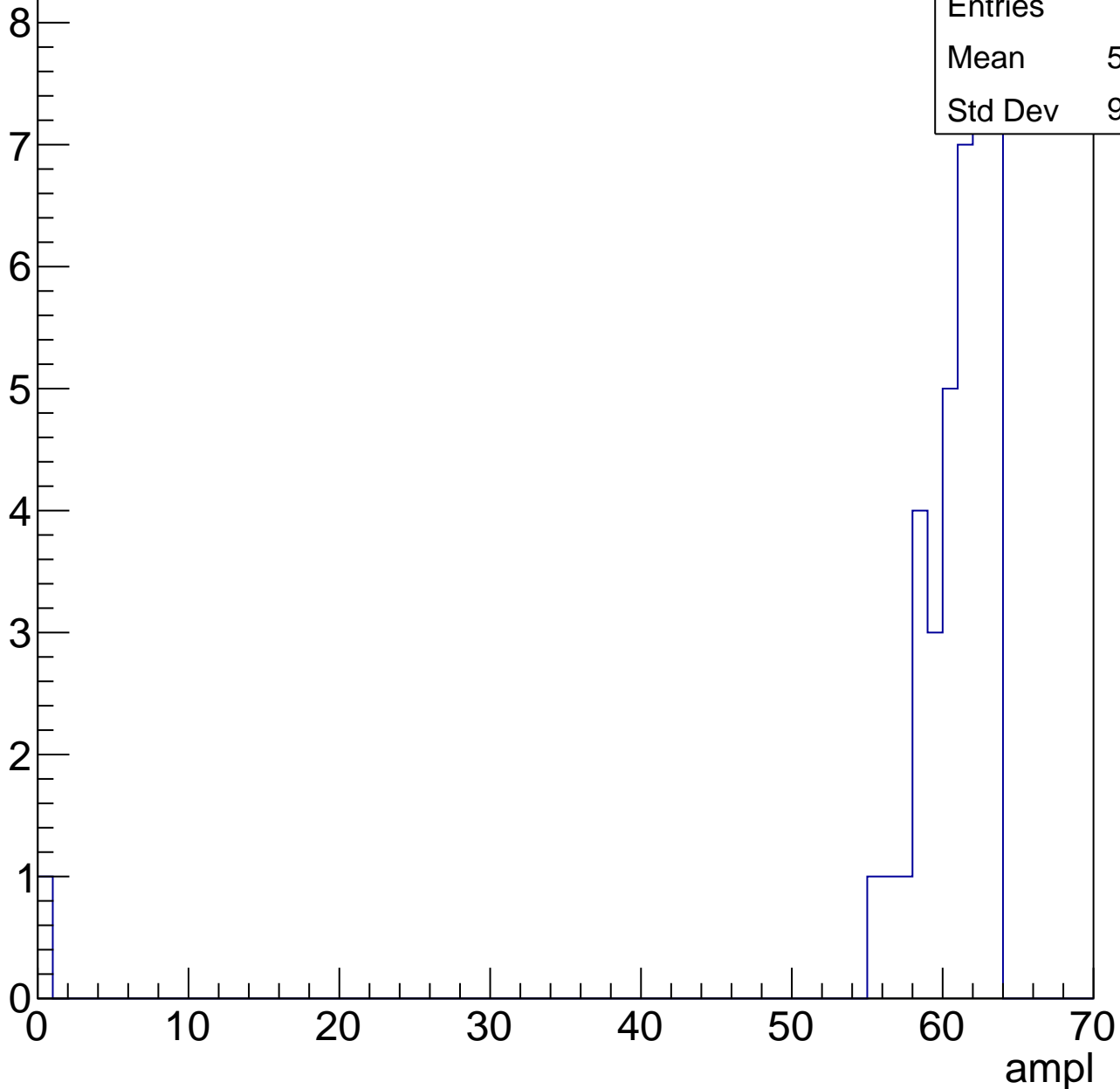


B1L100S, U13-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.08
Std Dev	9.802



B1L100S, U13-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch80, adc0

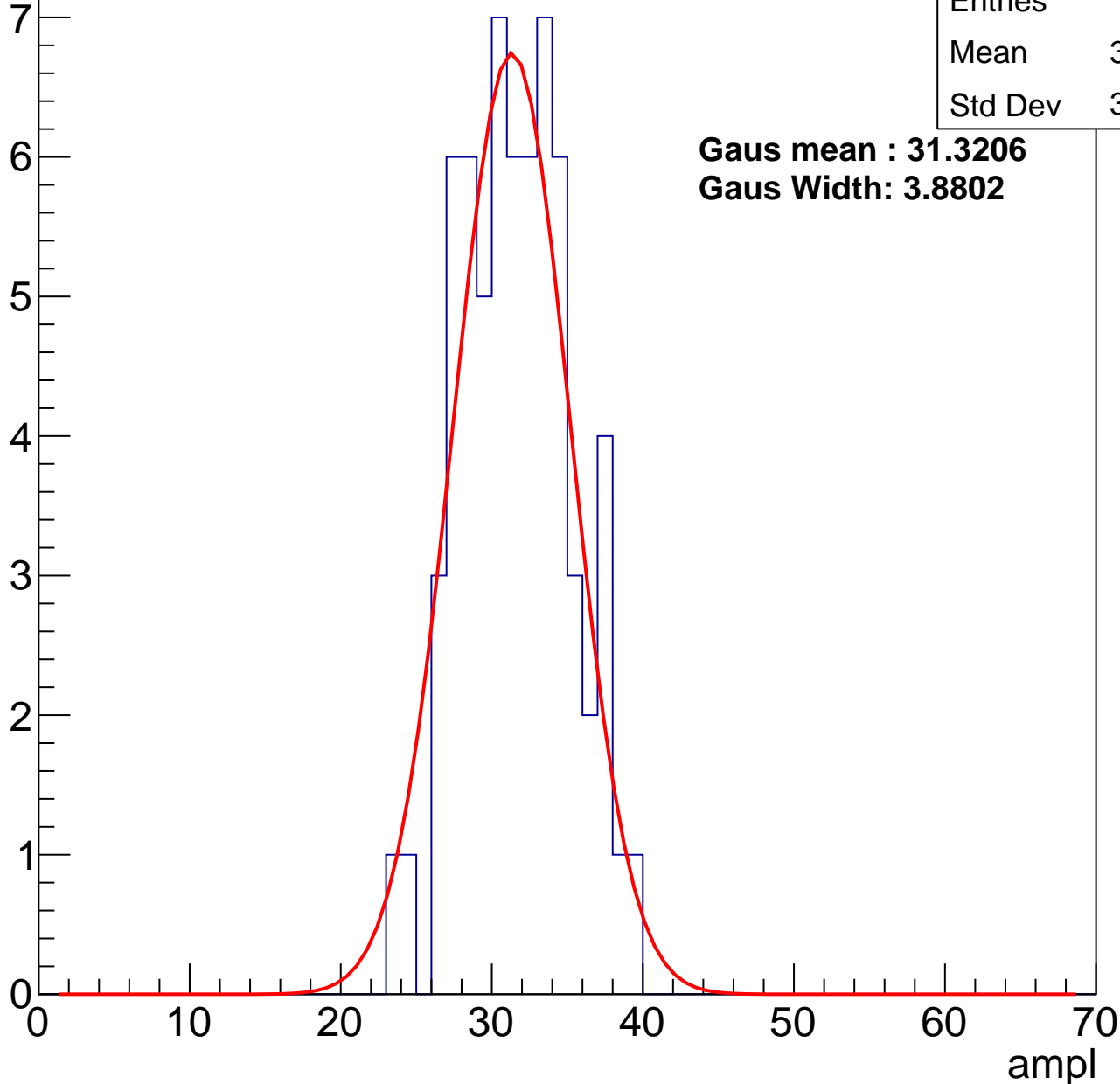
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	31.15
Std Dev	3.522

Gaus mean : 31.3206

Gaus Width: 3.8802



B1L100S, U13-ch80, adc1

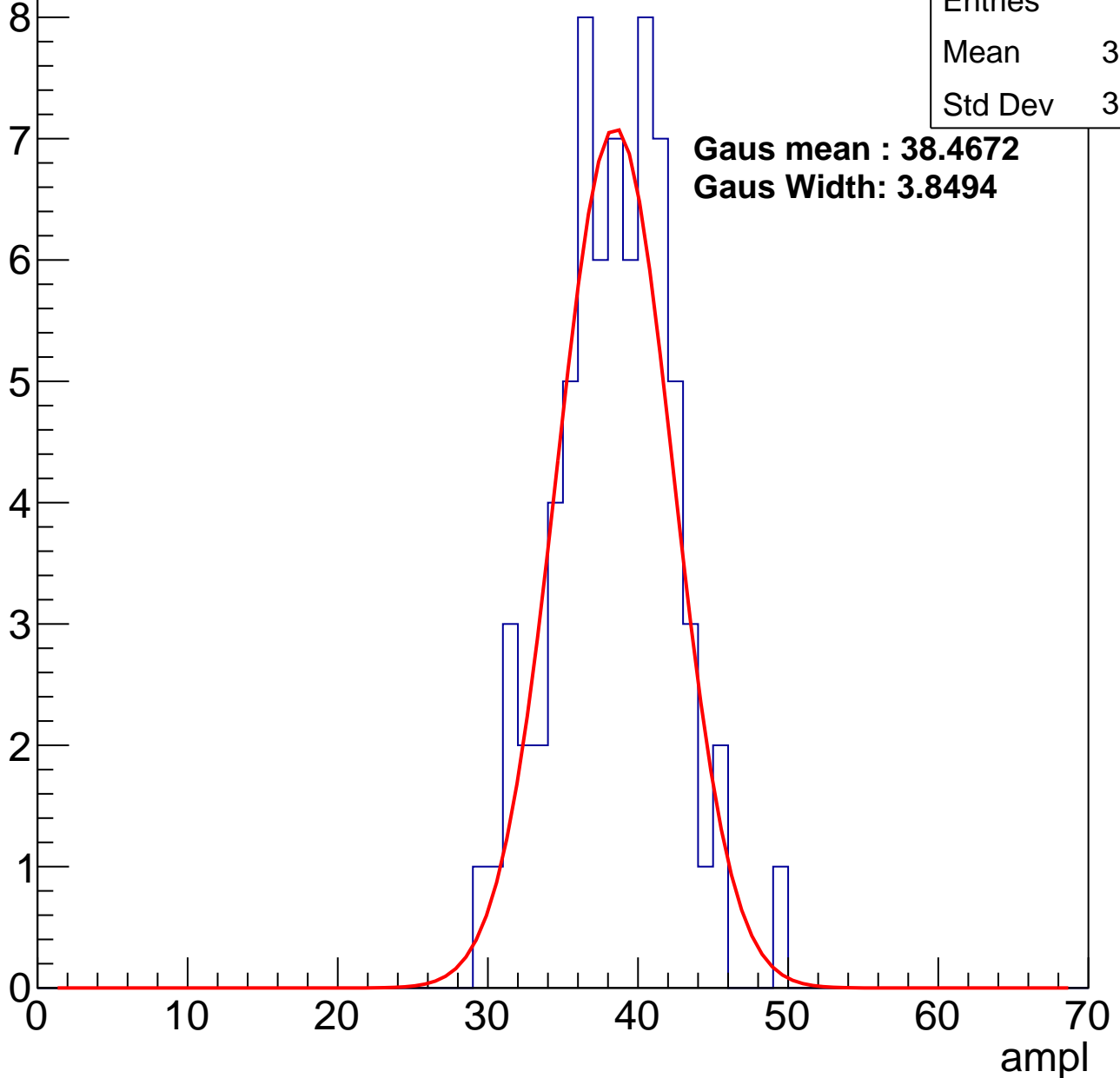
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.94
Std Dev	3.858

Gaus mean : 38.4672

Gaus Width: 3.8494



B1L100S, U13-ch80, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	59
Mean	44.39
Std Dev	3.469

Gaus mean : 44.5583

Gaus Width: 3.4094

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

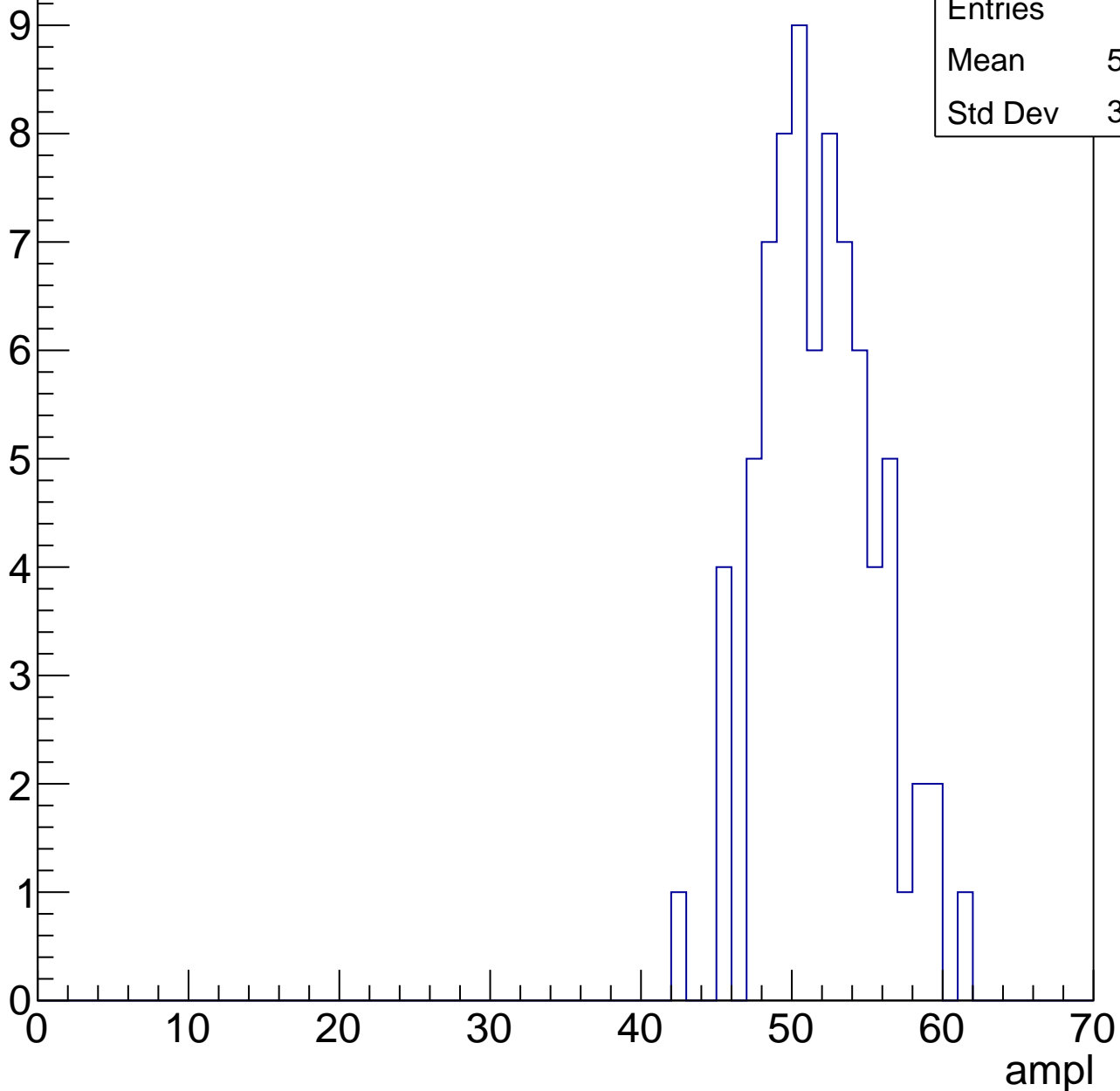
70

B1L100S, U13-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	51.37
Std Dev	3.699



B1L100S, U13-ch80, adc4

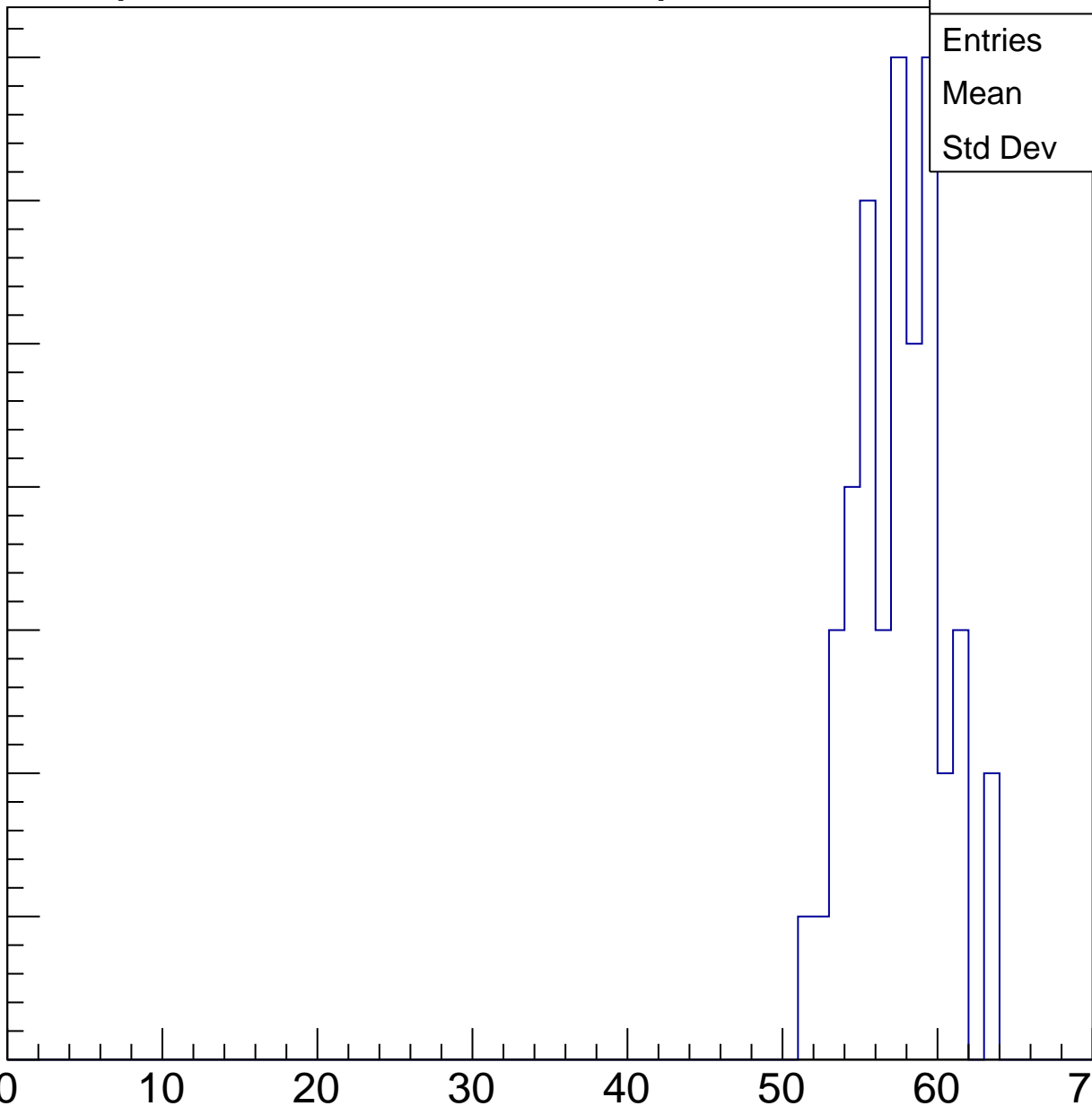
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	56.98
Std Dev	2.792

ampl



B1L100S, U13-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	40
Mean	59.23
Std Dev	9.691

Entry

10

8

6

4

2

0

0

10

20

30

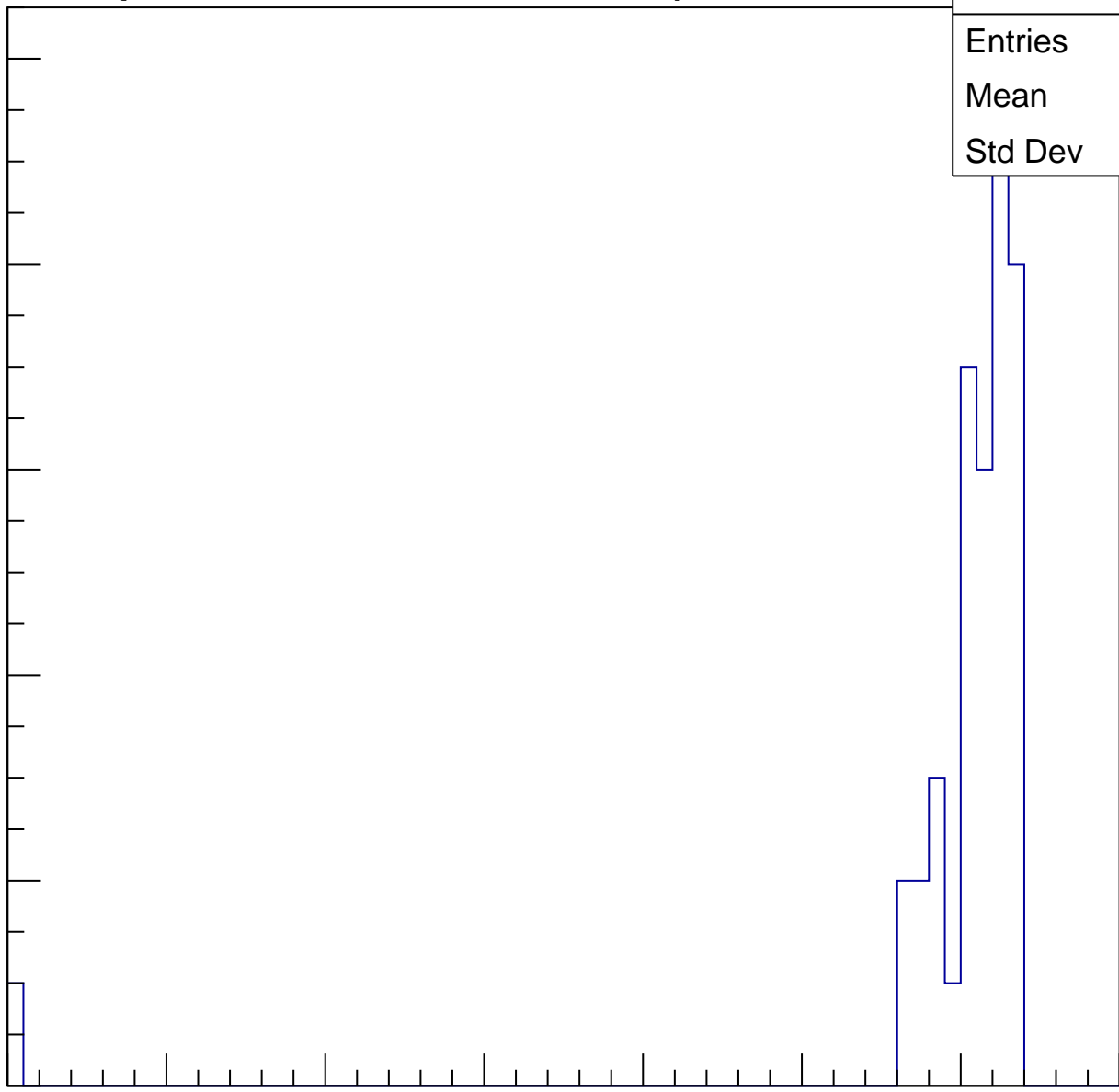
40

50

60

70

ampl



B1L100S, U13-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch81, adc0

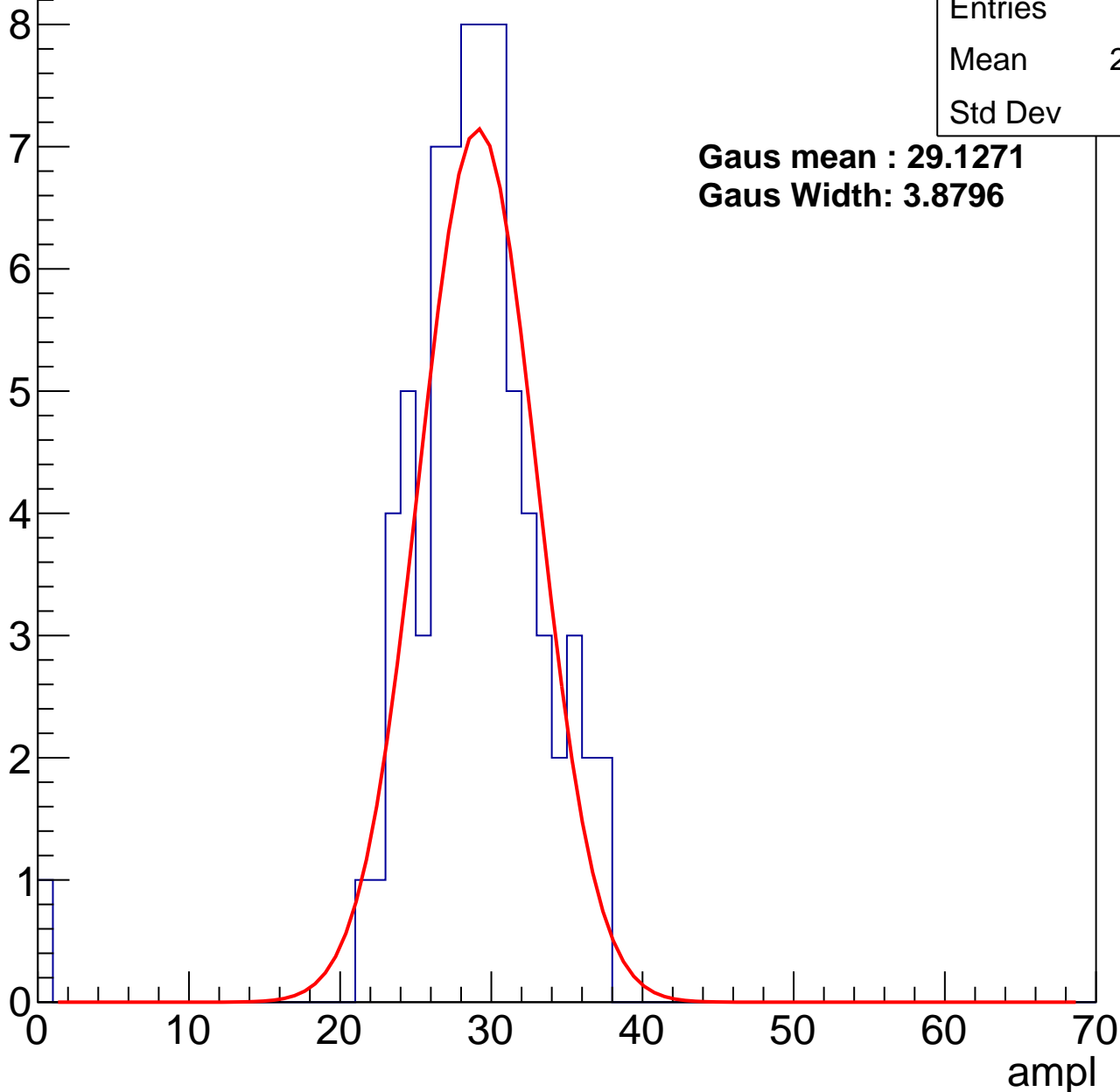
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.35
Std Dev	4.98

Gaus mean : 29.1271

Gaus Width: 3.8796



B1L100S, U13-ch81, adc1

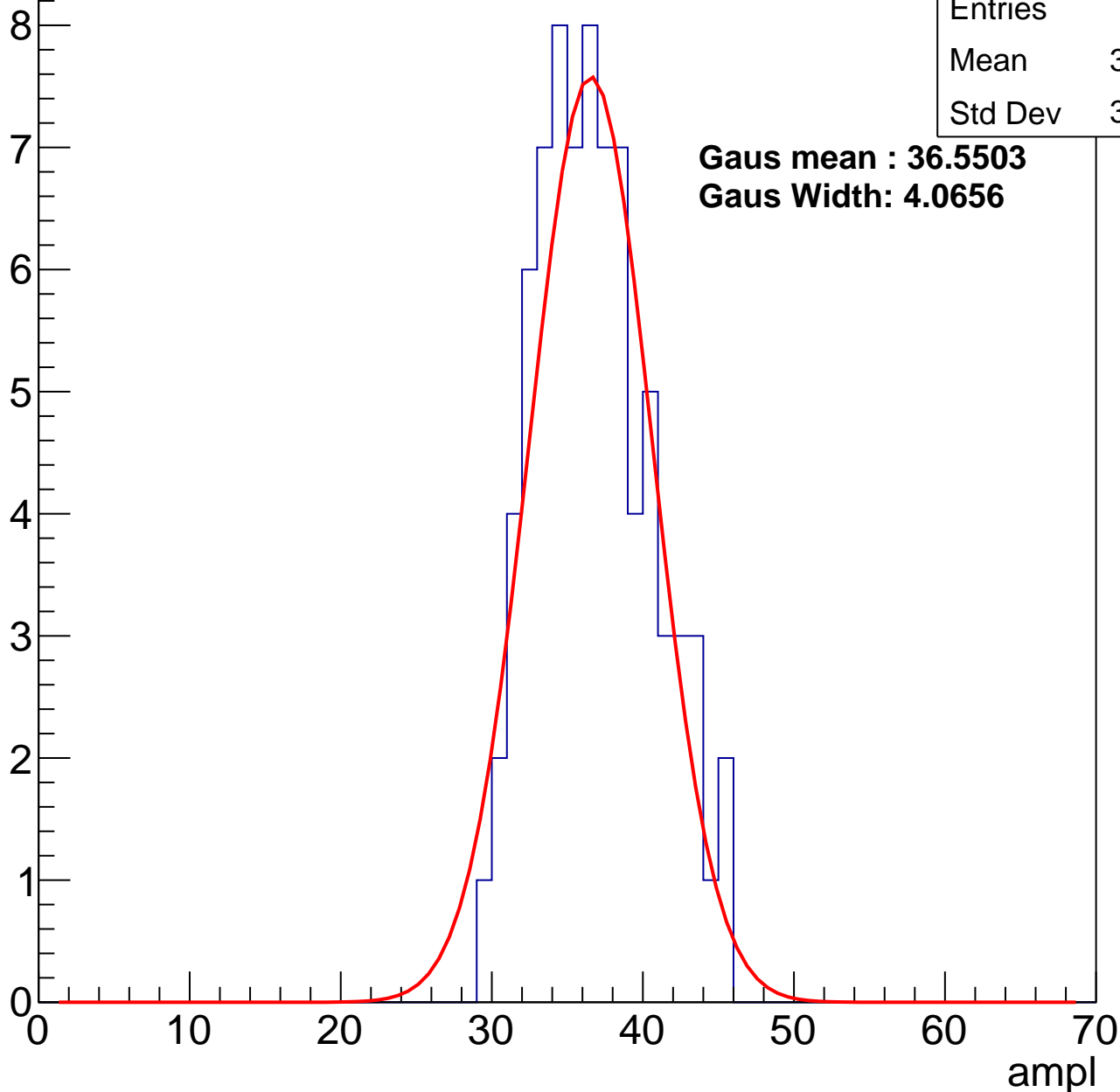
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.33
Std Dev	3.785

Gaus mean : 36.5503

Gaus Width: 4.0656



B1L100S, U13-ch81, adc2

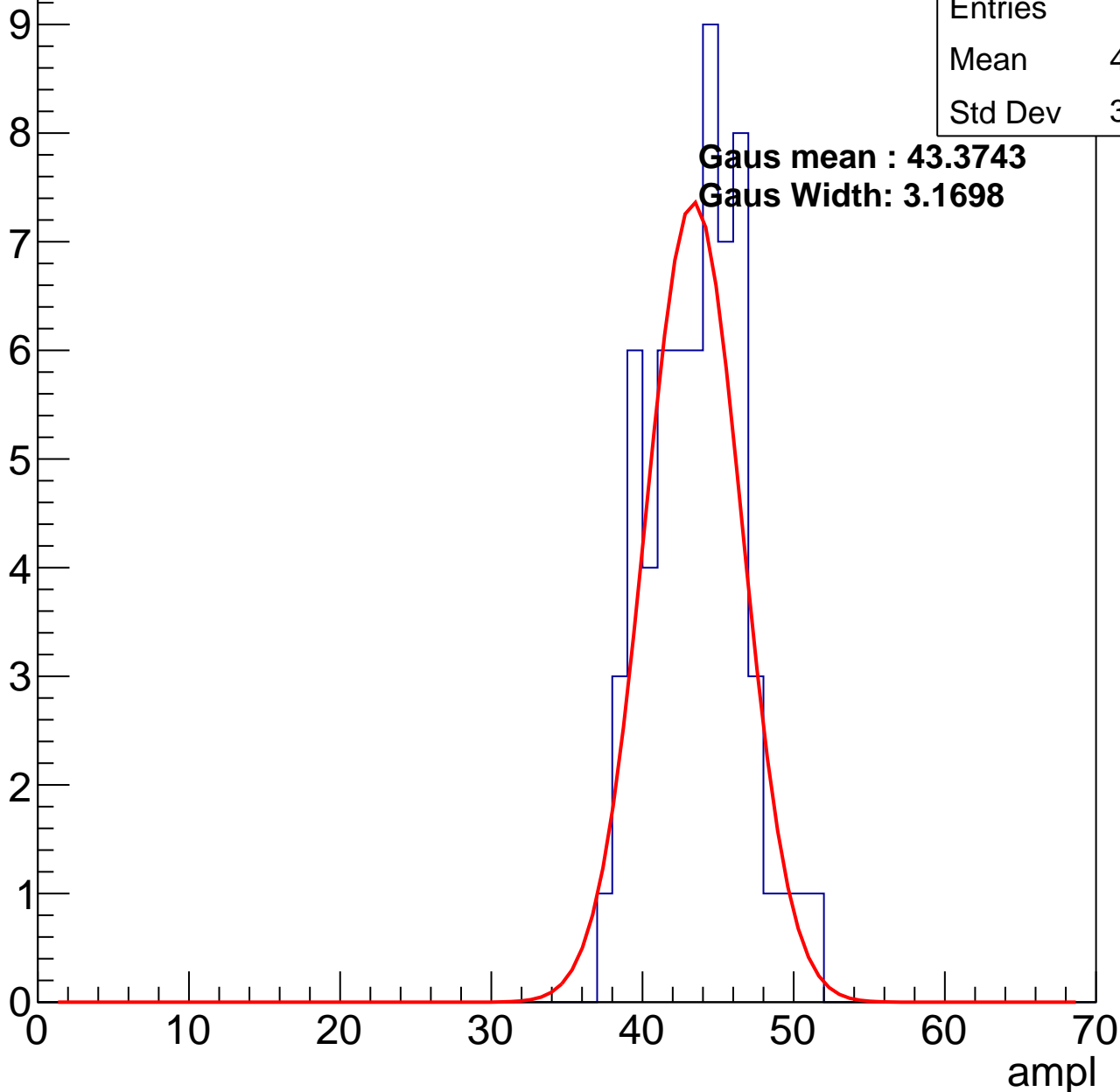
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.16
Std Dev	3.092

Gaus mean : 43.3743

Gaus Width: 3.1698

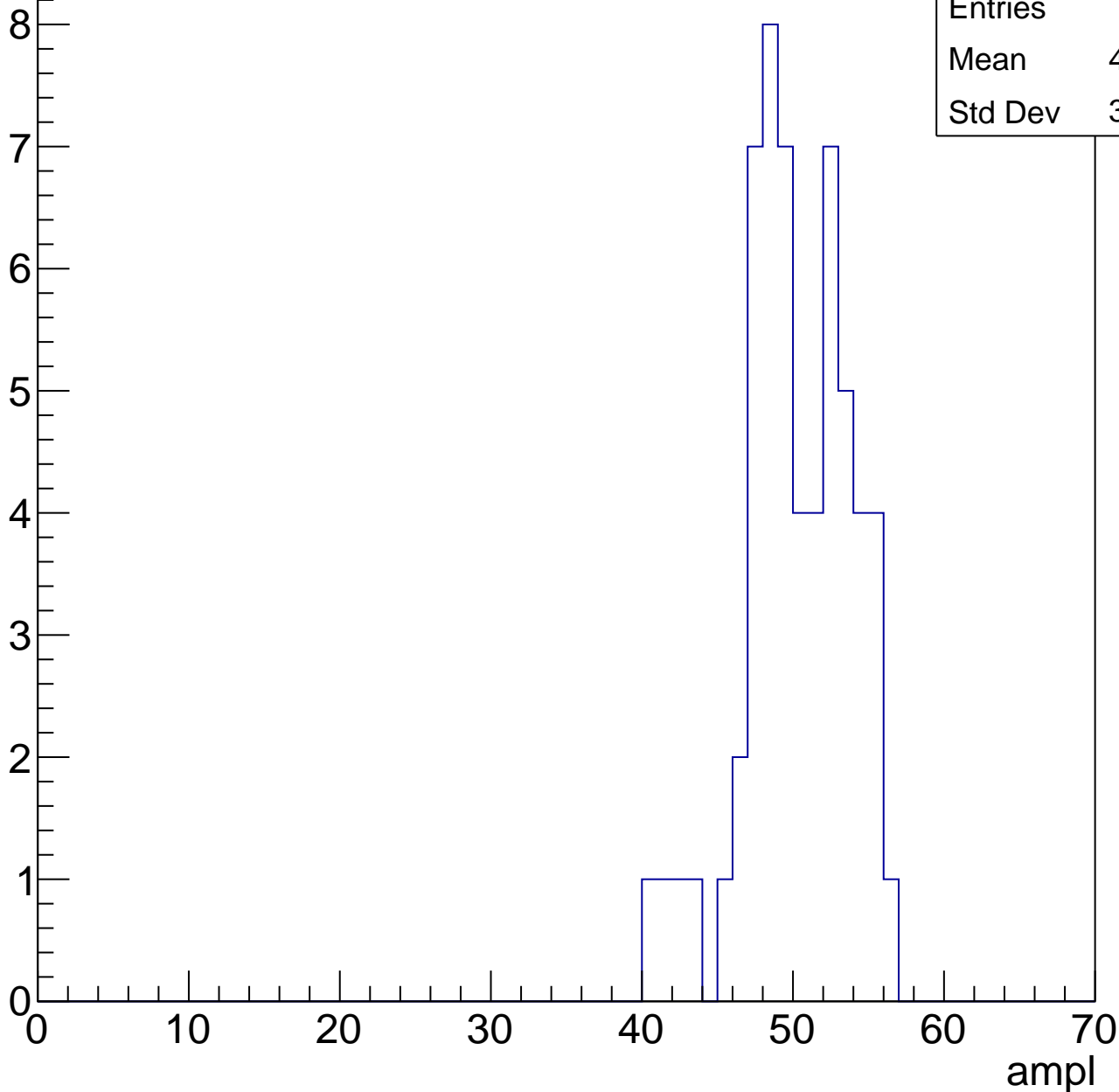


B1L100S, U13-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.72
Std Dev	3.537

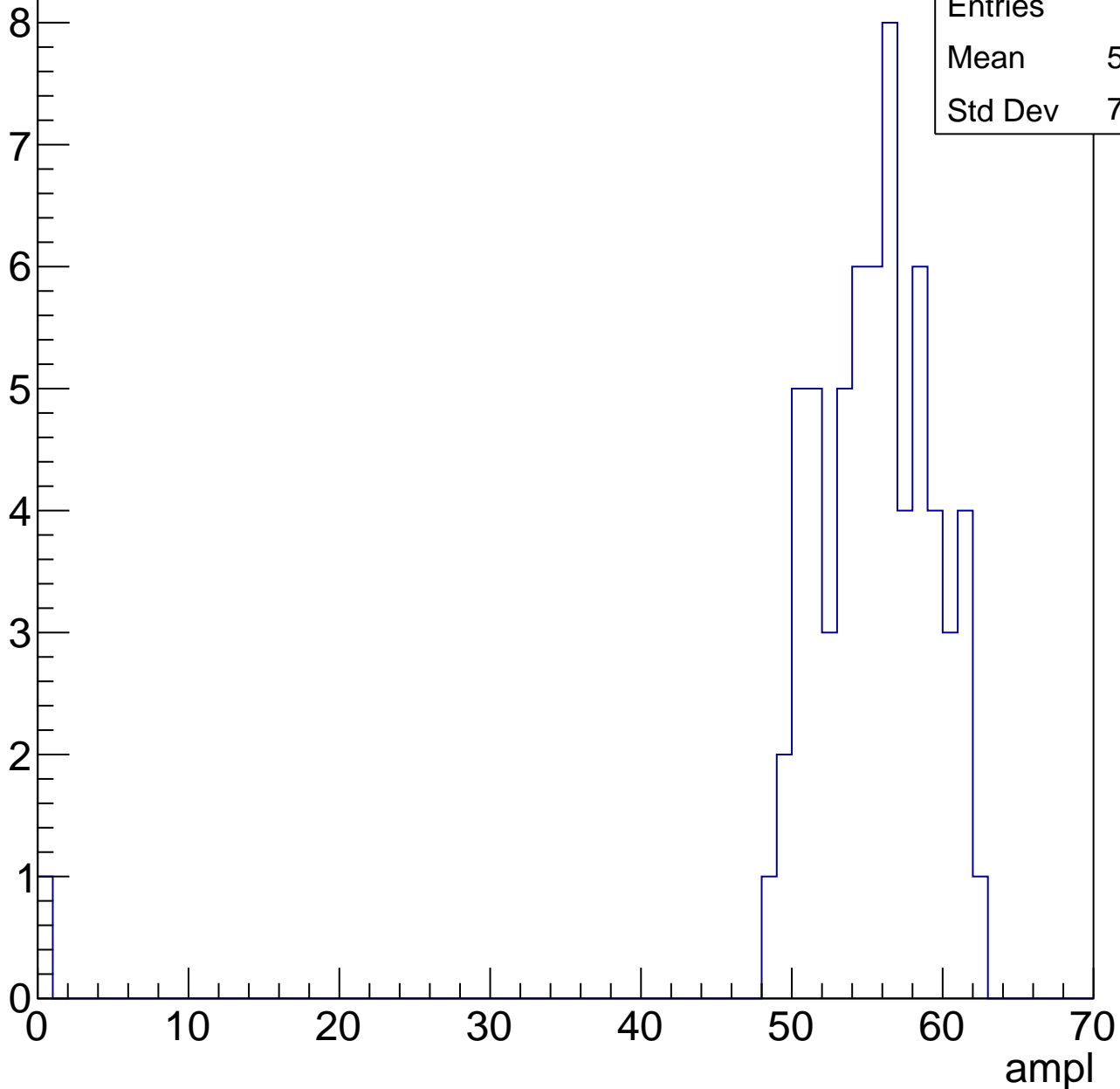


B1L100S, U13-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

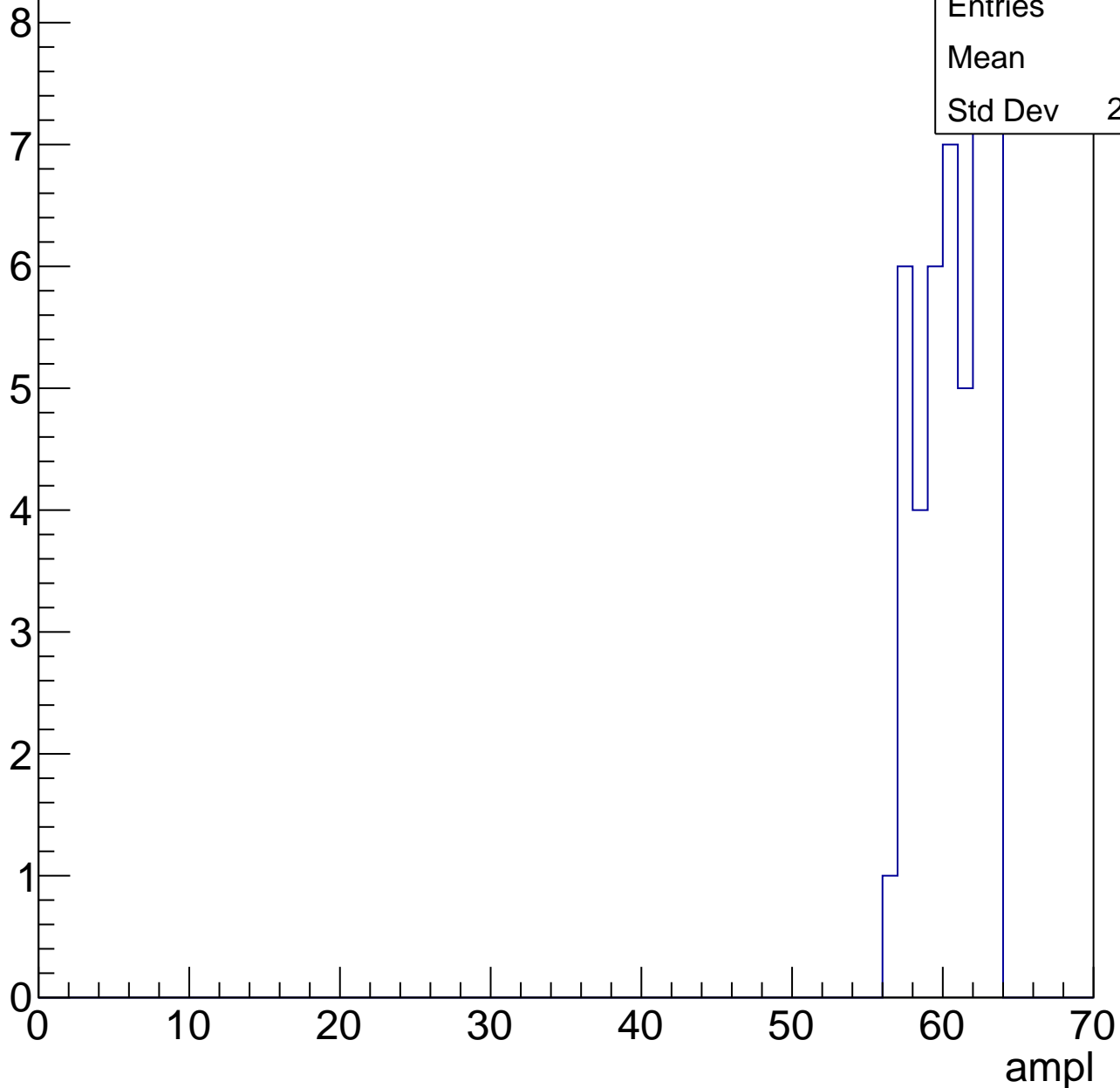
Entries	64
Mean	54.25
Std Dev	7.683



B1L100S, U13-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	45
Mean	60.2
Std Dev	2.104

B1L100S, U13-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch82, adc0

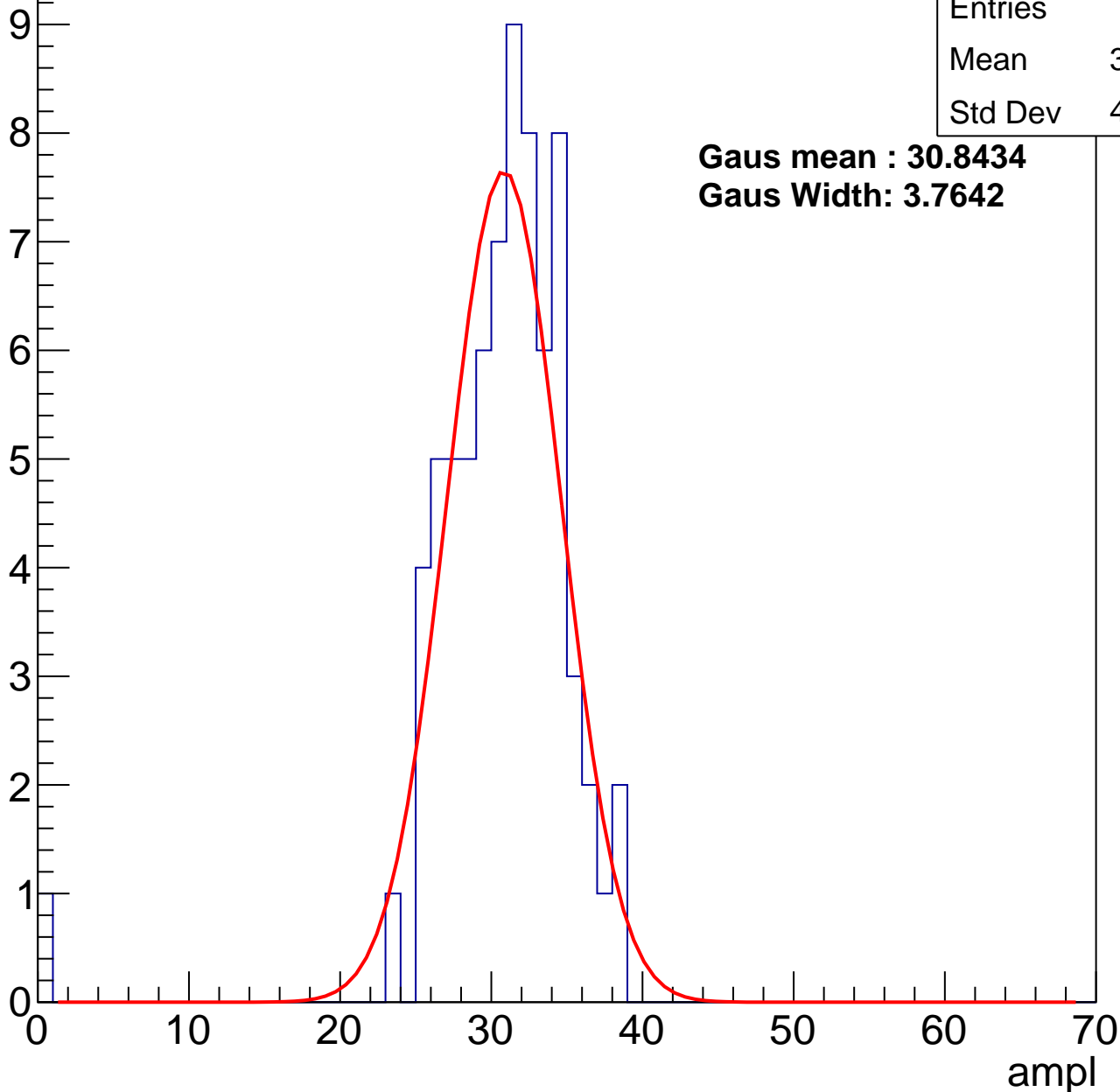
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.23
Std Dev	4.886

Gaus mean : 30.8434

Gaus Width: 3.7642



B1L100S, U13-ch82, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	38.49
Std Dev	3.898

Gaus mean : 39.2172

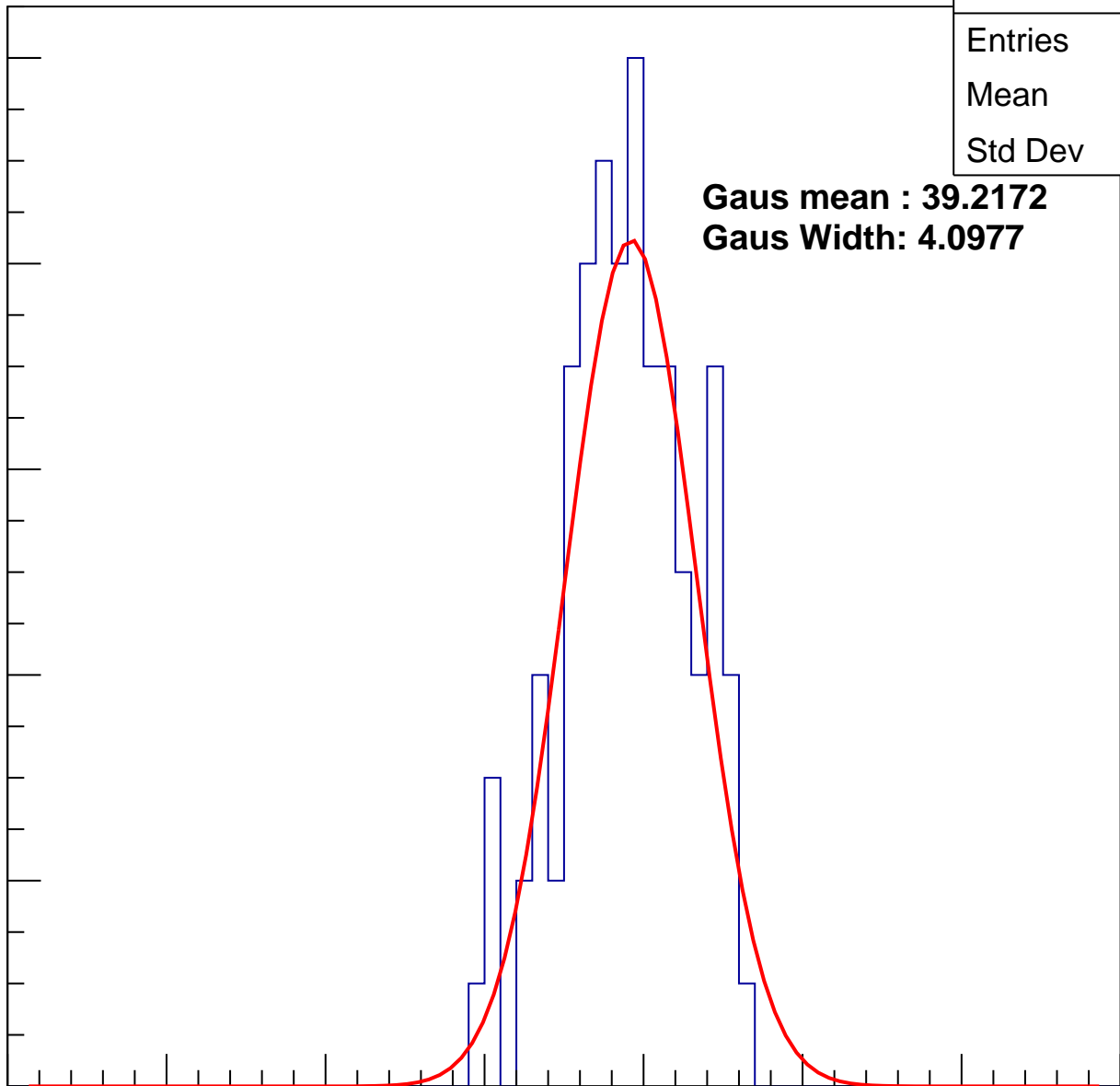
Gaus Width: 4.0977

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch82, adc2

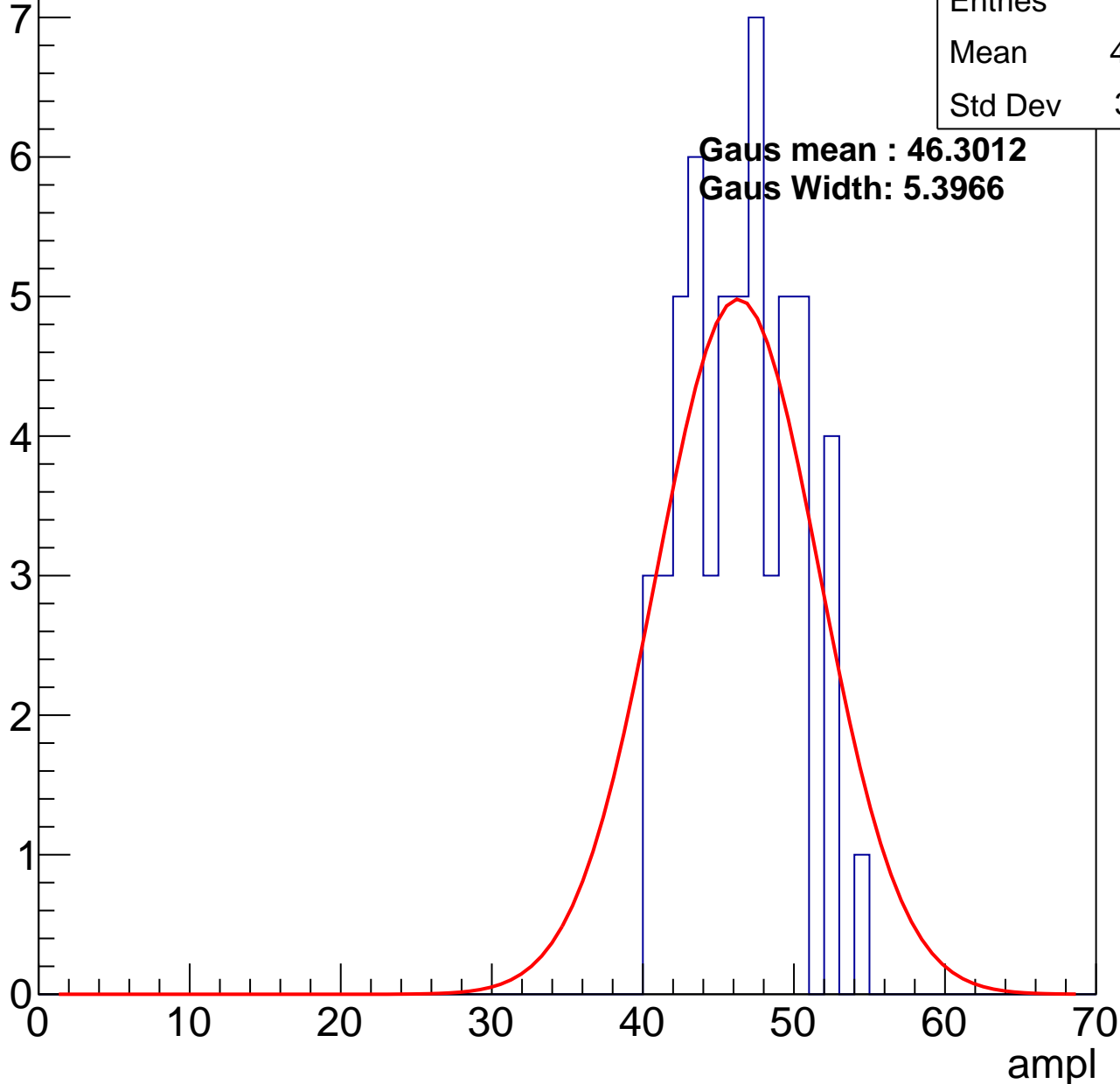
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	45.96
Std Dev	3.531

Gaus mean : 46.3012

Gaus Width: 5.3966

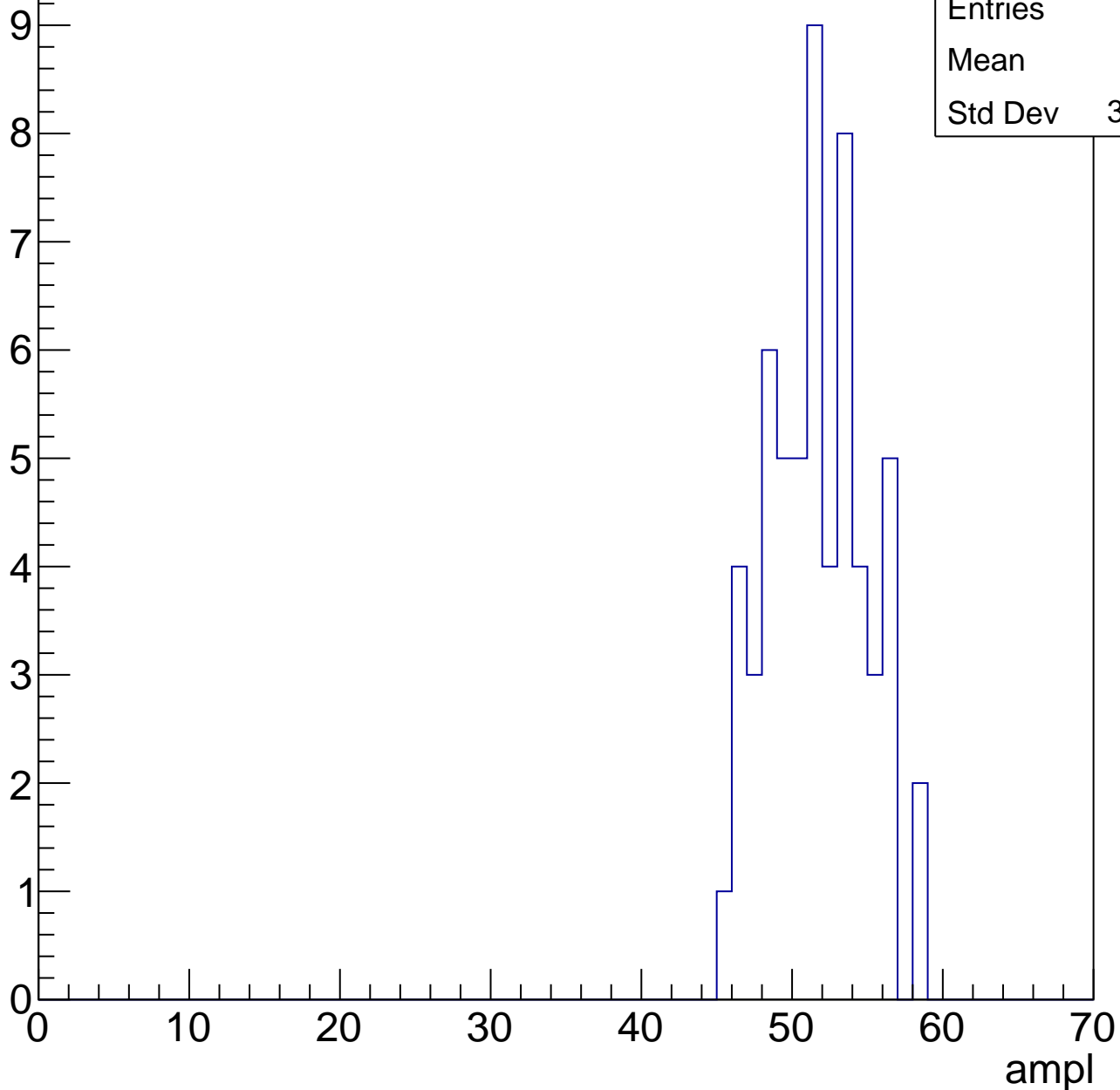


B1L100S, U13-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	51.2
Std Dev	3.198



B1L100S, U13-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	57.15
Std Dev	3.002

Entry

10

8

6

4

2

0

0

10

20

30

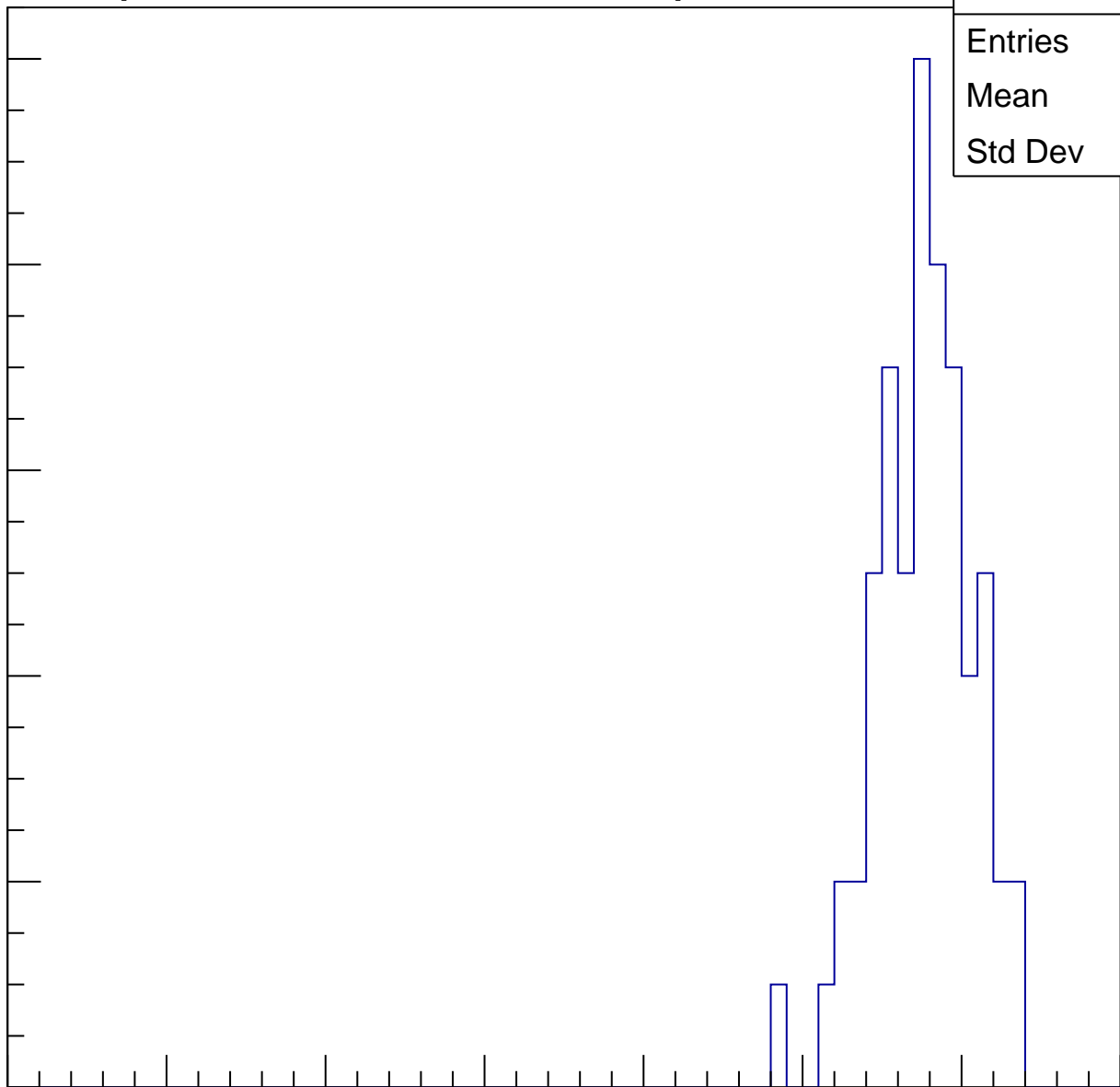
40

50

60

70

ampl

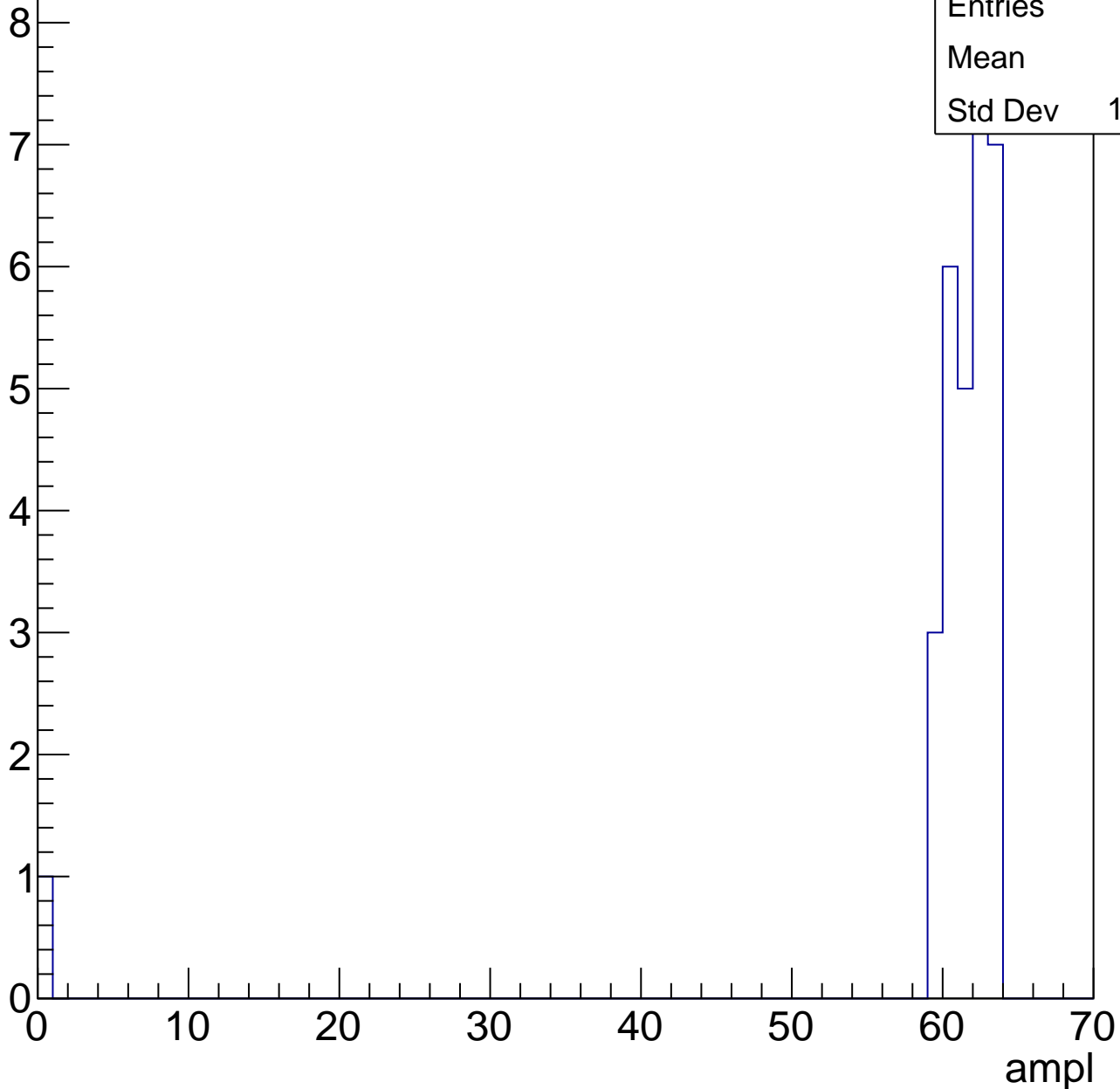


B1L100S, U13-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	59.3
Std Dev	11.09



B1L100S, U13-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch83, adc0

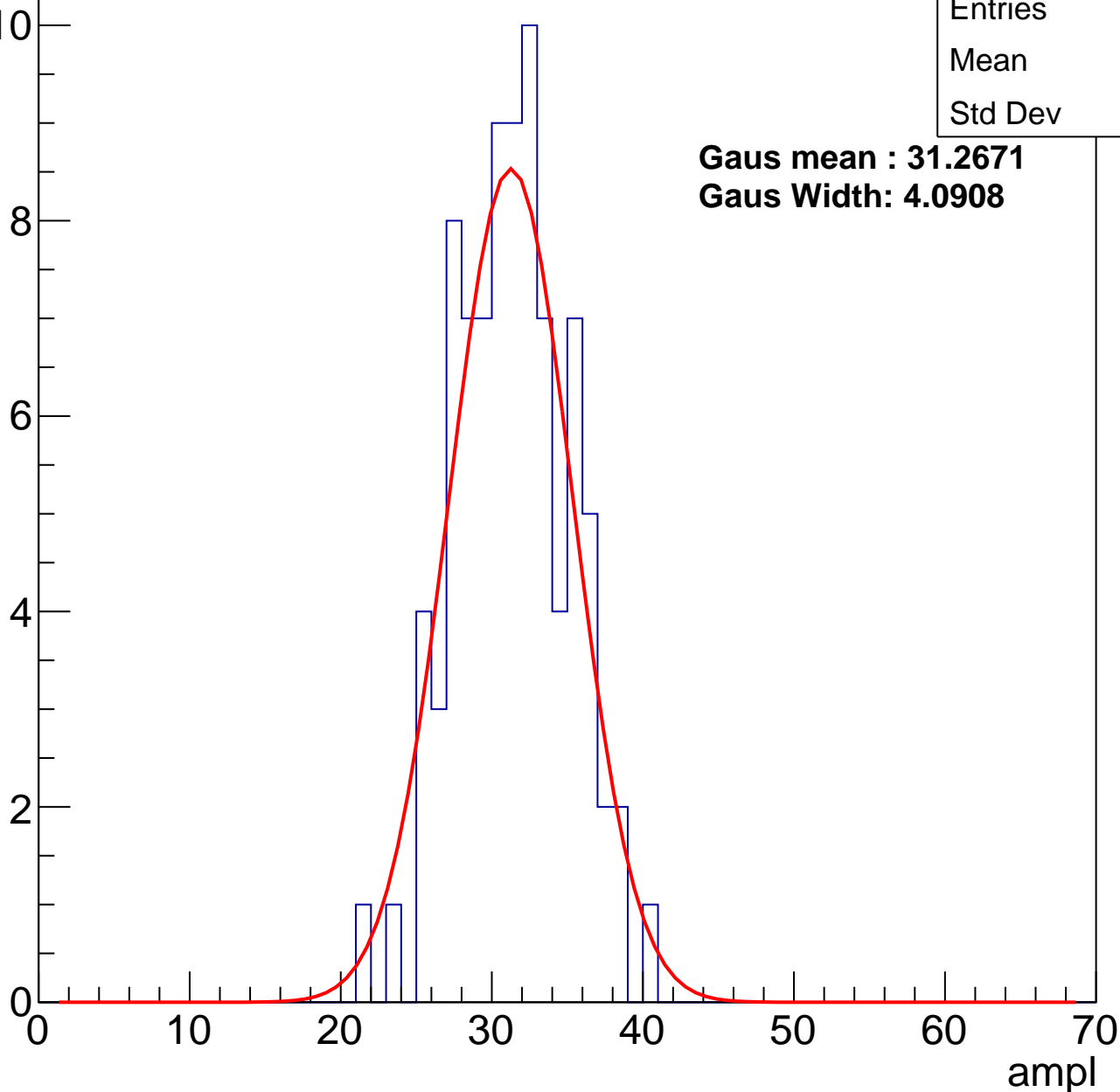
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	30.9
Std Dev	3.67

Gaus mean : 31.2671

Gaus Width: 4.0908



B1L100S, U13-ch83, adc1

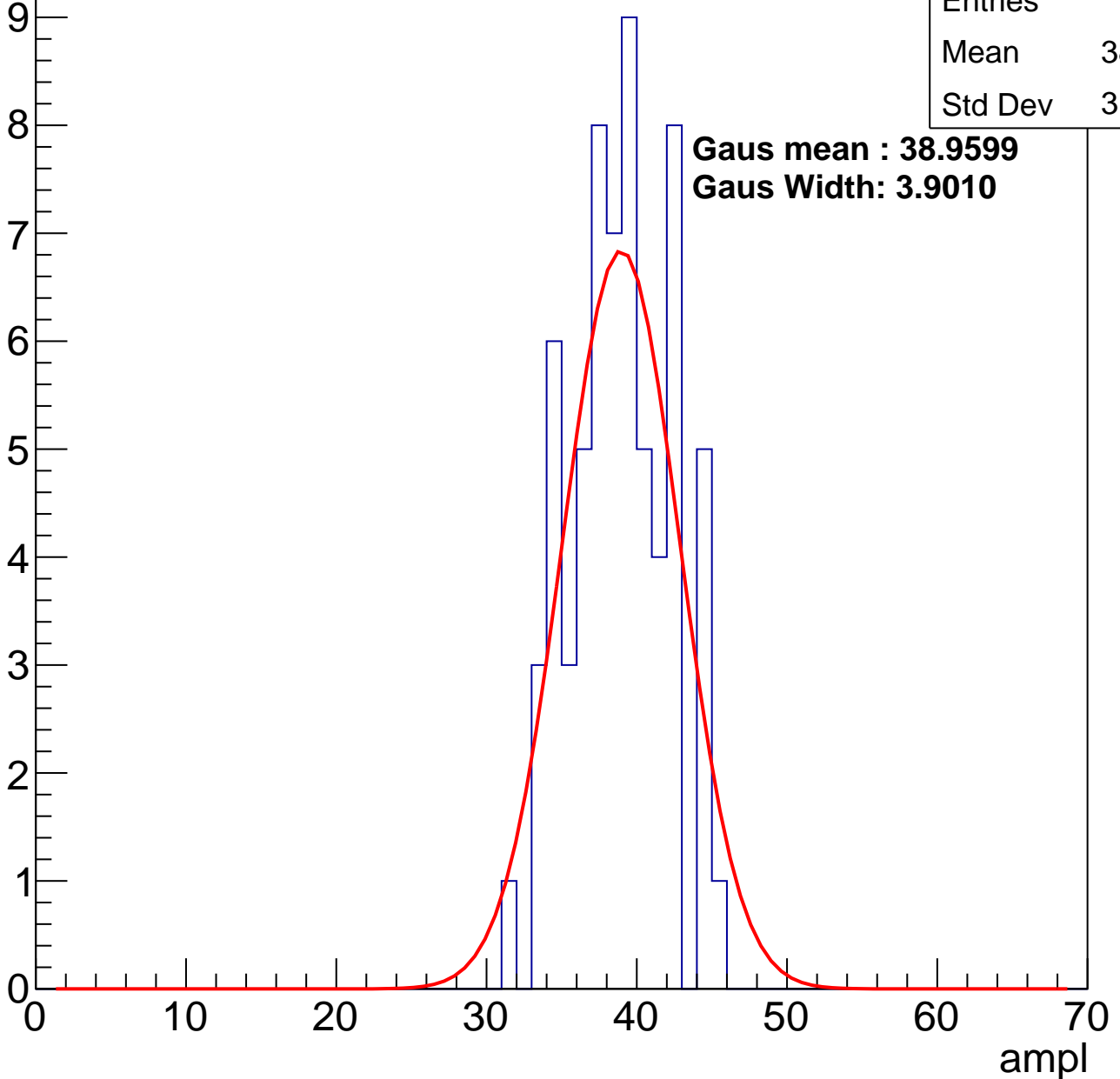
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	38.42
Std Dev	3.248

Gaus mean : 38.9599

Gaus Width: 3.9010



B1L100S, U13-ch83, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	45.27
Std Dev	3.389

Gaus mean : 46.4145

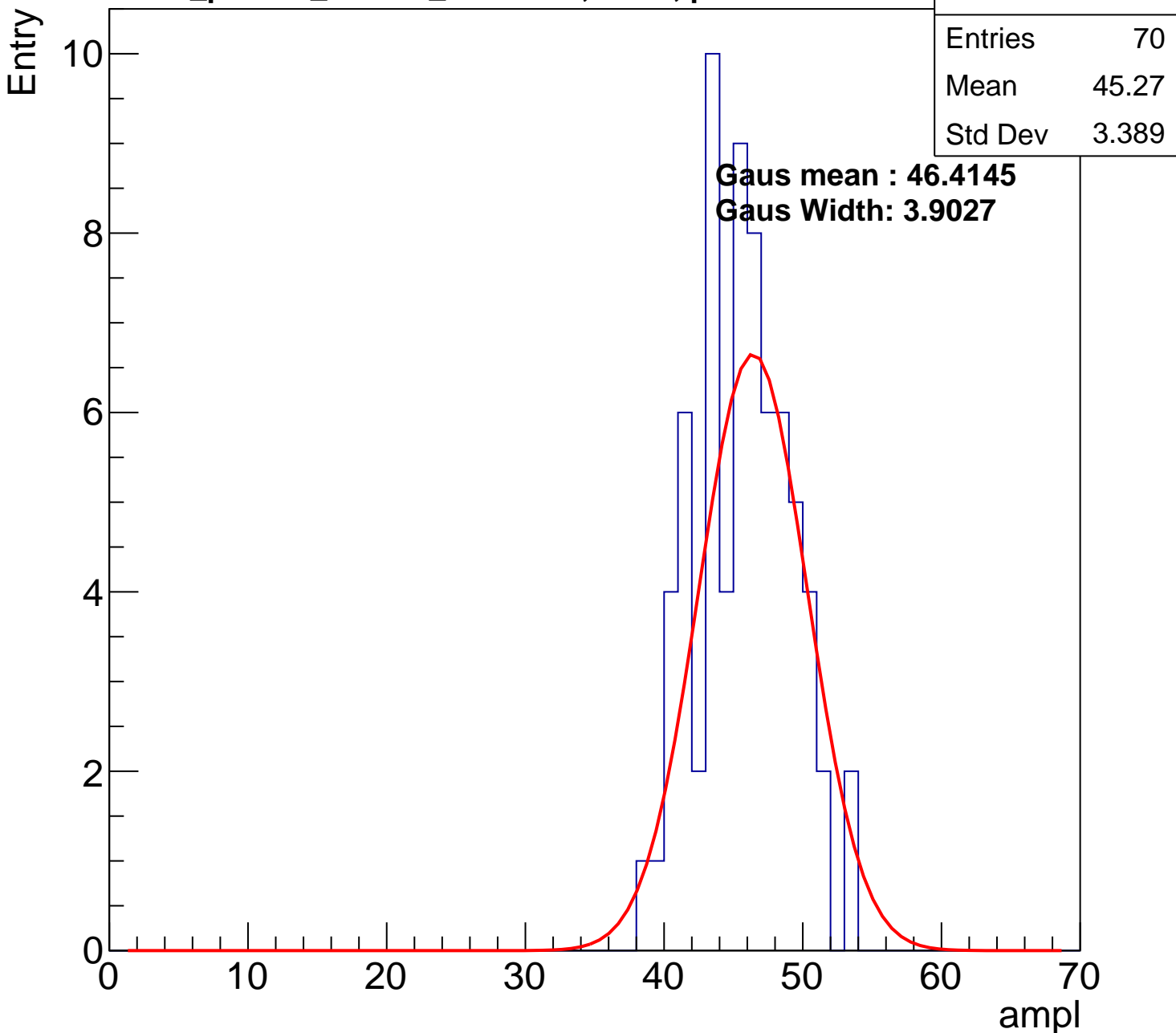
Gaus Width: 3.9027

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

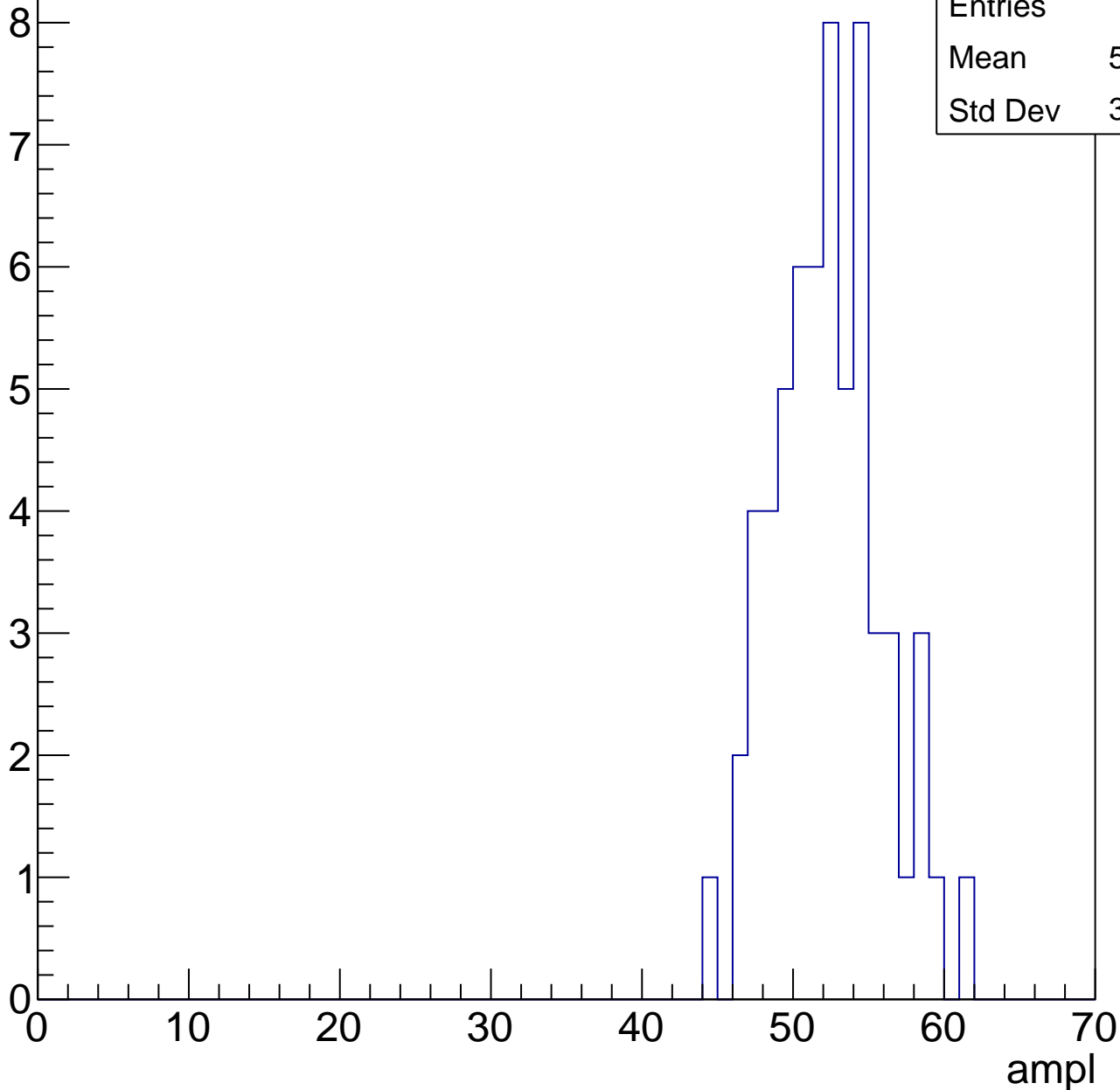


B1L100S, U13-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	51.87
Std Dev	3.504

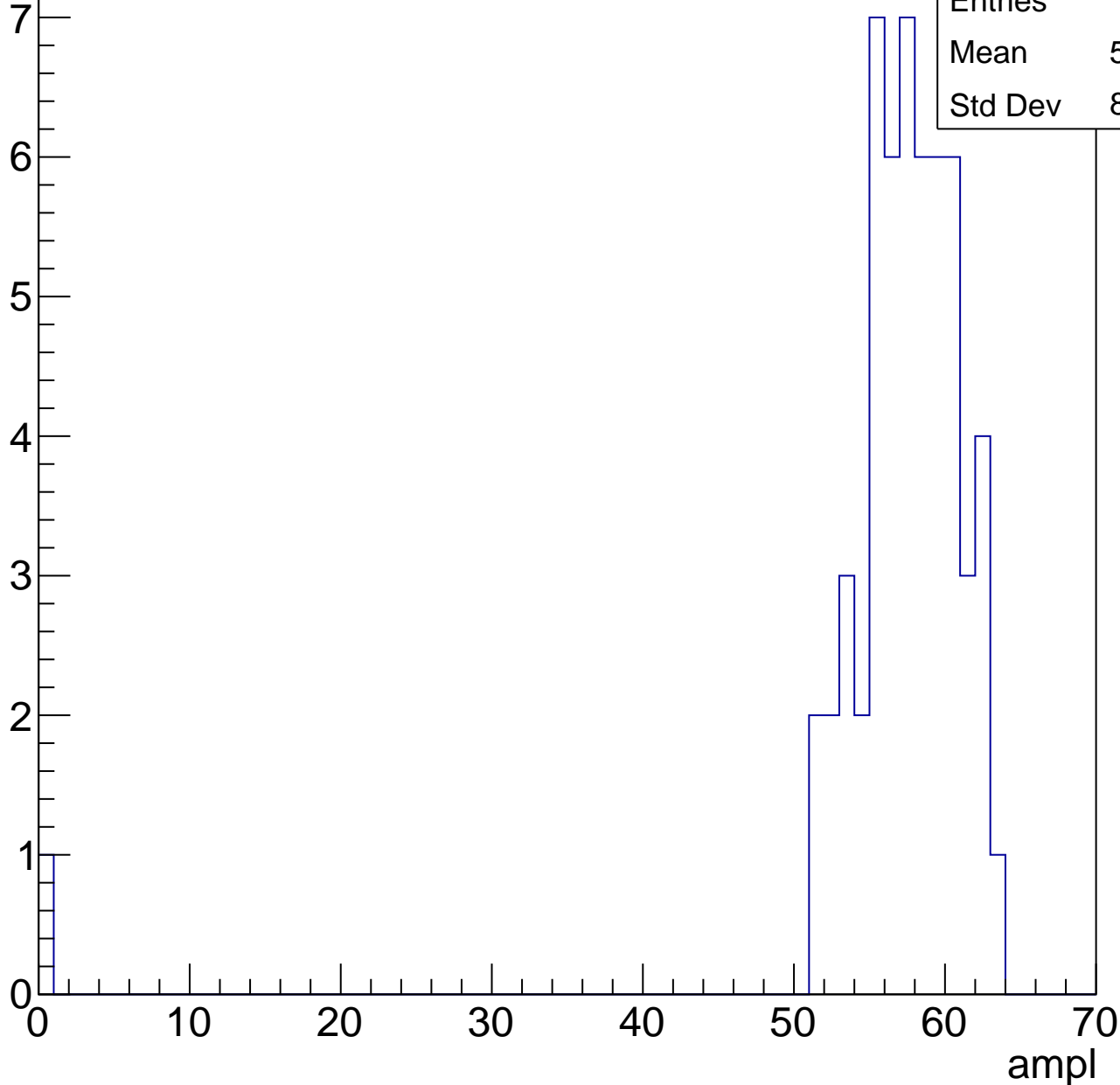


B1L100S, U13-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.23
Std Dev	8.135



B1L100S, U13-ch83, adc5

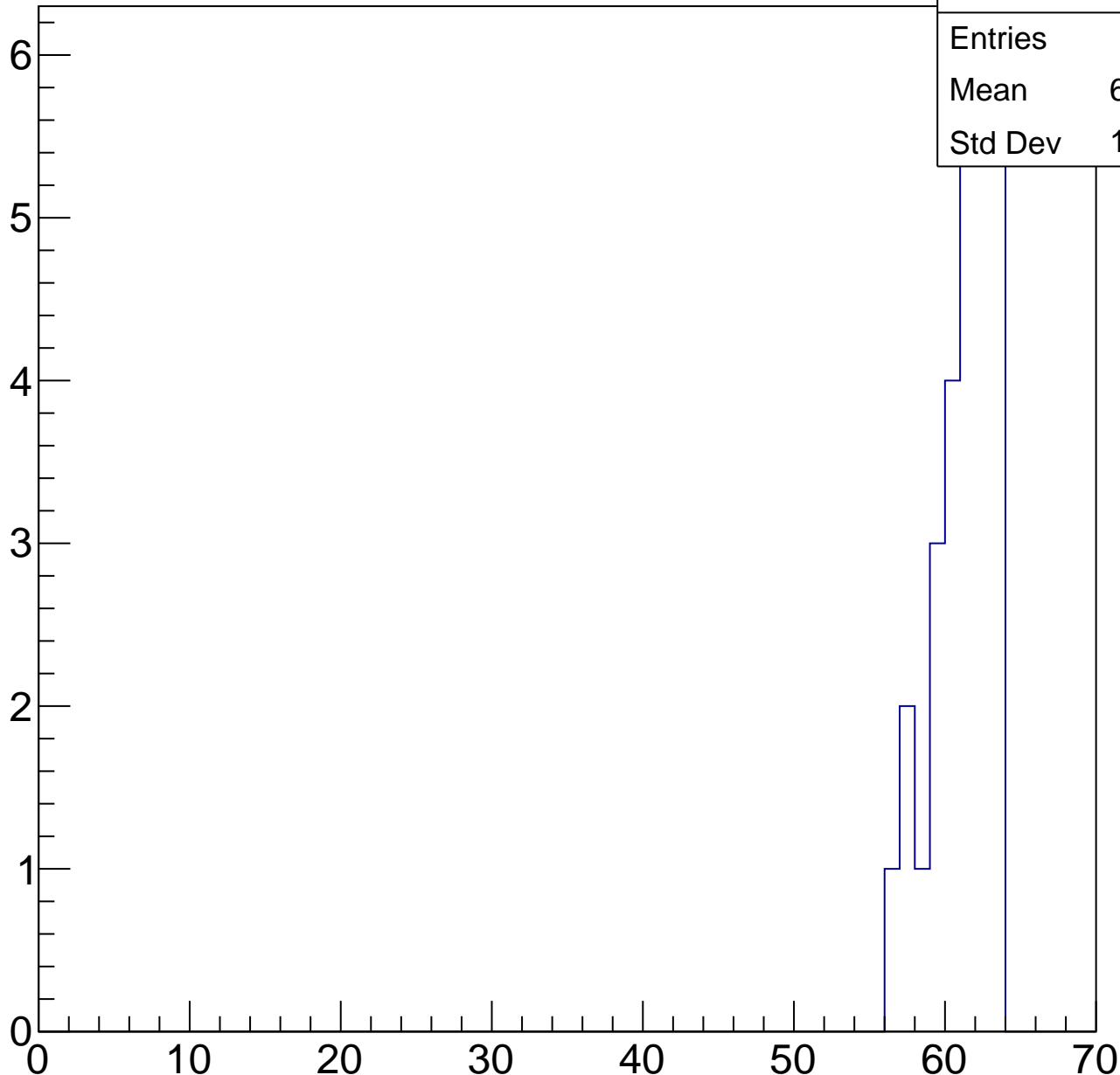
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	29
Mean	60.72
Std Dev	1.946

ampl



B1L100S, U13-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch84, adc0

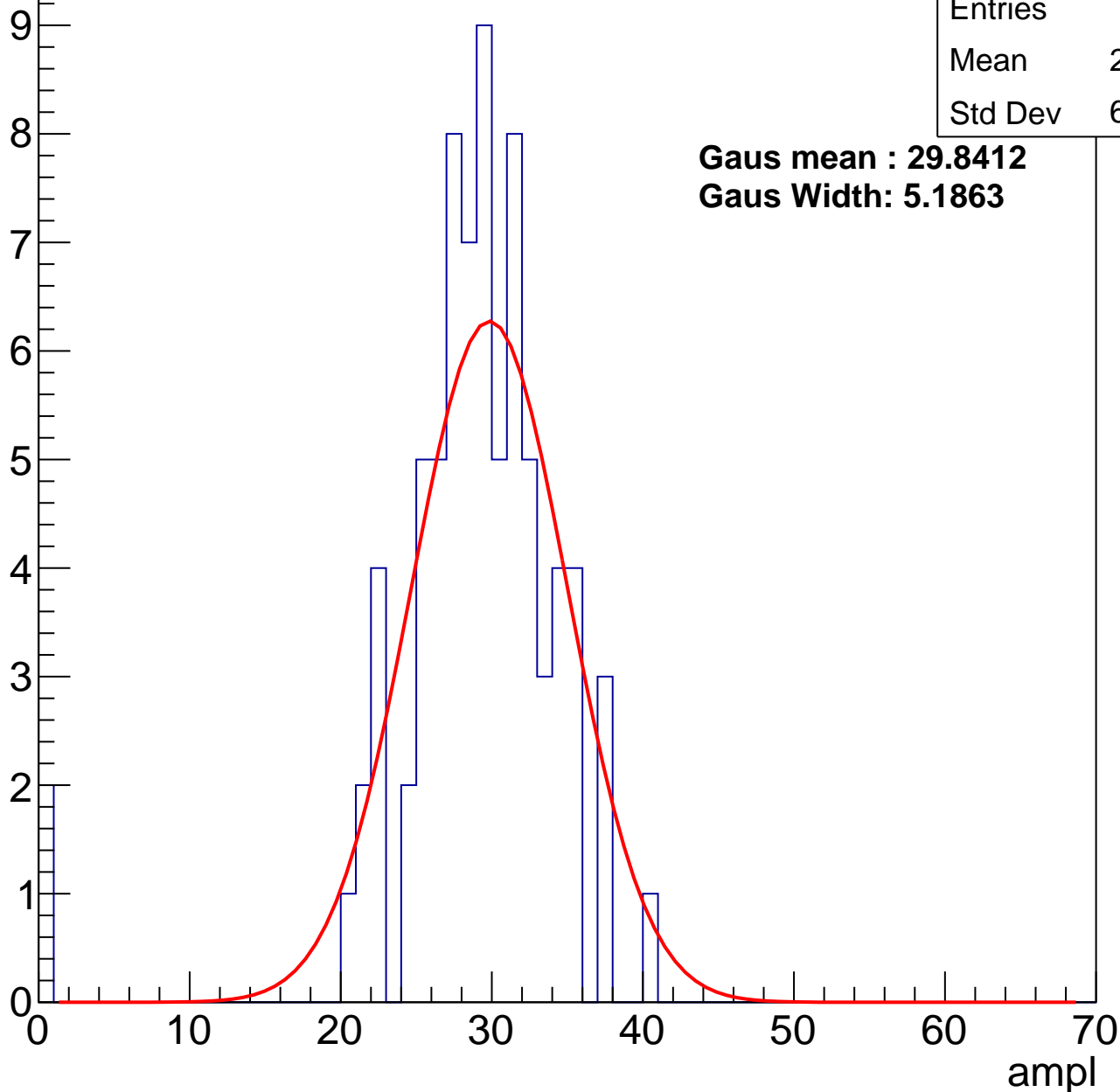
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.33
Std Dev	6.153

Gaus mean : 29.8412

Gaus Width: 5.1863



B1L100S, U13-ch84, adc1

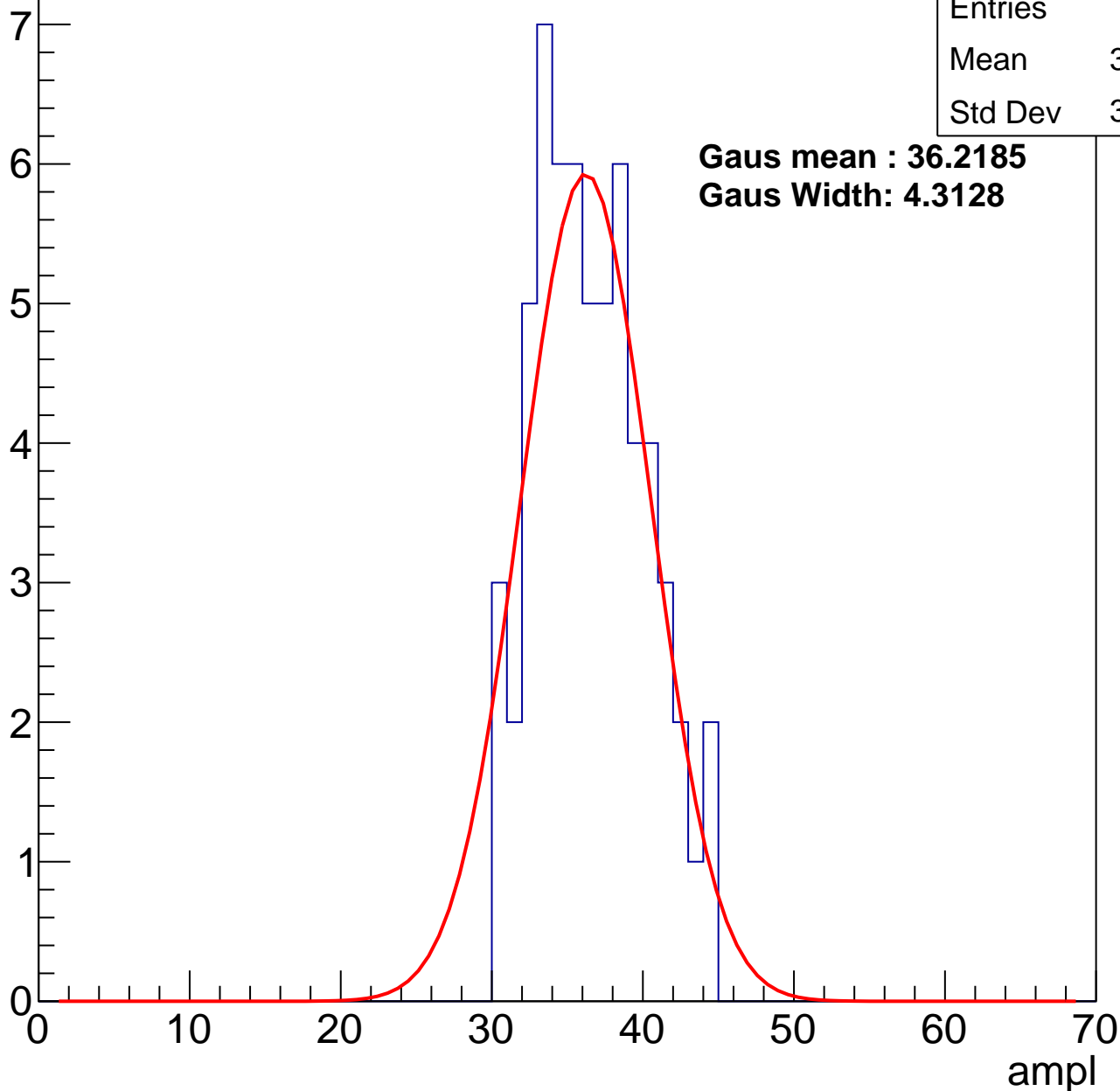
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.13
Std Dev	3.583

Gaus mean : 36.2185

Gaus Width: 4.3128



B1L100S, U13-ch84, adc2

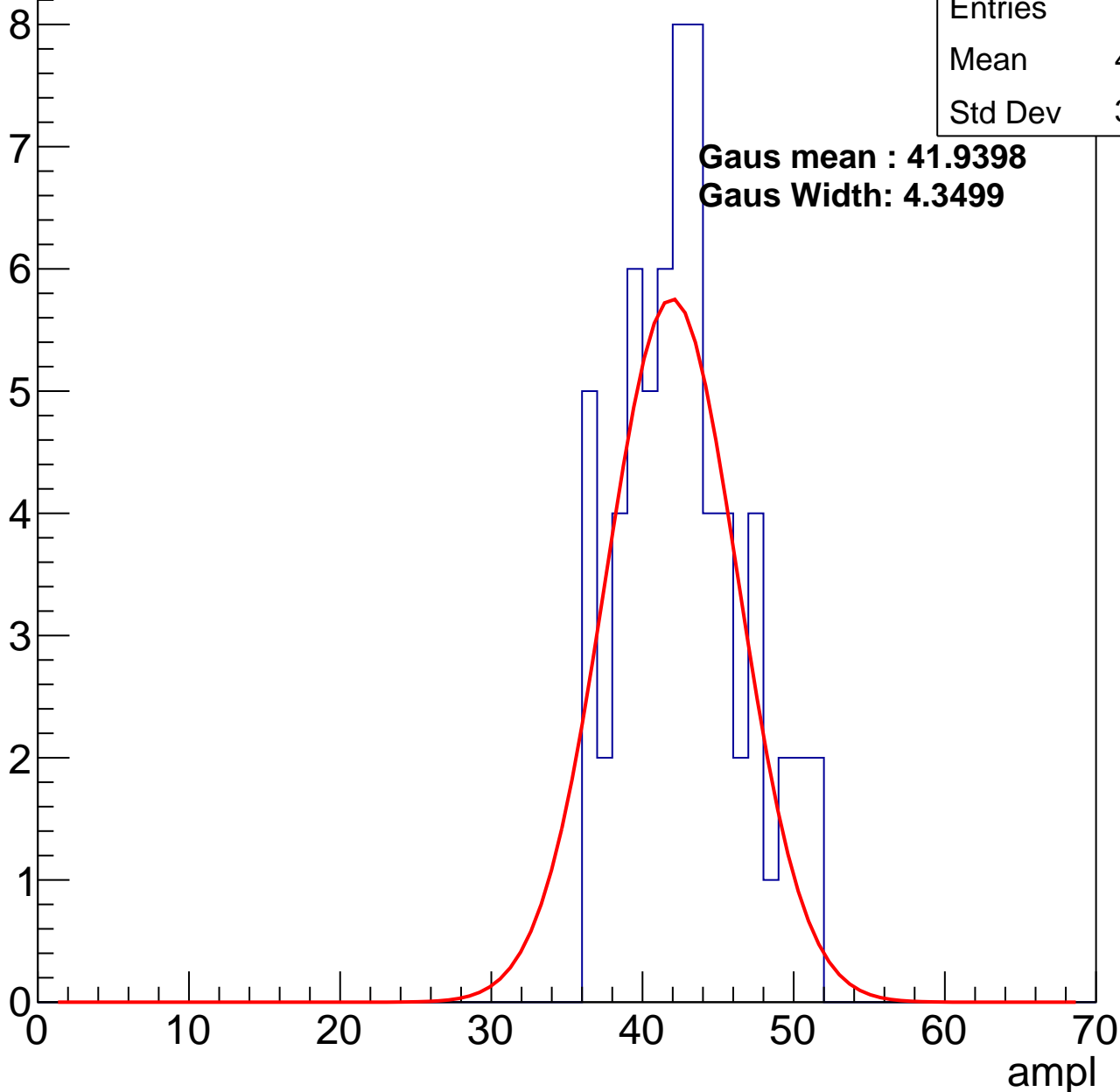
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.31
Std Dev	3.891

Gaus mean : 41.9398

Gaus Width: 4.3499

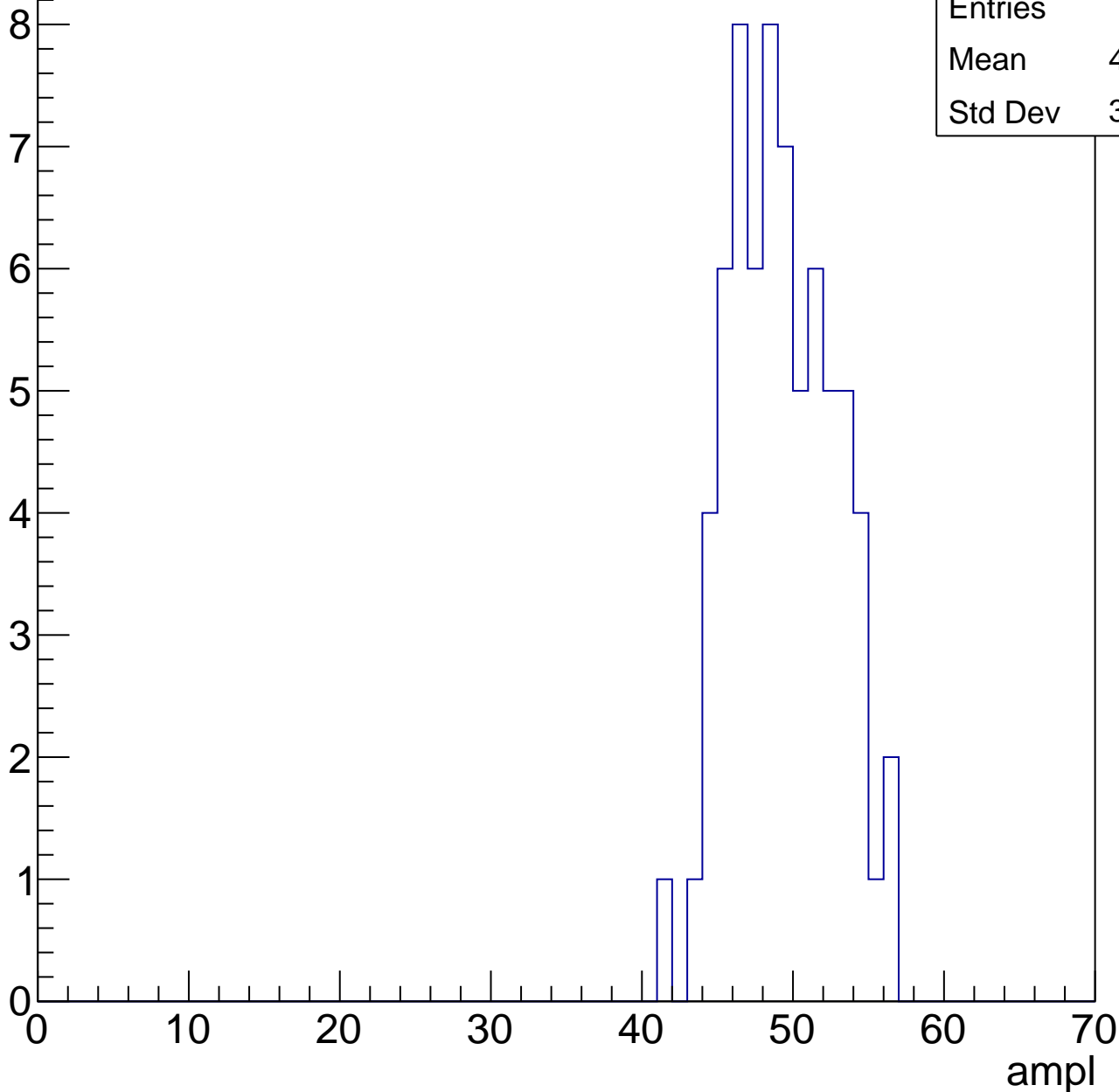


B1L100S, U13-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	48.86
Std Dev	3.376

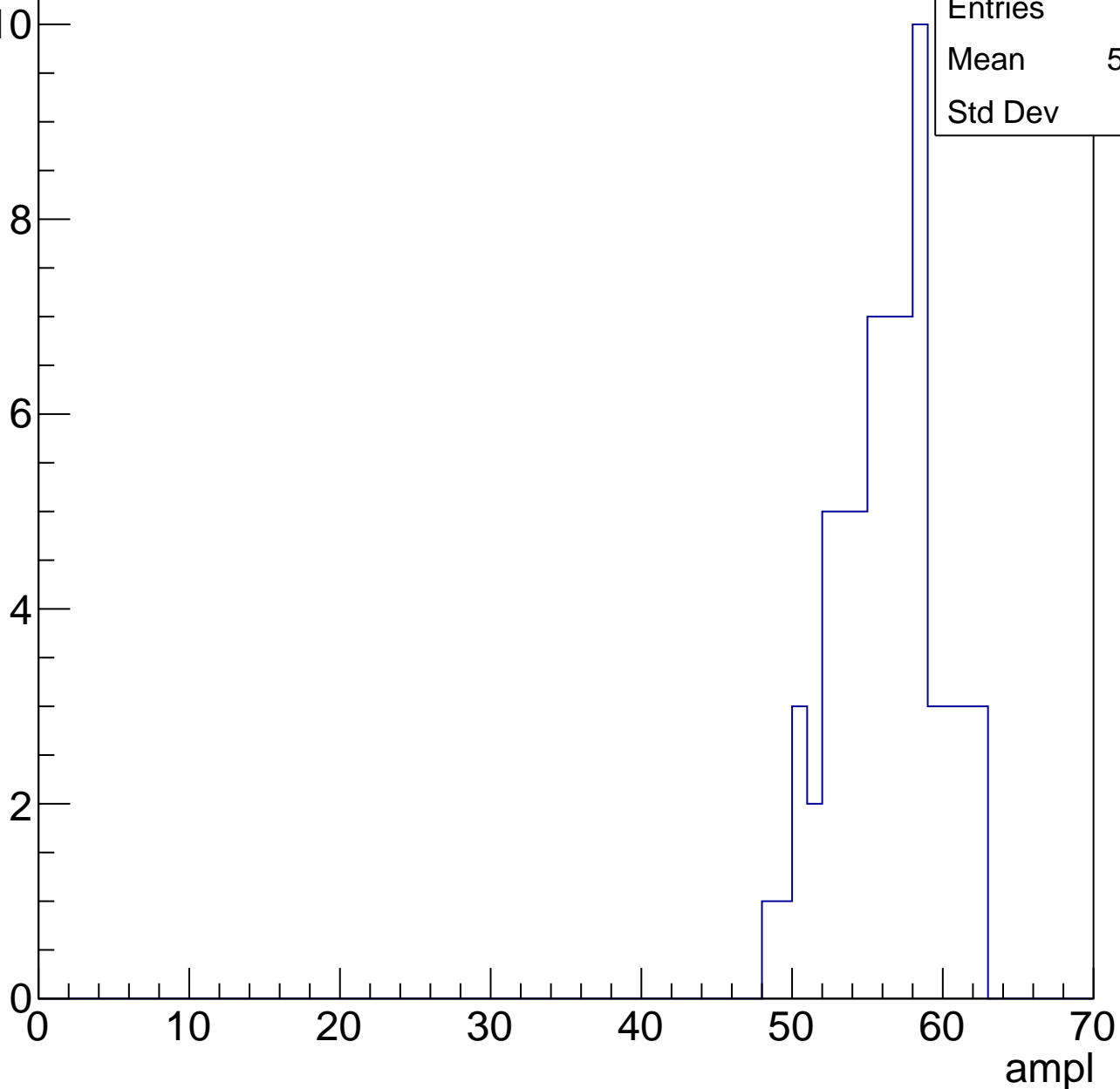


B1L100S, U13-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

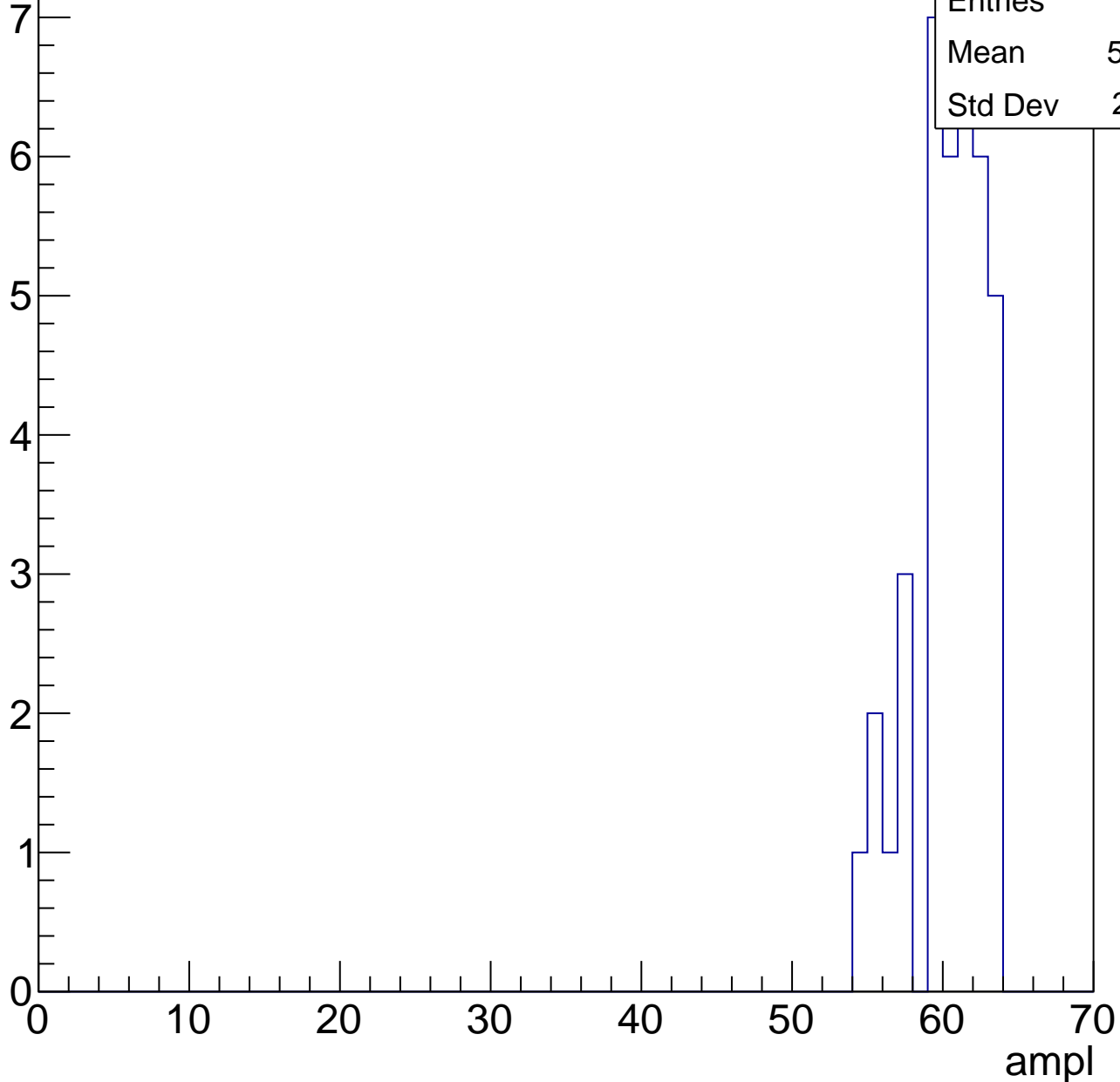
Entries	65
Mean	55.78
Std Dev	3.34



B1L100S, U13-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

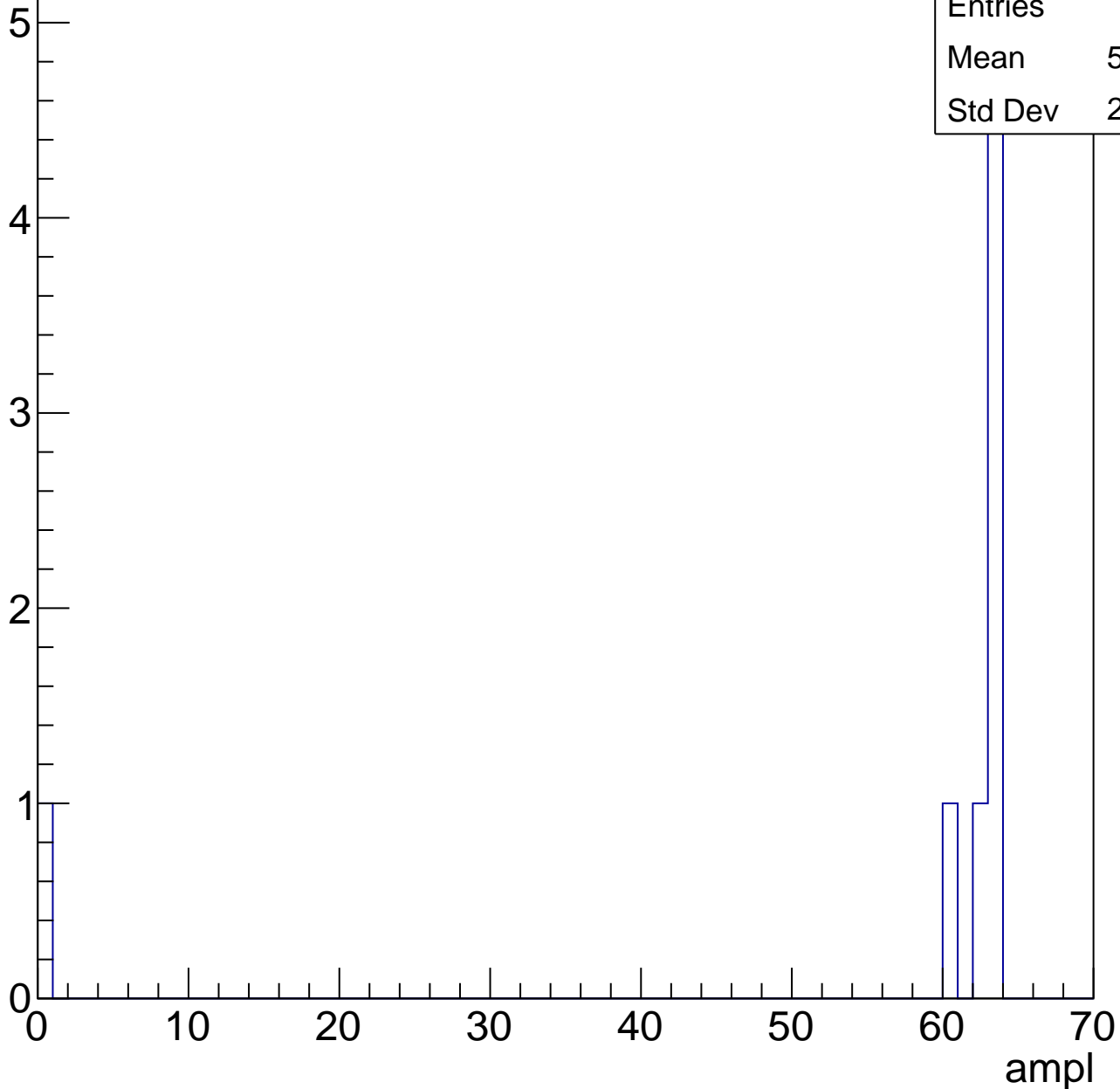


B1L100S, U13-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	8
Mean	54.62
Std Dev	20.67



B1L100S, U13-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch85, adc0

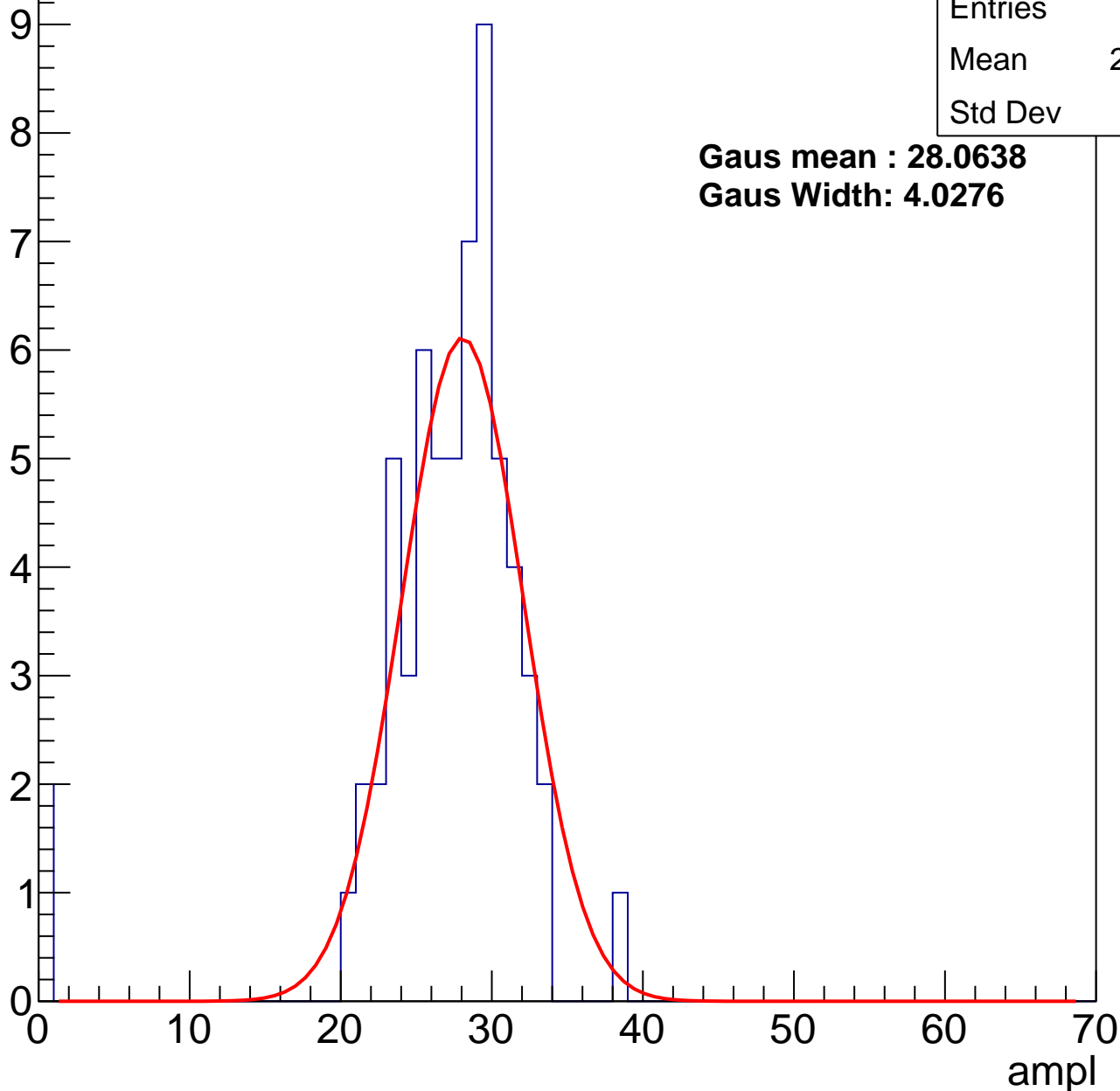
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	26.44
Std Dev	5.91

Gaus mean : 28.0638

Gaus Width: 4.0276



B1L100S, U13-ch85, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	35.07
Std Dev	3.435

Gaus mean : 35.1350

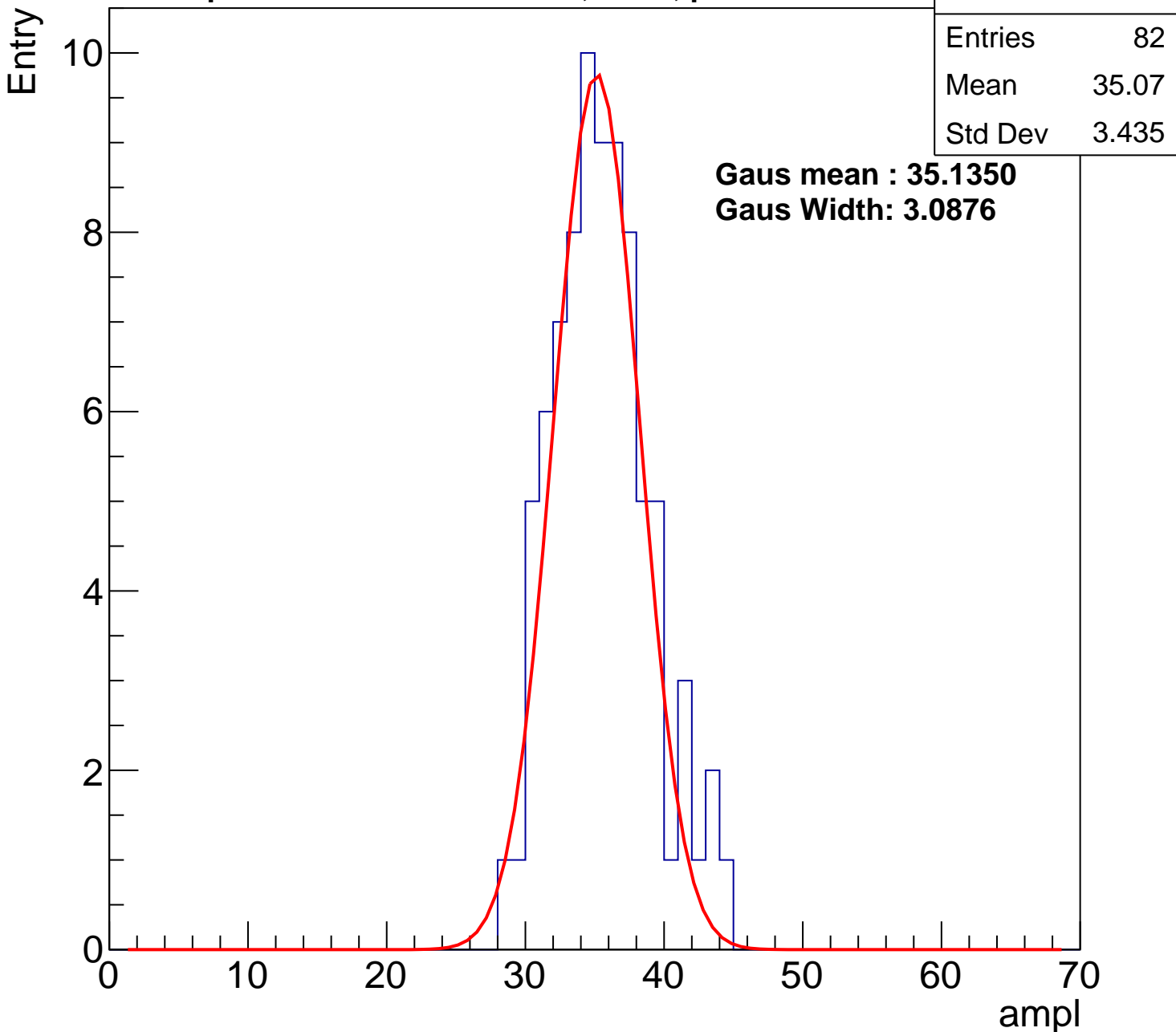
Gaus Width: 3.0876

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch85, adc2

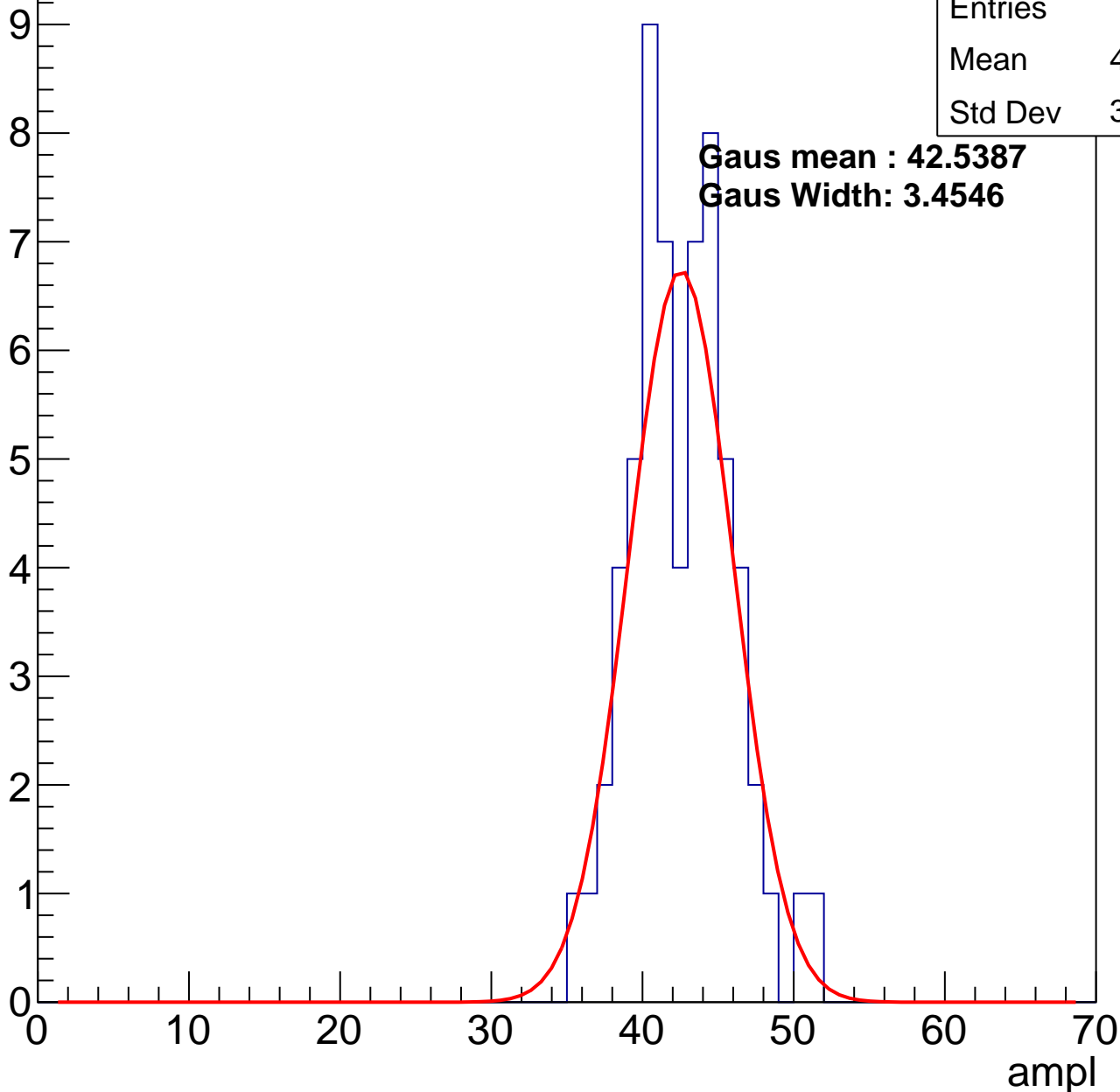
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.13
Std Dev	3.275

Gaus mean : 42.5387

Gaus Width: 3.4546



B1L100S, U13-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

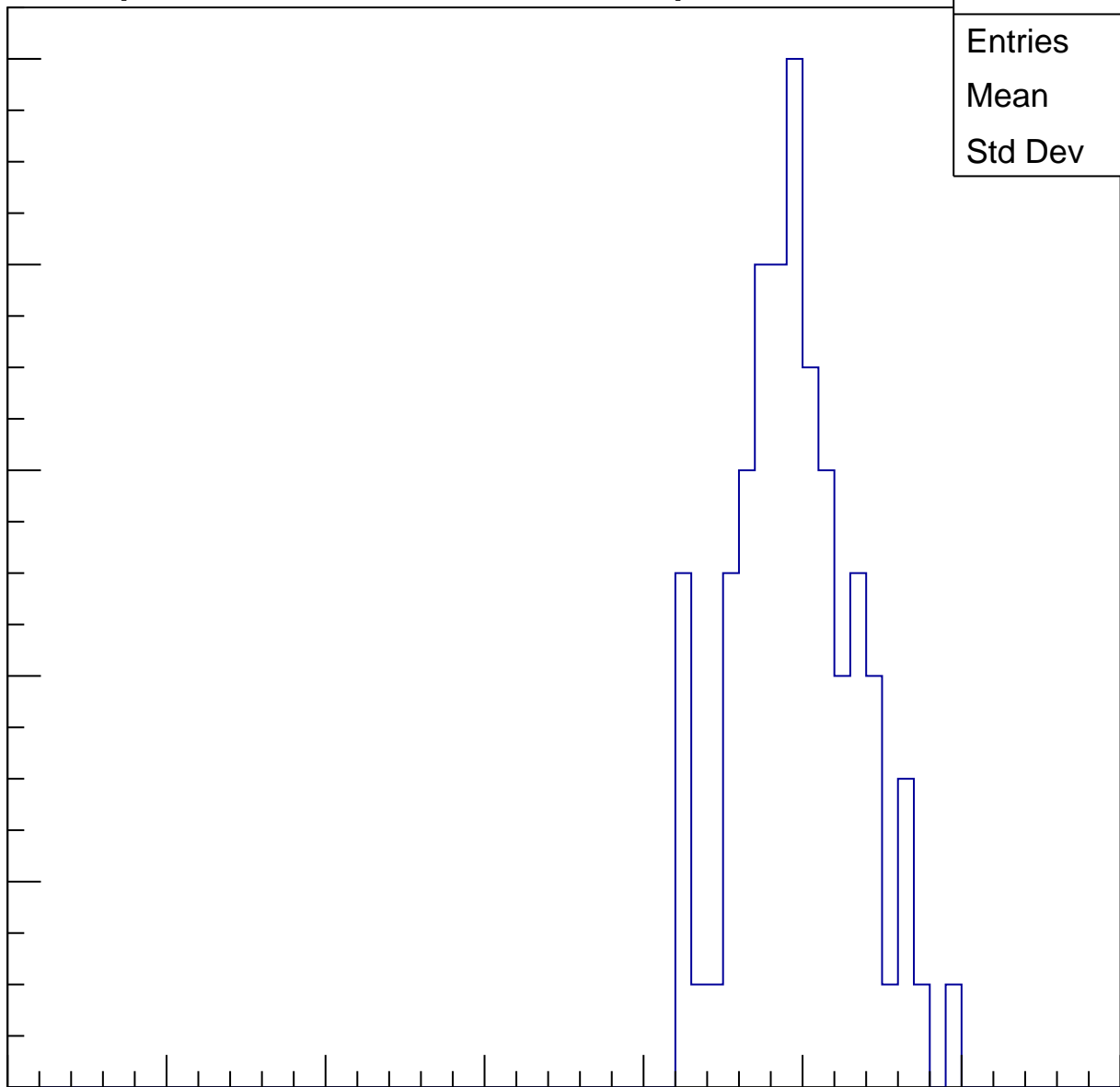
Entries	76
Mean	49.11
Std Dev	3.758

Entry

10
8
6
4
2
0

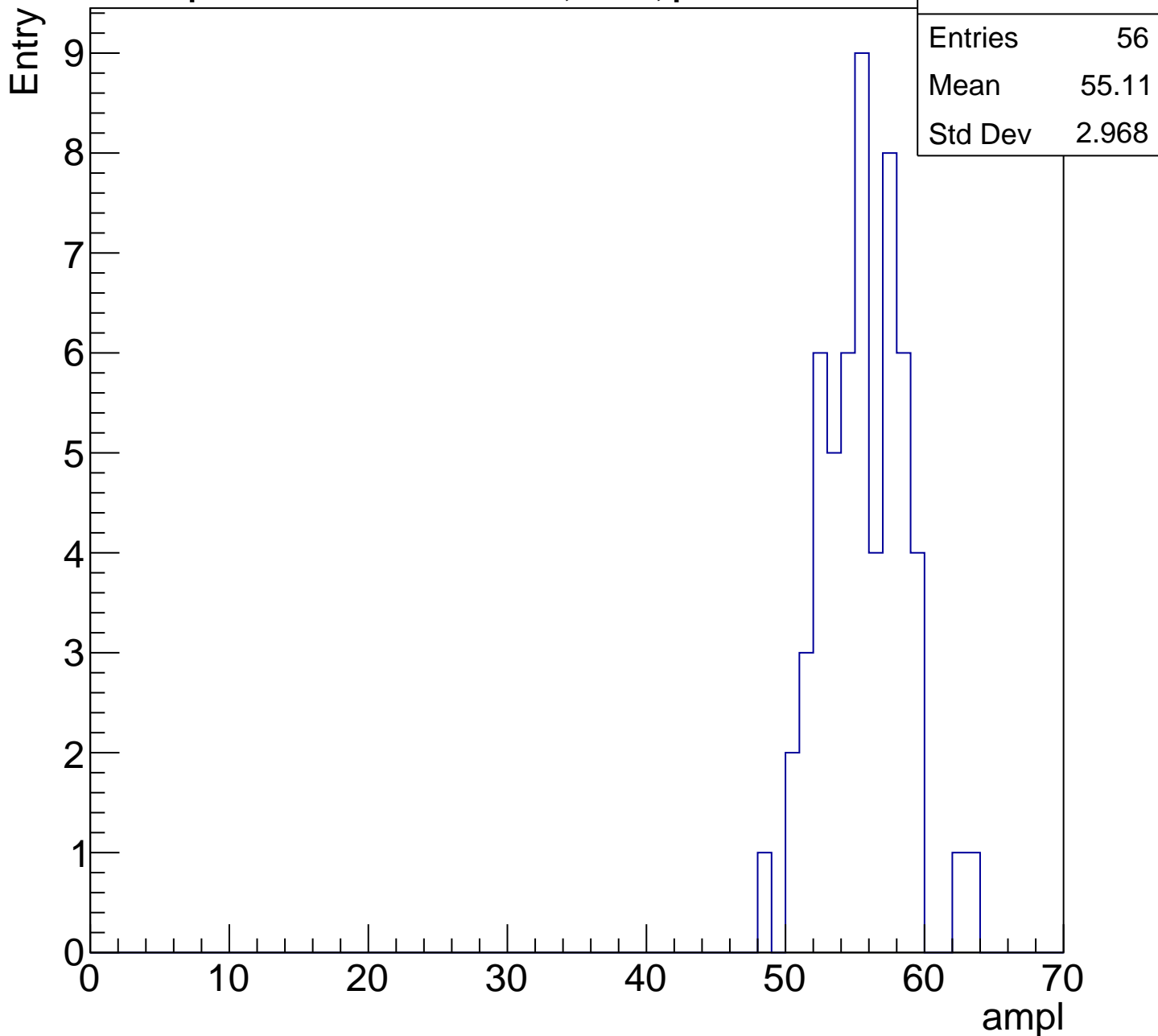
0 10 20 30 40 50 60 70

ampl



B1L100S, U13-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

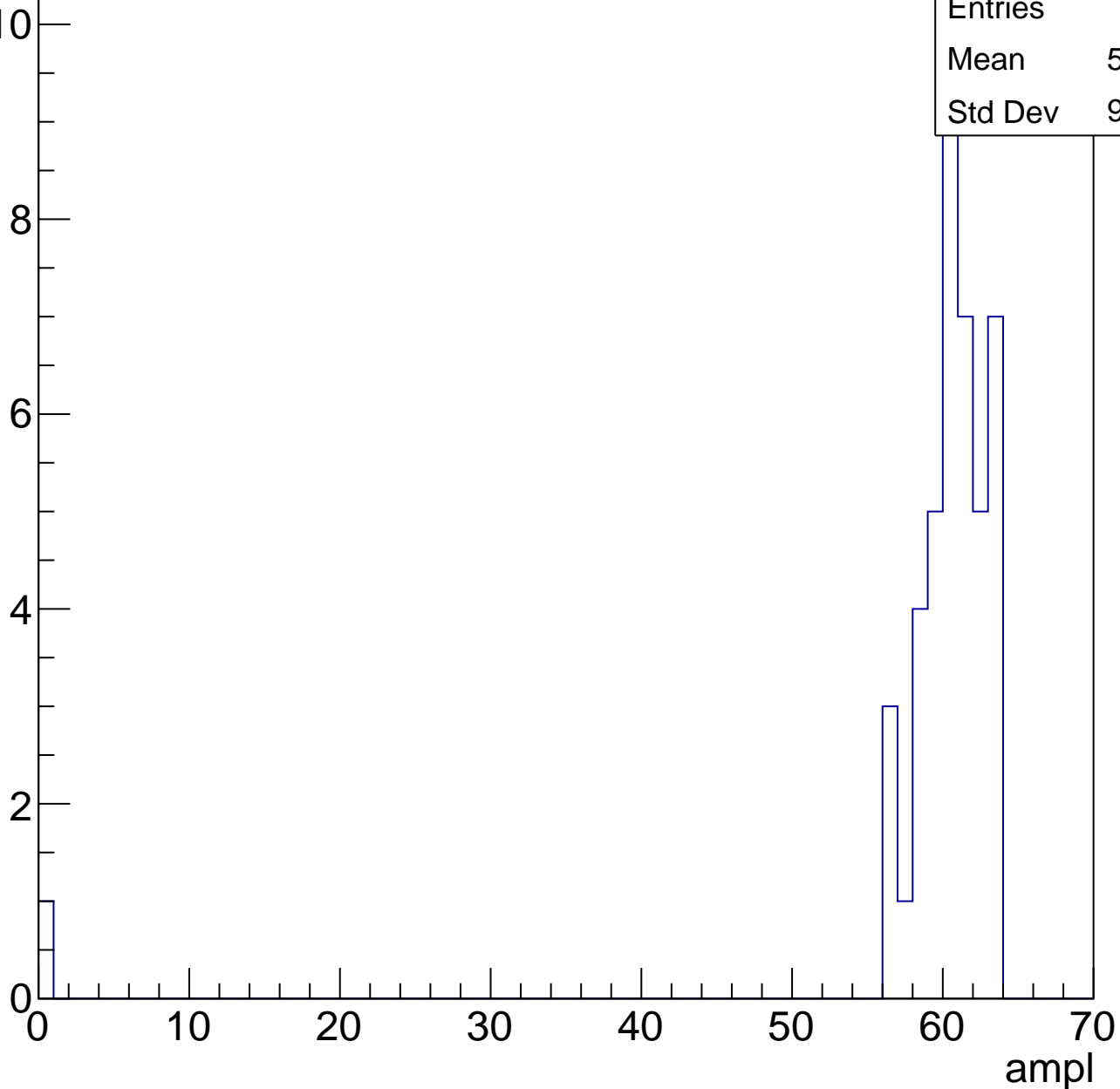


B1L100S, U13-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	58.84
Std Dev	9.288



B1L100S, U13-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.88

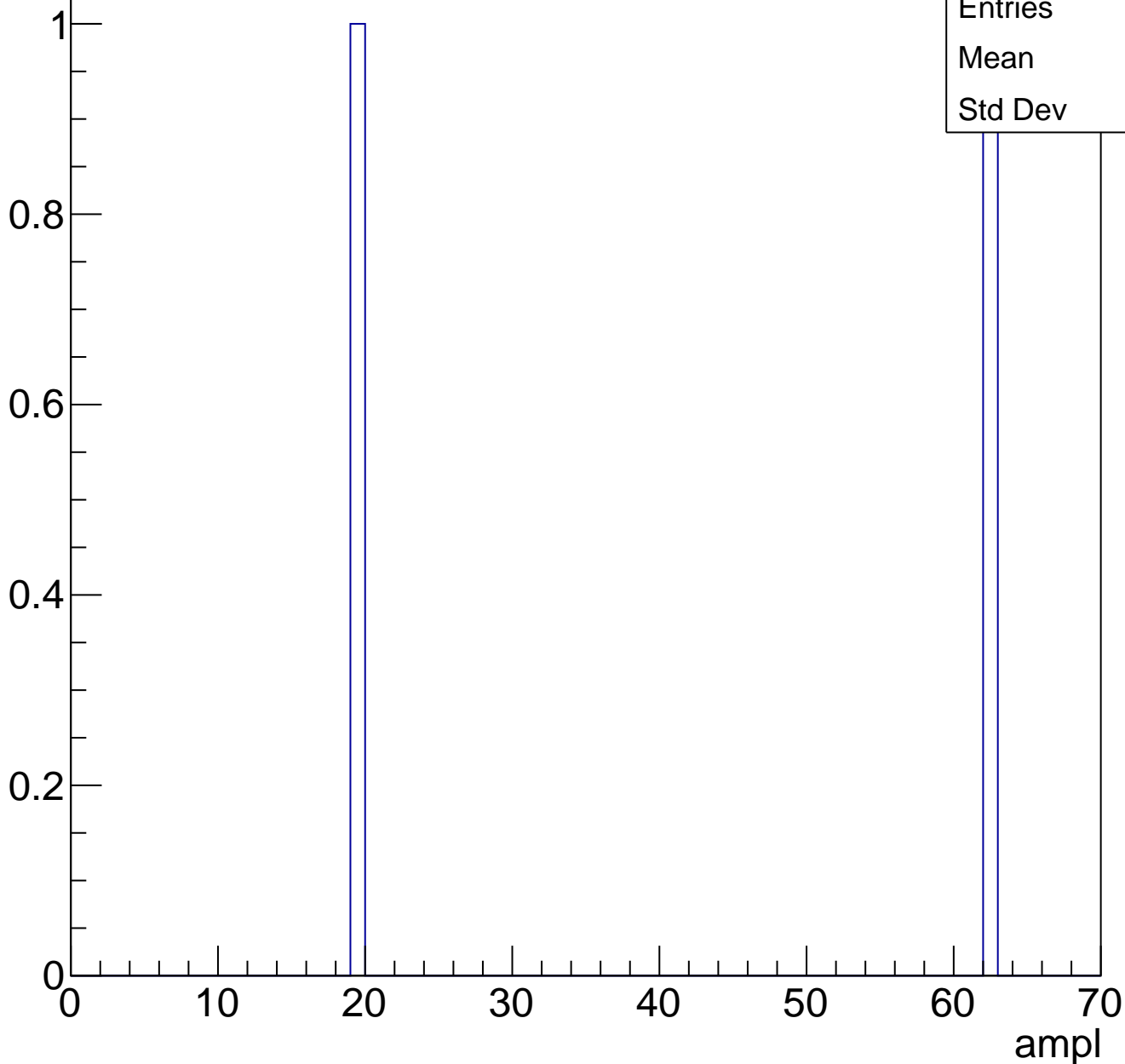
Std Dev

0.7806

B1L100S, U13-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch86, adc0

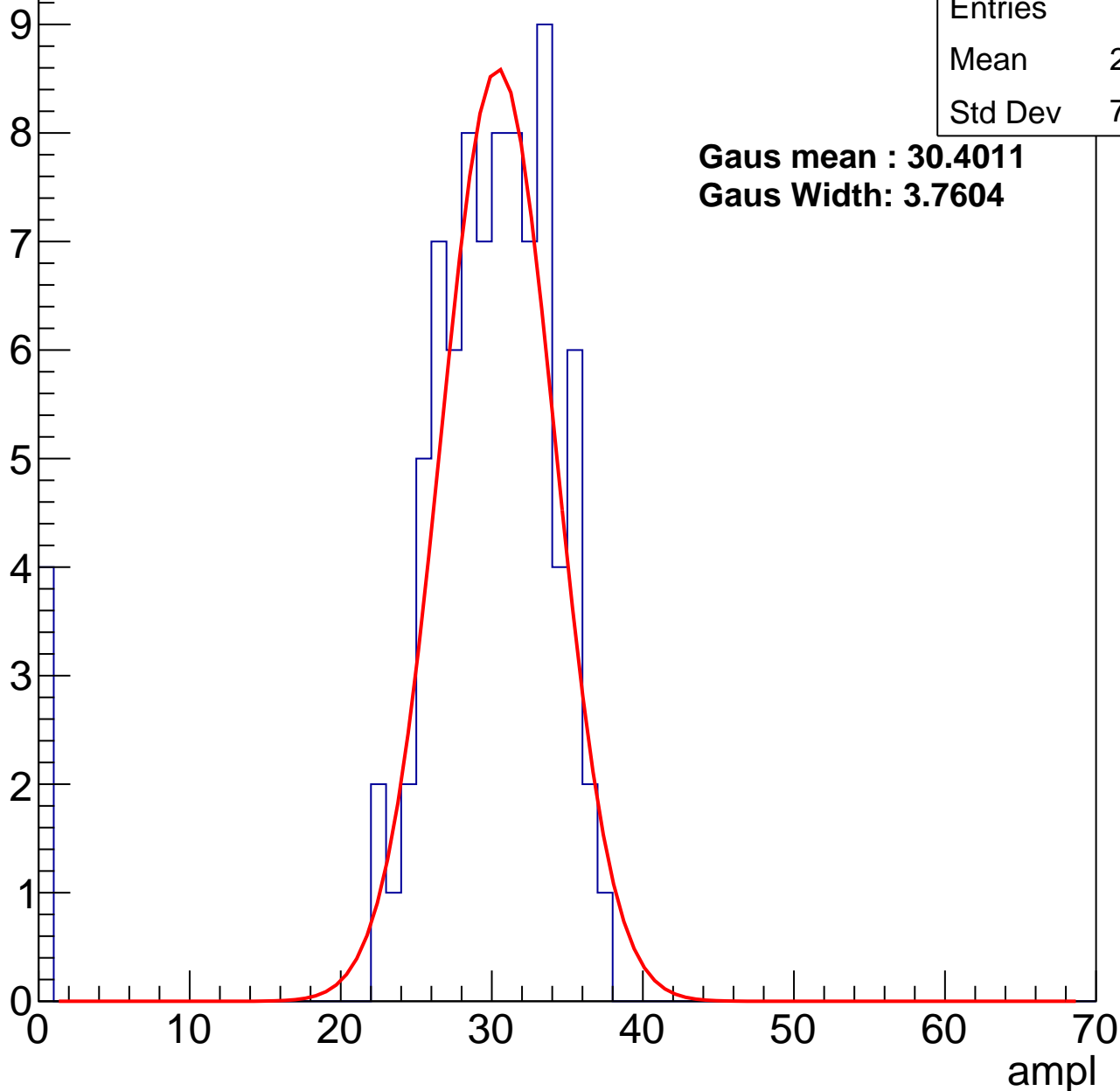
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	28.45
Std Dev	7.129

Gaus mean : 30.4011

Gaus Width: 3.7604



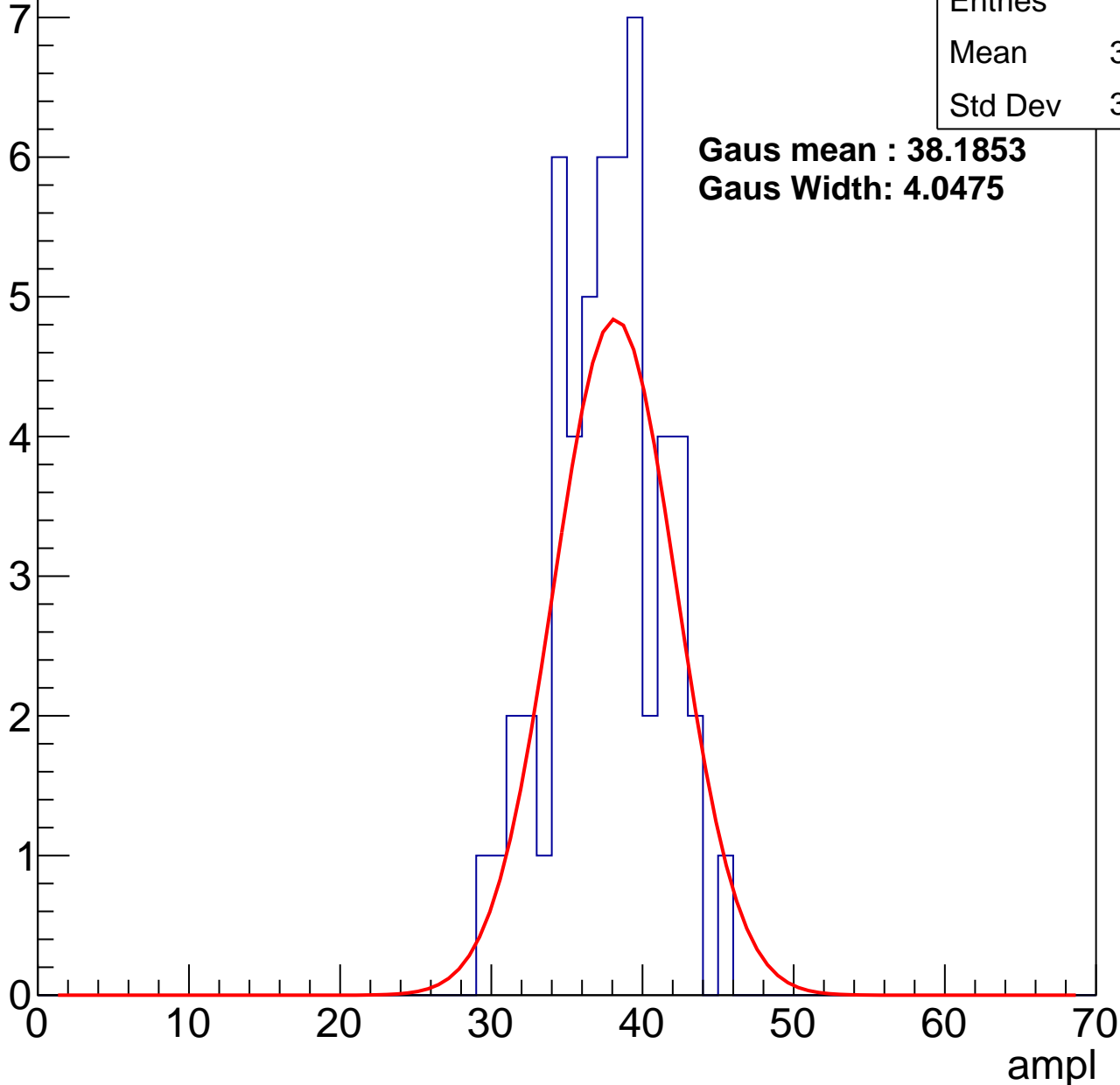
B1L100S, U13-ch86, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	37.19
Std Dev	3.539

Gaus mean : 38.1853
Gaus Width: 4.0475



B1L100S, U13-ch86, adc2

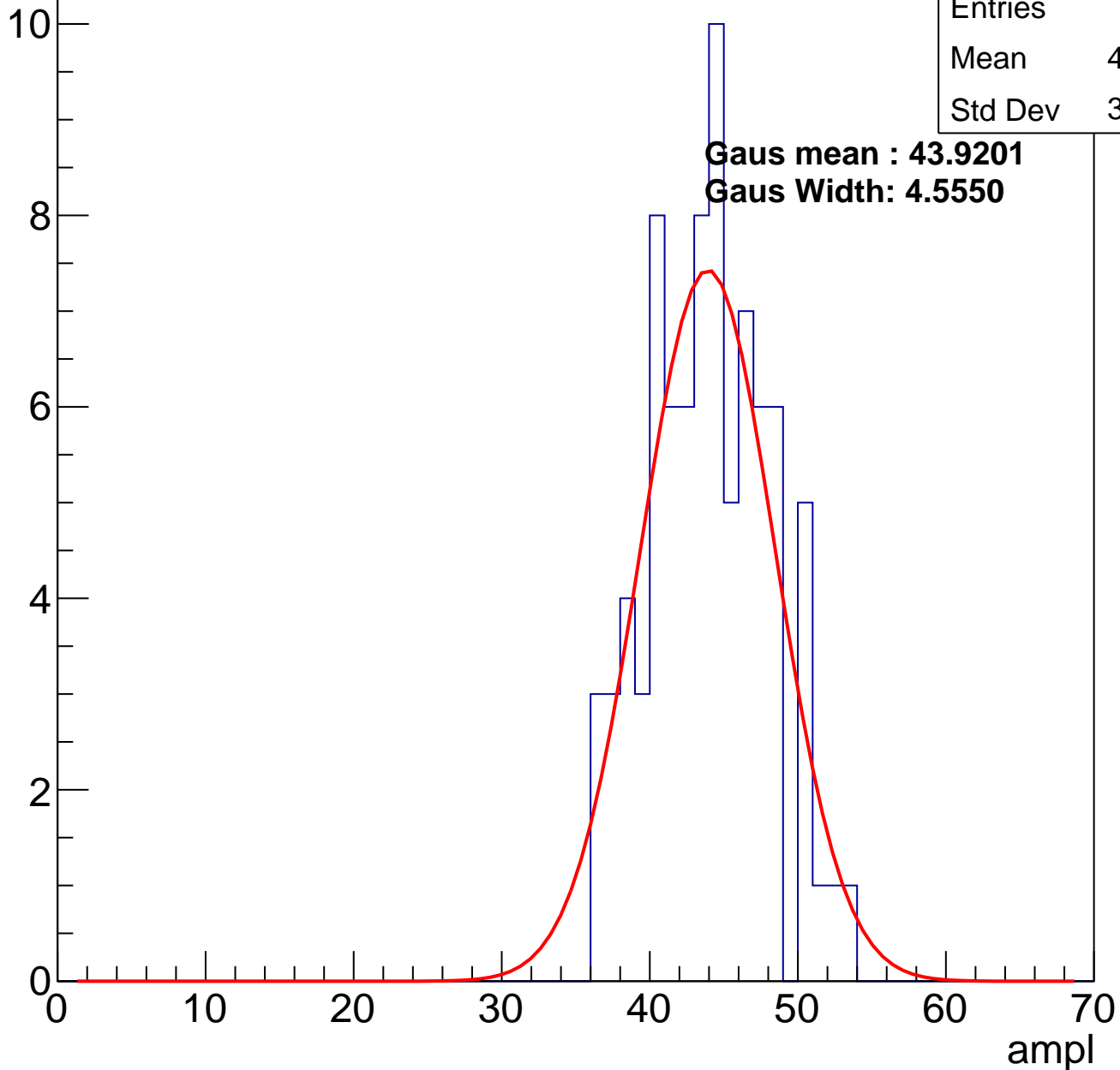
calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	43.53
Std Dev	3.974

Gaus mean : 43.9201

Gaus Width: 4.5550

Entry



B1L100S, U13-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	51.21
Std Dev	3.362

Entry

10

8

6

4

2

0

0

10

20

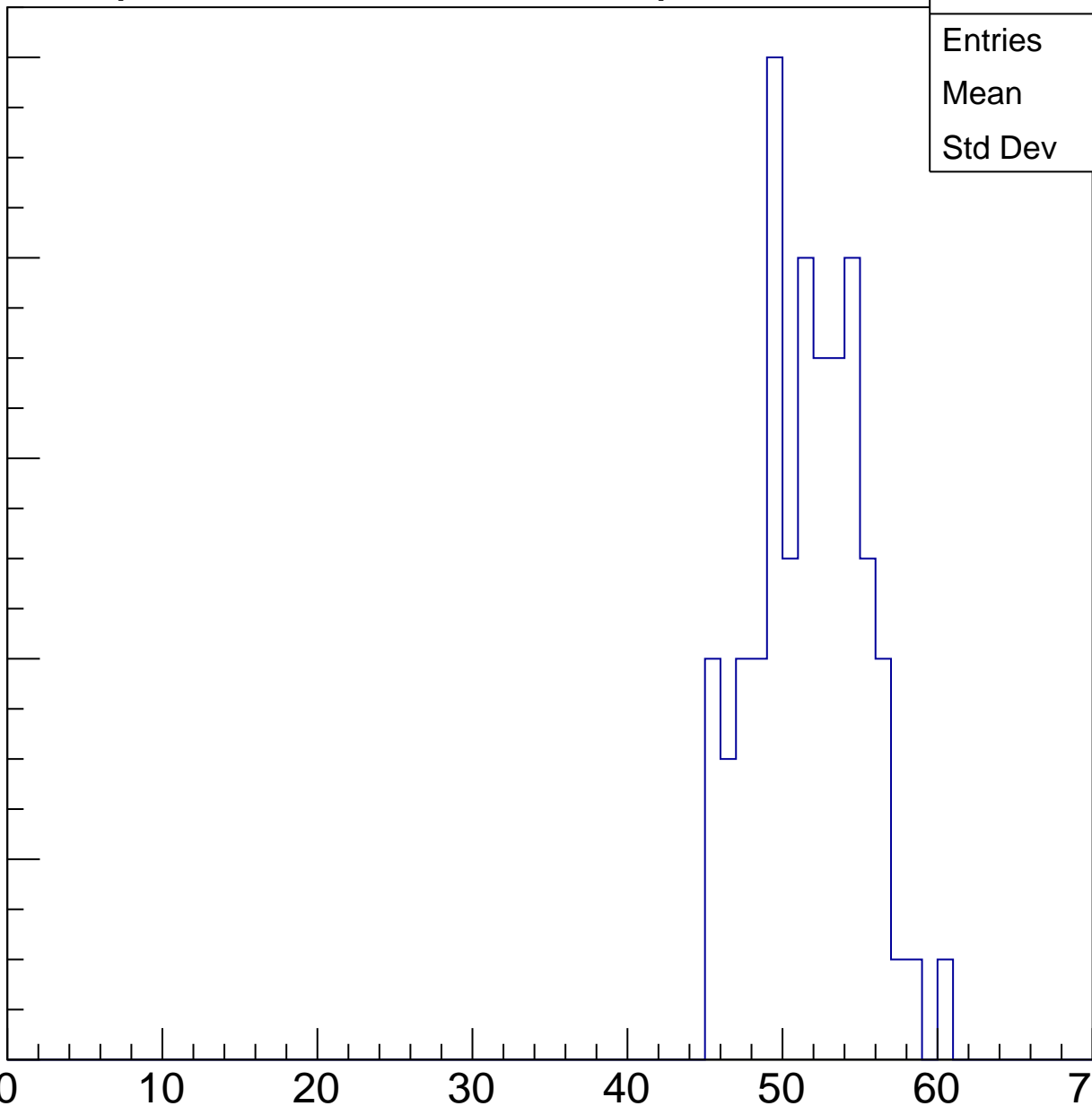
30

40

50

60

ampl

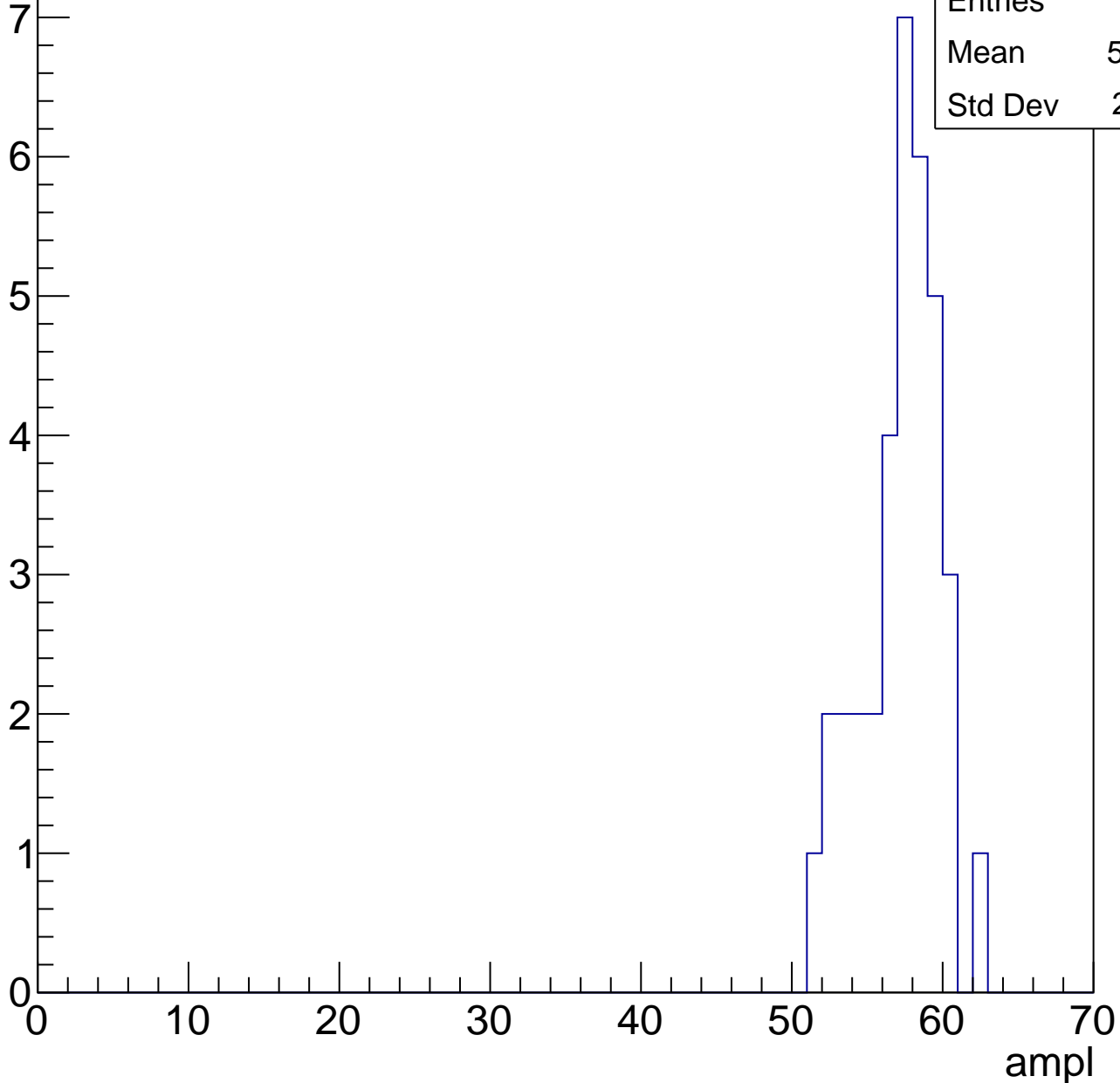


B1L100S, U13-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

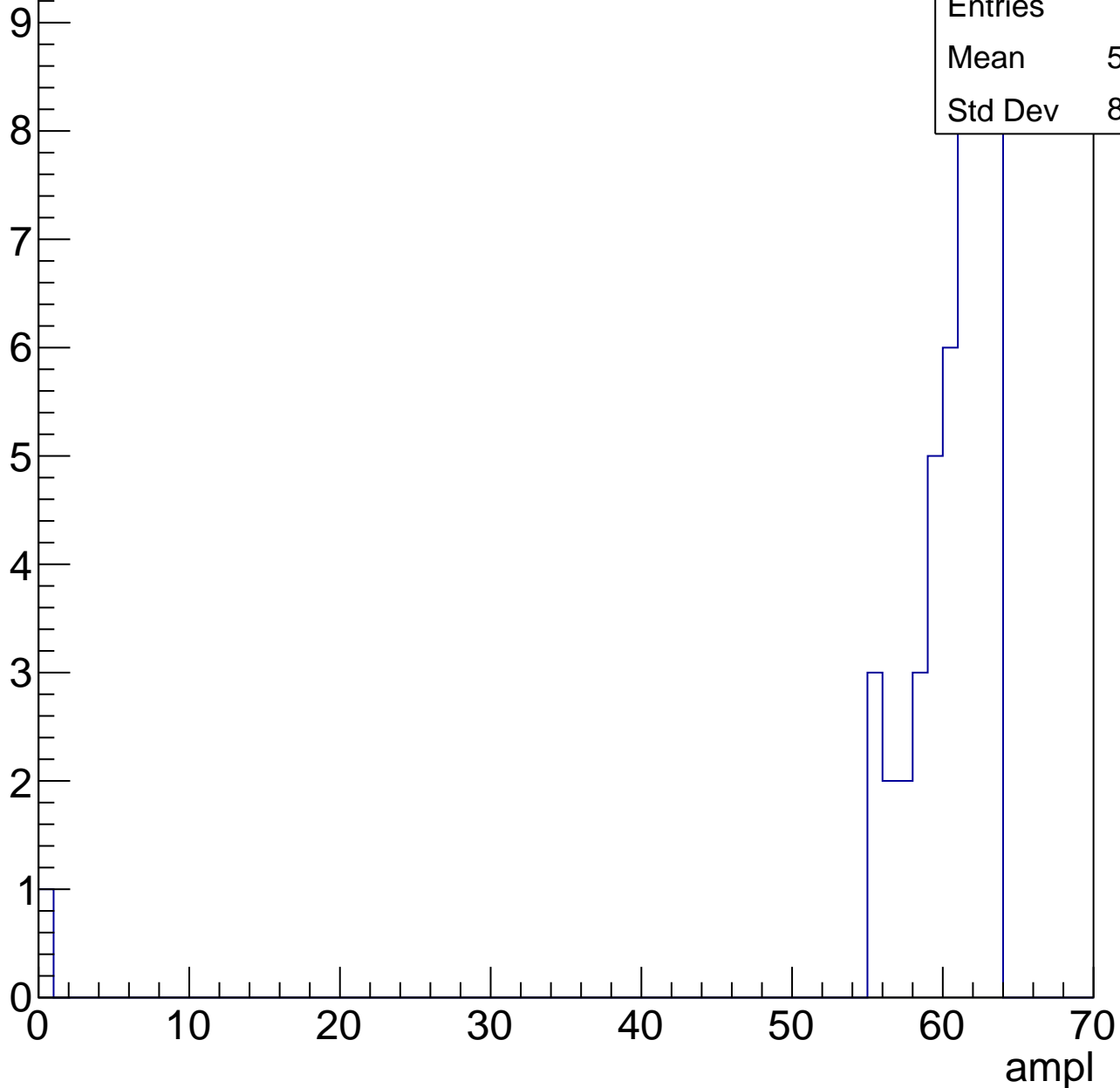
Entries	35
Mean	56.77
Std Dev	2.531



B1L100S, U13-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch87, adc0

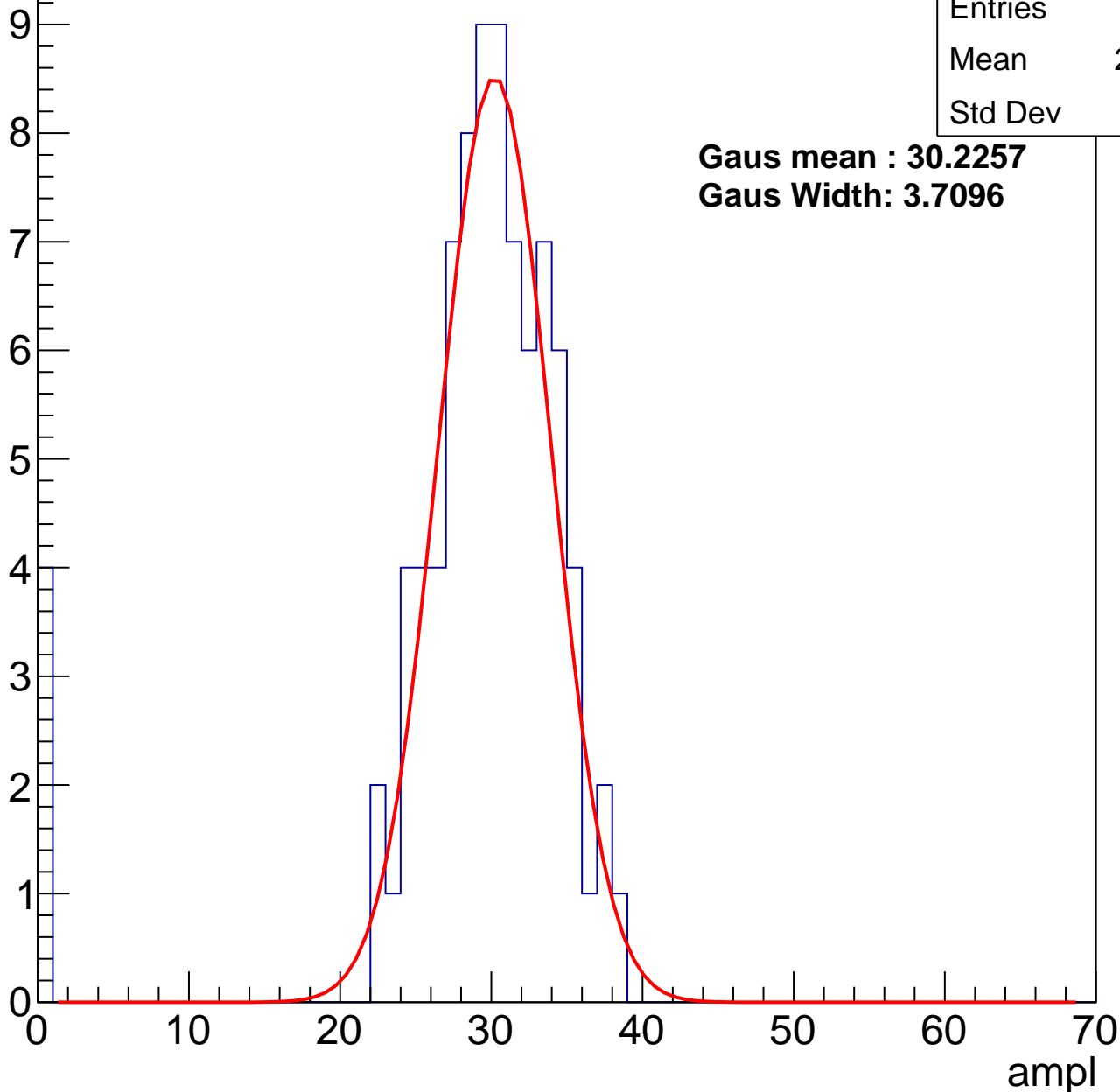
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	28.41
Std Dev	7.2

Gaus mean : 30.2257

Gaus Width: 3.7096



B1L100S, U13-ch87, adc1

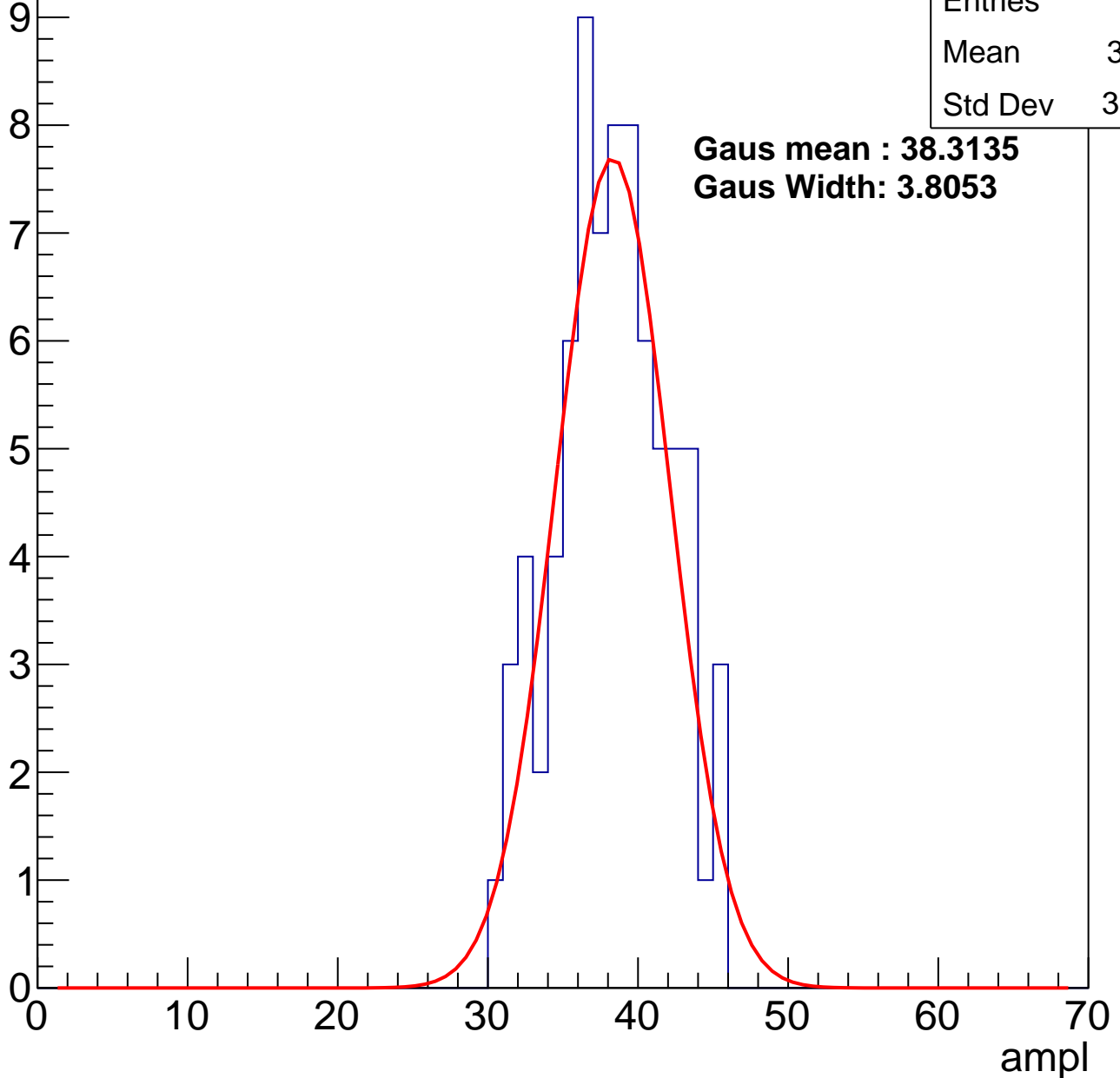
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	37.81
Std Dev	3.654

Gaus mean : 38.3135

Gaus Width: 3.8053



B1L100S, U13-ch87, adc2

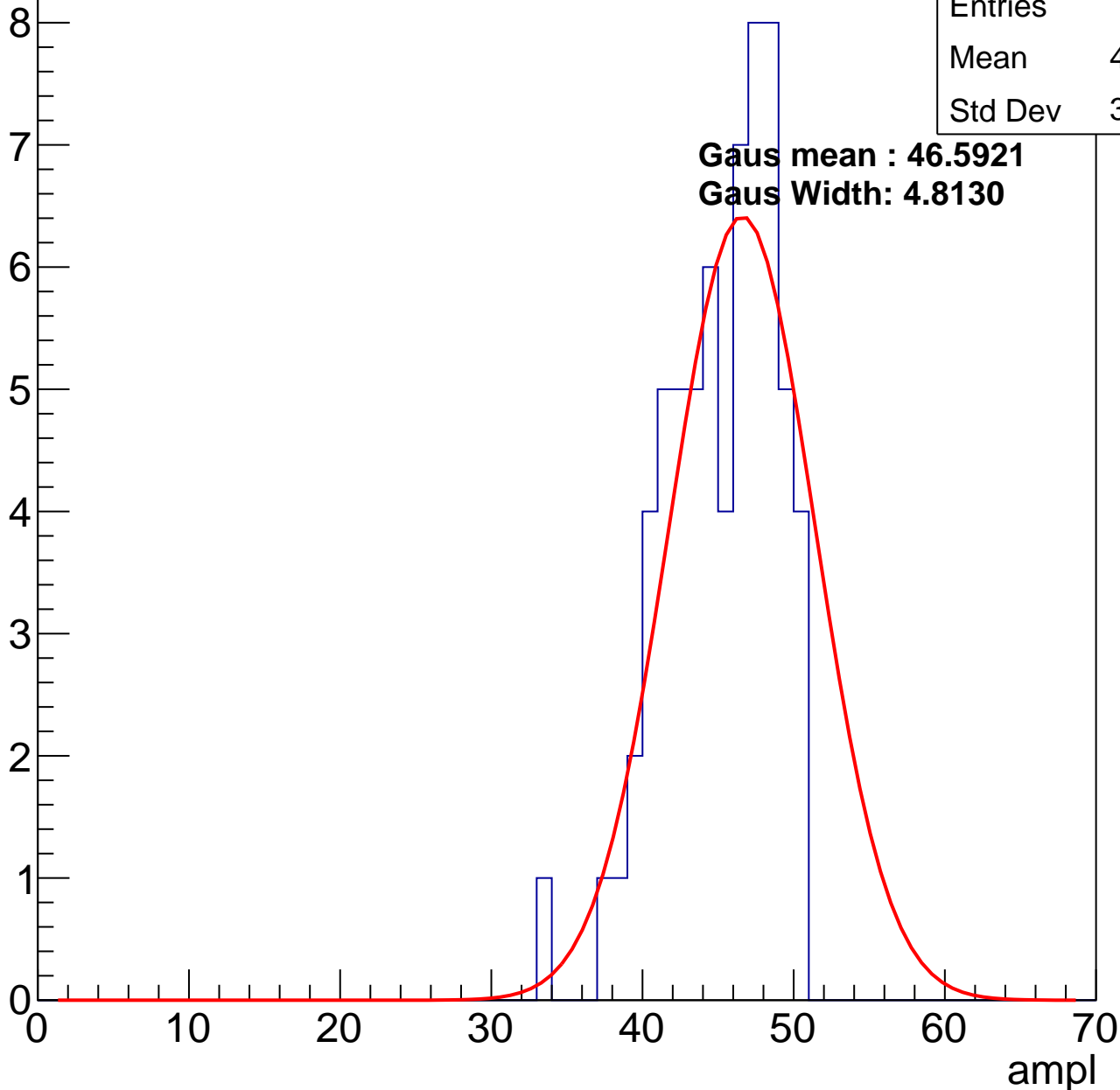
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.65
Std Dev	3.616

Gaus mean : 46.5921

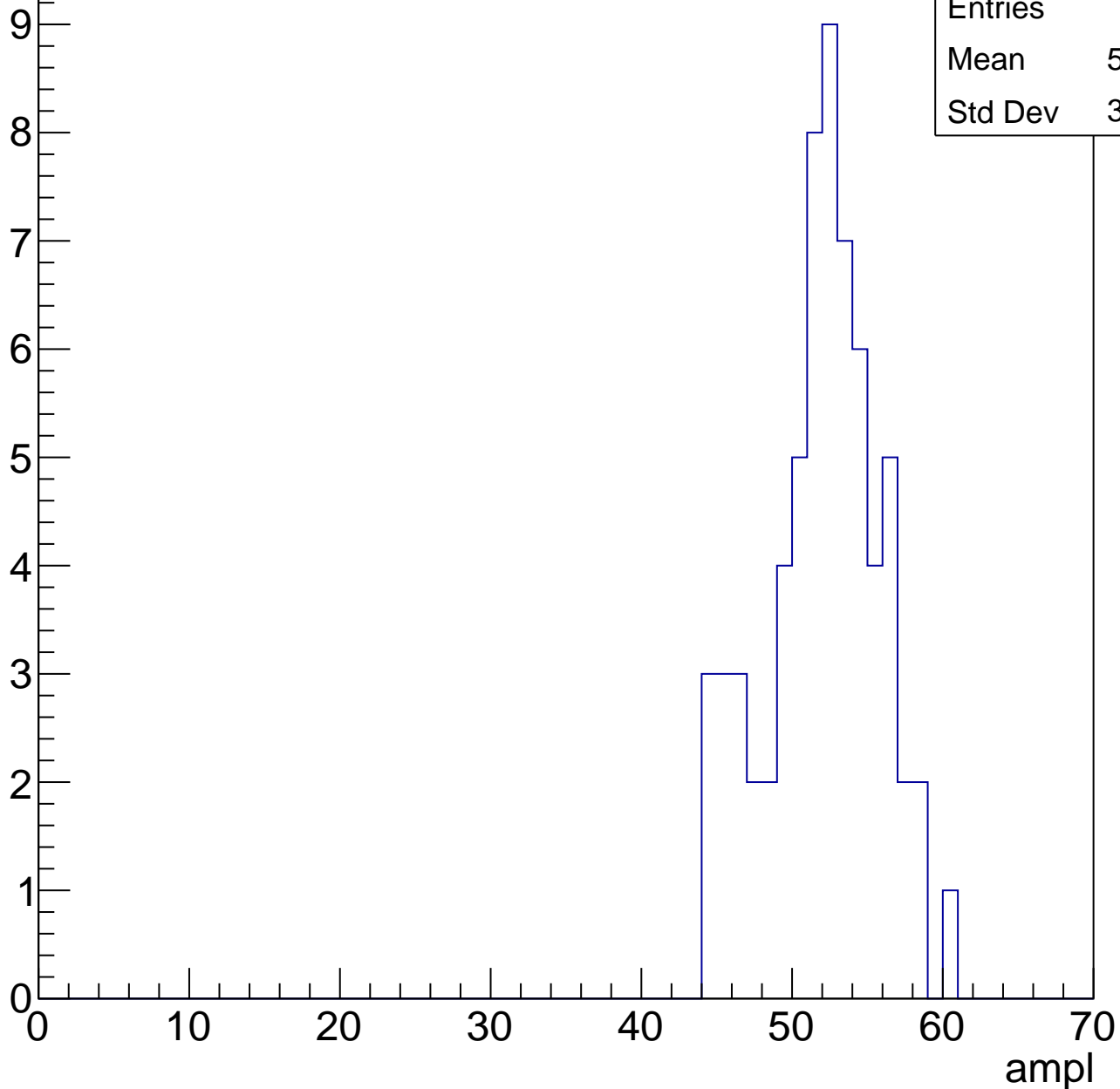
Gaus Width: 4.8130



B1L100S, U13-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

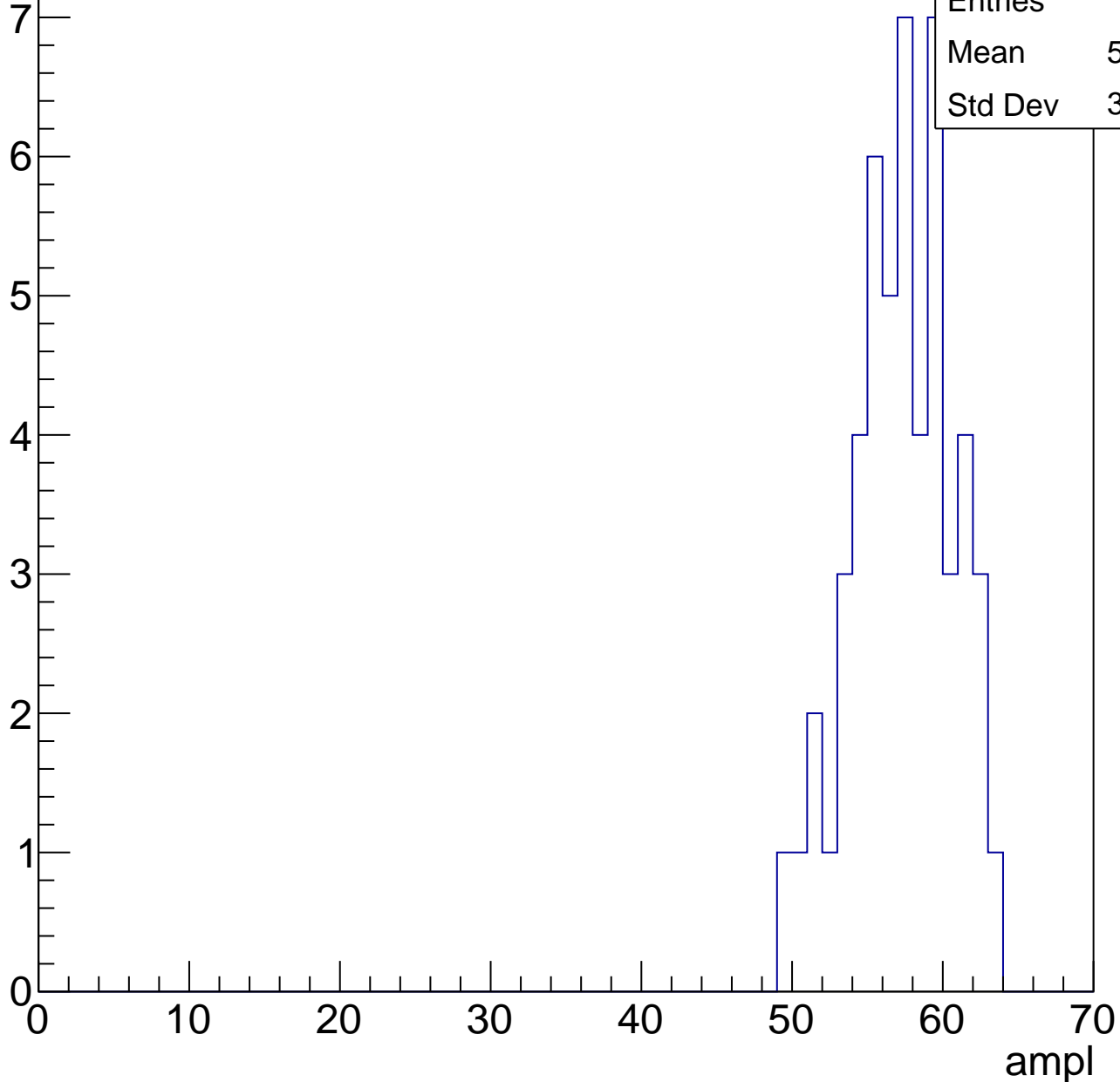


Entries	66
Mean	51.55
Std Dev	3.718

B1L100S, U13-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

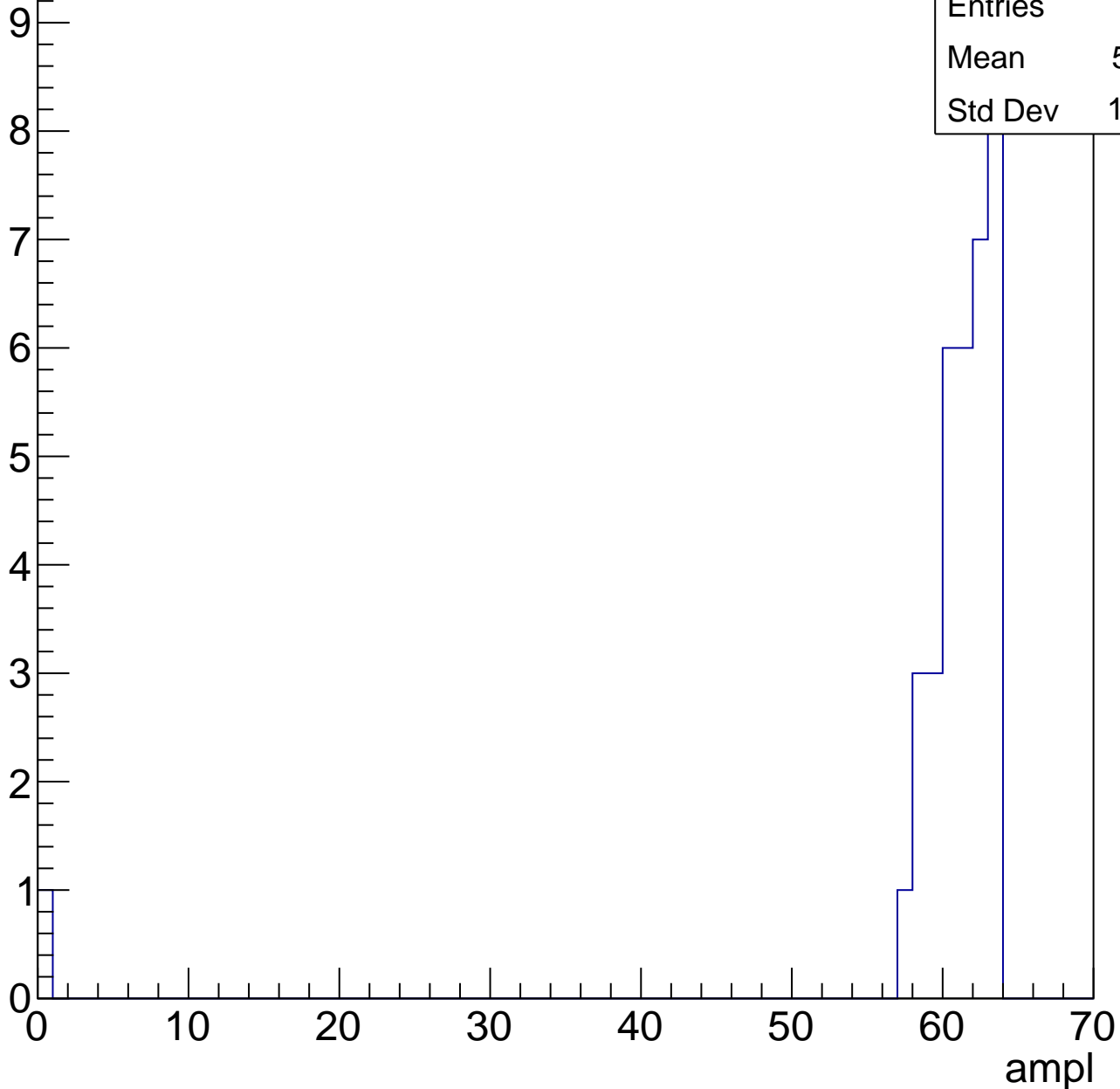


B1L100S, U13-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

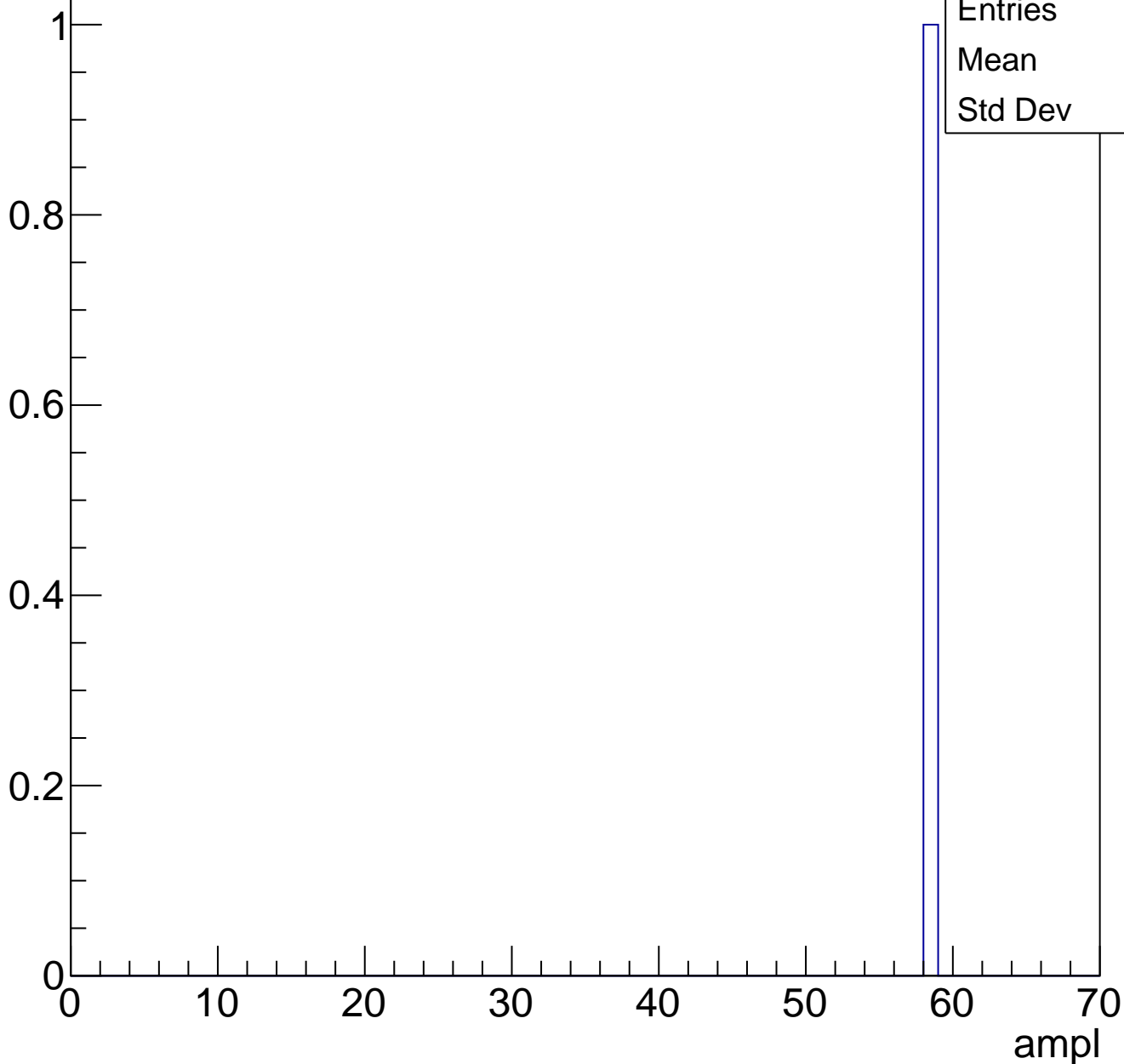
Entries	36
Mean	59.31
Std Dev	10.17



B1L100S, U13-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch88, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	27.6
Std Dev	6.911

Gaus mean : 29.0651

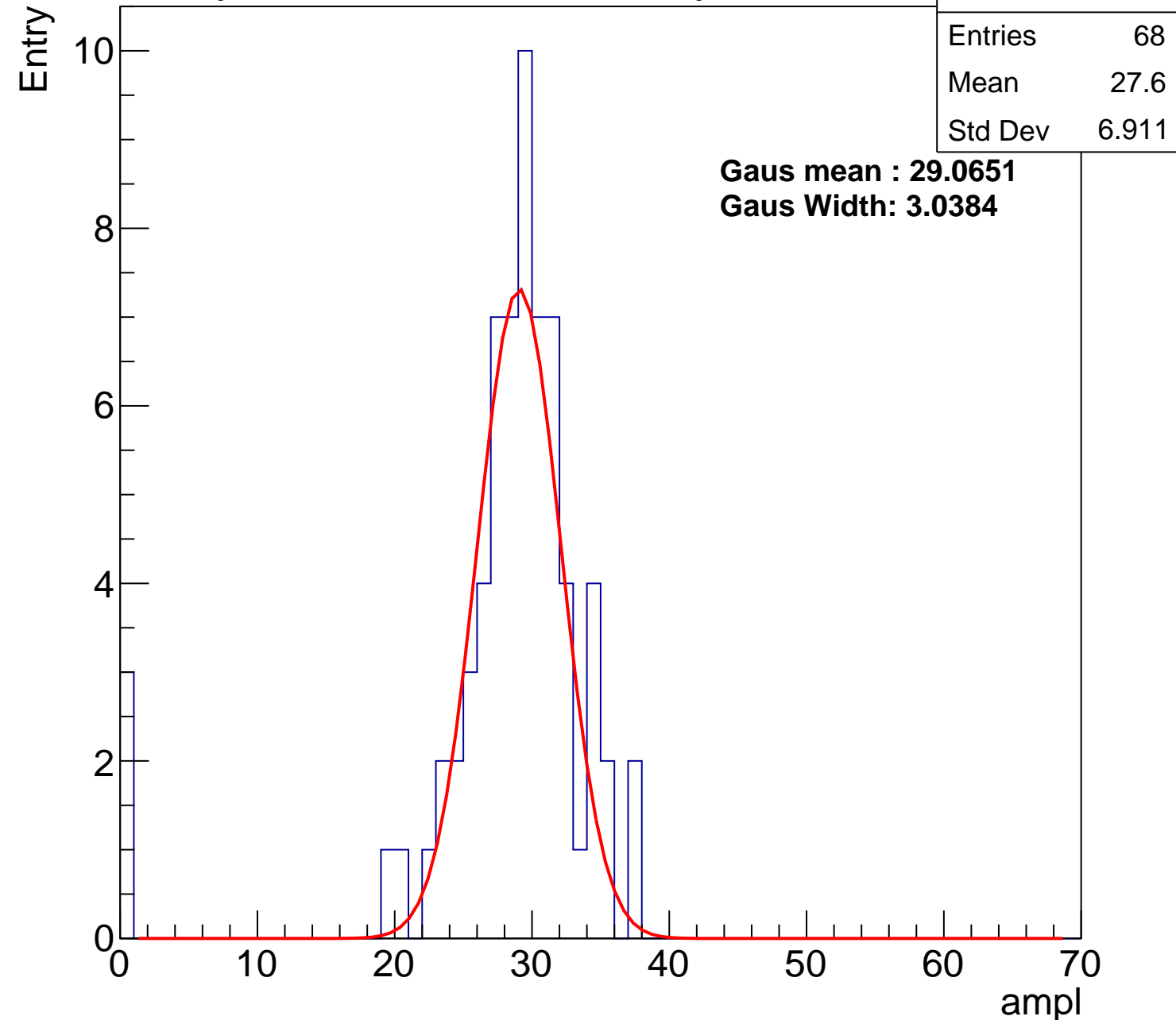
Gaus Width: 3.0384

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch88, adc1

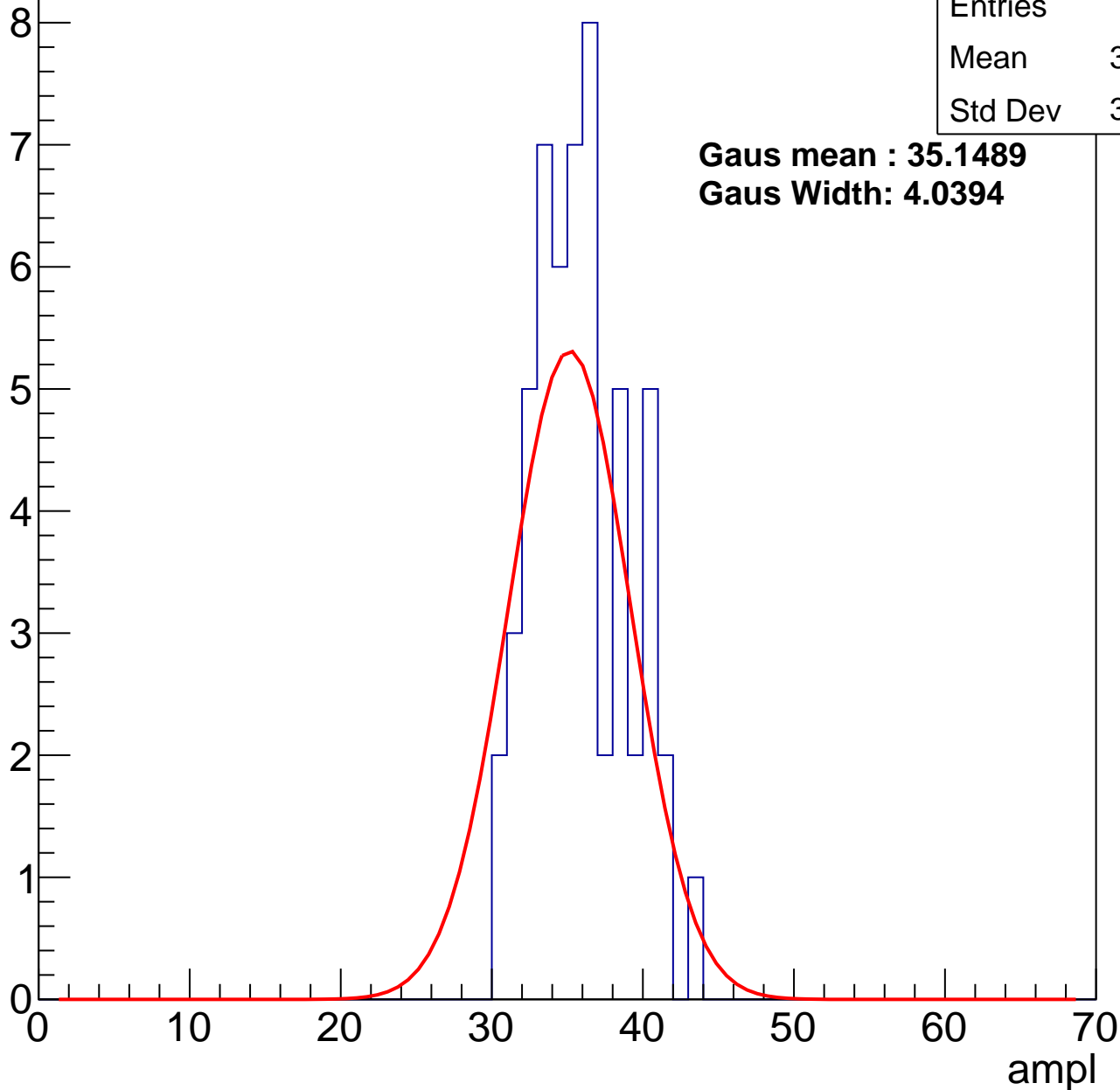
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.42
Std Dev	3.079

Gaus mean : 35.1489

Gaus Width: 4.0394



B1L100S, U13-ch88, adc2

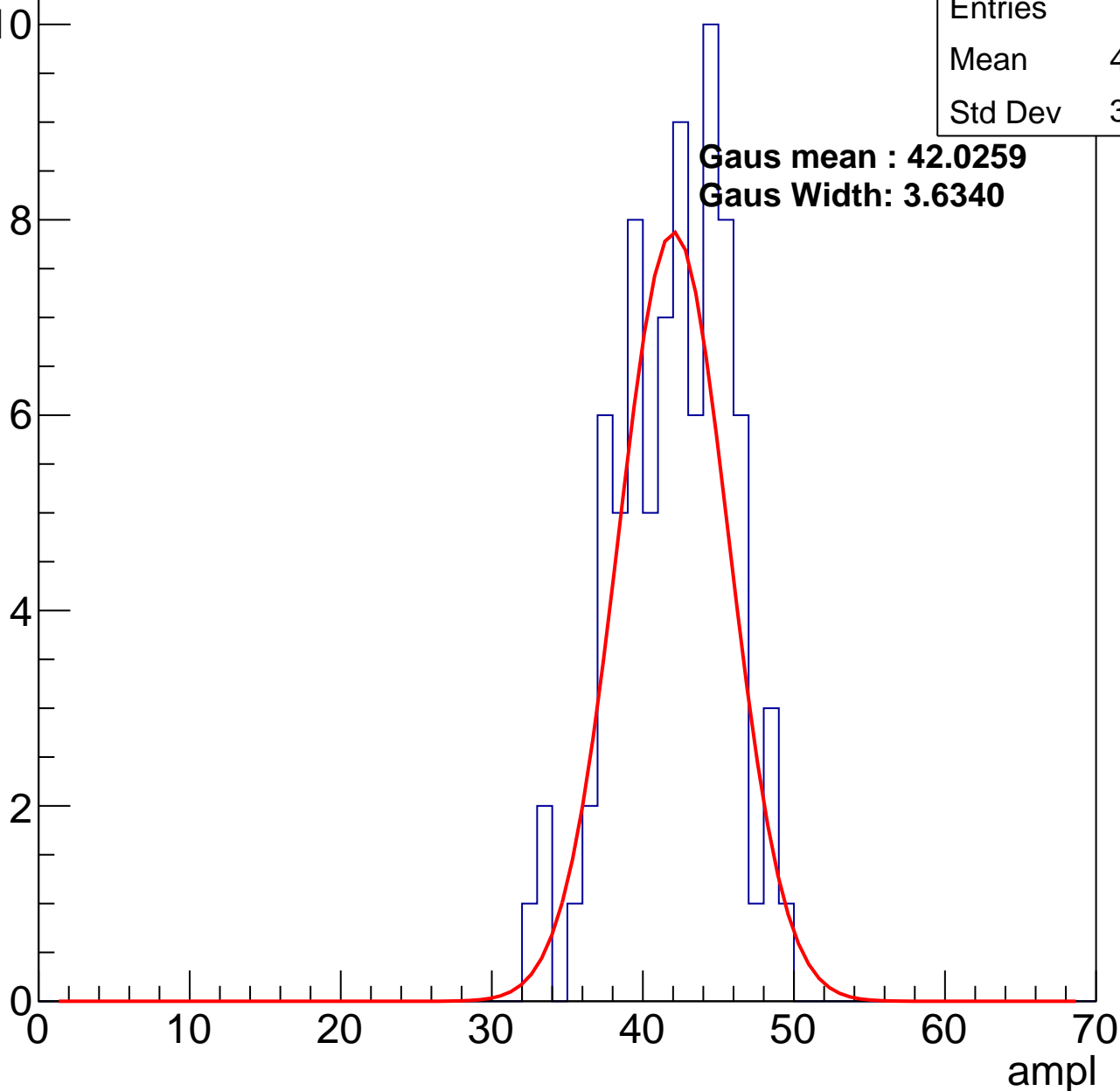
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	41.58
Std Dev	3.675

Gaus mean : 42.0259

Gaus Width: 3.6340

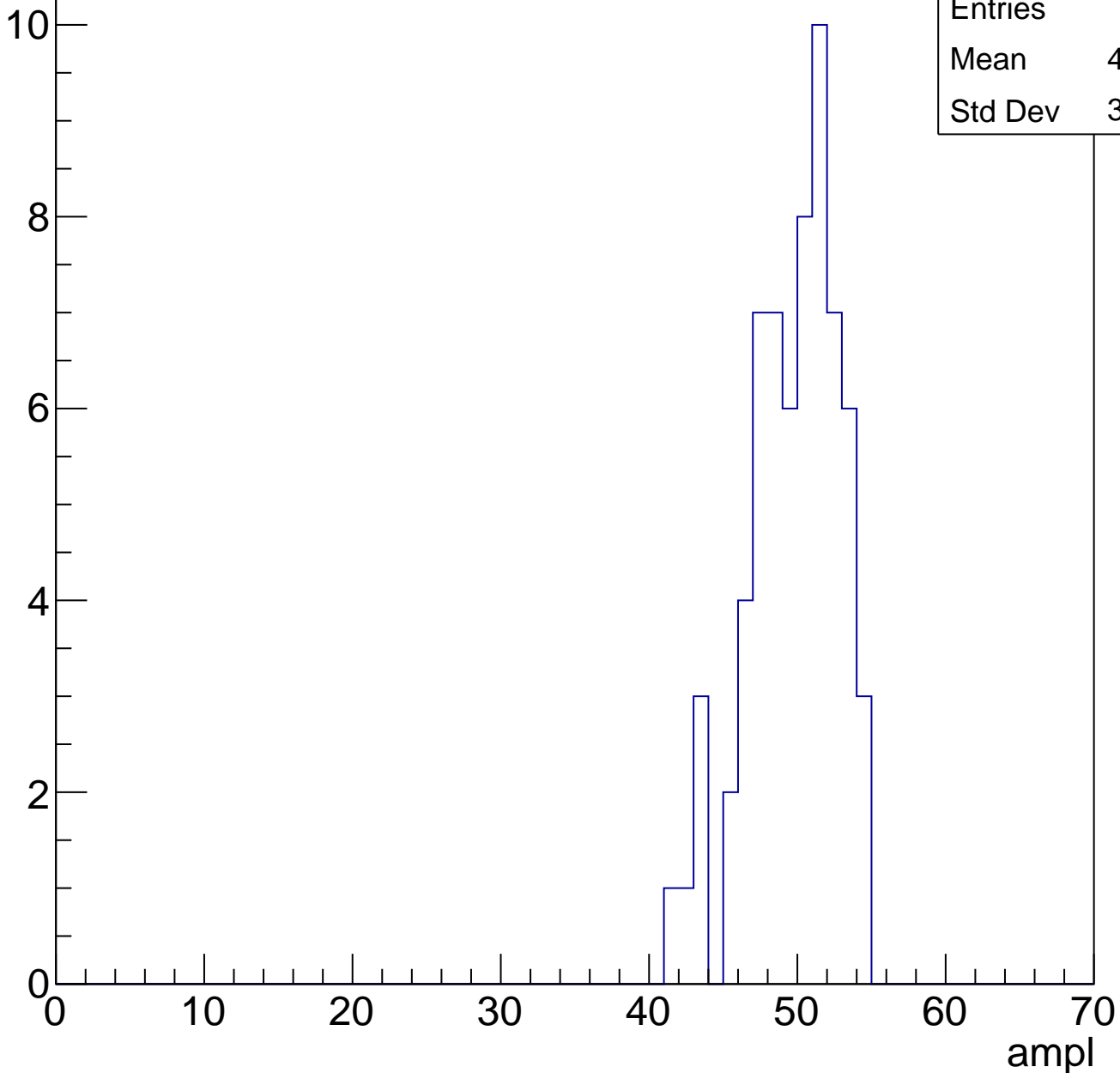


B1L100S, U13-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.22
Std Dev	3.046



B1L100S, U13-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	55.89
Std Dev	3.589

Entry

10

8

6

4

2

0

0

10

20

30

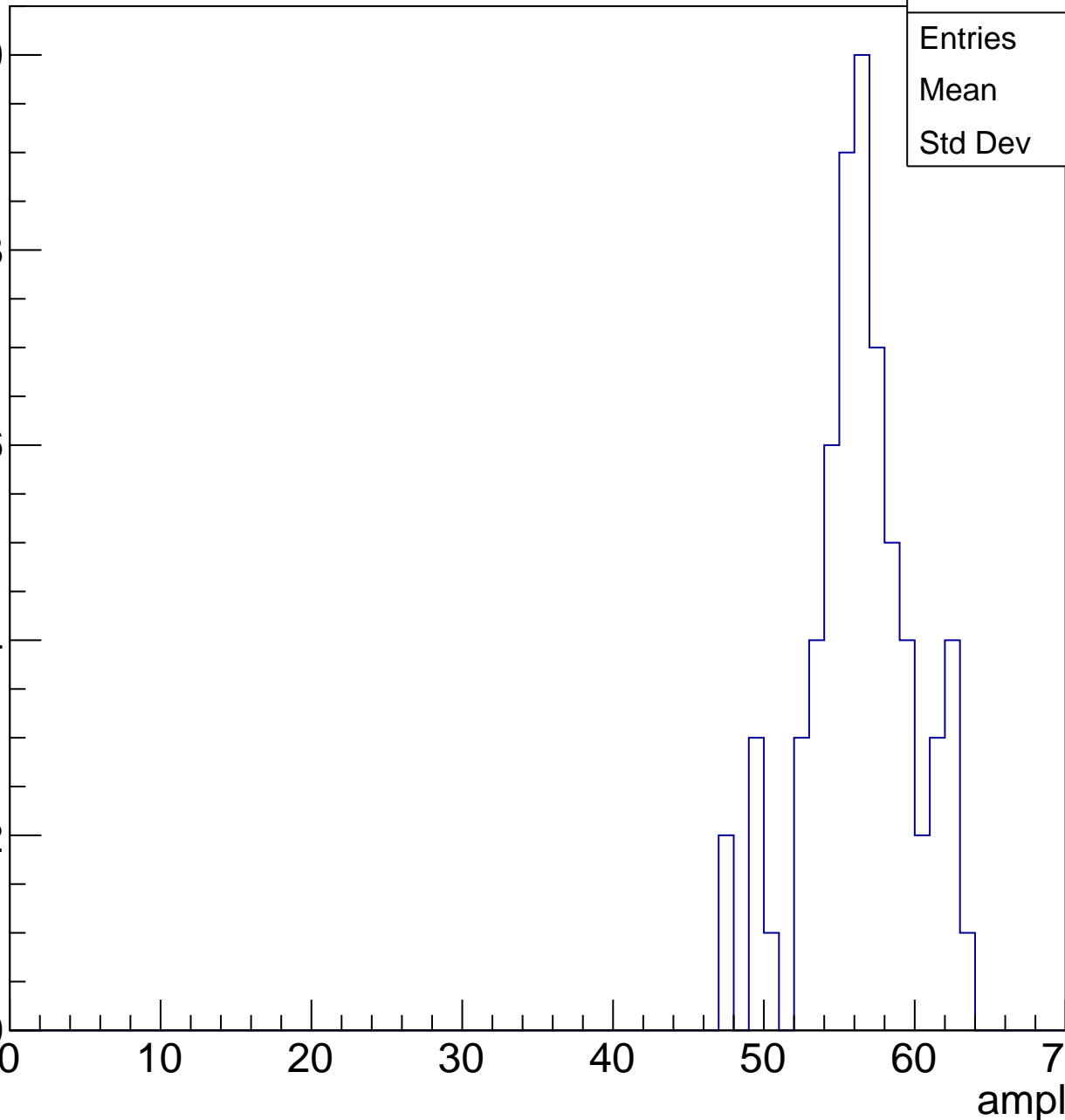
40

50

60

70

ampl



B1L100S, U13-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	59.81
Std Dev	2.657

0

10

20

30

40

50

60

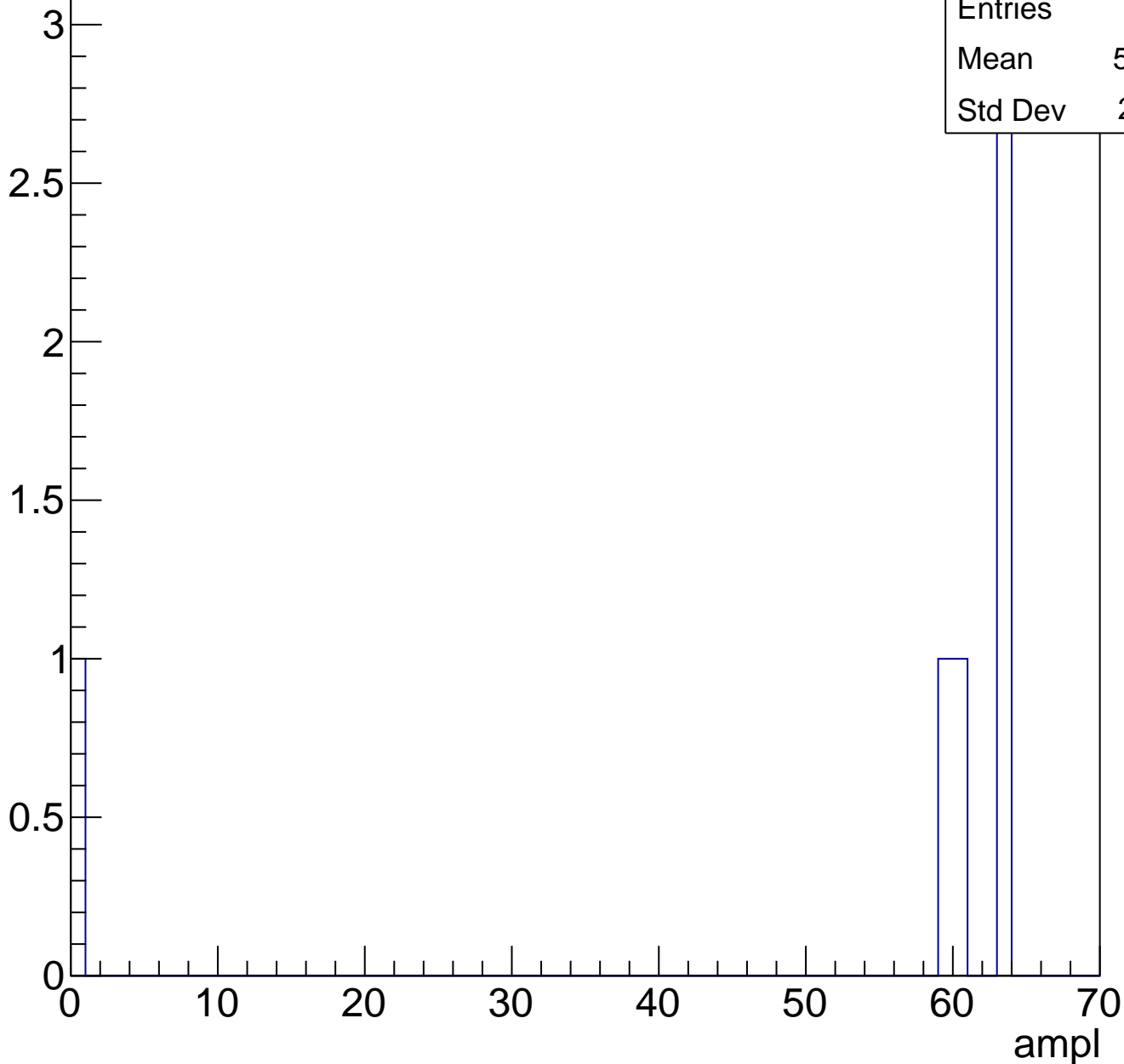
70

ampl

B1L100S, U13-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

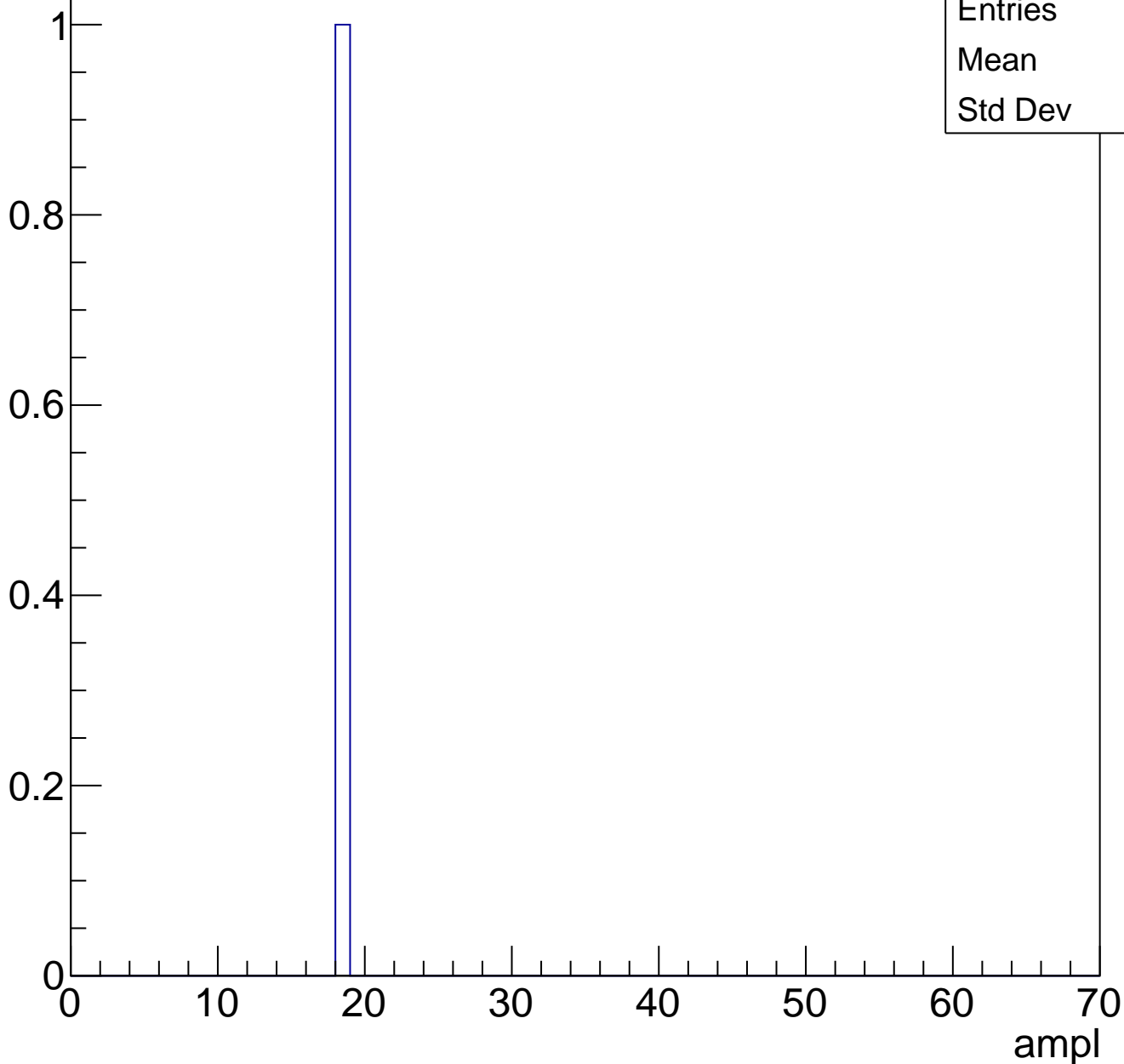
Entry



B1L100S, U13-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch89, adc0

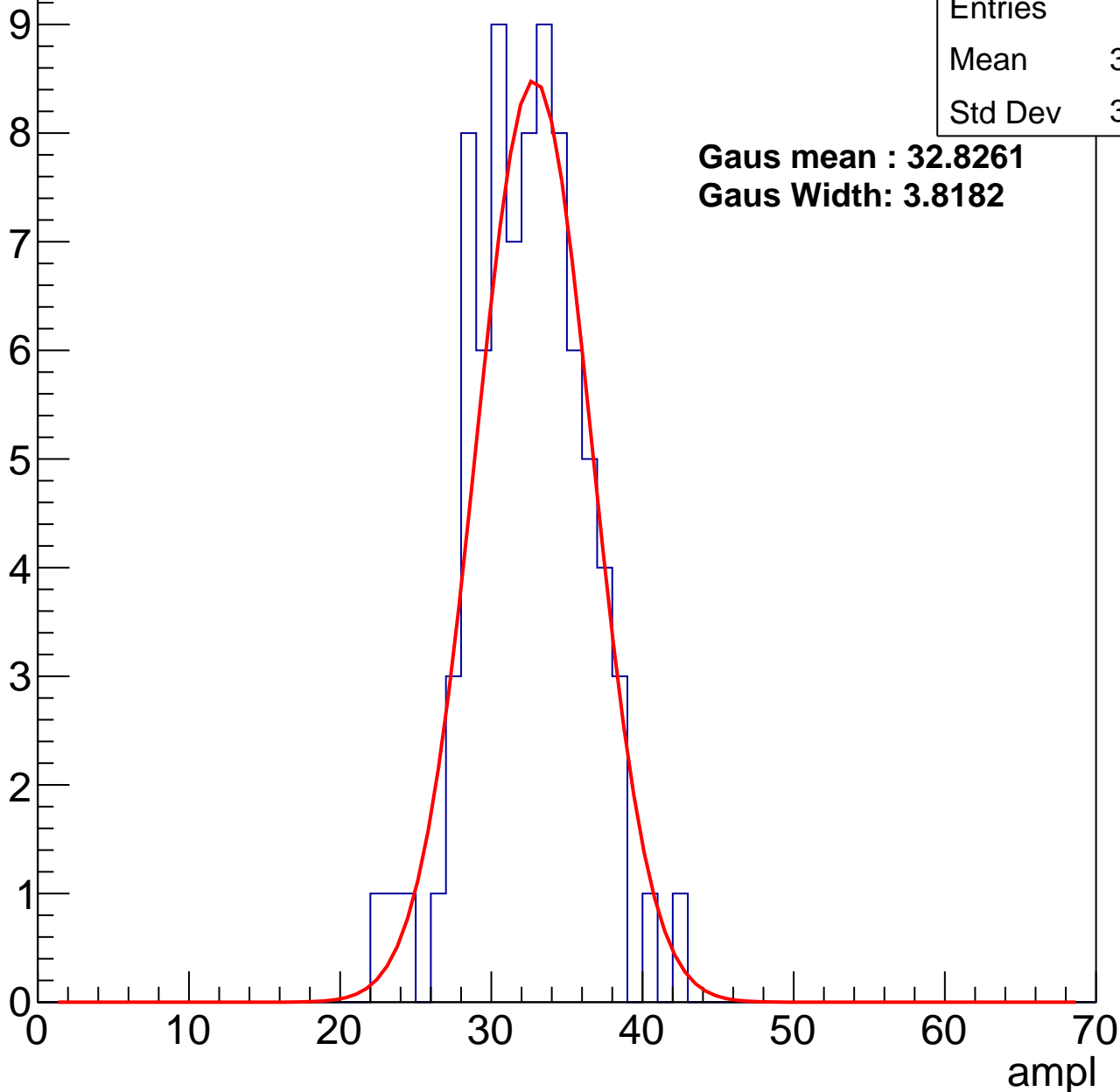
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	31.95
Std Dev	3.709

Gaus mean : 32.8261

Gaus Width: 3.8182



B1L100S, U13-ch89, adc1

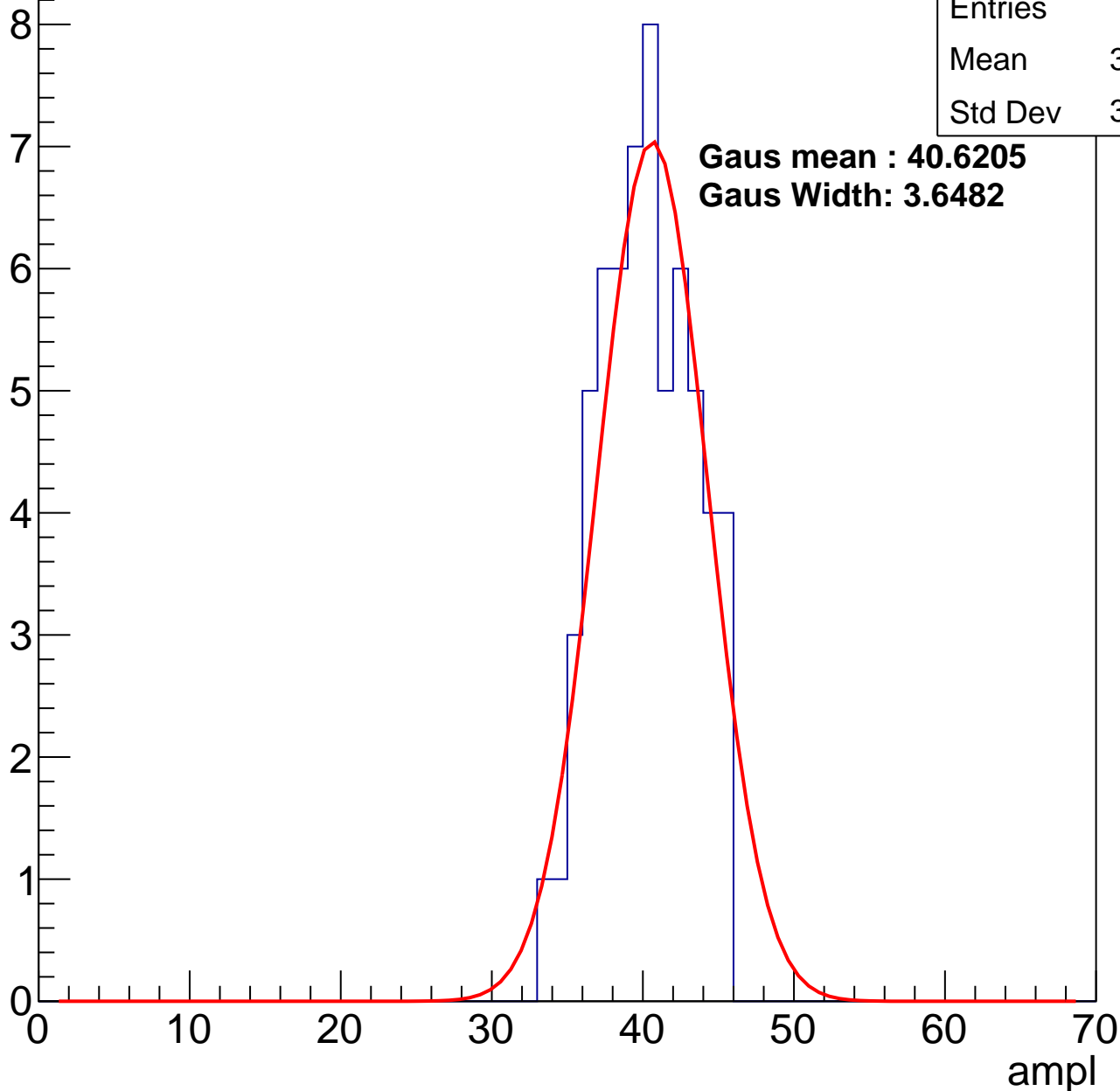
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	39.72
Std Dev	3.025

Gaus mean : 40.6205

Gaus Width: 3.6482



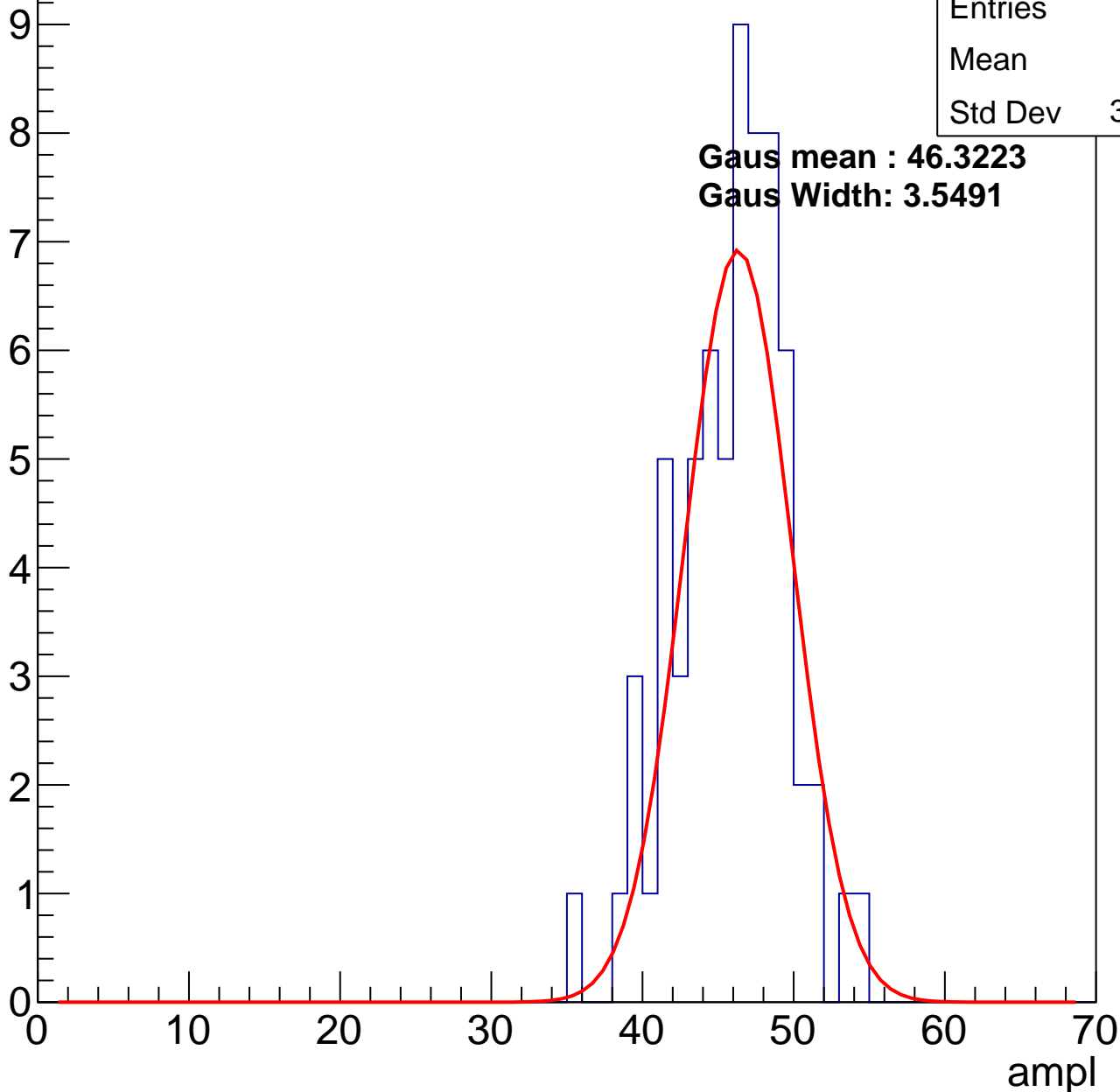
B1L100S, U13-ch89, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	45.4
Std Dev	3.624

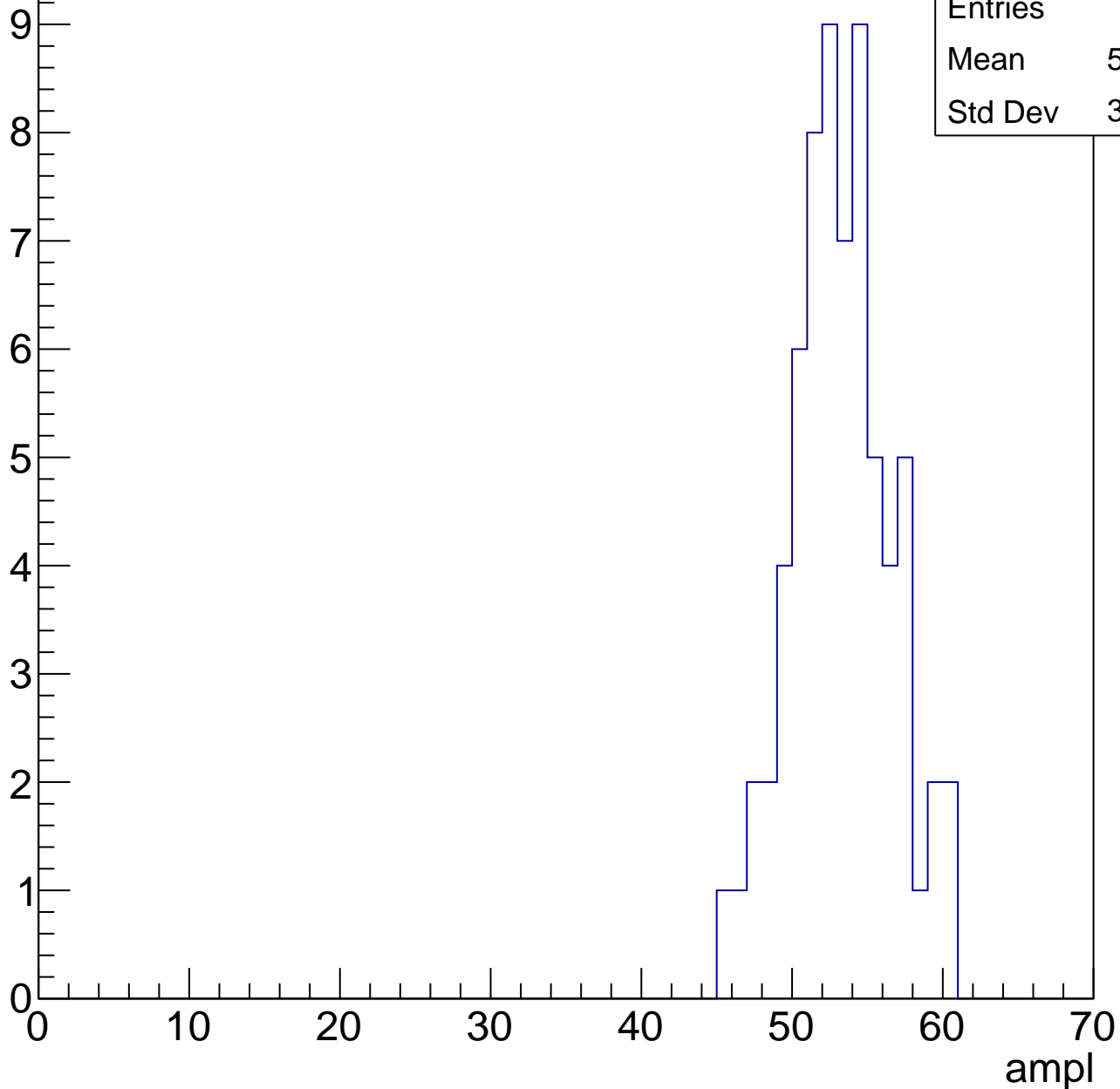
Gaus mean : 46.3223
Gaus Width: 3.5491



B1L100S, U13-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

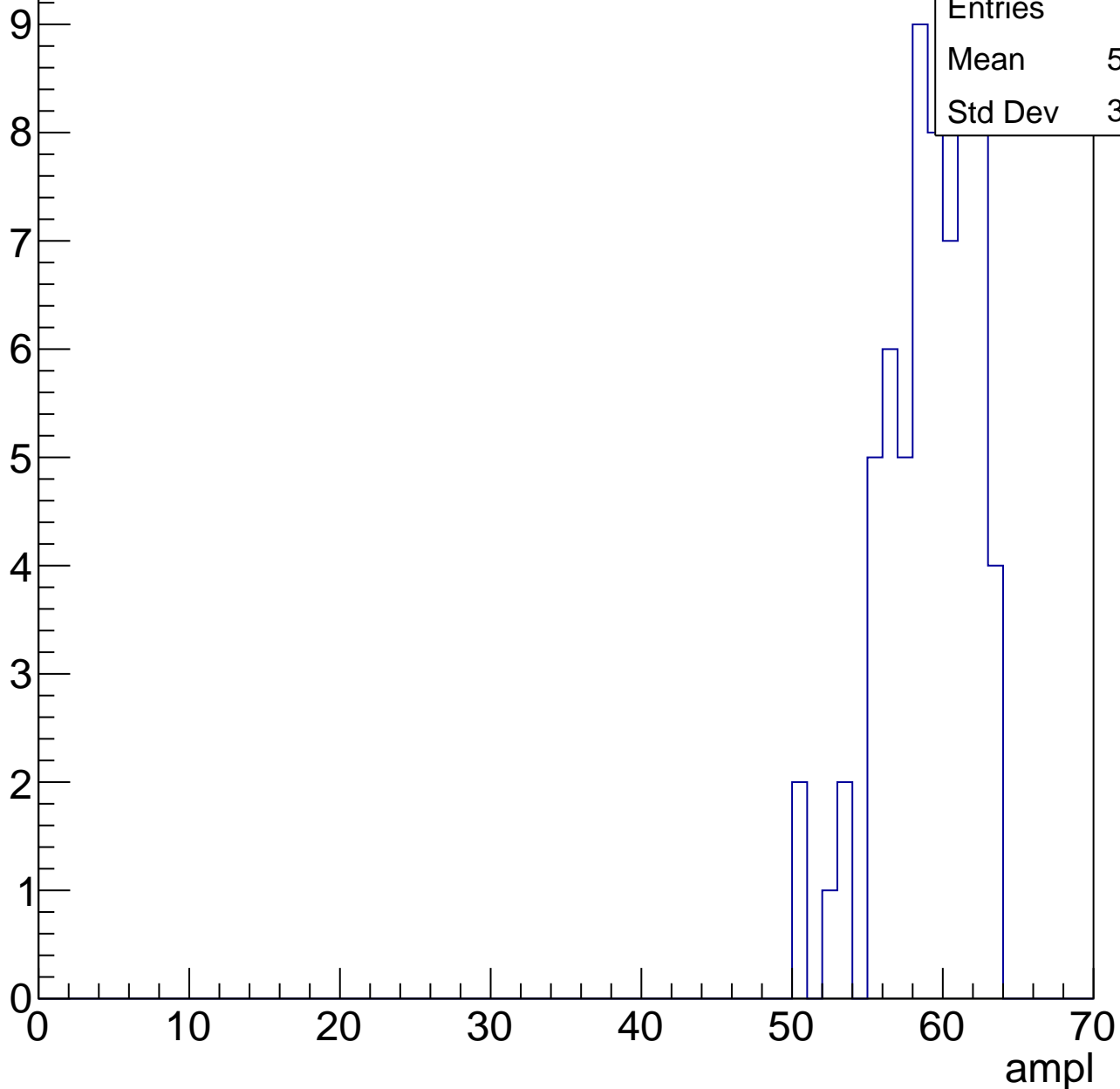


Entries	68
Mean	52.79
Std Dev	3.279

B1L100S, U13-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

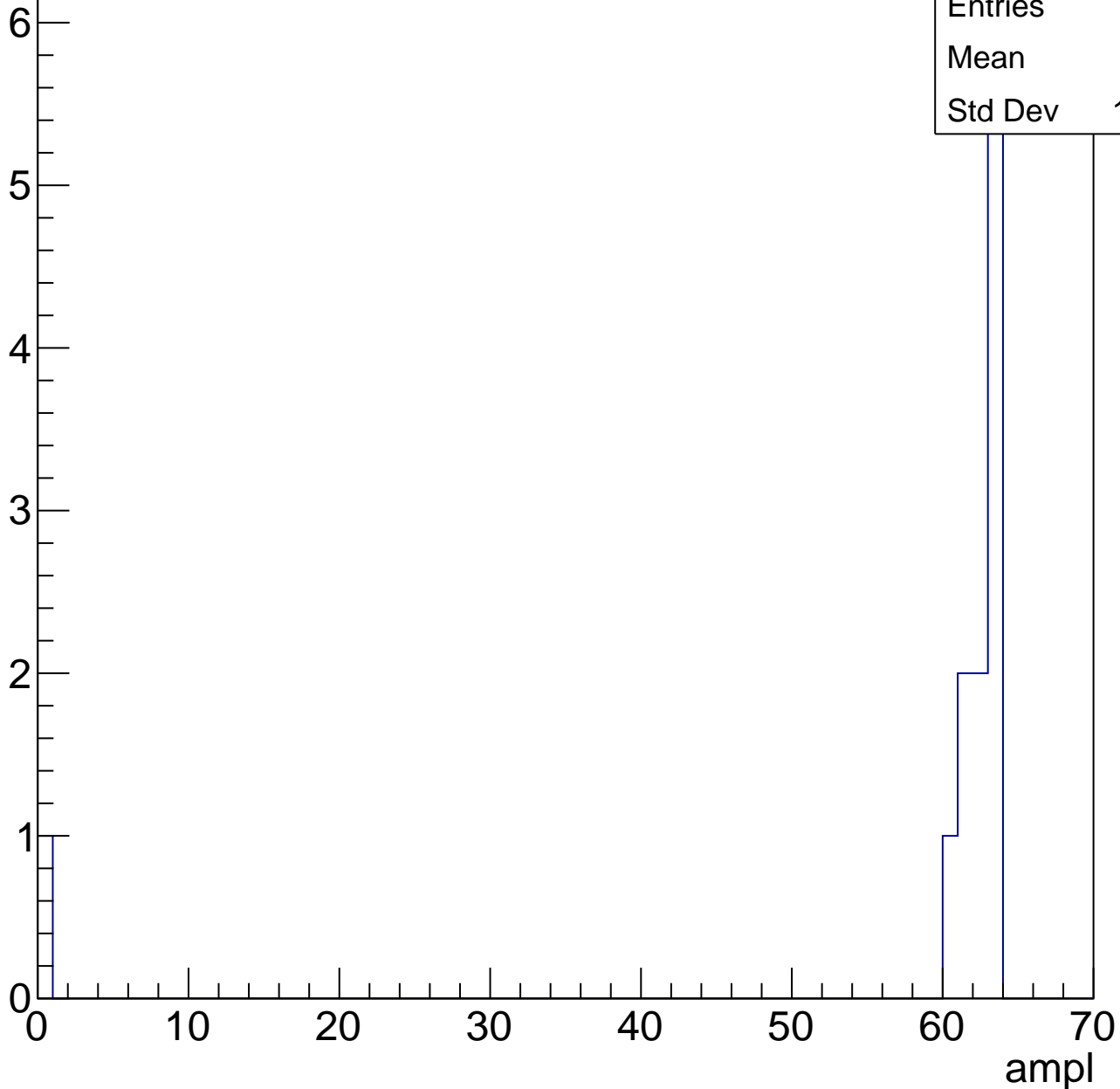


B1L100S, U13-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	57
Std Dev	17.21



B1L100S, U13-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch90, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	29.66
Std Dev	7.032

Gaus mean : 31.1185

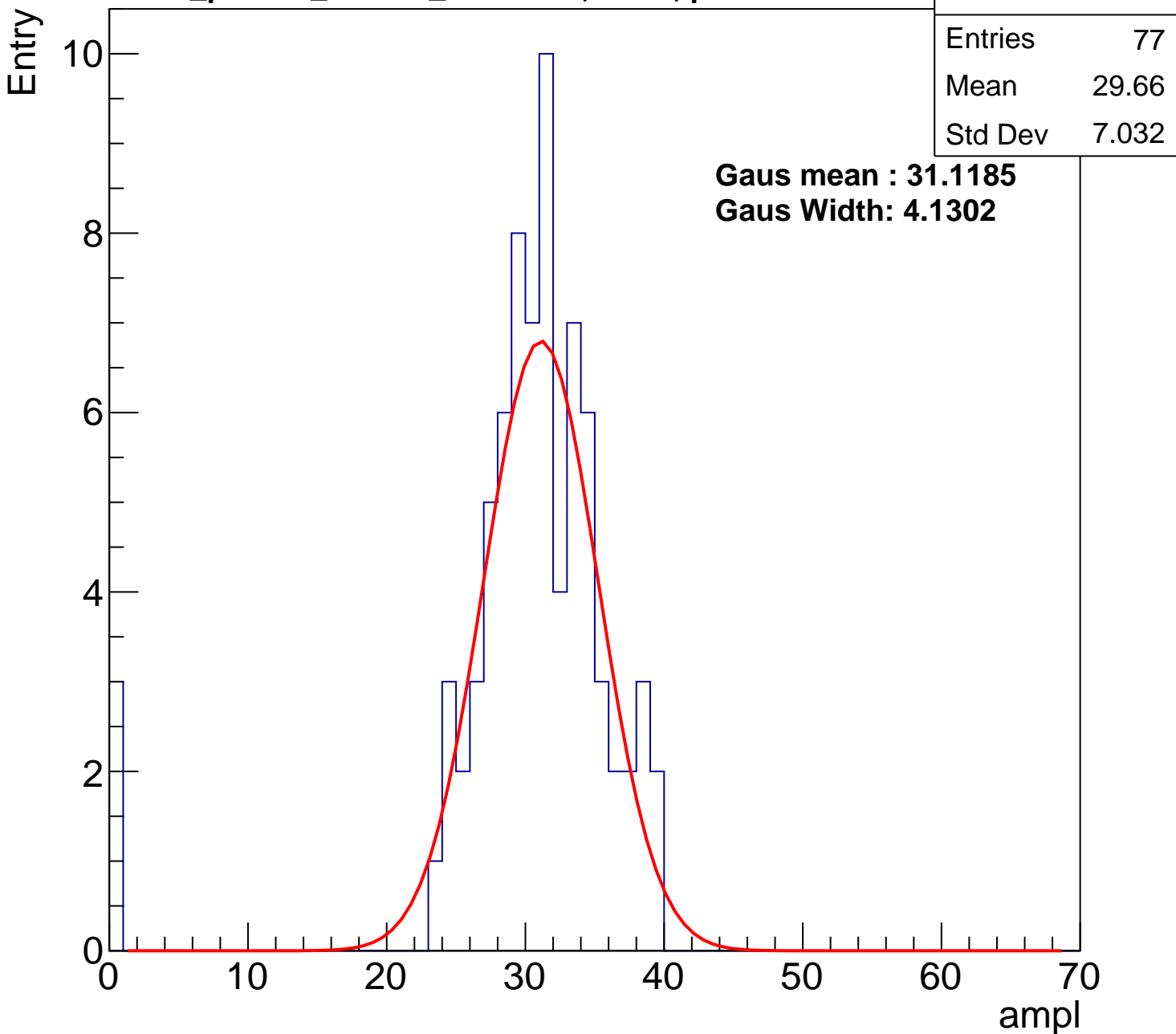
Gaus Width: 4.1302

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch90, adc1

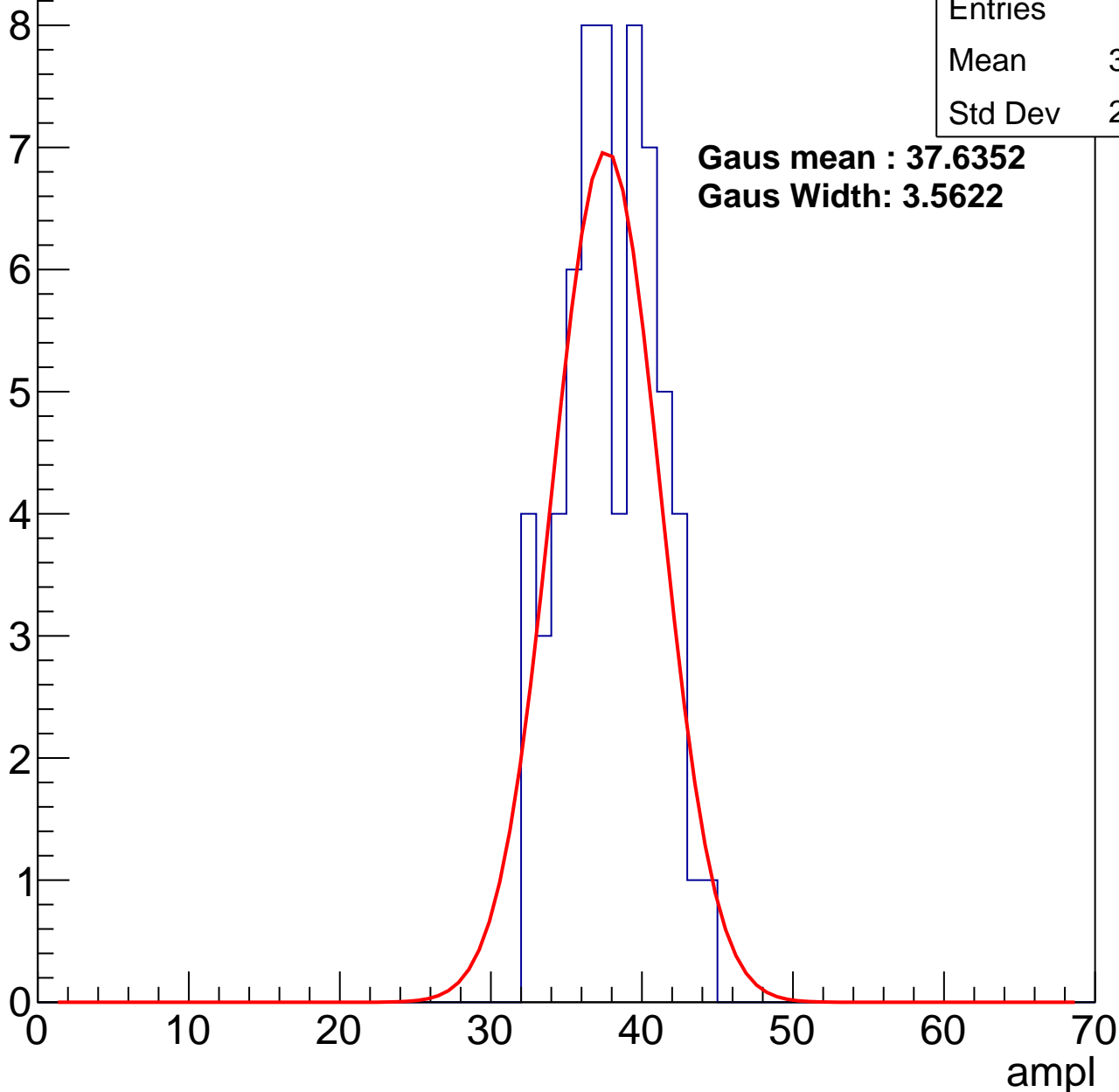
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.48
Std Dev	2.997

Gaus mean : 37.6352

Gaus Width: 3.5622



B1L100S, U13-ch90, adc2

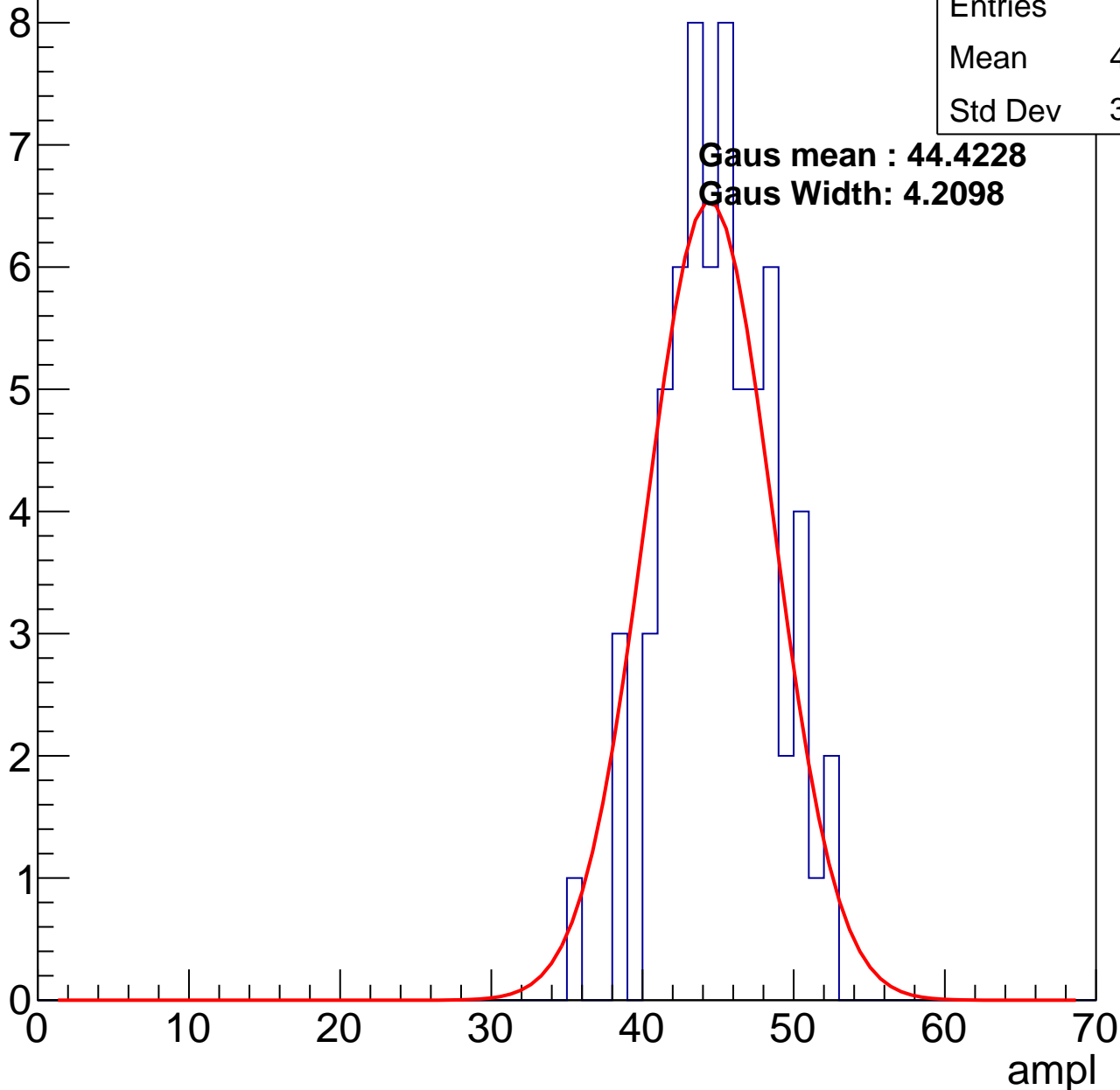
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.62
Std Dev	3.572

Gaus mean : 44.4228

Gaus Width: 4.2098

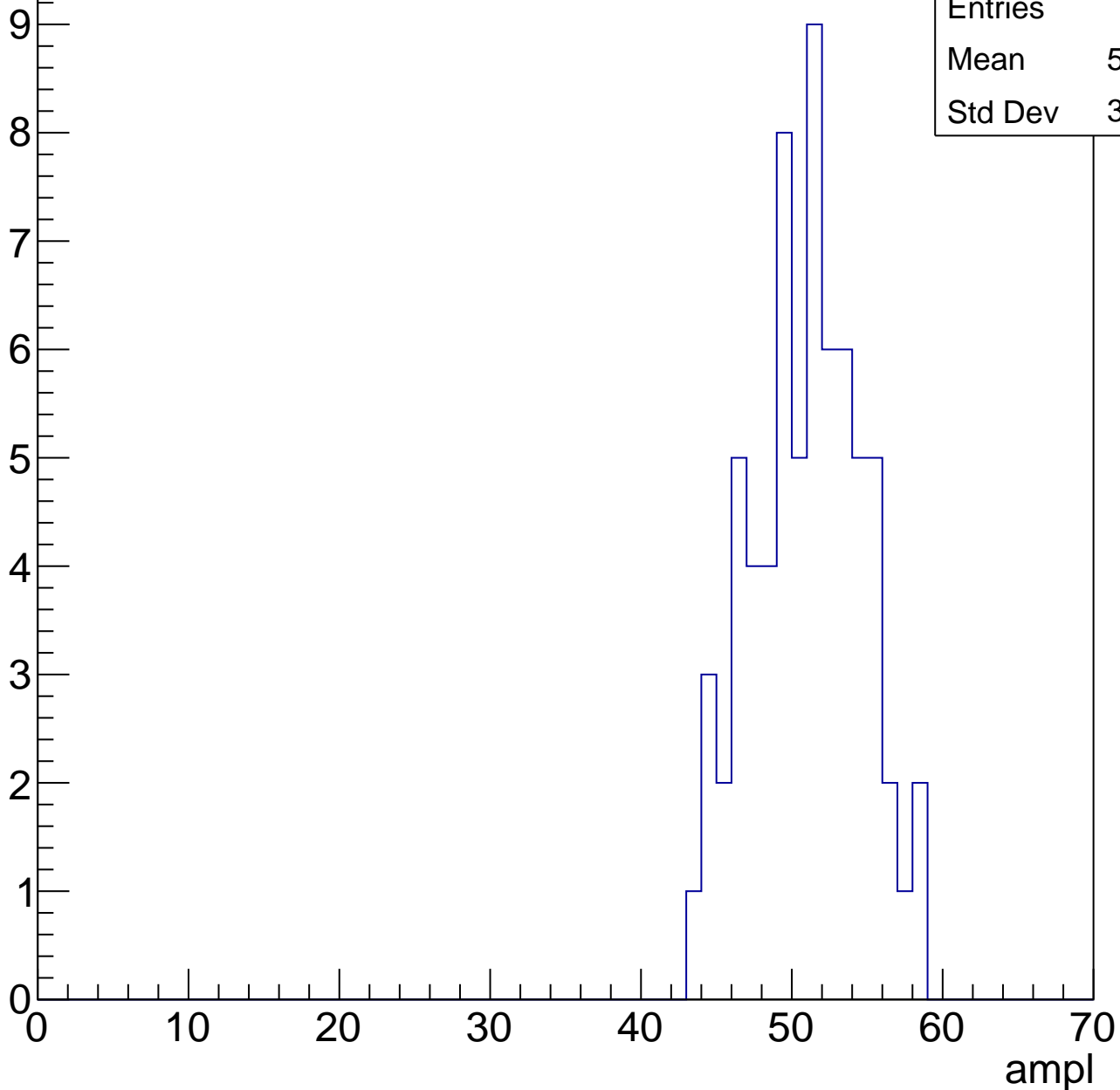


B1L100S, U13-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

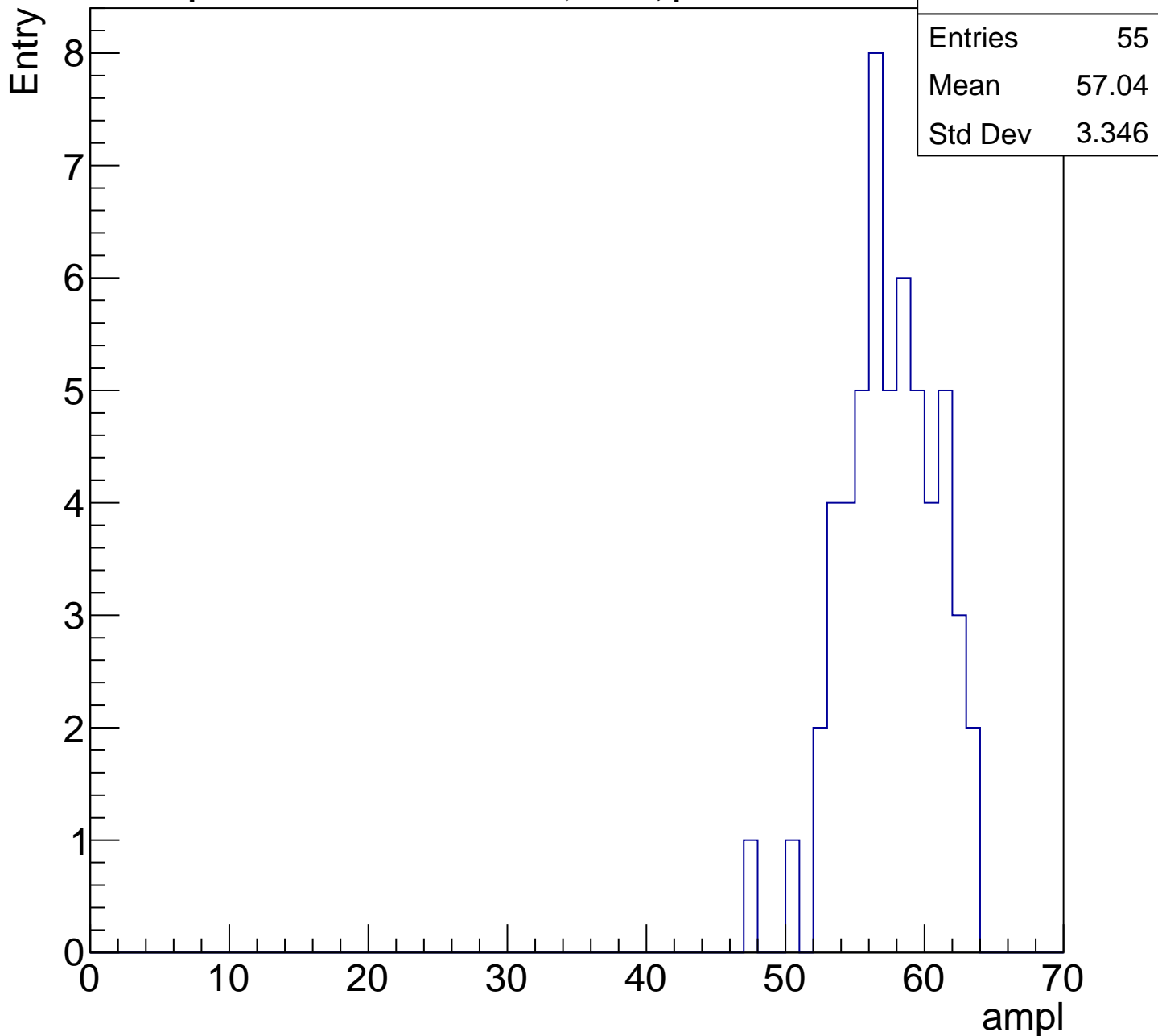
Entry

Entries	68
Mean	50.53
Std Dev	3.575



B1L100S, U13-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

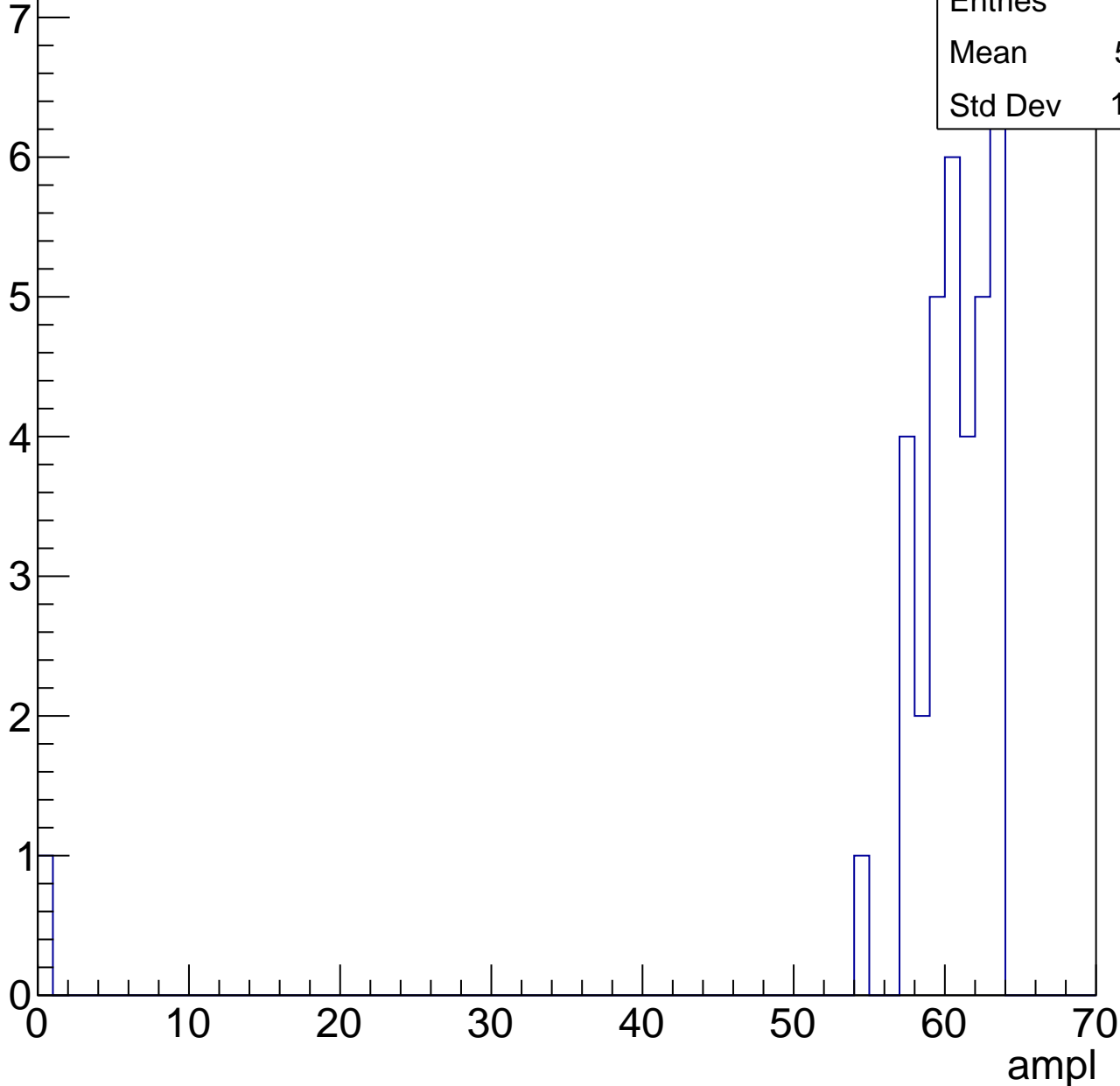


B1L100S, U13-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	58.51
Std Dev	10.27



B1L100S, U13-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

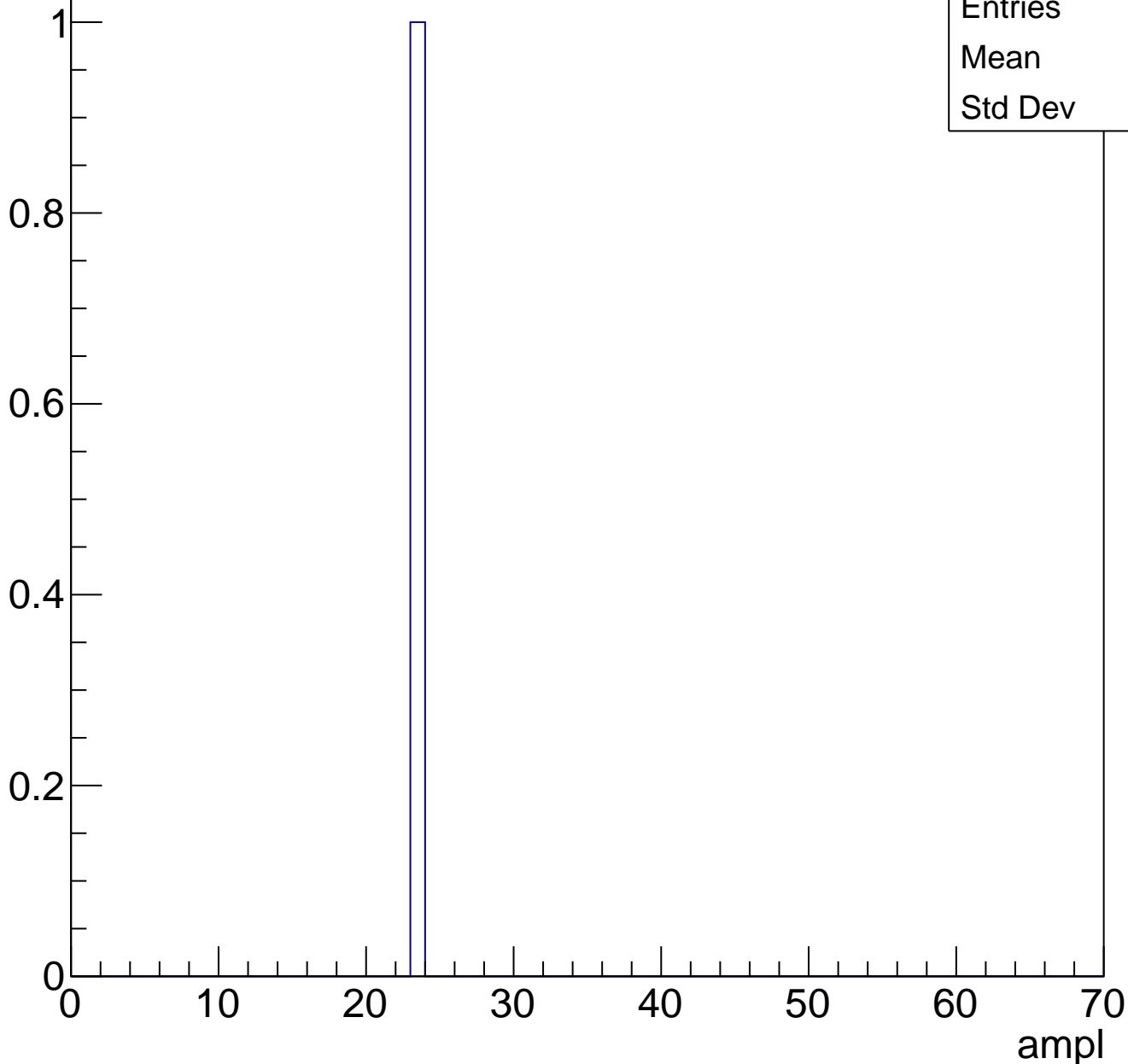
Entry



B1L100S, U13-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch91, adc0

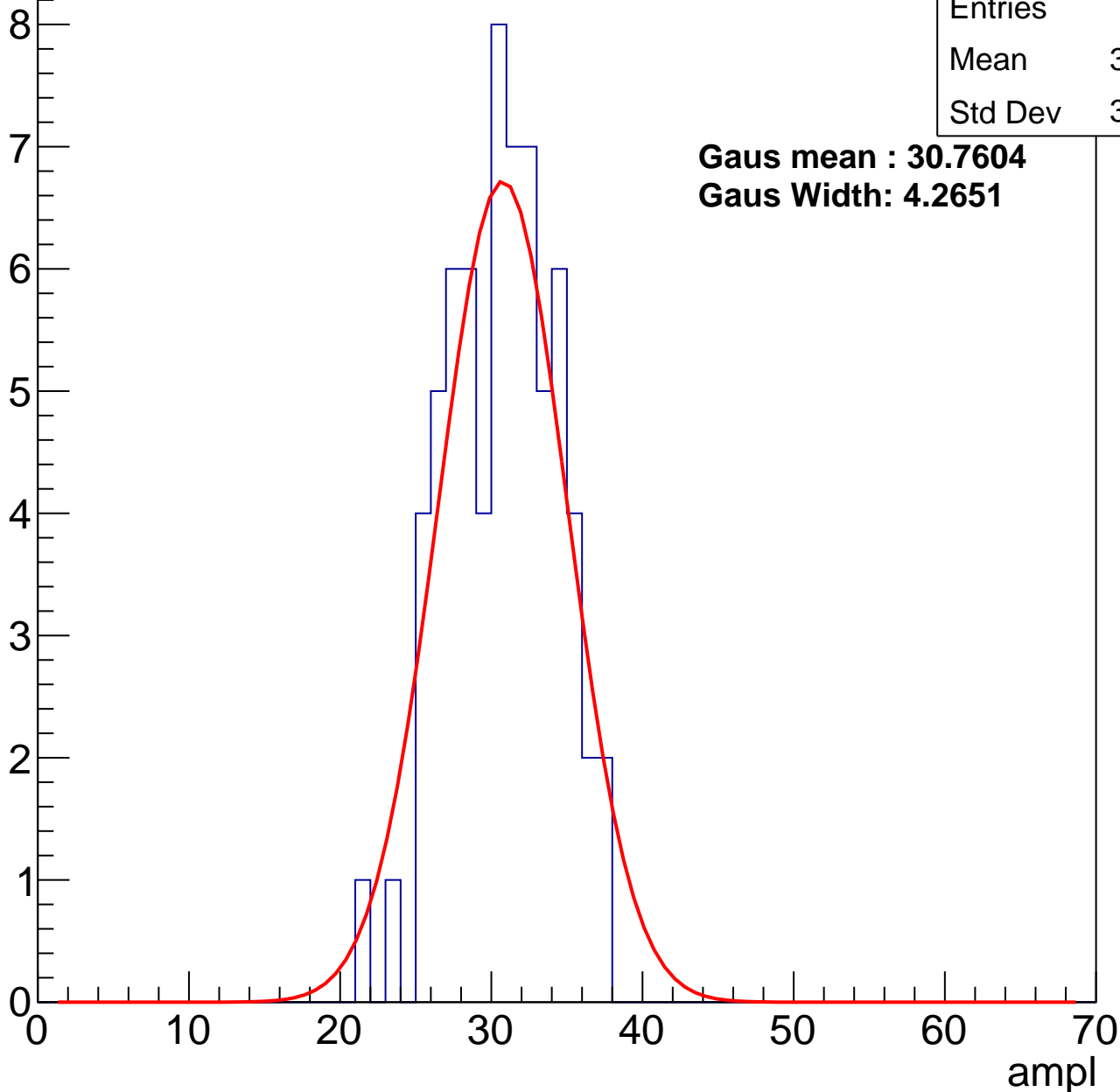
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.24
Std Dev	3.503

Gaus mean : 30.7604

Gaus Width: 4.2651



B1L100S, U13-ch91, adc1

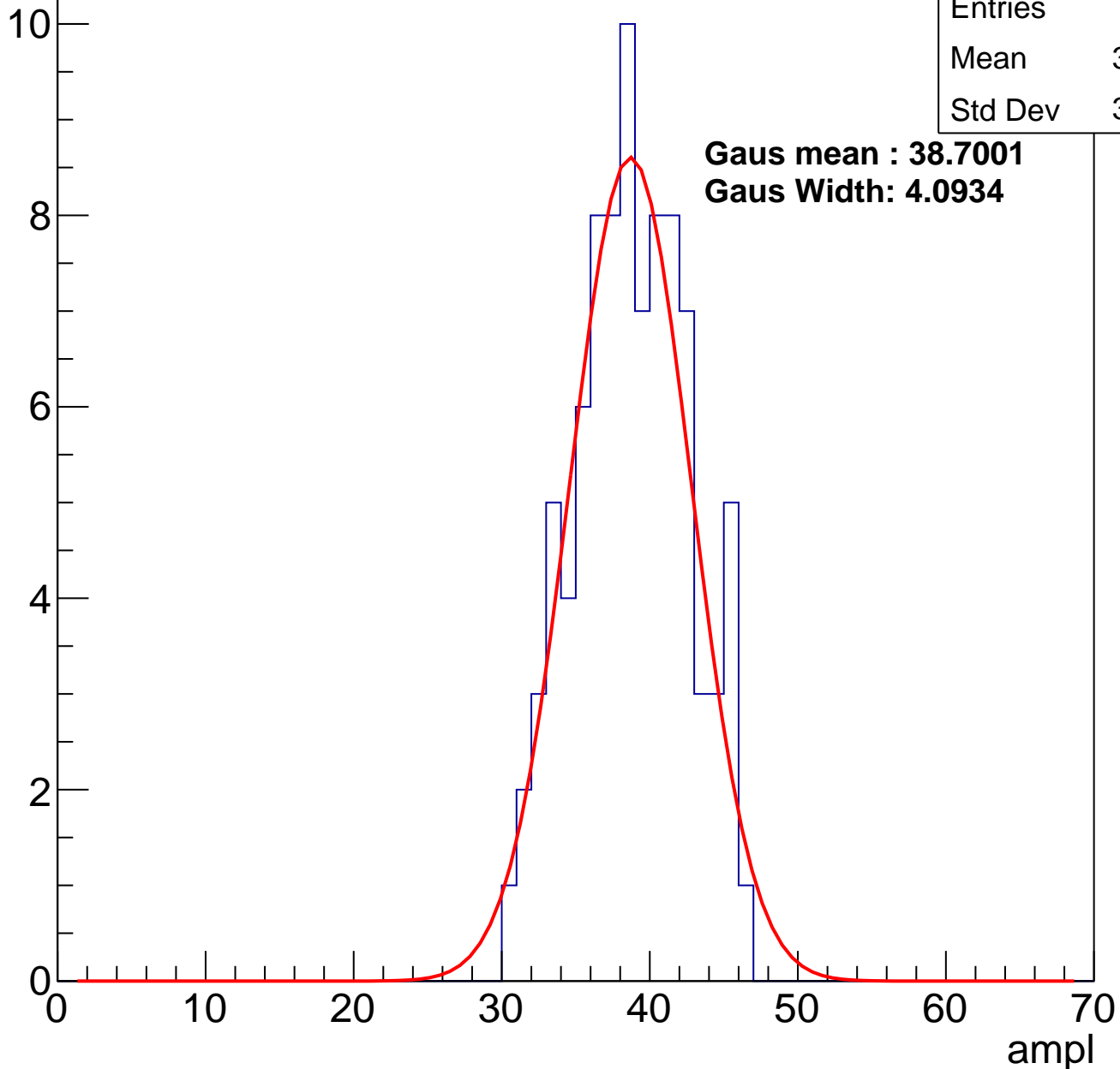
calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	38.31
Std Dev	3.761

Gaus mean : 38.7001

Gaus Width: 4.0934

Entry



B1L100S, U13-ch91, adc2

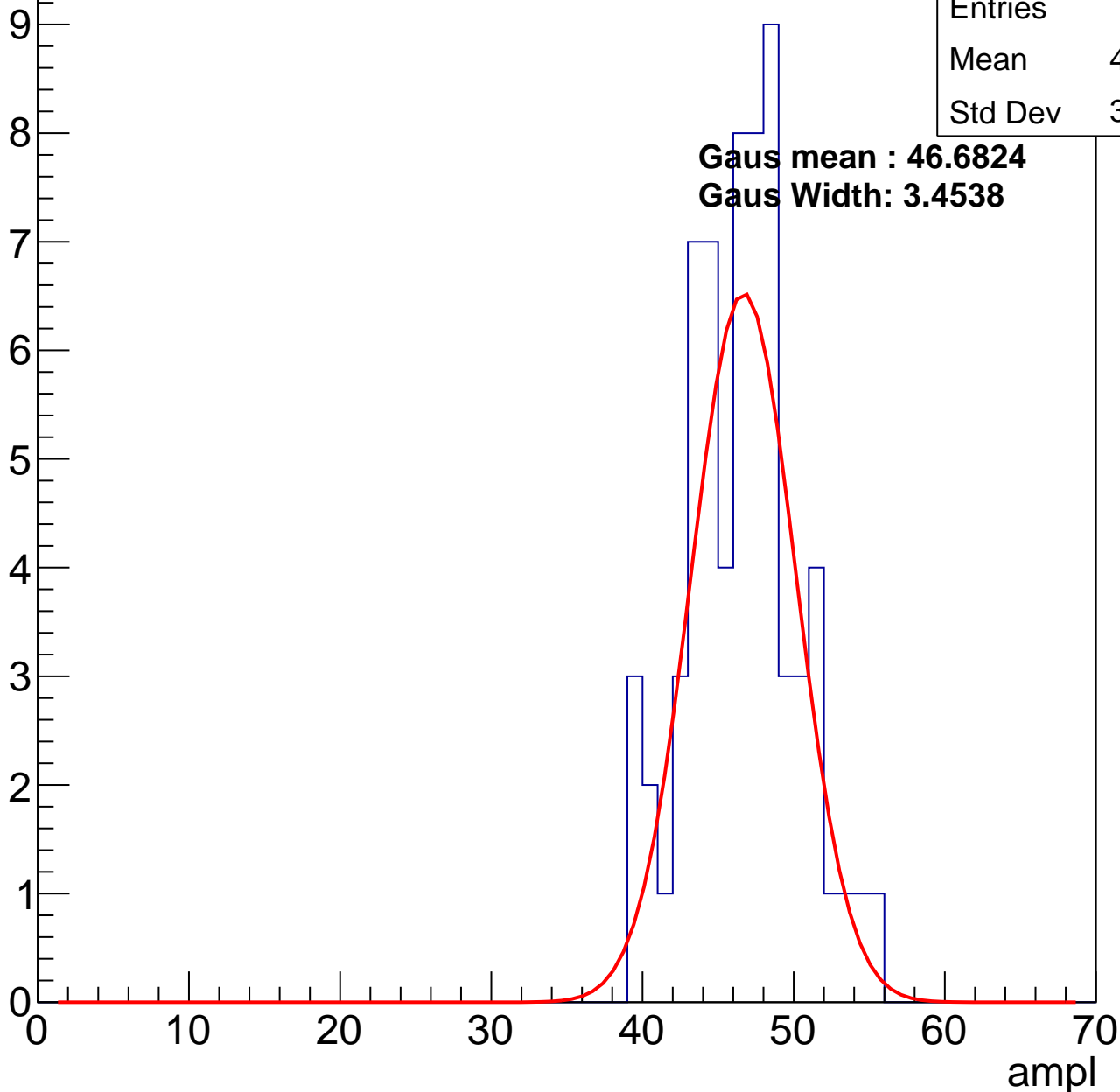
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	46.12
Std Dev	3.557

Gaus mean : 46.6824

Gaus Width: 3.4538

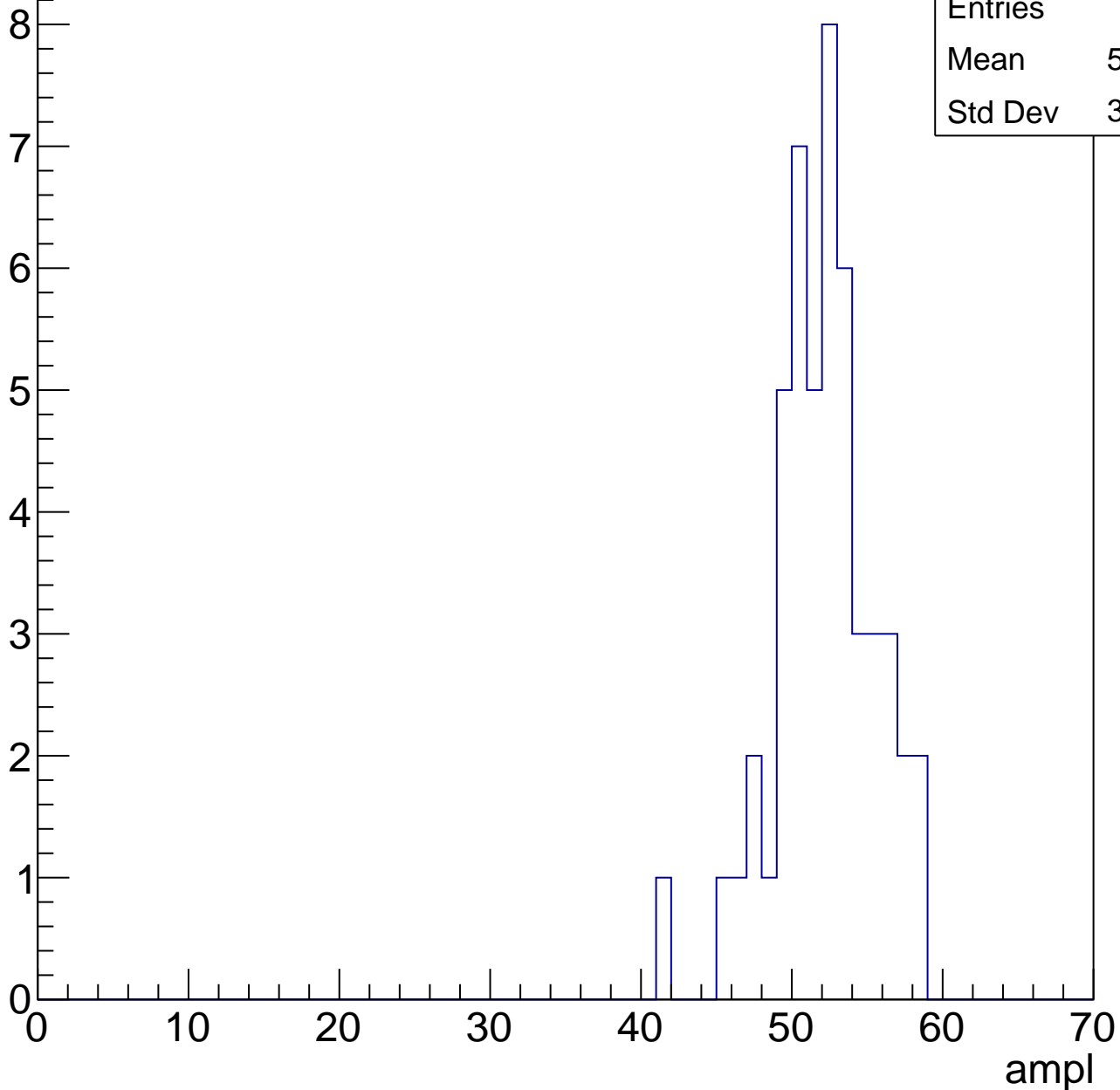


B1L100S, U13-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	51.66
Std Dev	3.344

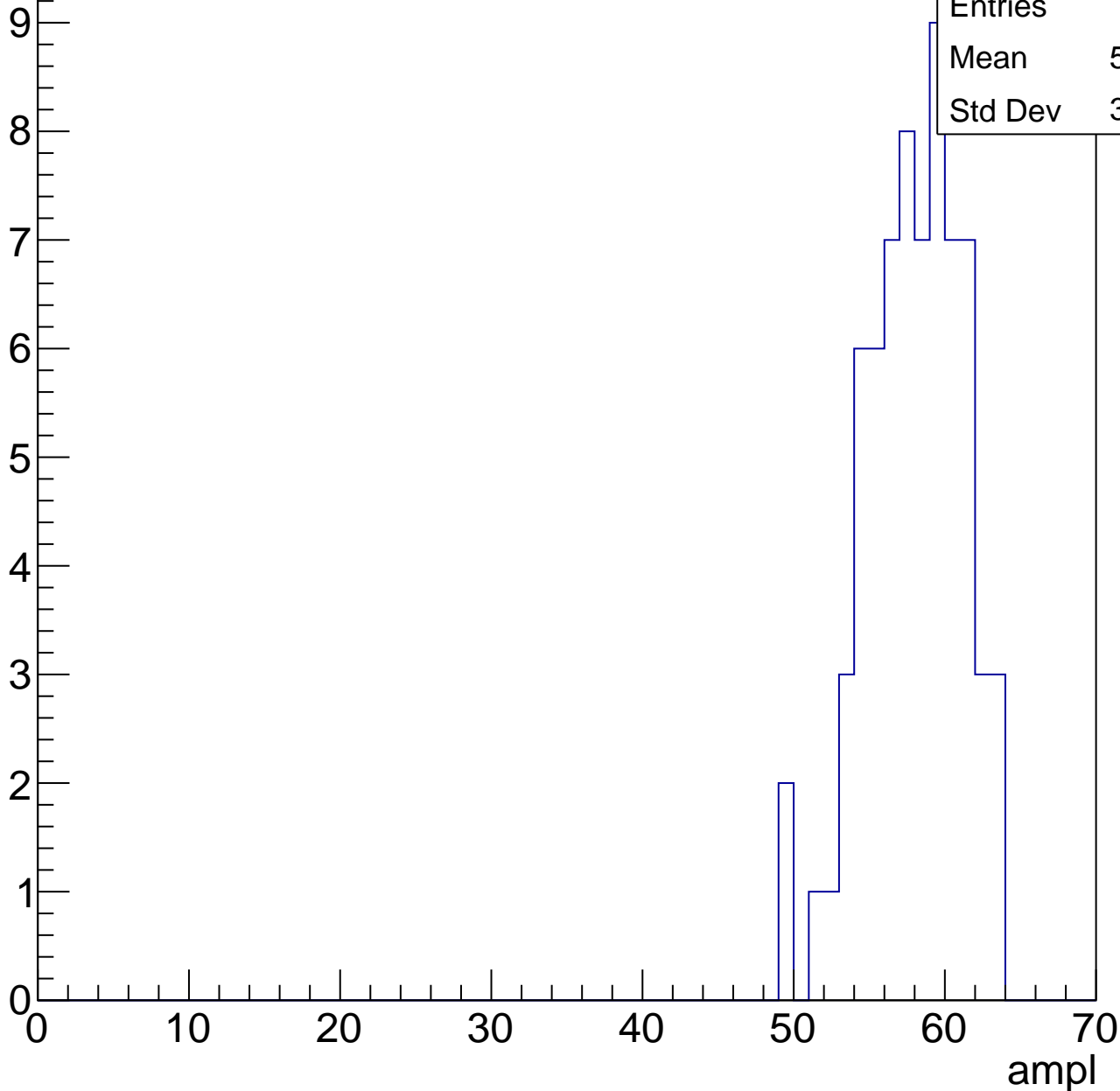


B1L100S, U13-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	57.44
Std Dev	3.183

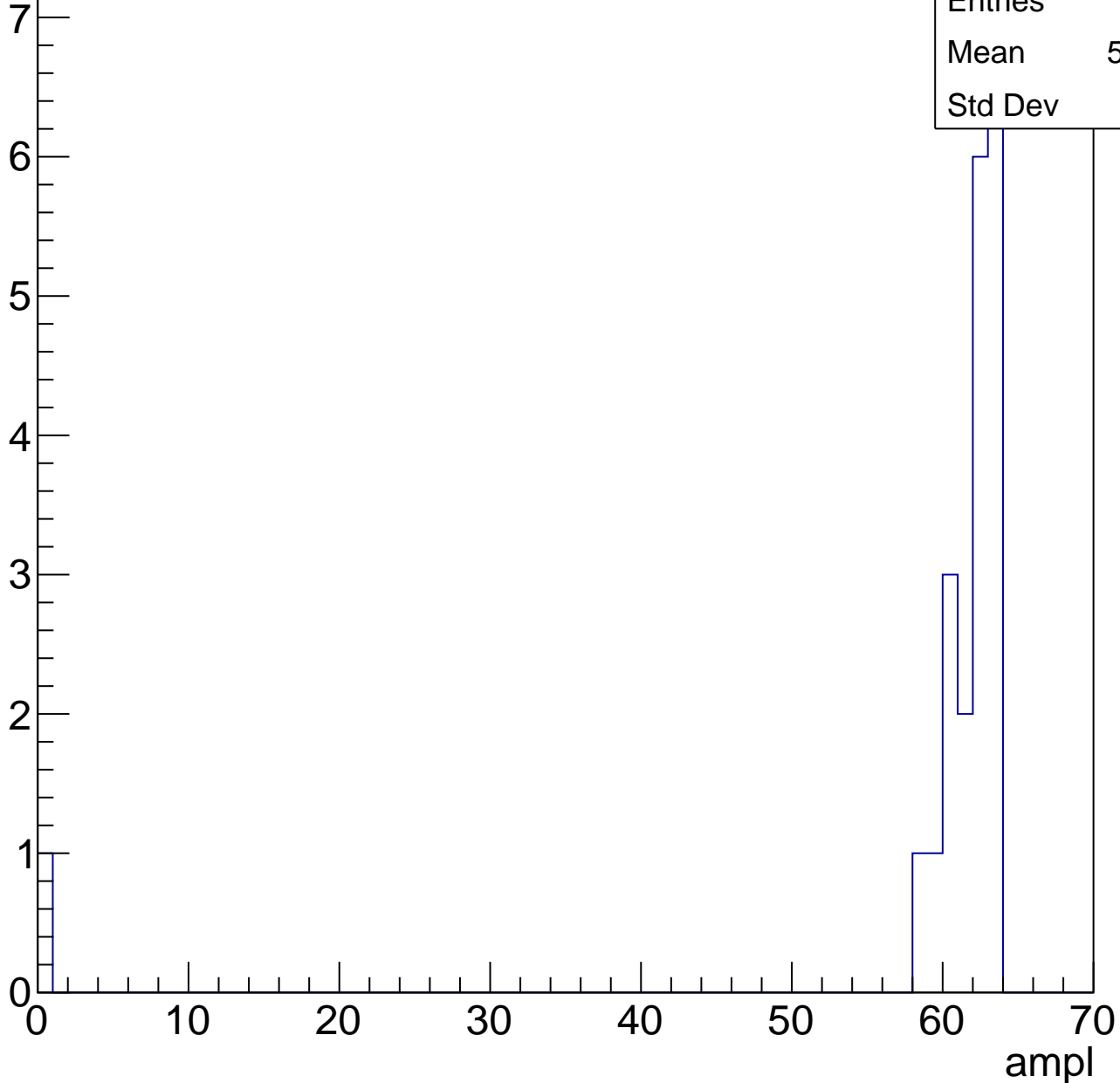


B1L100S, U13-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

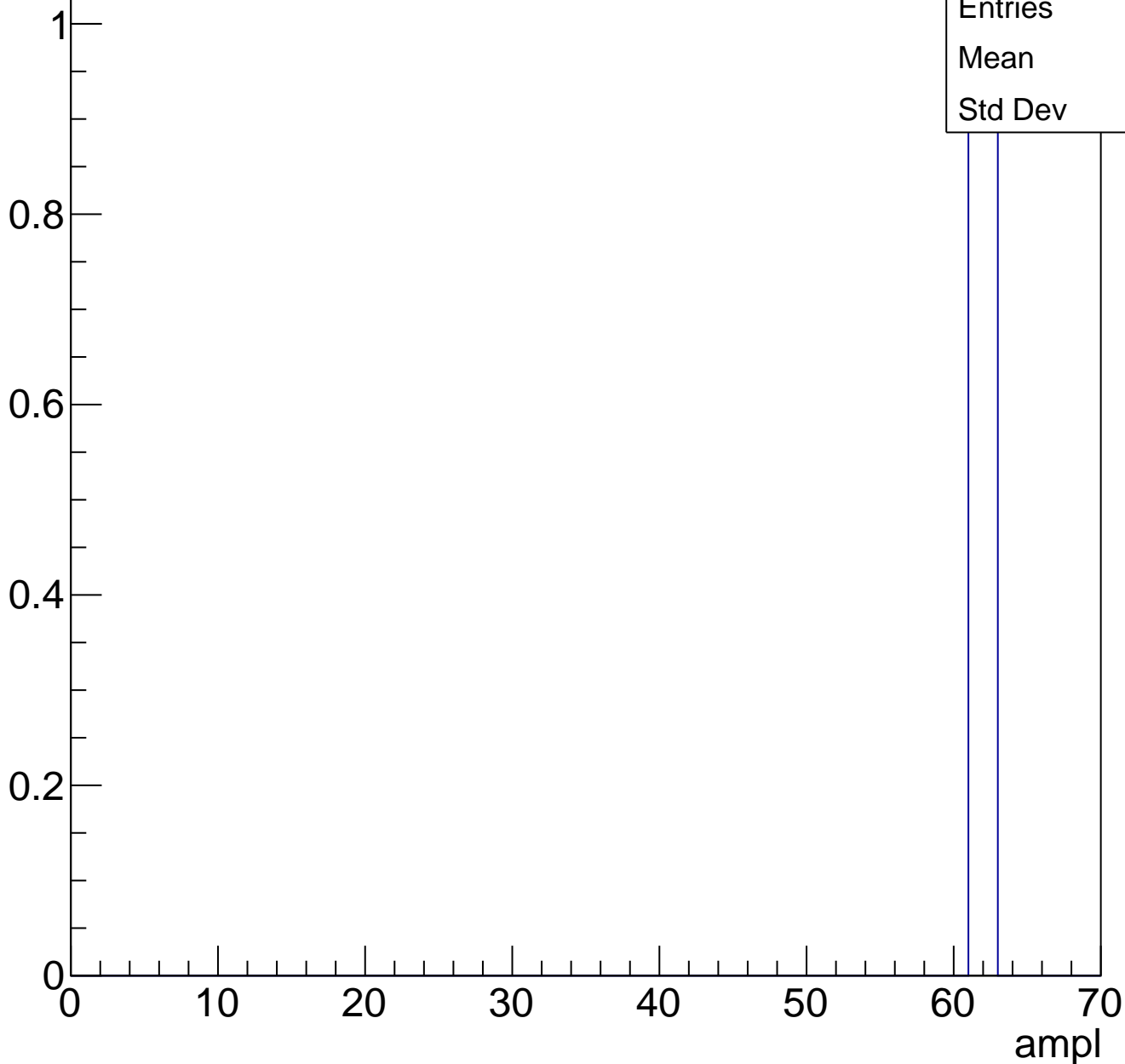
Entries	21
Mean	58.67
Std Dev	13.2



B1L100S, U13-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch92, adc0

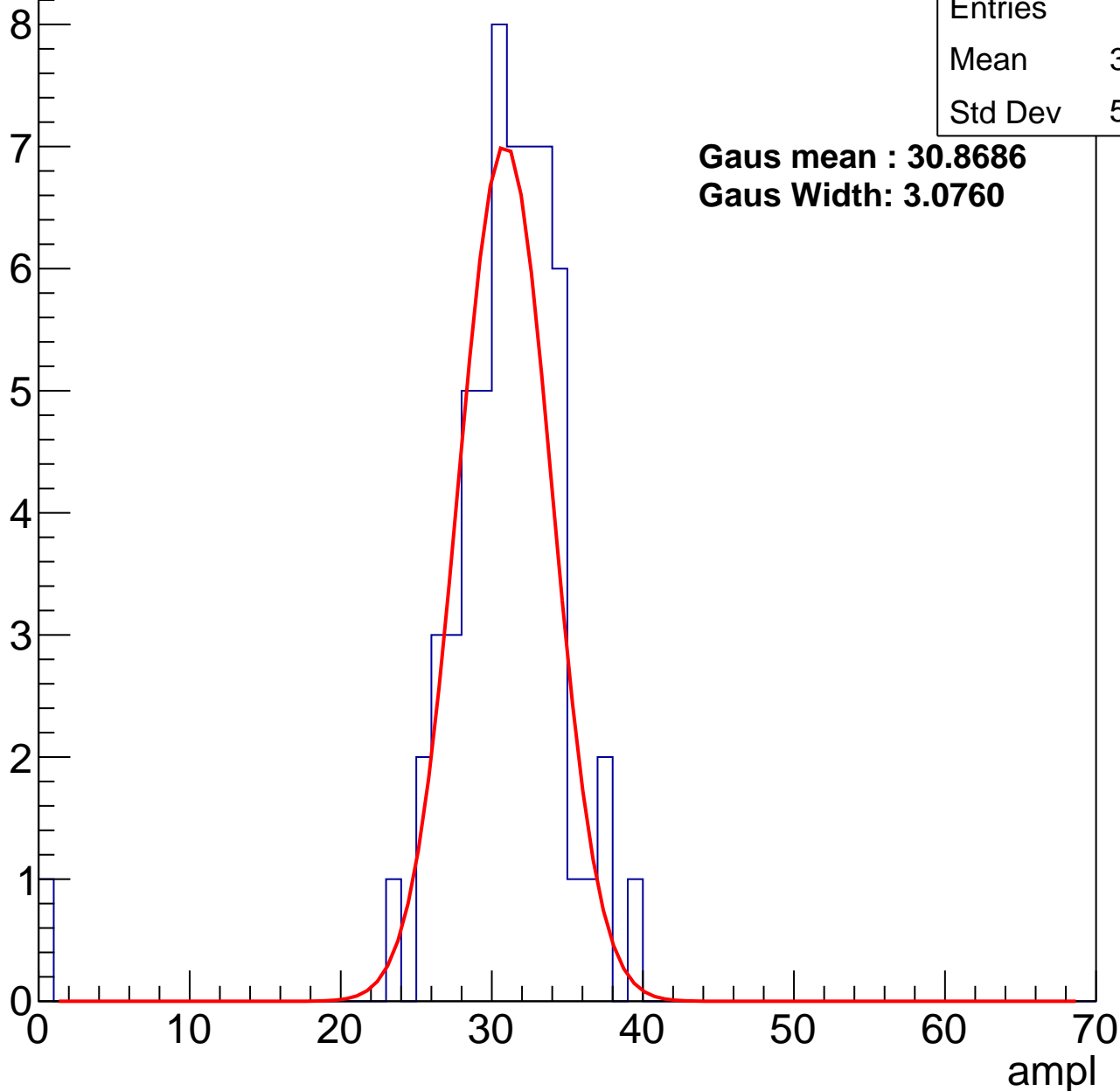
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	30.28
Std Dev	5.047

Gaus mean : 30.8686

Gaus Width: 3.0760



B1L100S, U13-ch92, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	37.96
Std Dev	3.053

Gaus mean : 38.2414

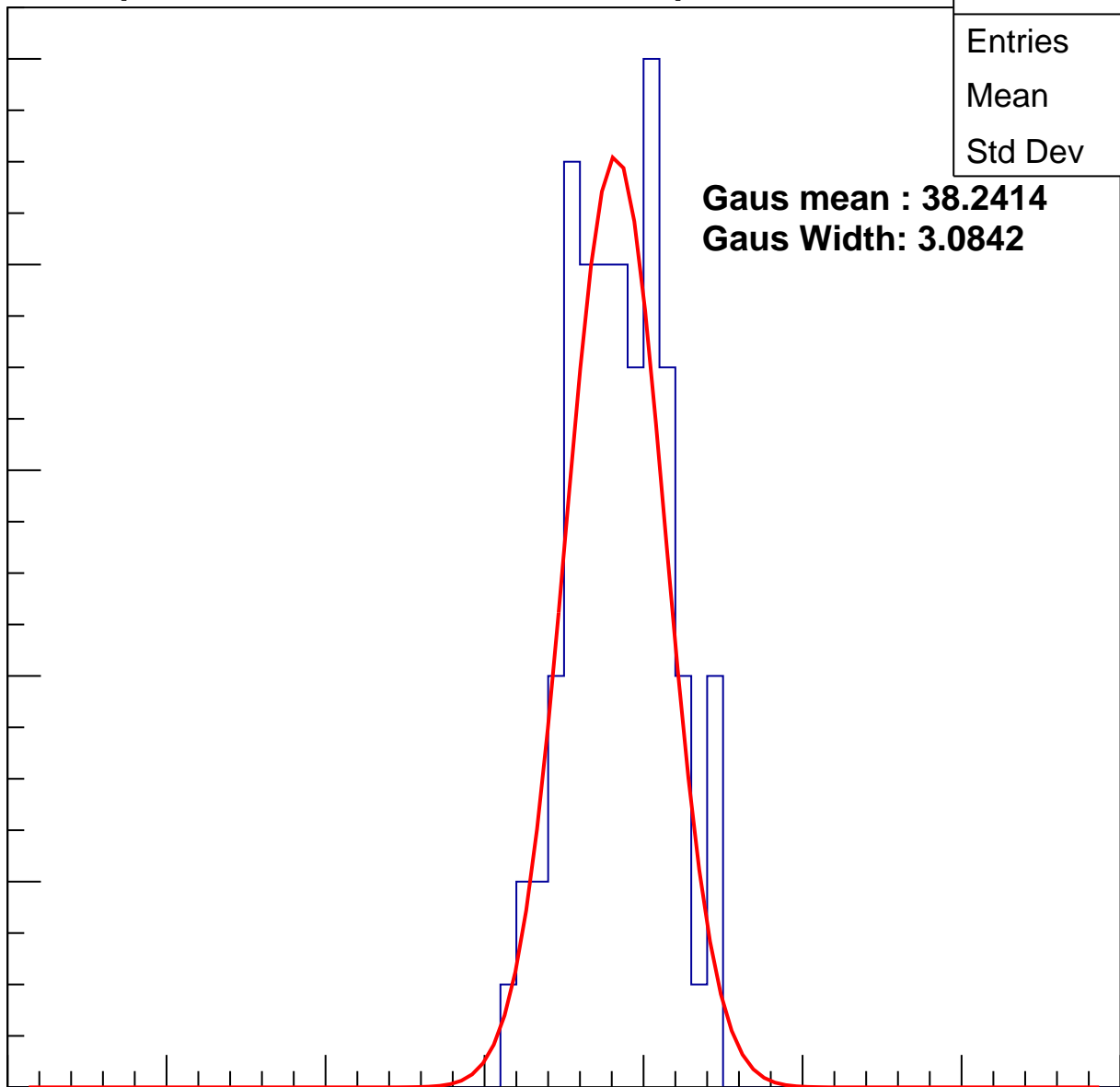
Gaus Width: 3.0842

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch92, adc2

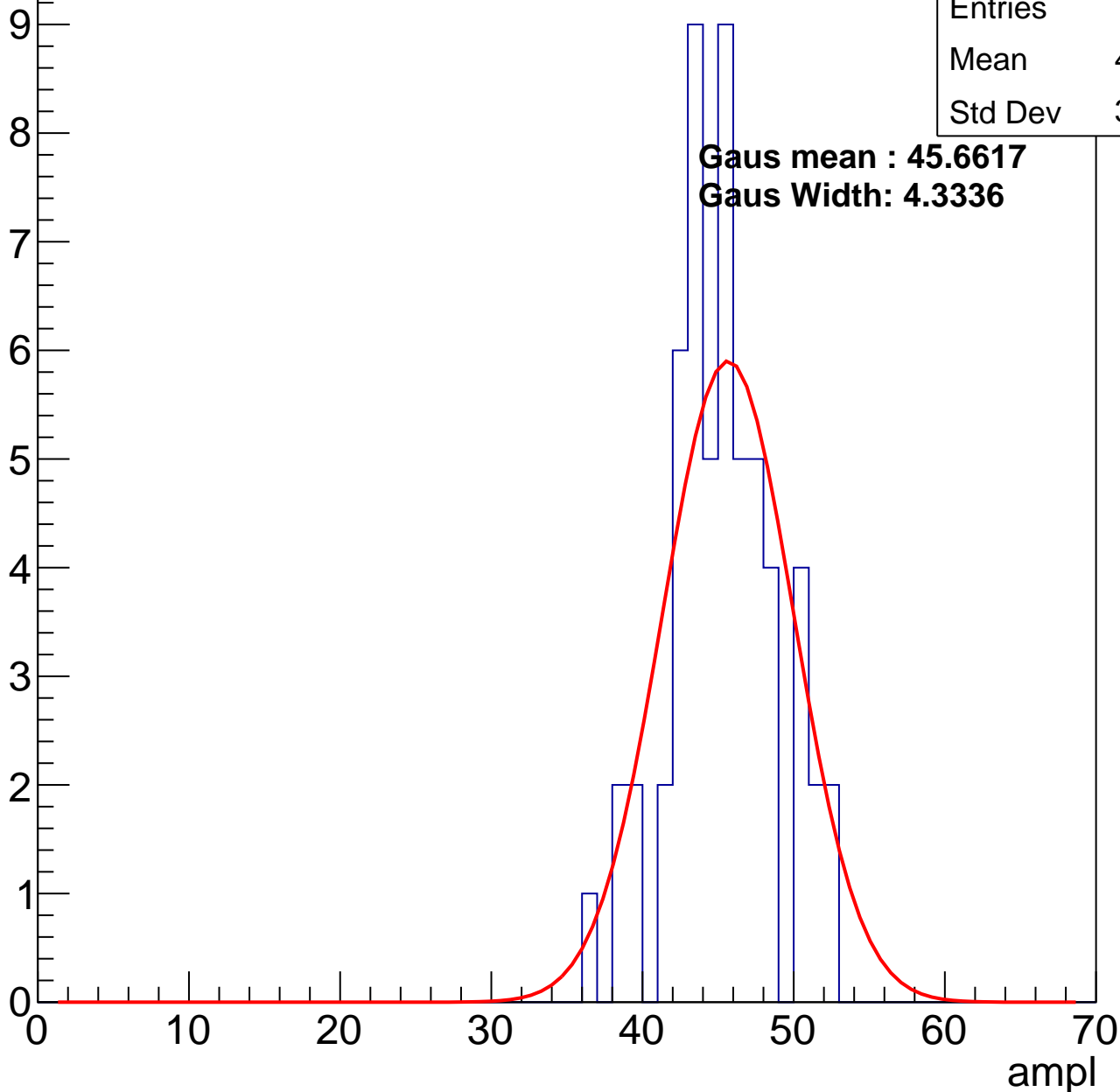
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	44.81
Std Dev	3.491

Gaus mean : 45.6617

Gaus Width: 4.3336

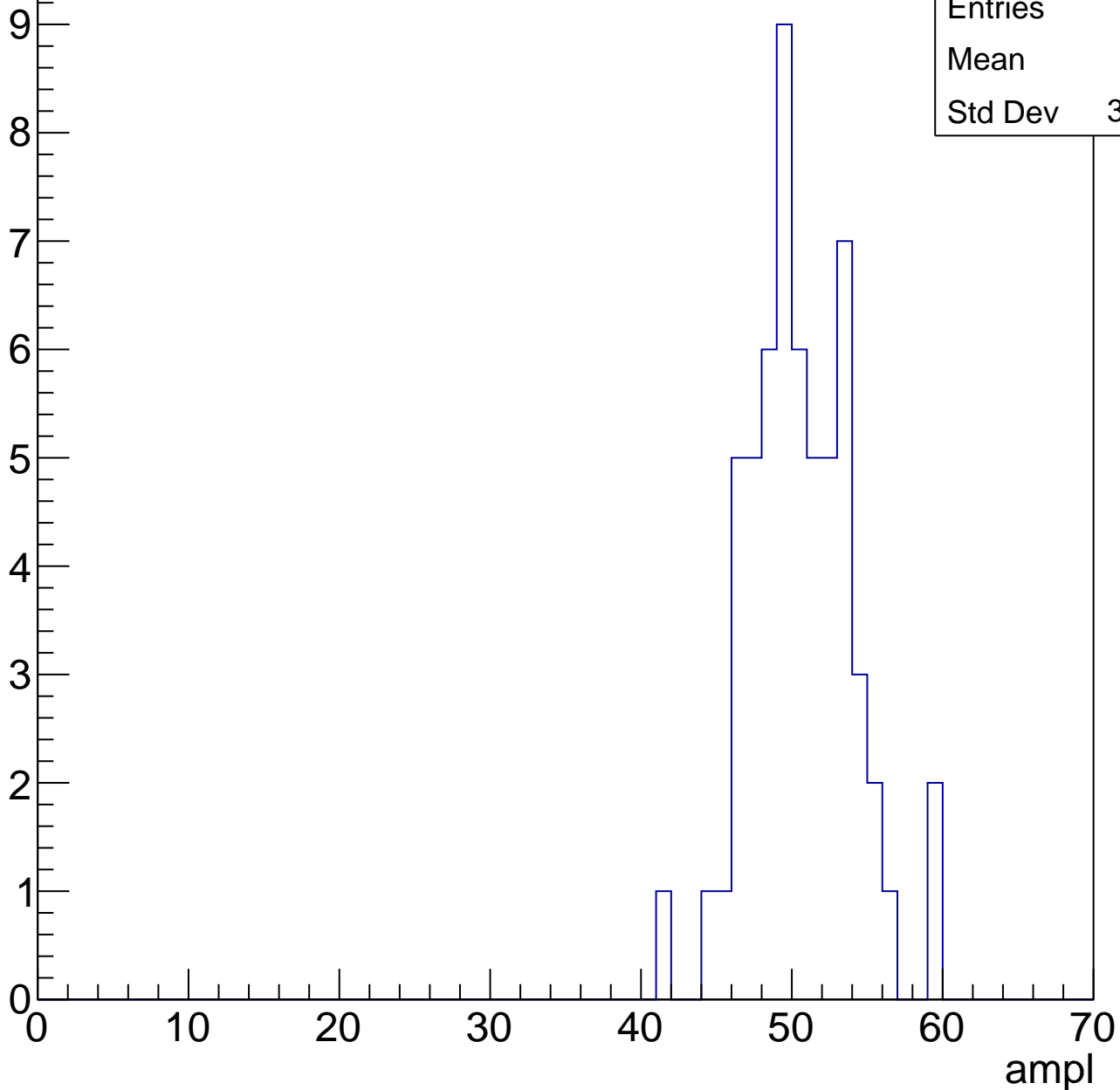


B1L100S, U13-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

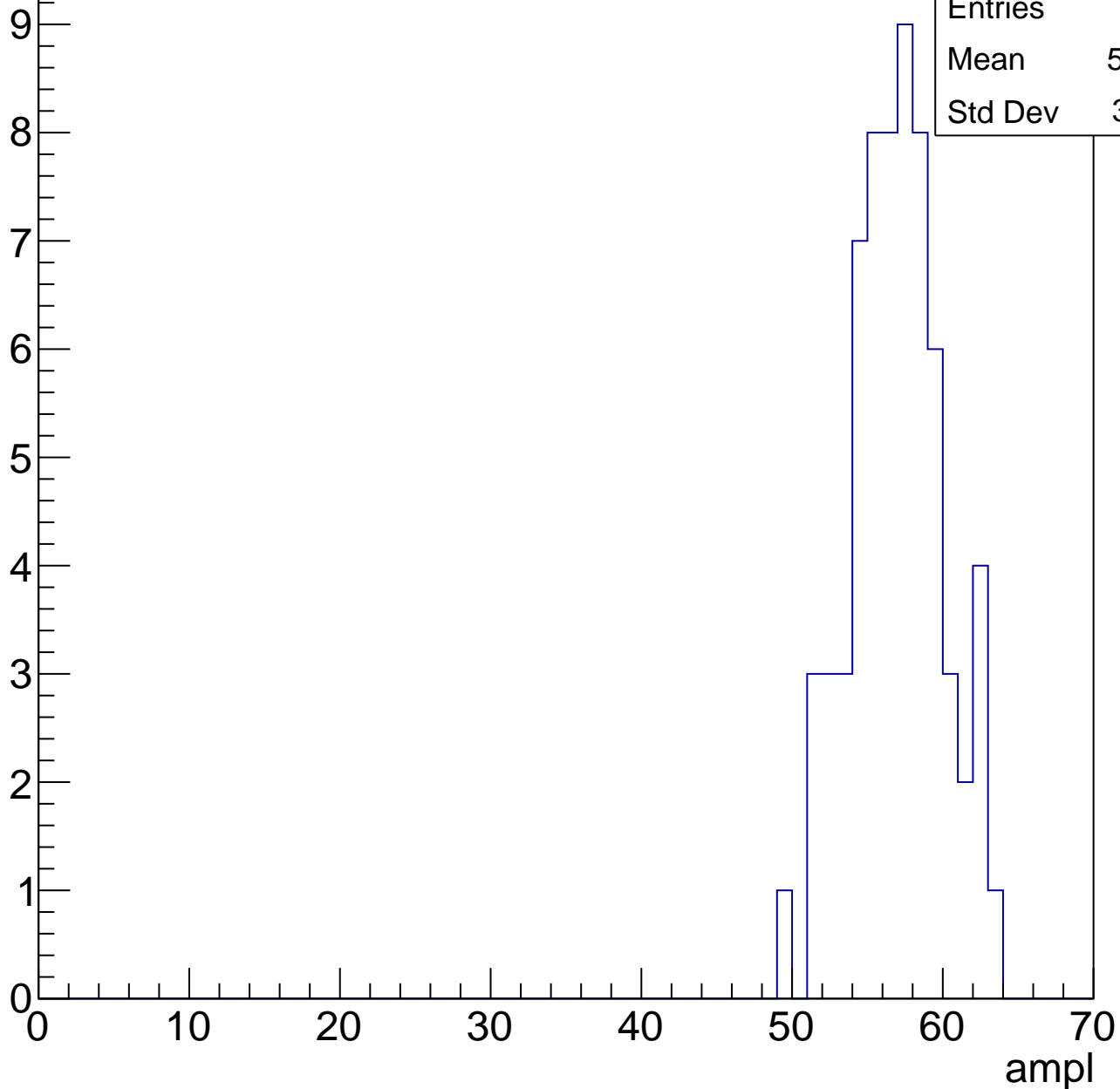
Entries	59
Mean	50.1
Std Dev	3.403



B1L100S, U13-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

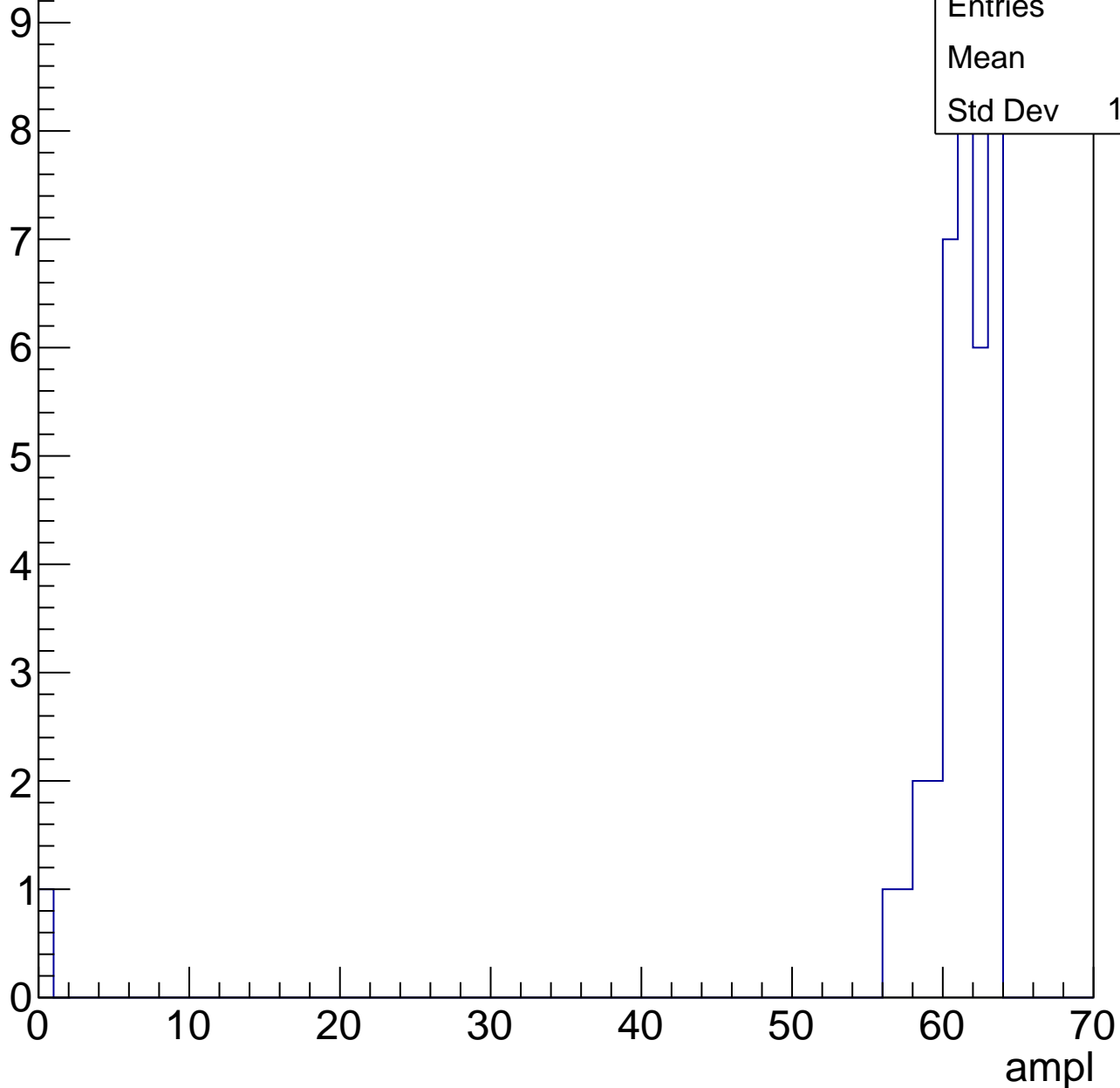
Entry



B1L100S, U13-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	30.78
Std Dev	3.462

Gaus mean : 31.2927

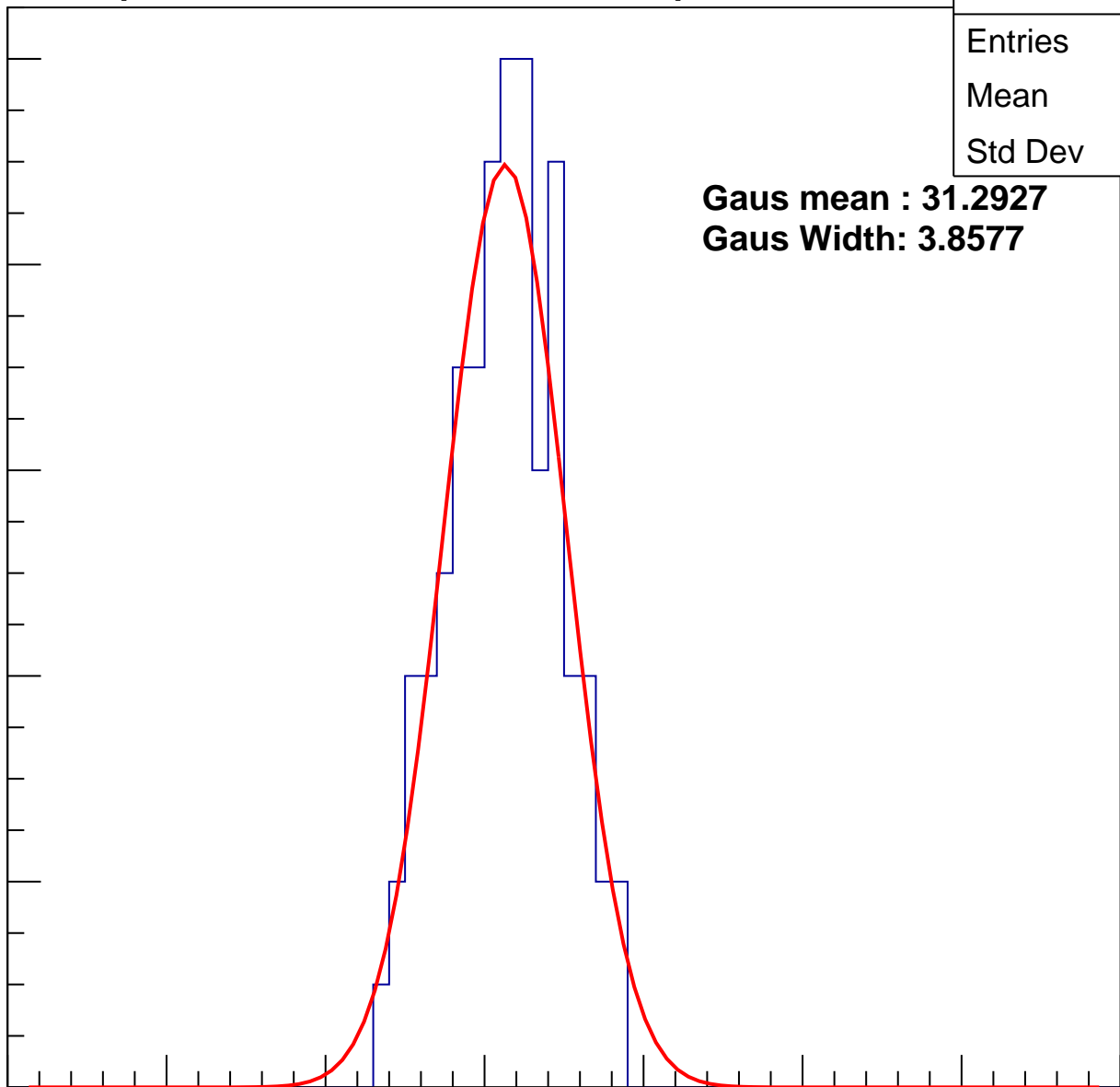
Gaus Width: 3.8577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



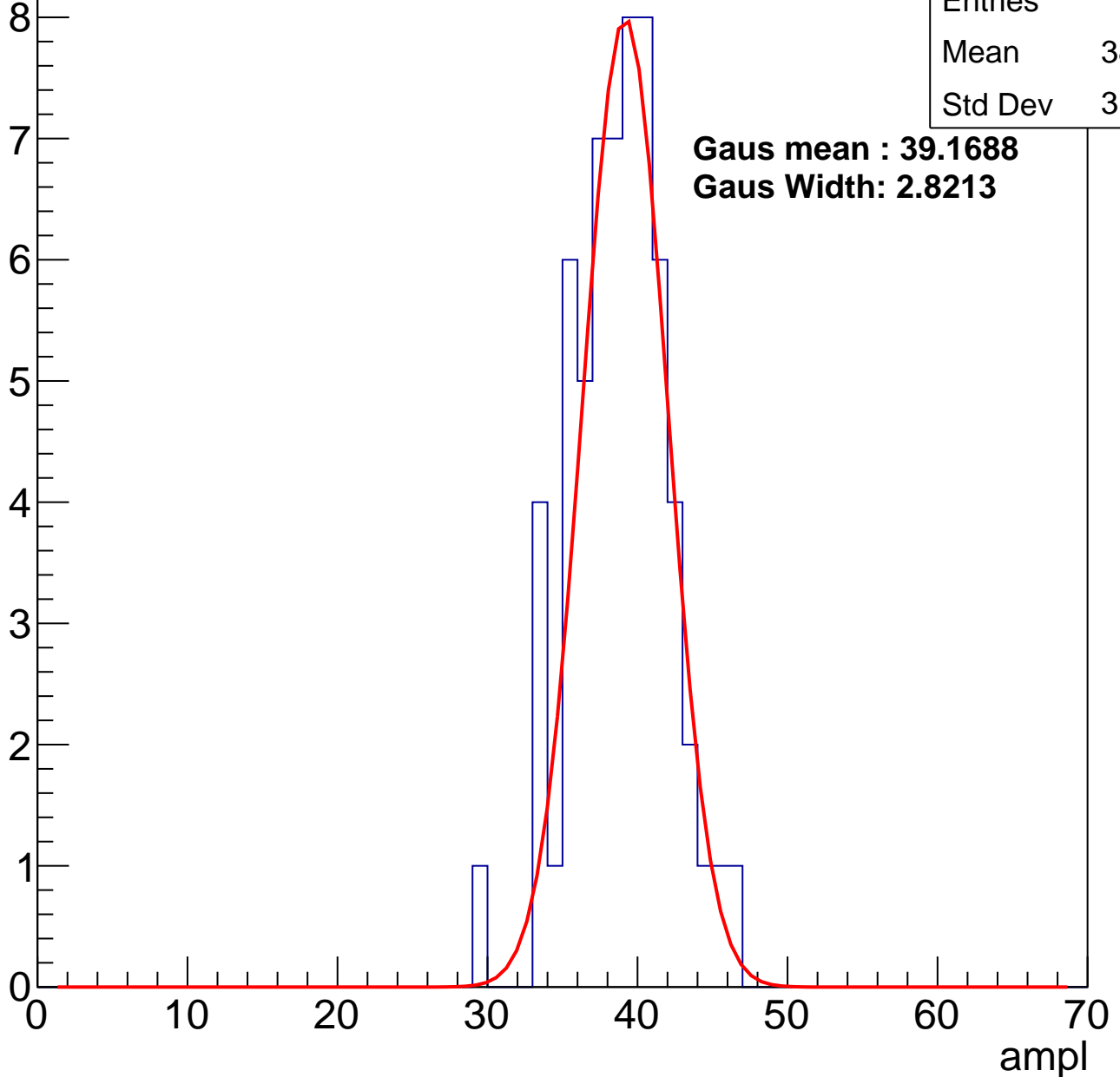
B1L100S, U13-ch93, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	38.34
Std Dev	3.182

Gaus mean : 39.1688
Gaus Width: 2.8213



B1L100S, U13-ch93, adc2

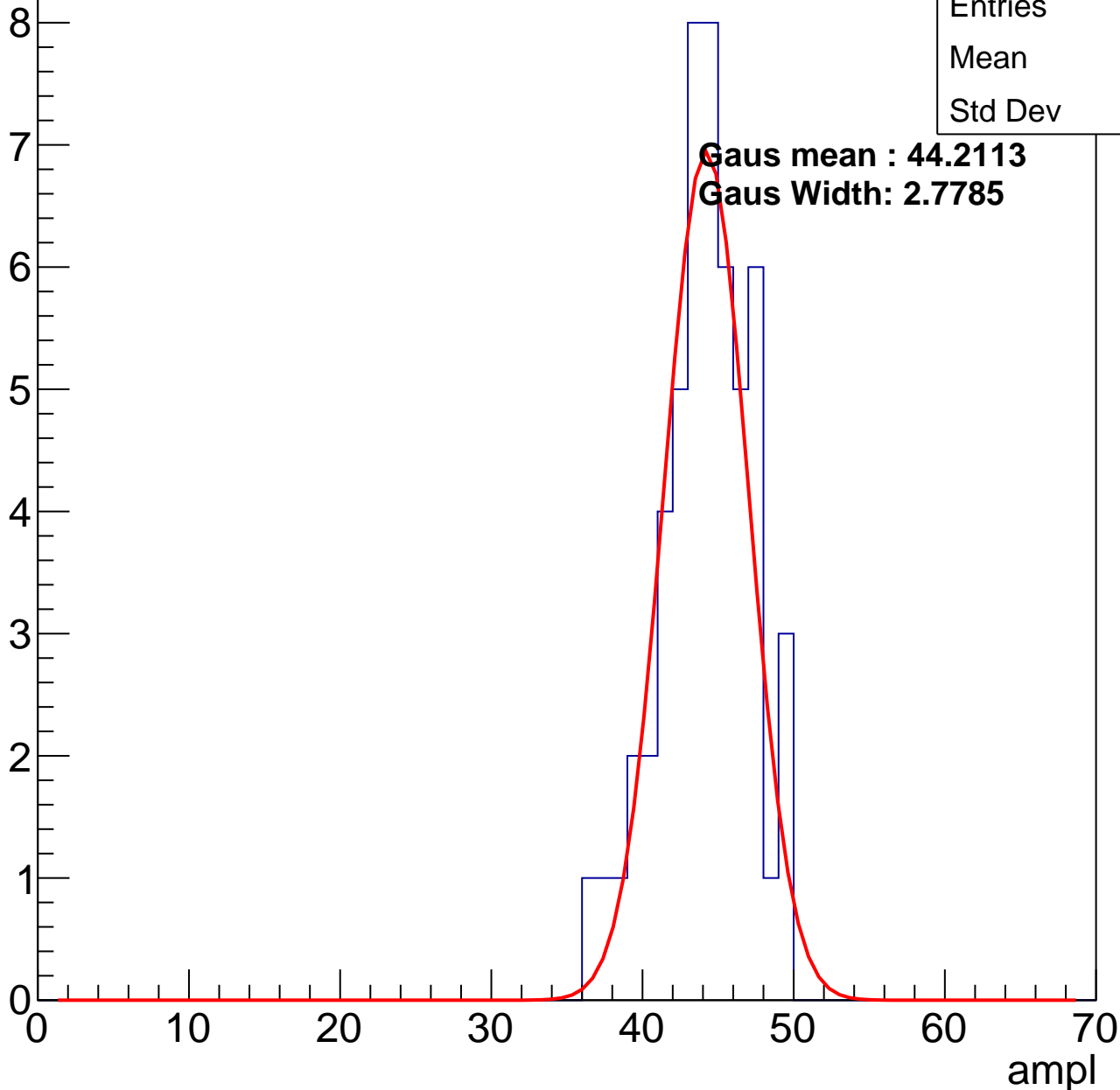
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	43.7
Std Dev	2.95

Gaus mean : 44.2113

Gaus Width: 2.7785

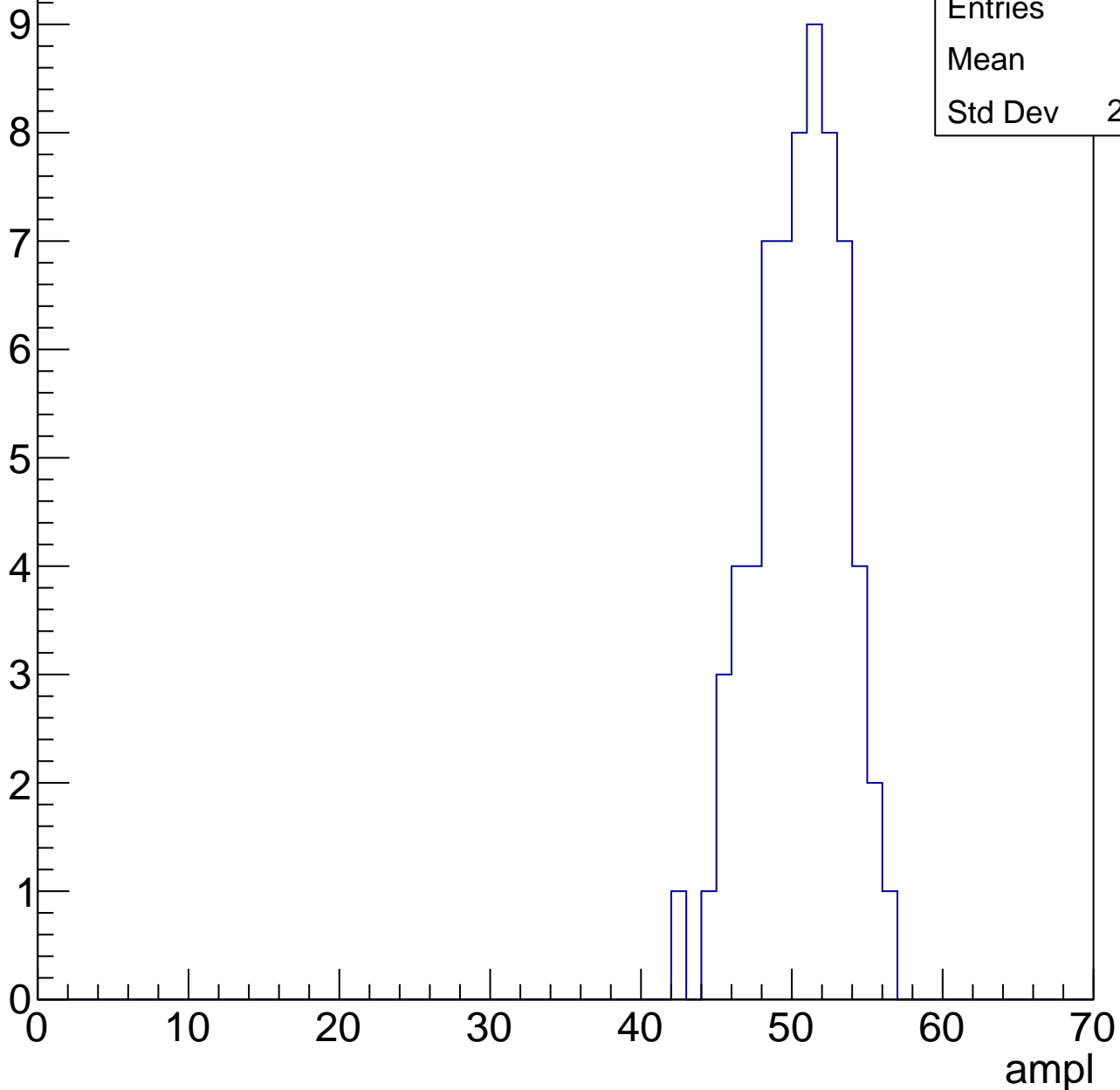


B1L100S, U13-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	50
Std Dev	2.923



B1L100S, U13-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	56.84
Std Dev	3.303

Entry

10

8

6

4

2

0

0

10

20

30

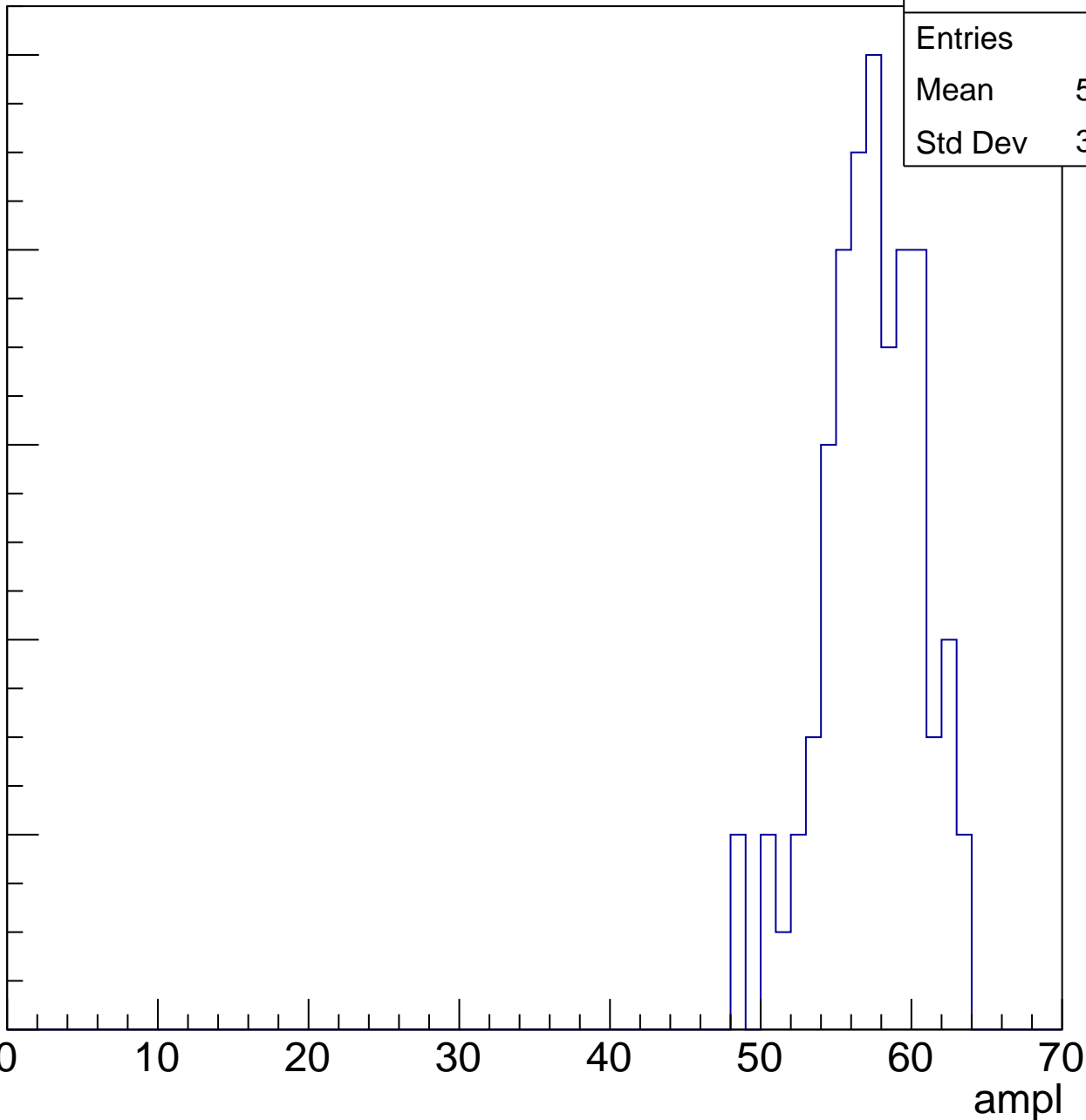
40

50

60

70

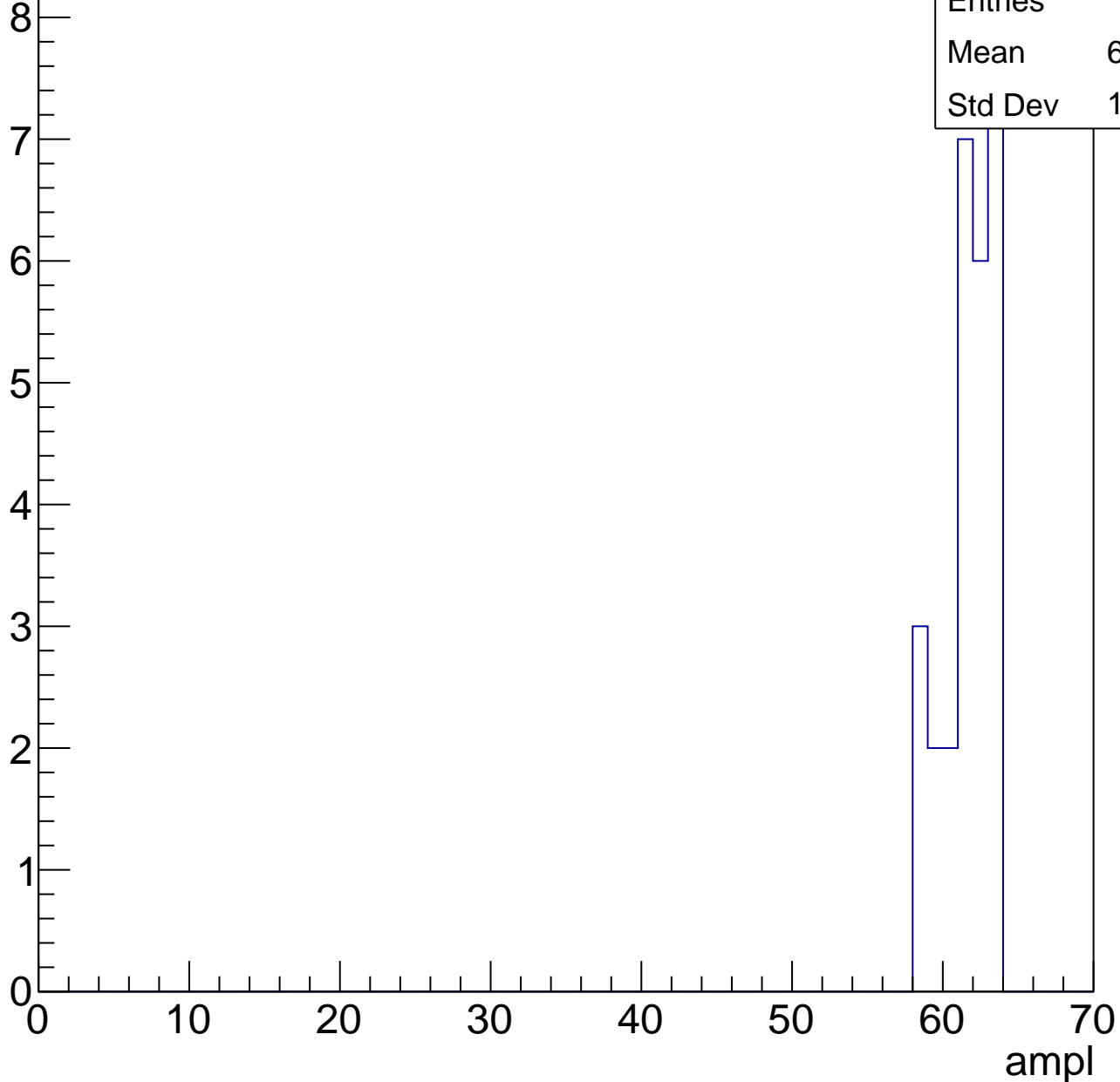
ampl



B1L100S, U13-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch94, adc0

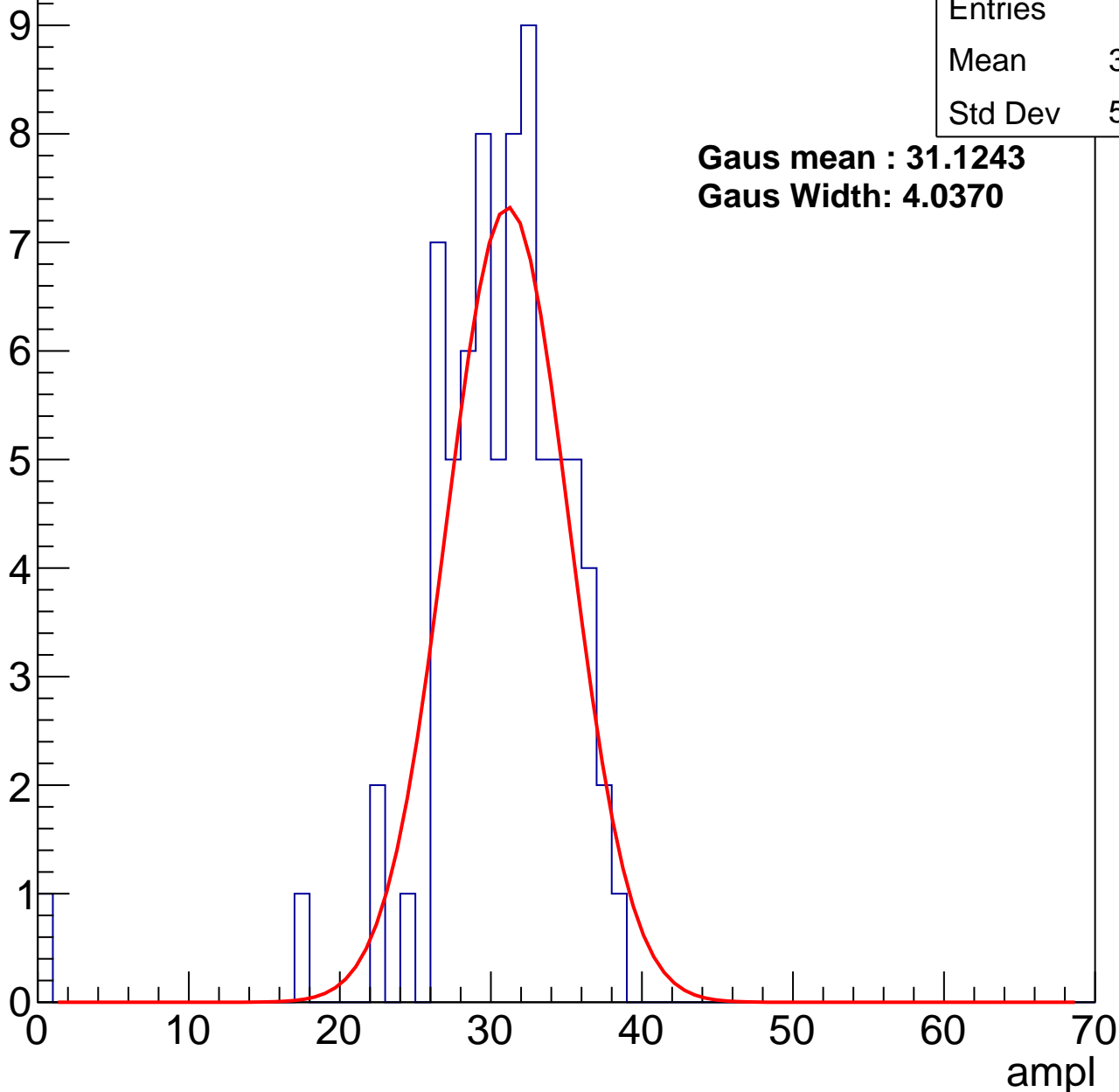
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.05
Std Dev	5.192

Gaus mean : 31.1243

Gaus Width: 4.0370



B1L100S, U13-ch94, adc1

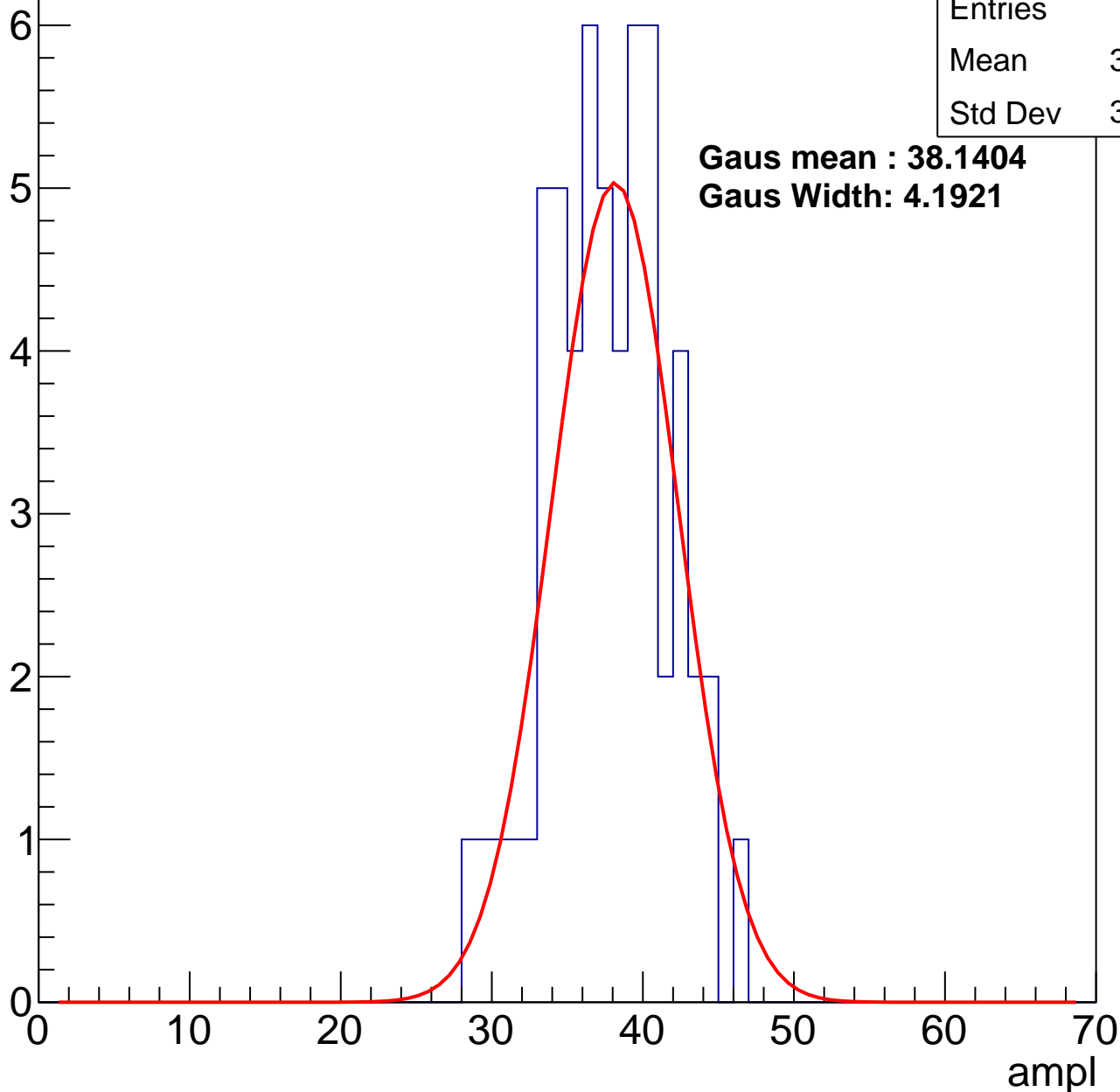
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	37.23
Std Dev	3.893

Gaus mean : 38.1404

Gaus Width: 4.1921



B1L100S, U13-ch94, adc2

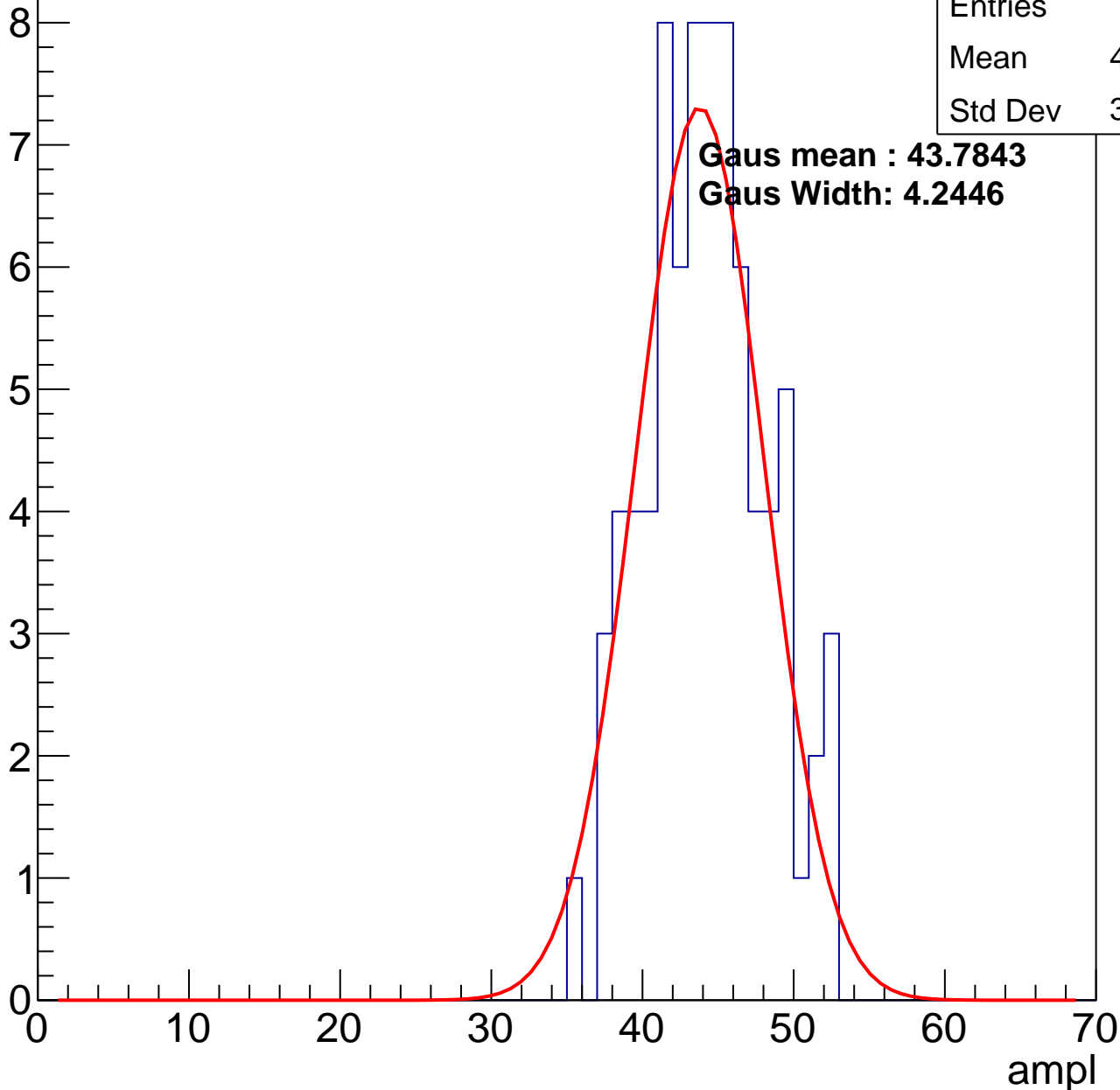
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	43.78
Std Dev	3.929

Gaus mean : 43.7843

Gaus Width: 4.2446

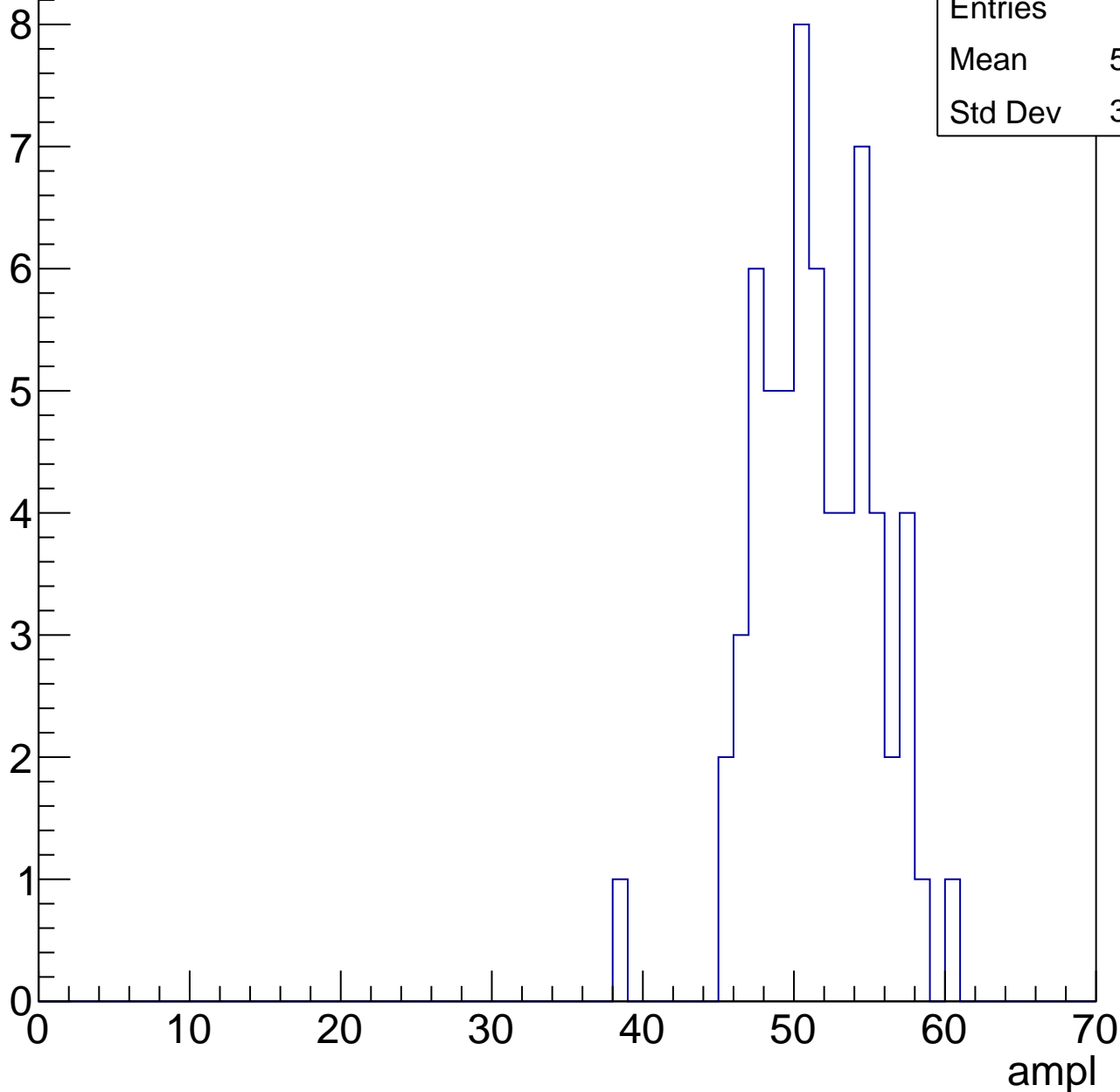


B1L100S, U13-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	51.03
Std Dev	3.895

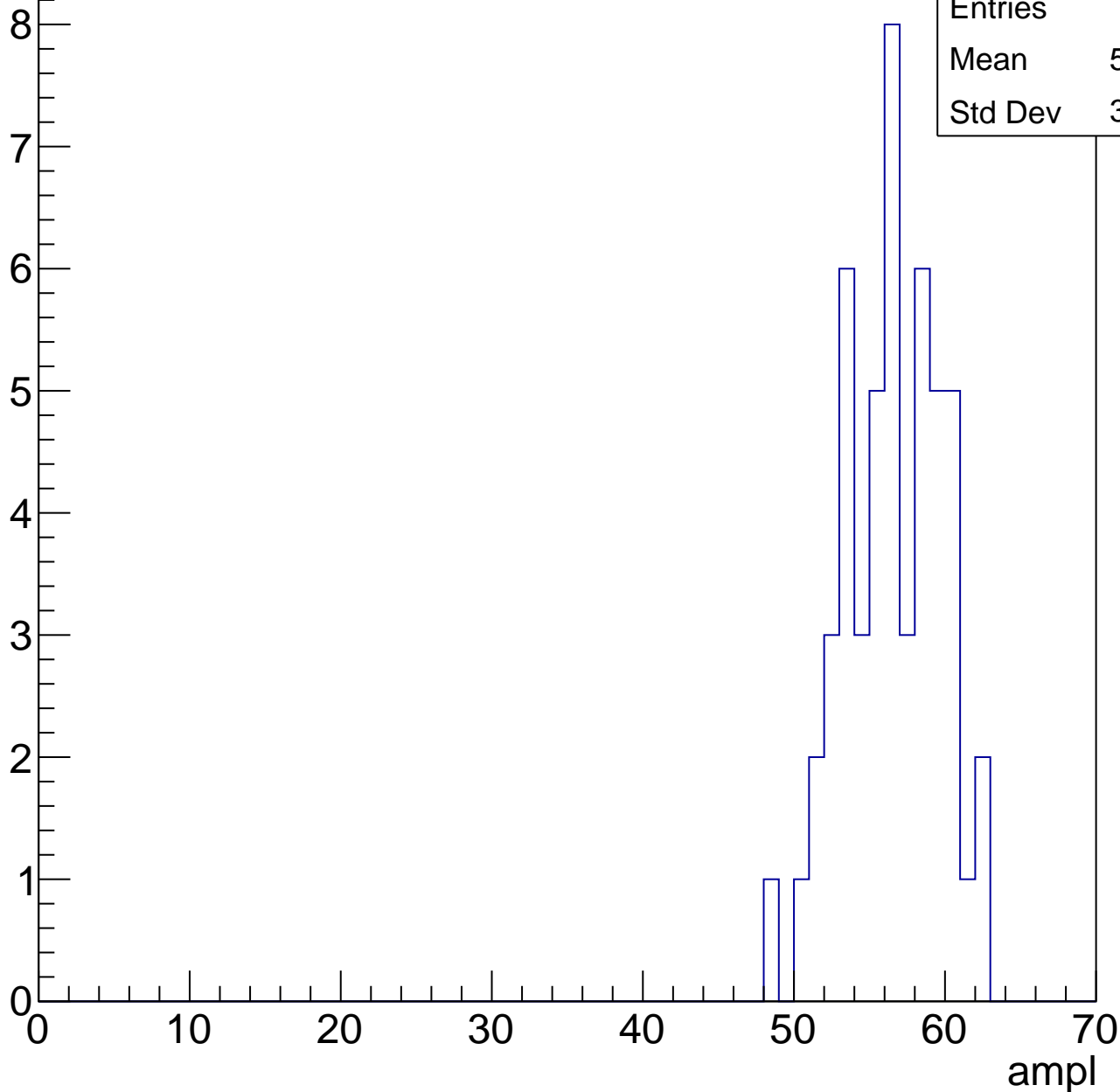


B1L100S, U13-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	56.04
Std Dev	3.187

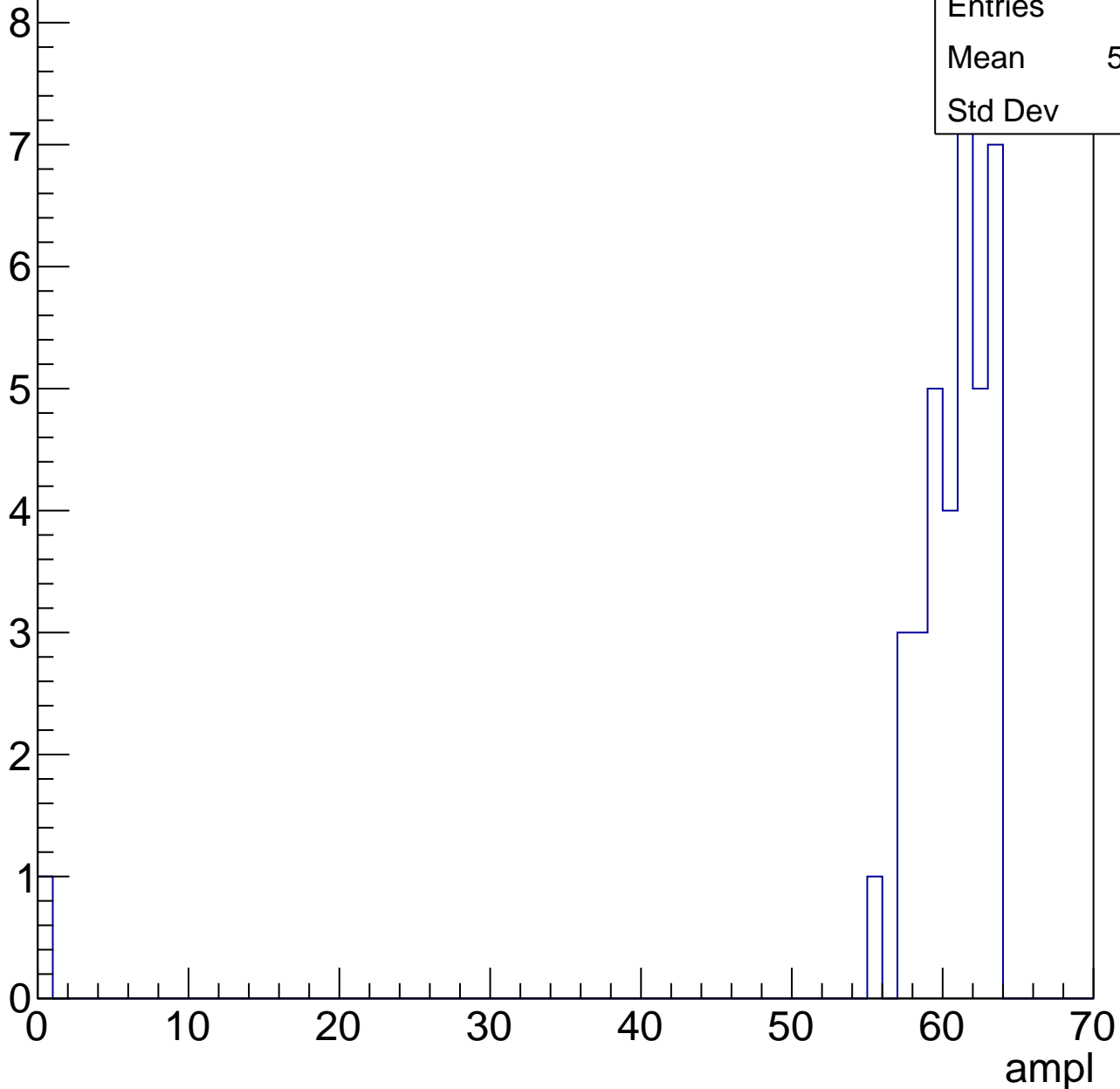


B1L100S, U13-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.76
Std Dev	10



B1L100S, U13-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch95, adc0

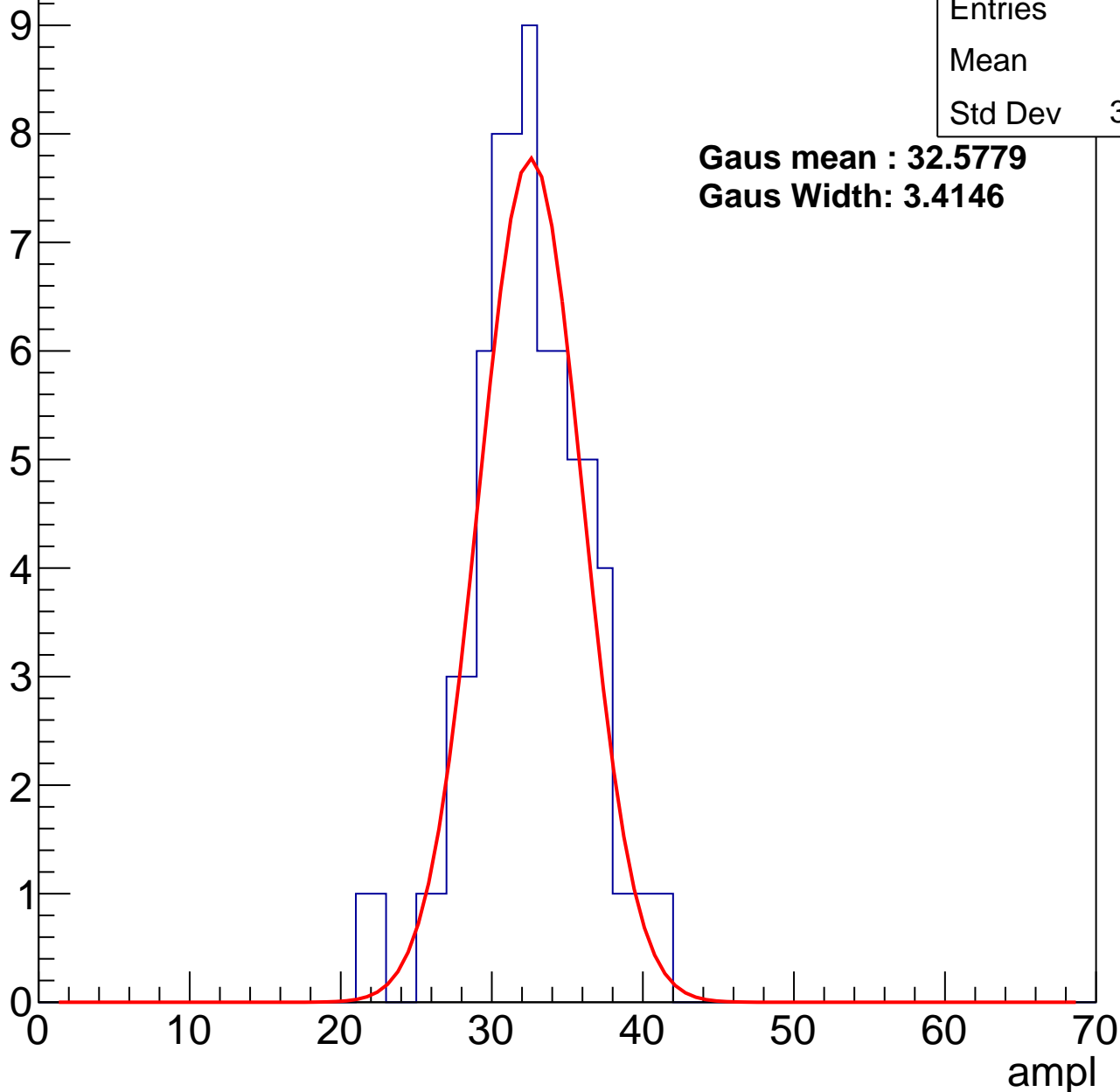
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	32
Std Dev	3.768

Gaus mean : 32.5779

Gaus Width: 3.4146



B1L100S, U13-ch95, adc1

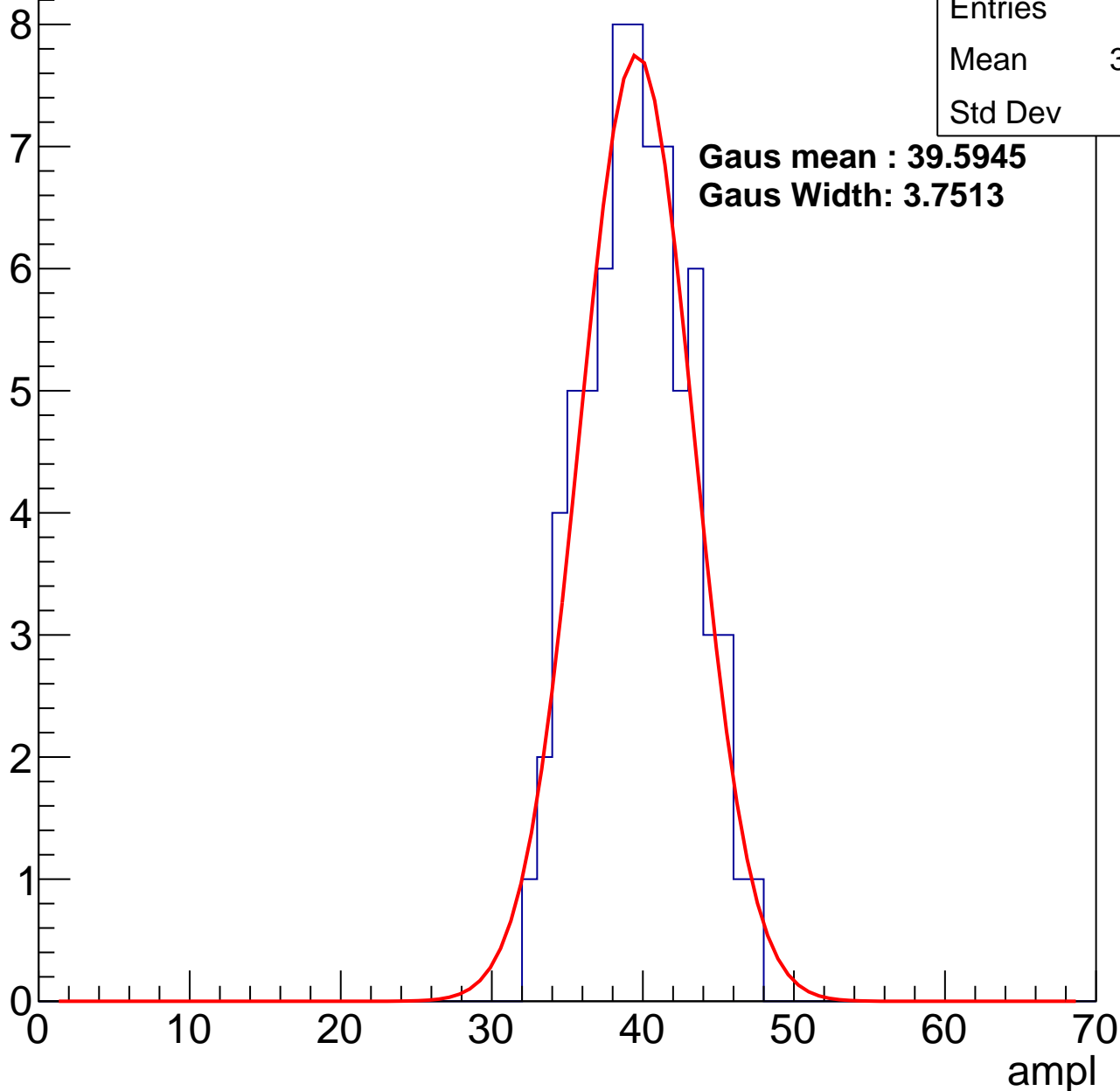
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	39.19
Std Dev	3.43

Gaus mean : 39.5945

Gaus Width: 3.7513



B1L100S, U13-ch95, adc2

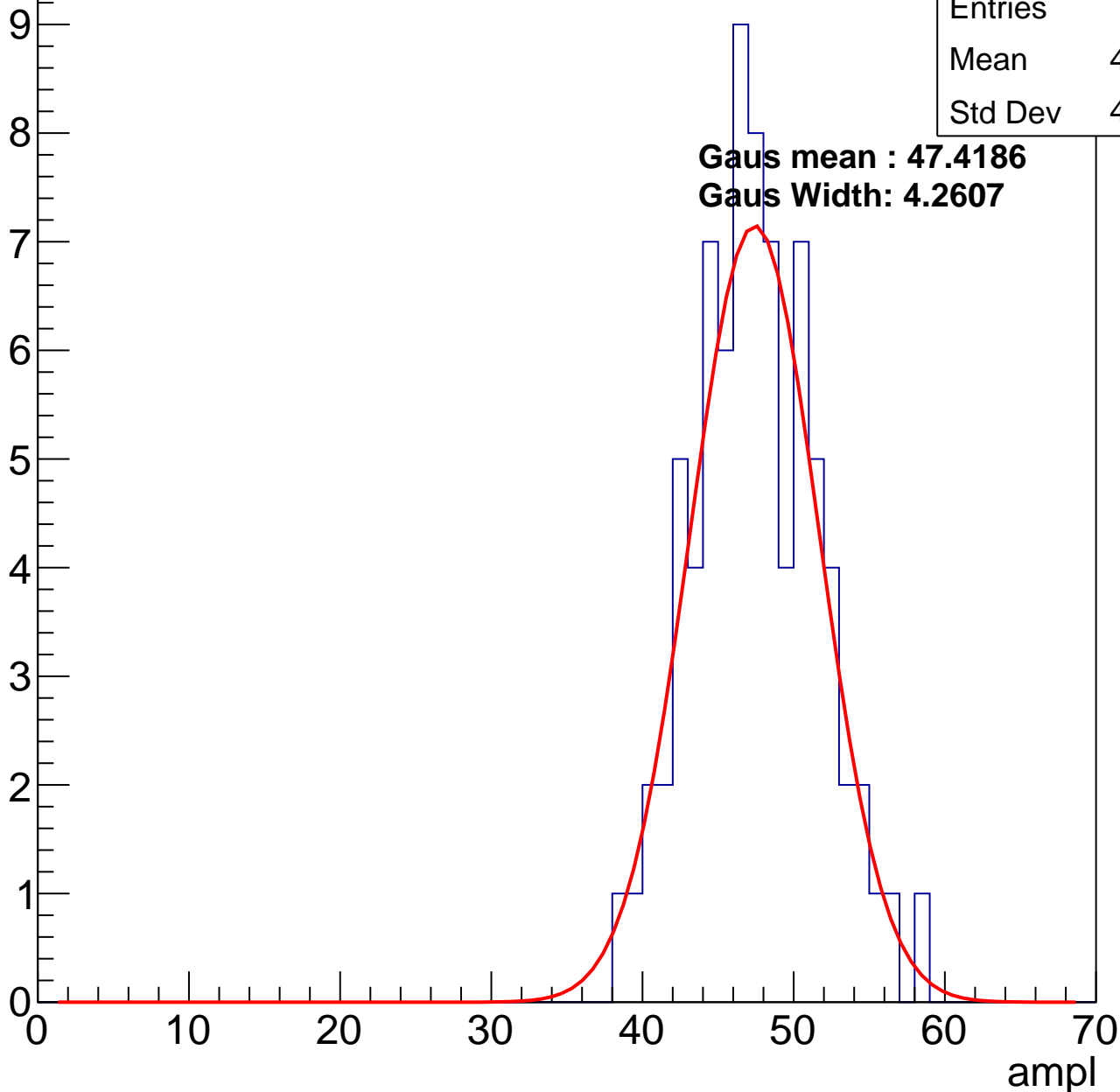
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	47.05
Std Dev	4.059

Gaus mean : 47.4186

Gaus Width: 4.2607

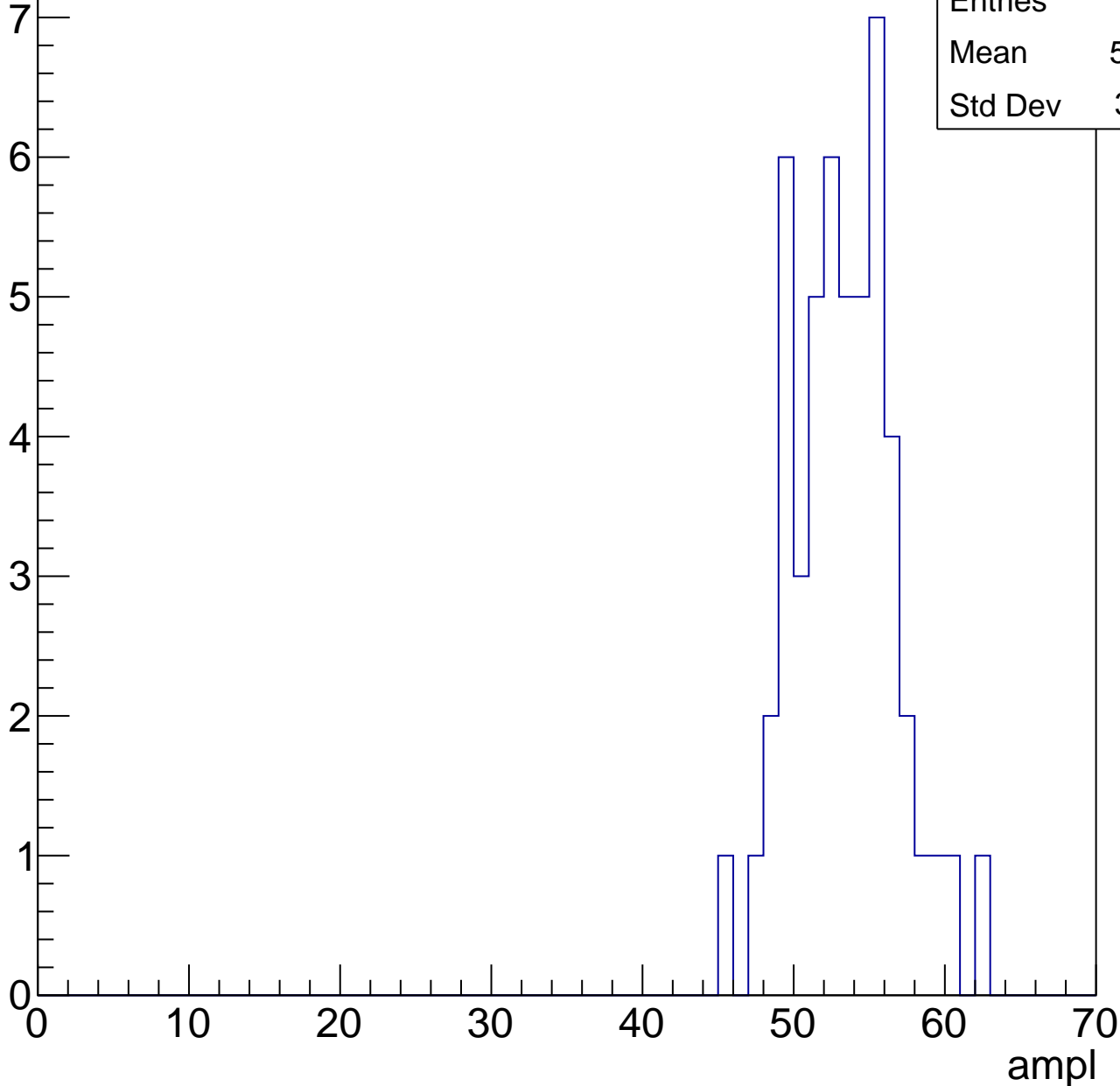


B1L100S, U13-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	52.86
Std Dev	3.401

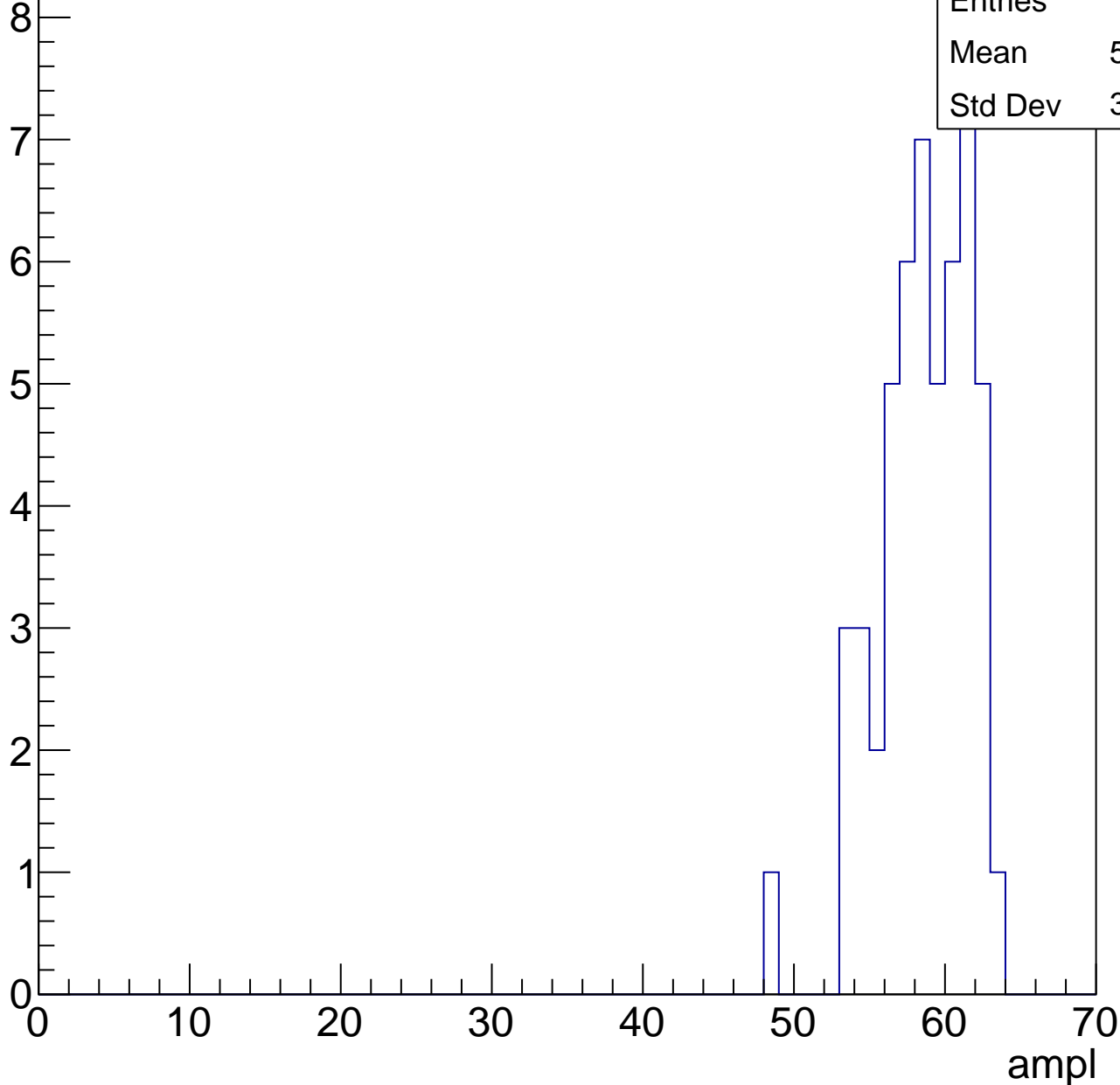


B1L100S, U13-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.13
Std Dev	3.013

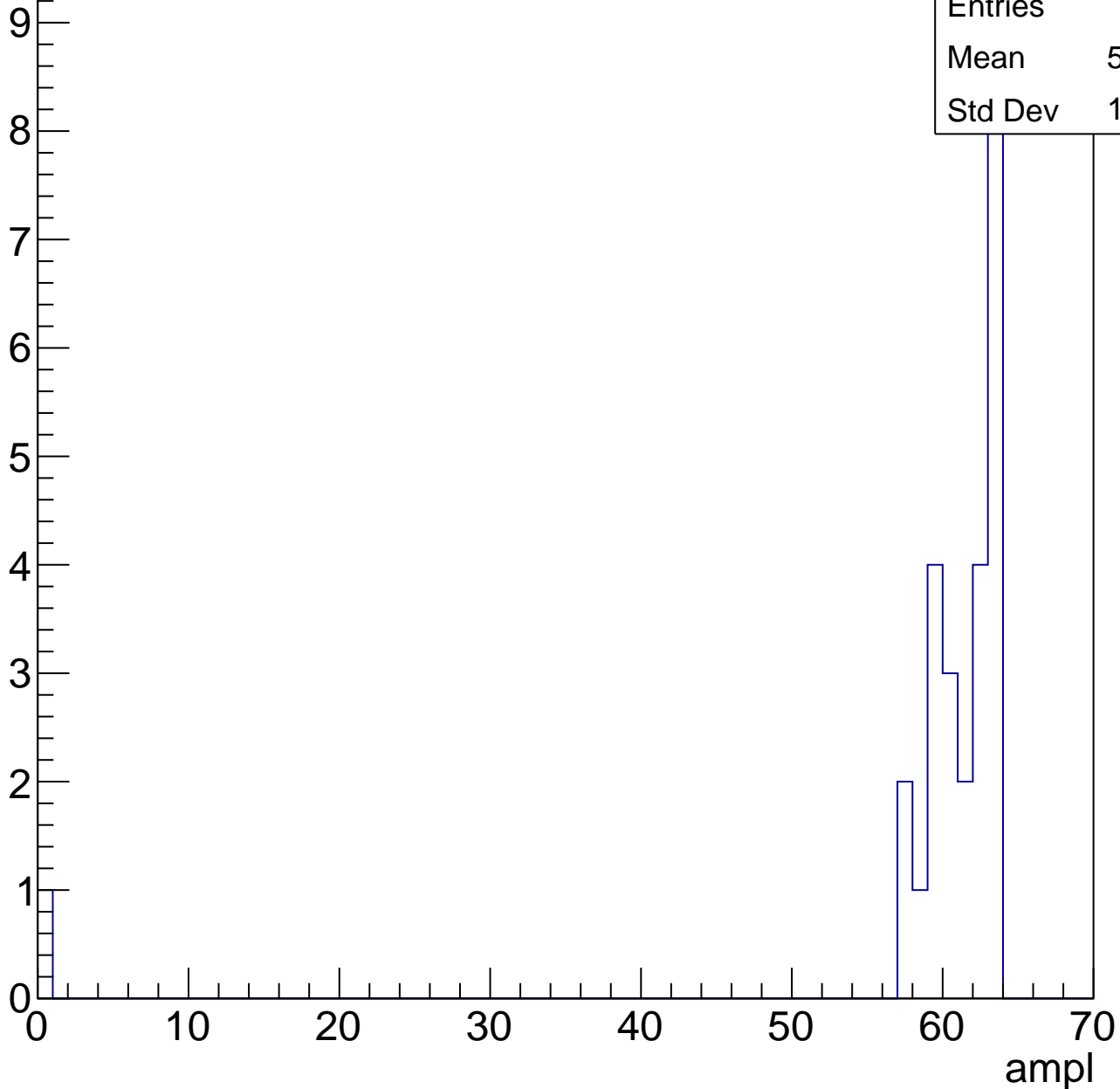


B1L100S, U13-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.65
Std Dev	11.89



B1L100S, U13-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch96, adc0

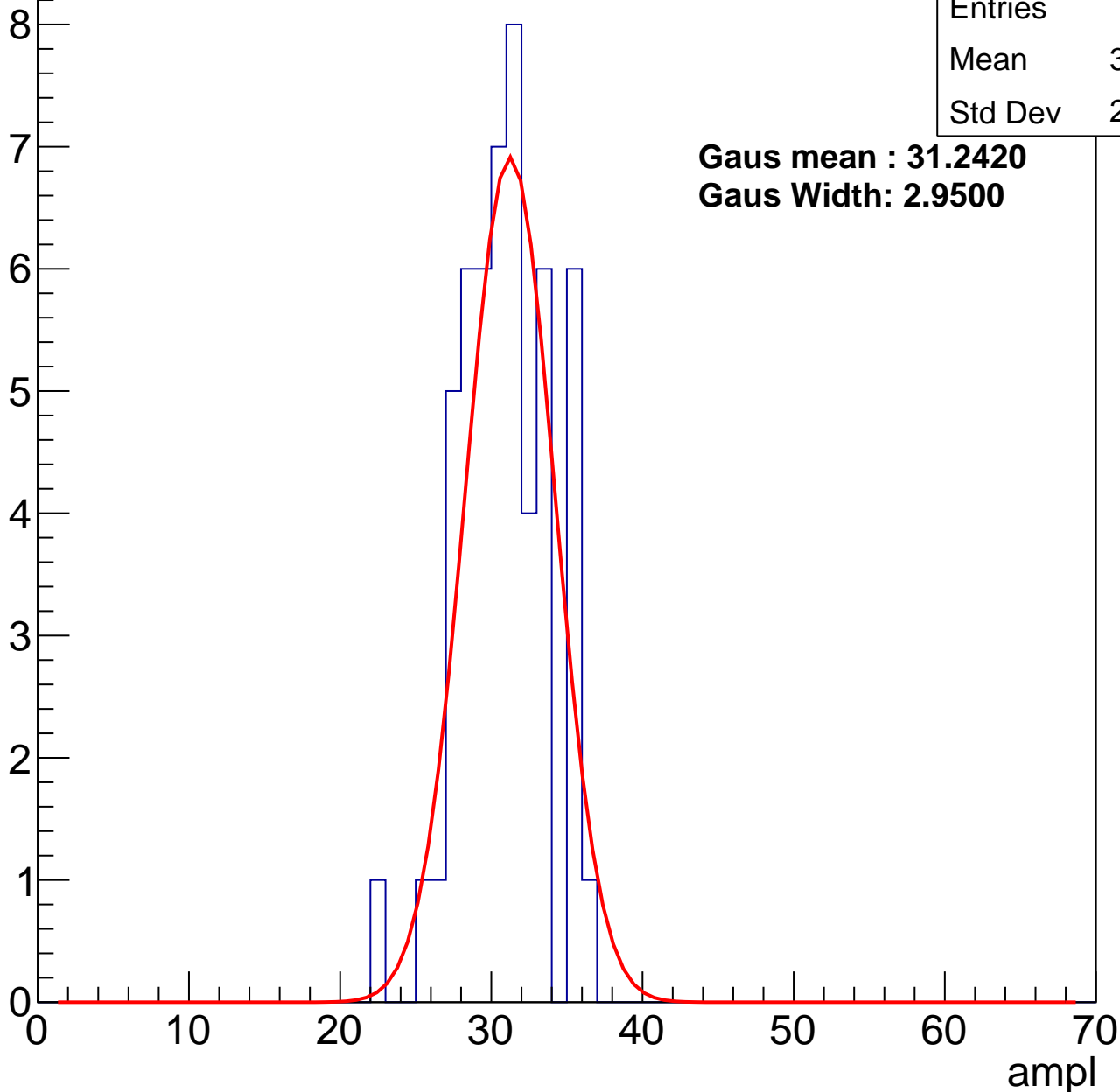
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	30.38
Std Dev	2.897

Gaus mean : 31.2420

Gaus Width: 2.9500



B1L100S, U13-ch96, adc1

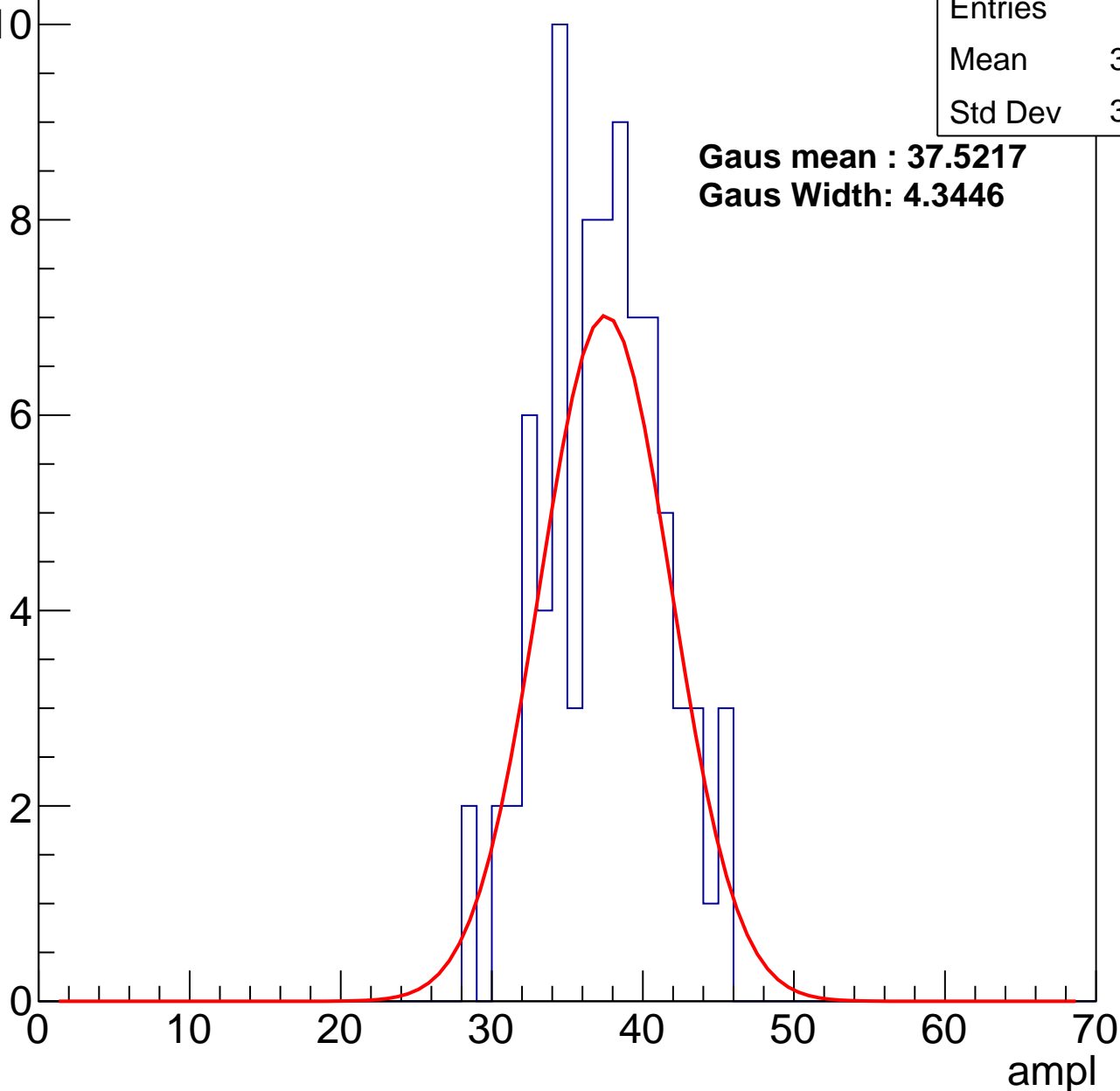
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	36.93
Std Dev	3.896

Gaus mean : 37.5217

Gaus Width: 4.3446



B1L100S, U13-ch96, adc2

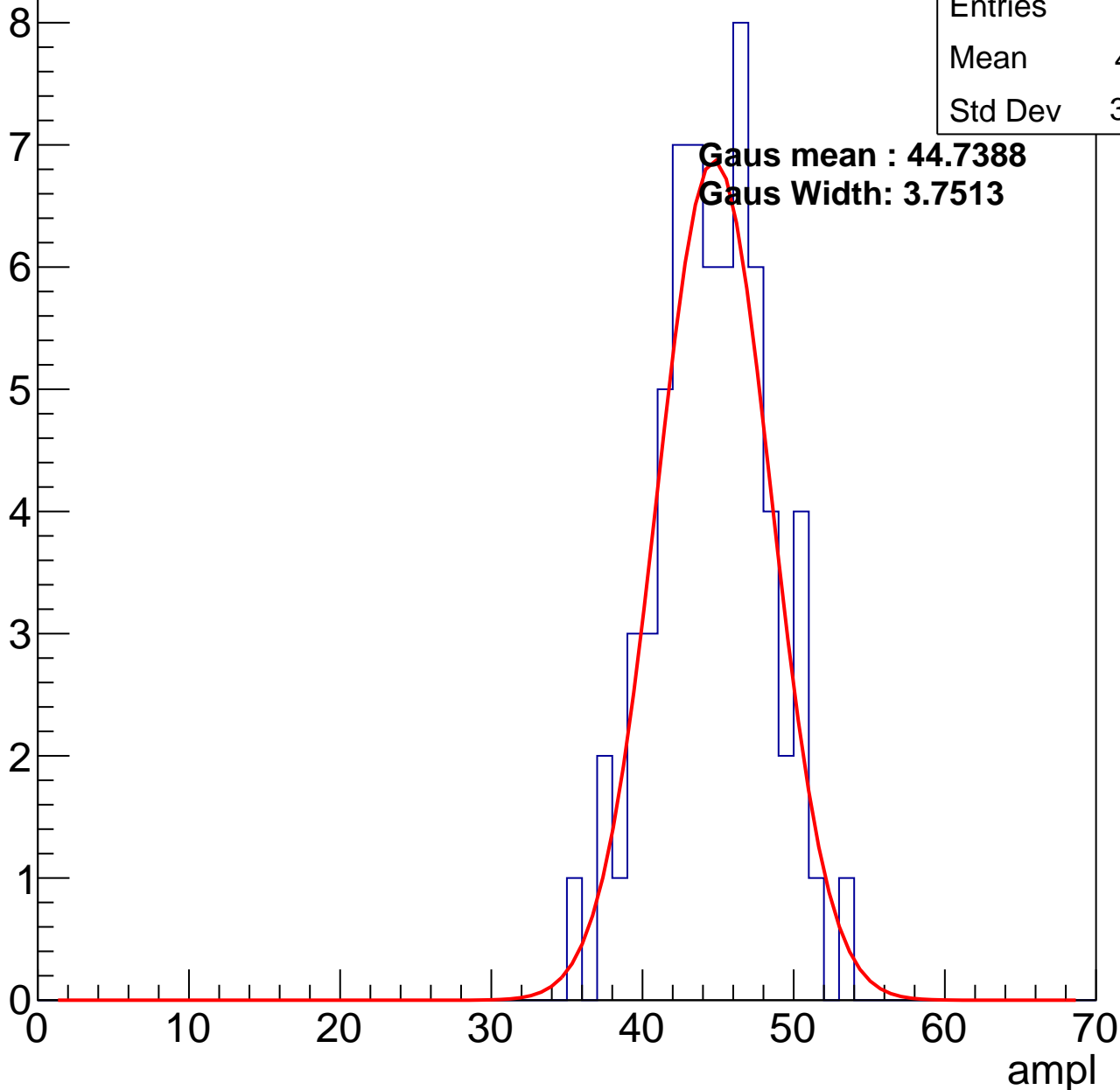
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.21
Std Dev	3.655

Gaus mean : 44.7388

Gaus Width: 3.7513

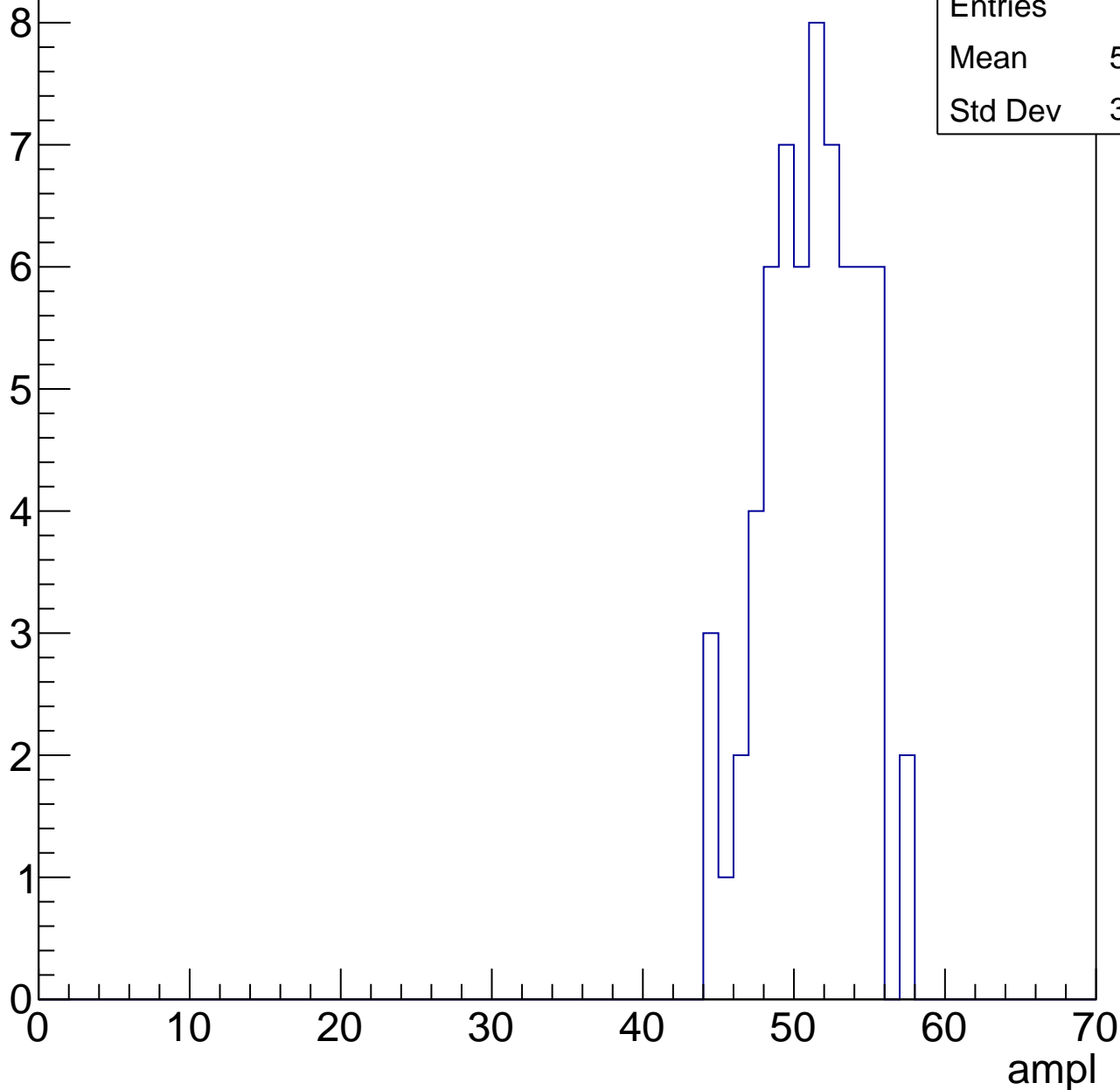


B1L100S, U13-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

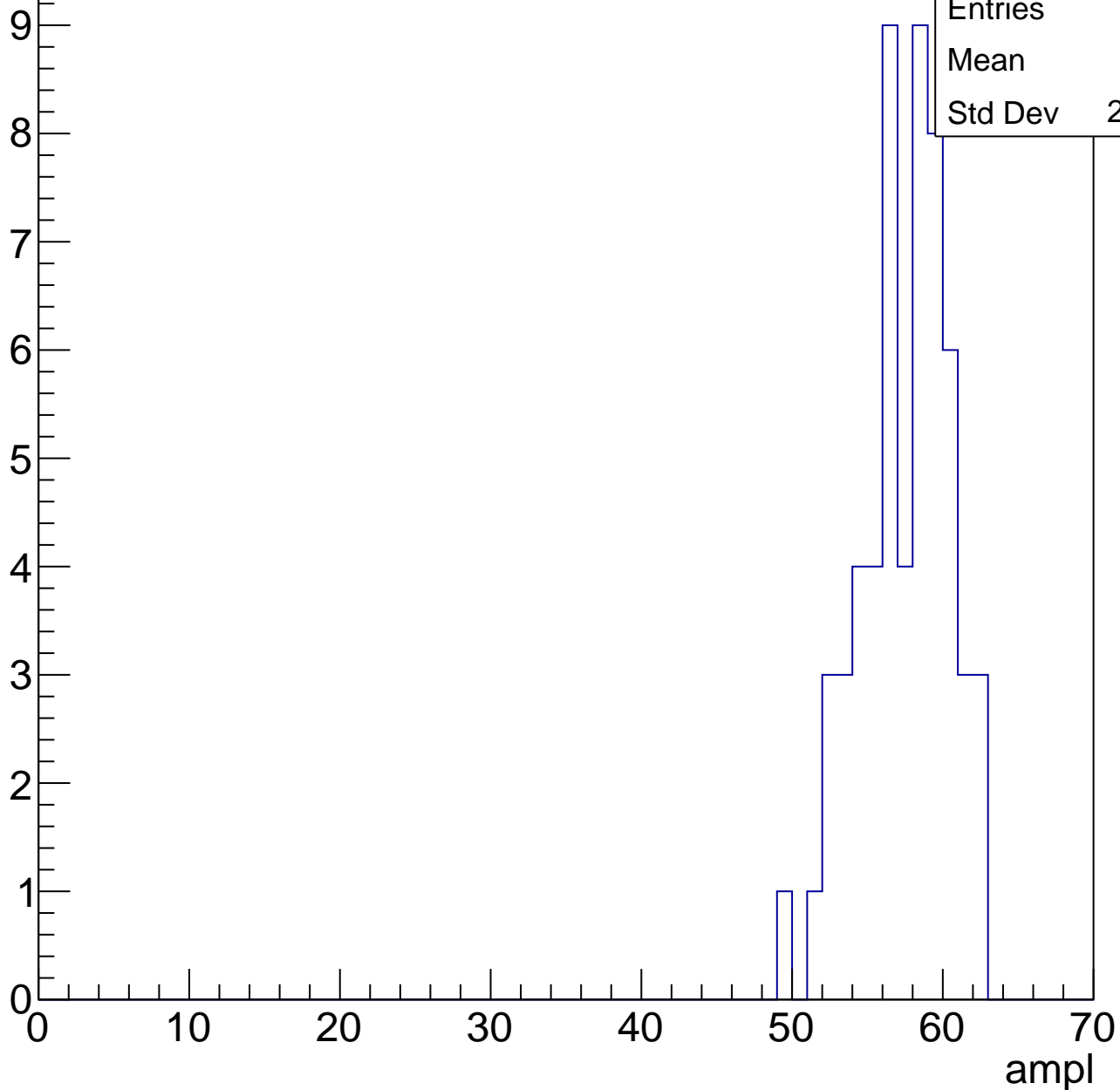
Entries	64
Mean	50.72
Std Dev	3.145



B1L100S, U13-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

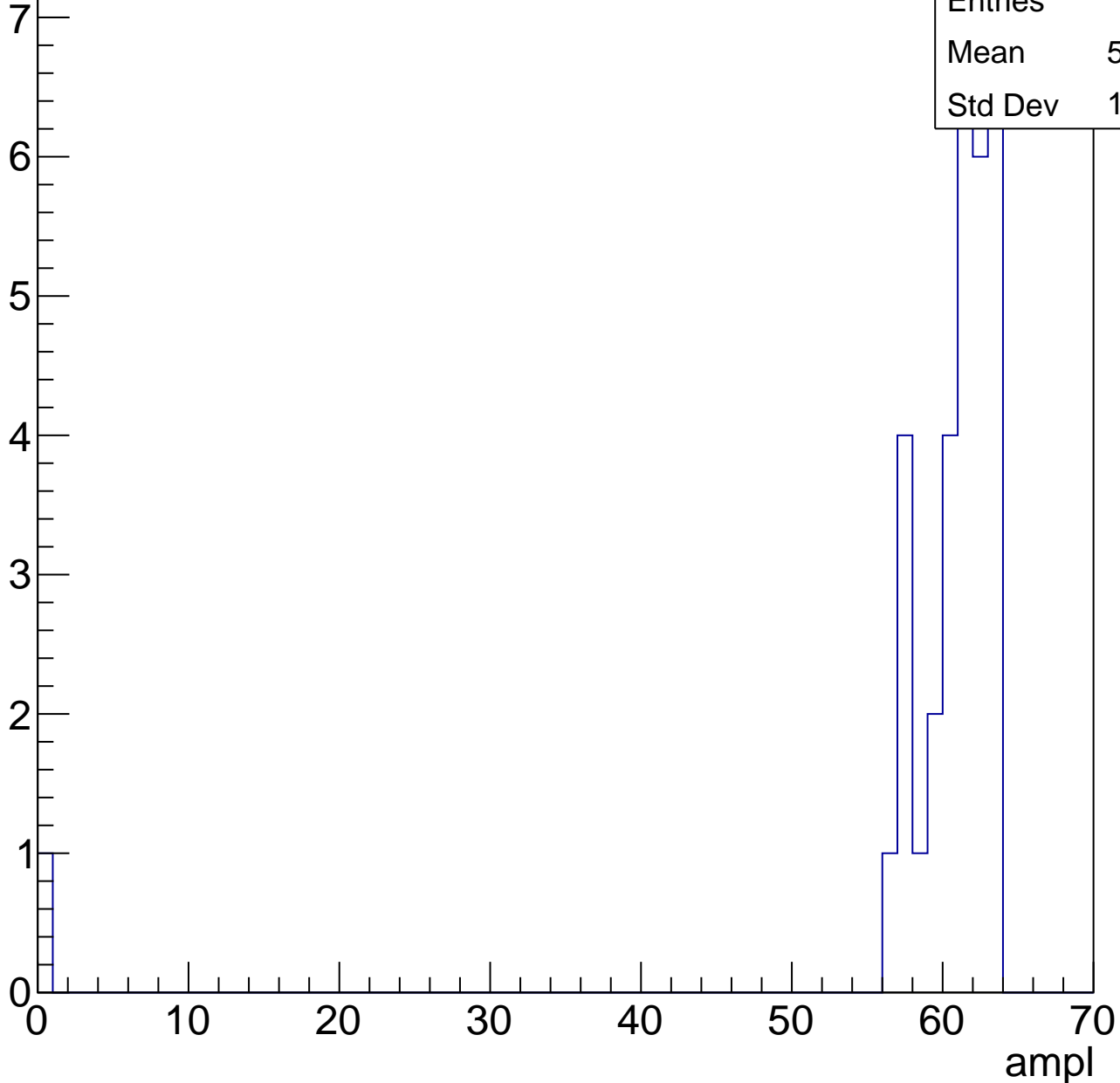


B1L100S, U13-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.79
Std Dev	10.59



B1L100S, U13-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch97, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	29.54
Std Dev	5.934

Gaus mean : 30.5305

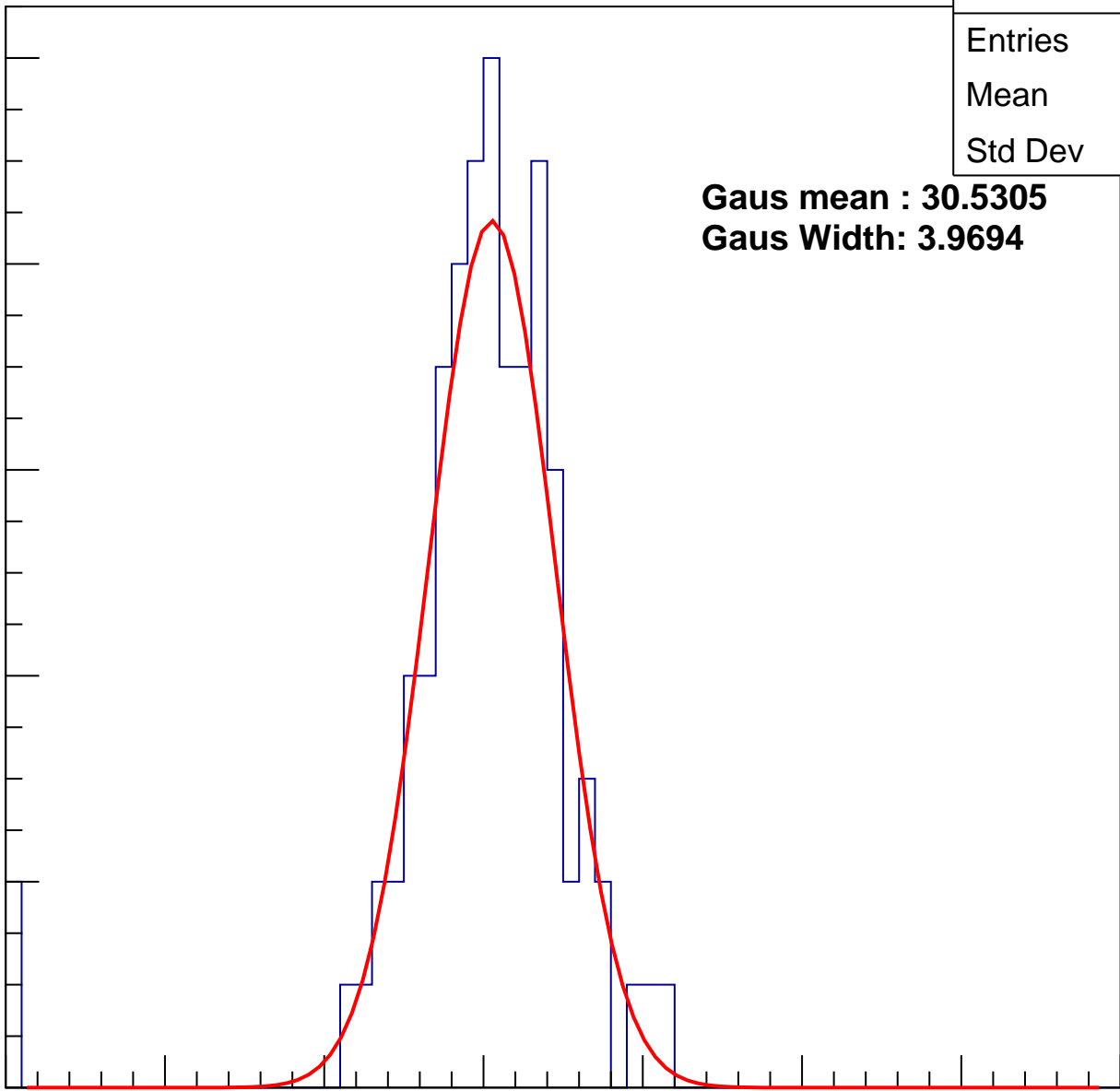
Gaus Width: 3.9694

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch97, adc1

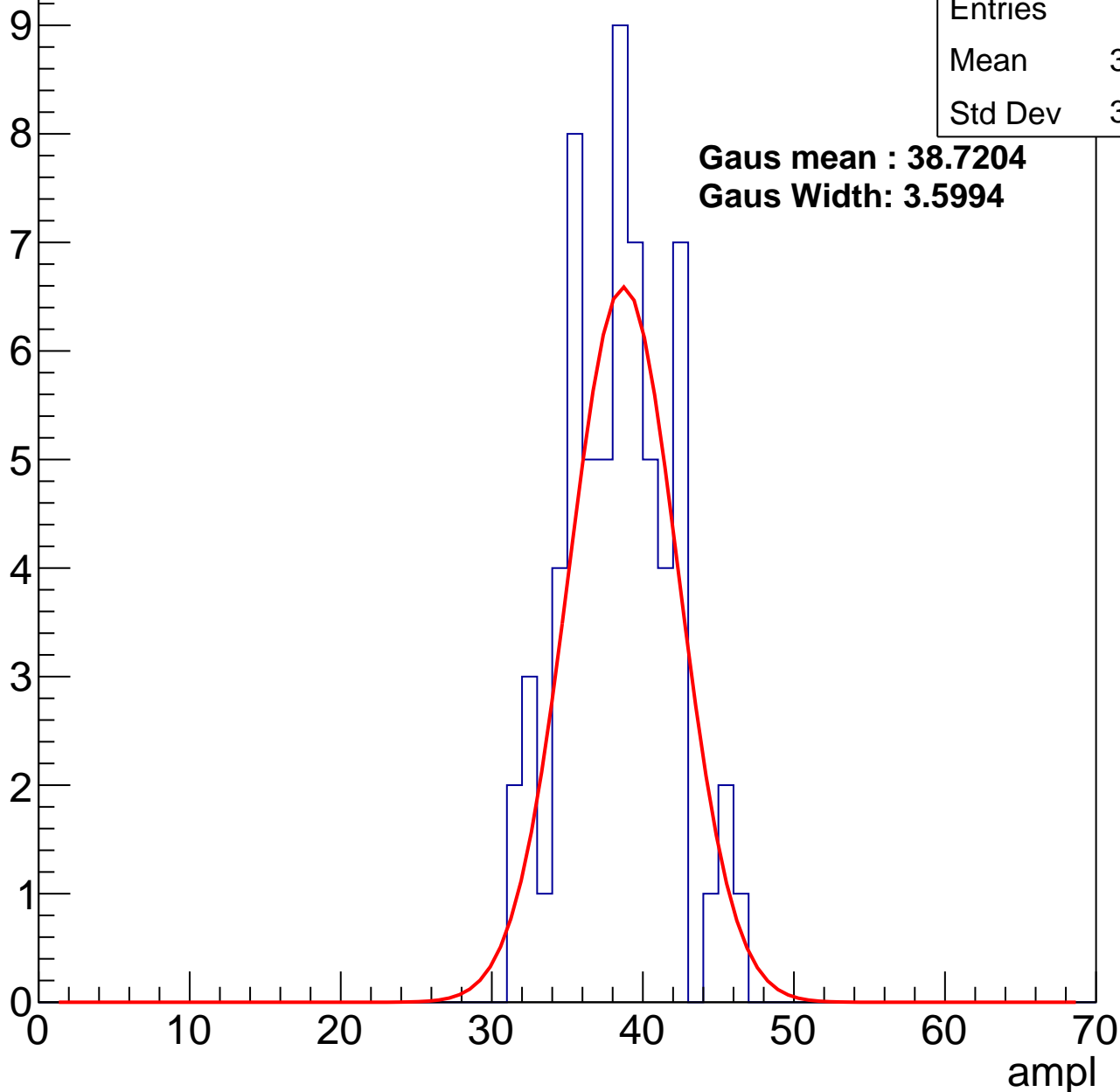
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.89
Std Dev	3.456

Gaus mean : 38.7204

Gaus Width: 3.5994



B1L100S, U13-ch97, adc2

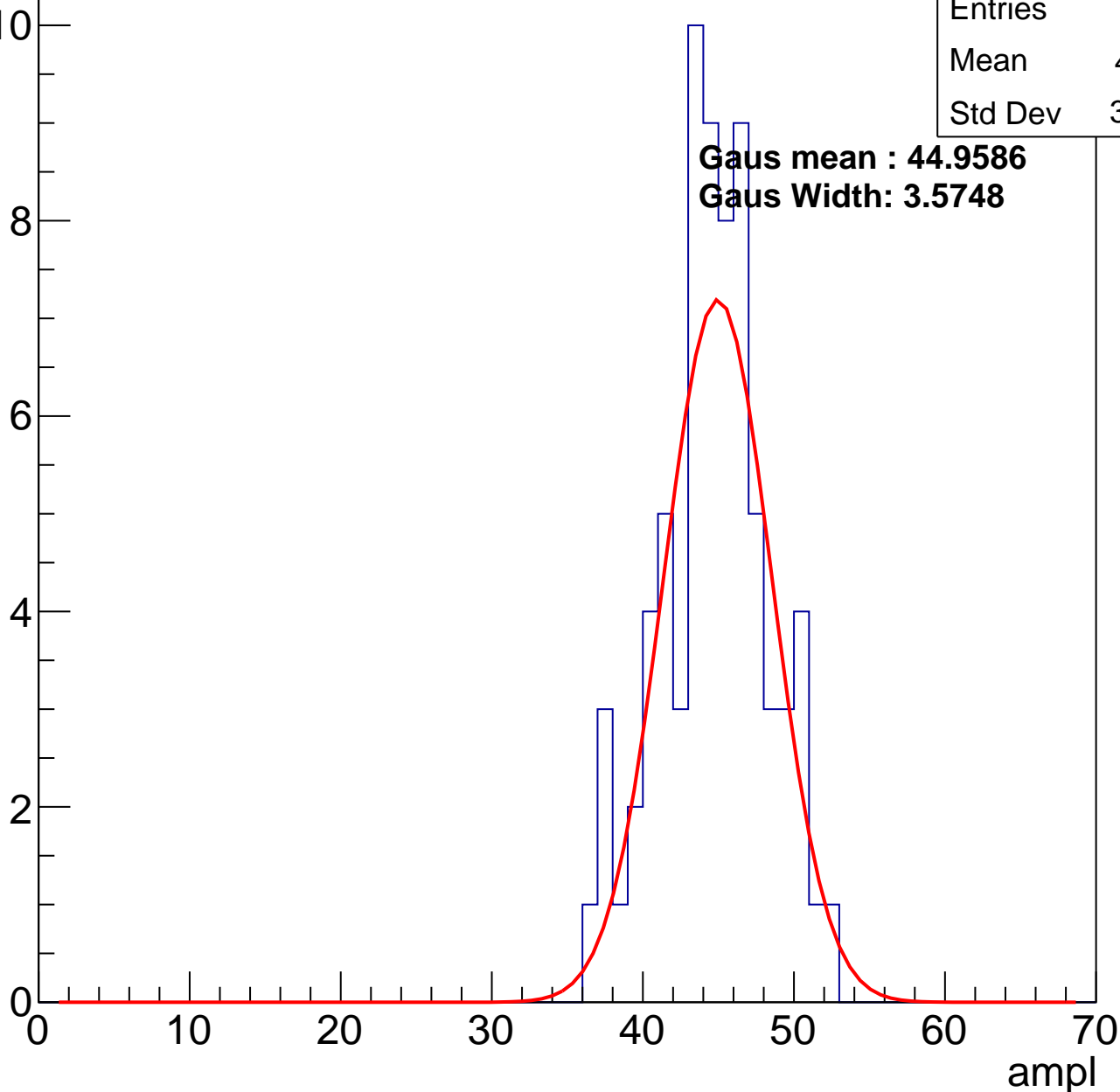
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.21
Std Dev	3.523

Gaus mean : 44.9586

Gaus Width: 3.5748

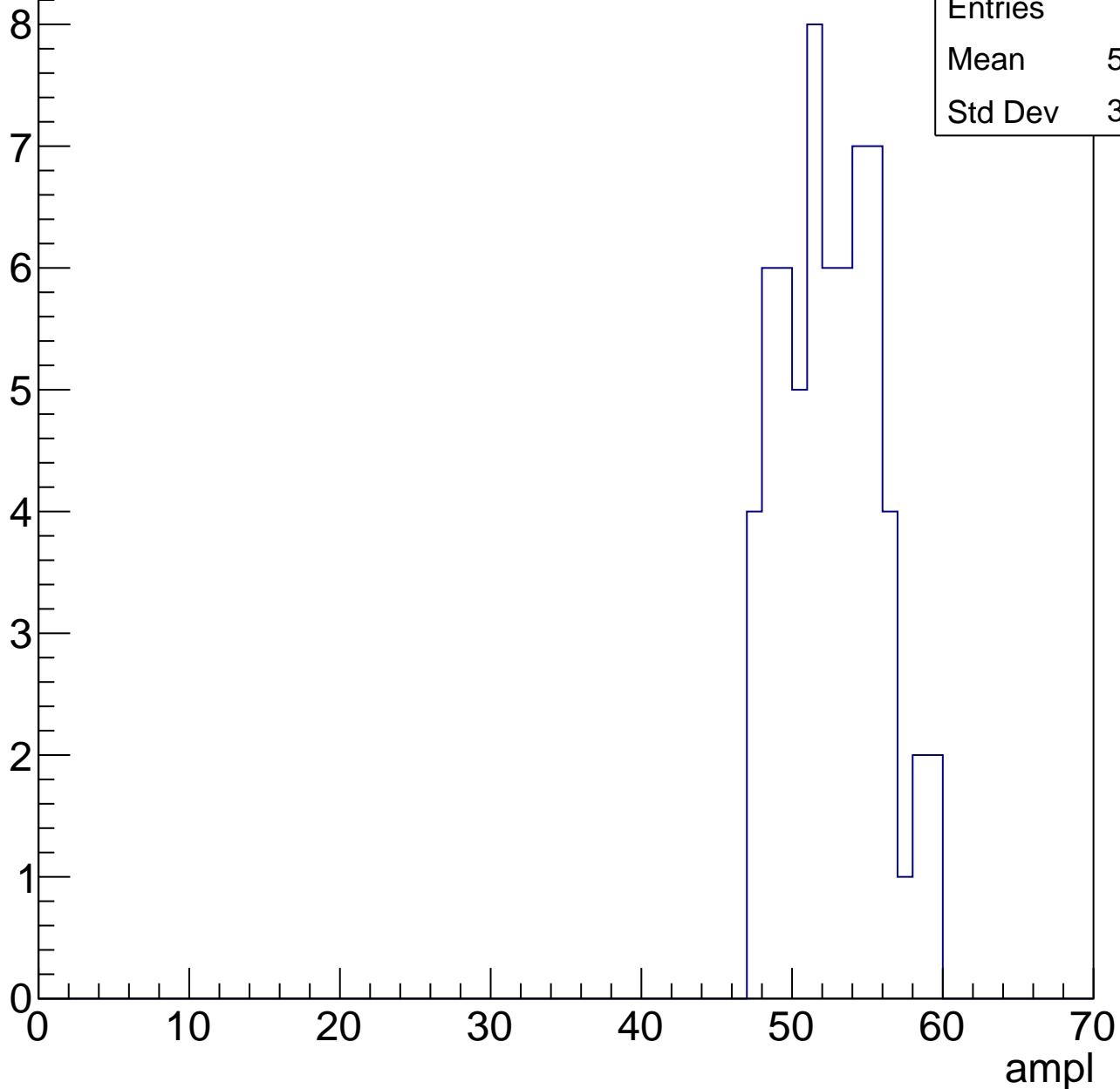


B1L100S, U13-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	52.12
Std Dev	3.145

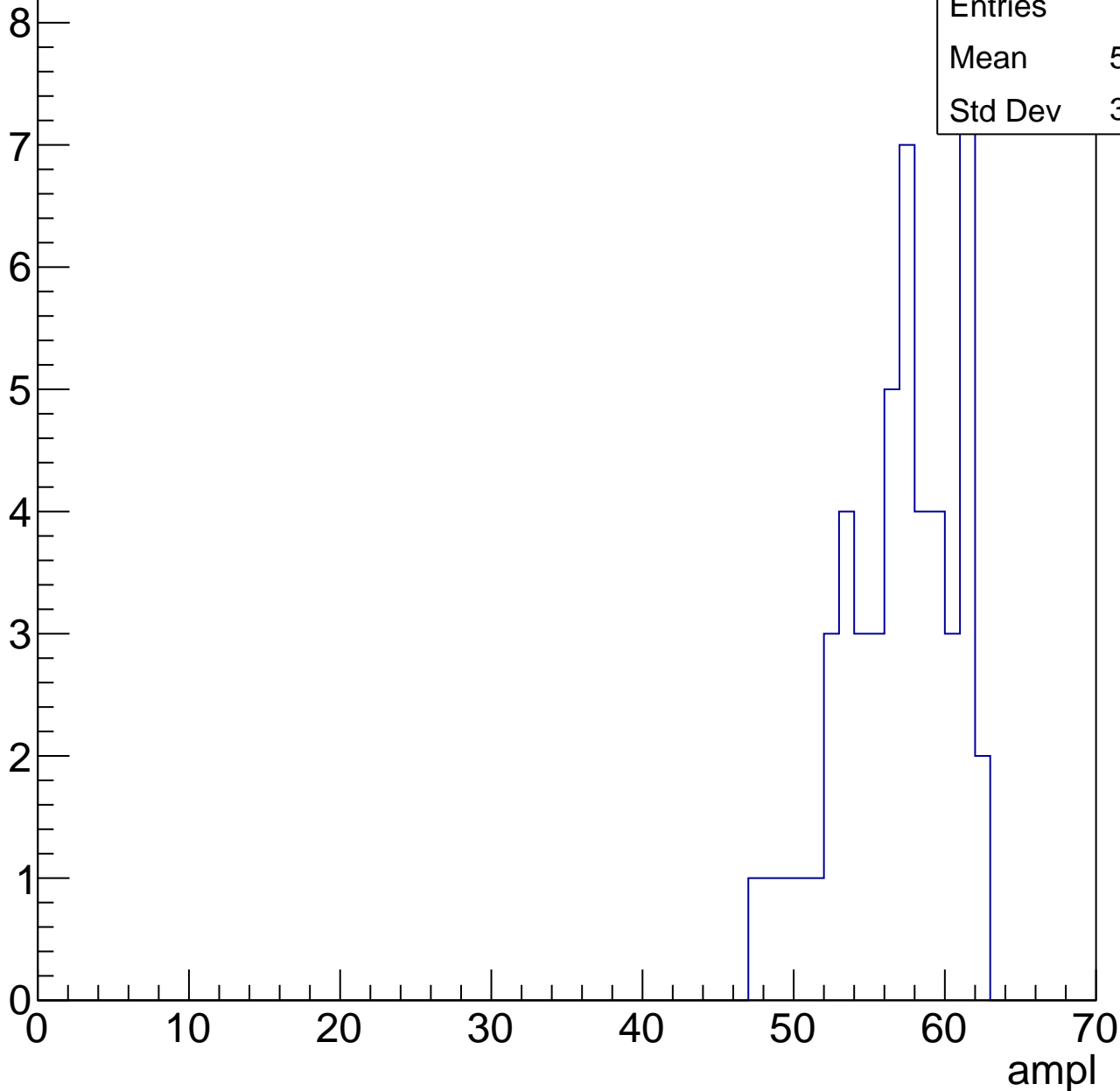


B1L100S, U13-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

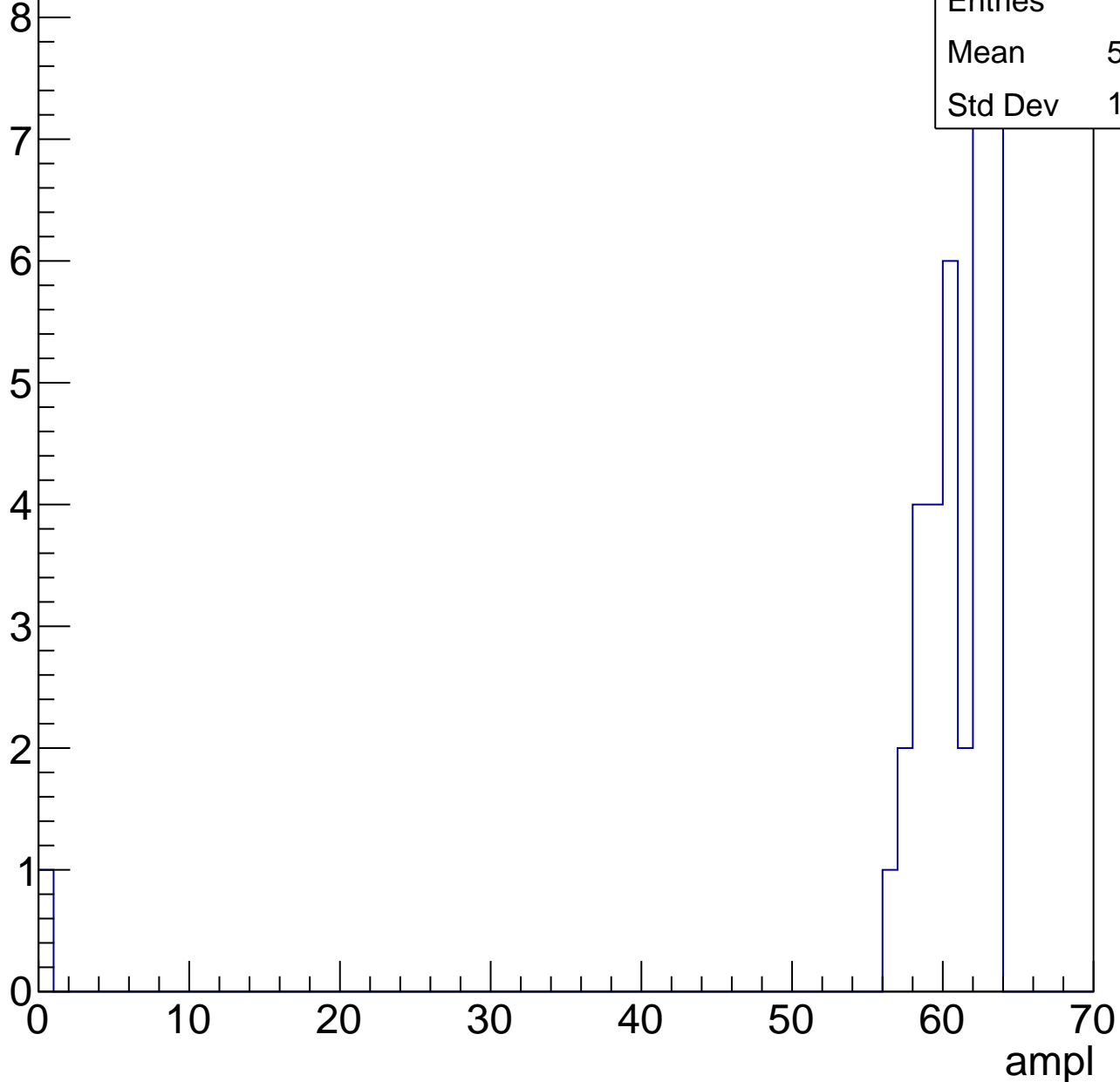
Entries	51
Mean	56.45
Std Dev	3.764



B1L100S, U13-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L100S, U13-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U13-ch98, adc0

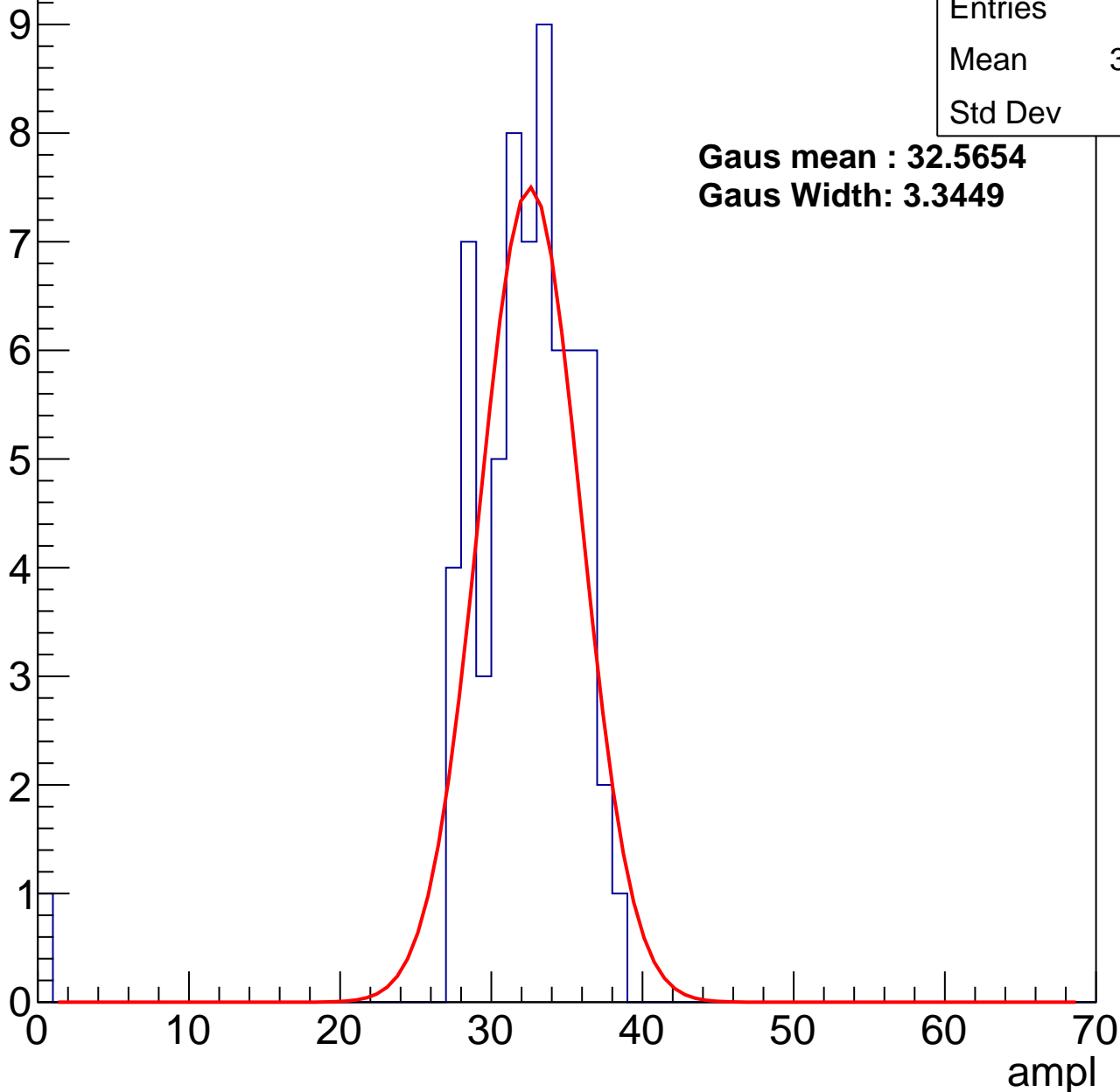
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	31.57
Std Dev	4.88

Gaus mean : 32.5654

Gaus Width: 3.3449



B1L100S, U13-ch98, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	37.95
Std Dev	3.853

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Gaus mean : 39.3678

Gaus Width: 3.7286

0

1

2

3

4

5

6

7

0

1

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B1L100S, U13-ch98, adc2

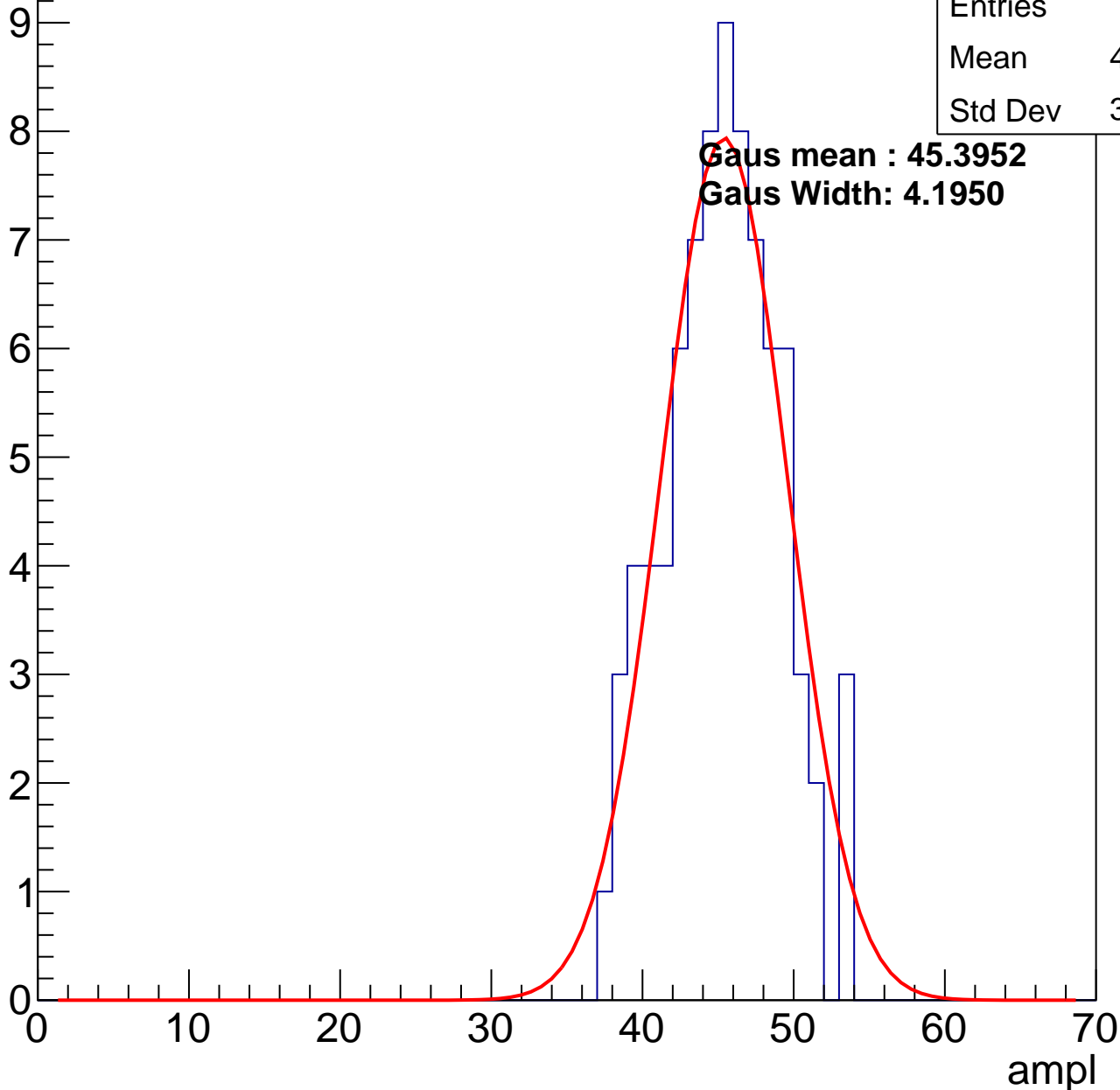
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	44.83
Std Dev	3.738

Gaus mean : 45.3952

Gaus Width: 4.1950

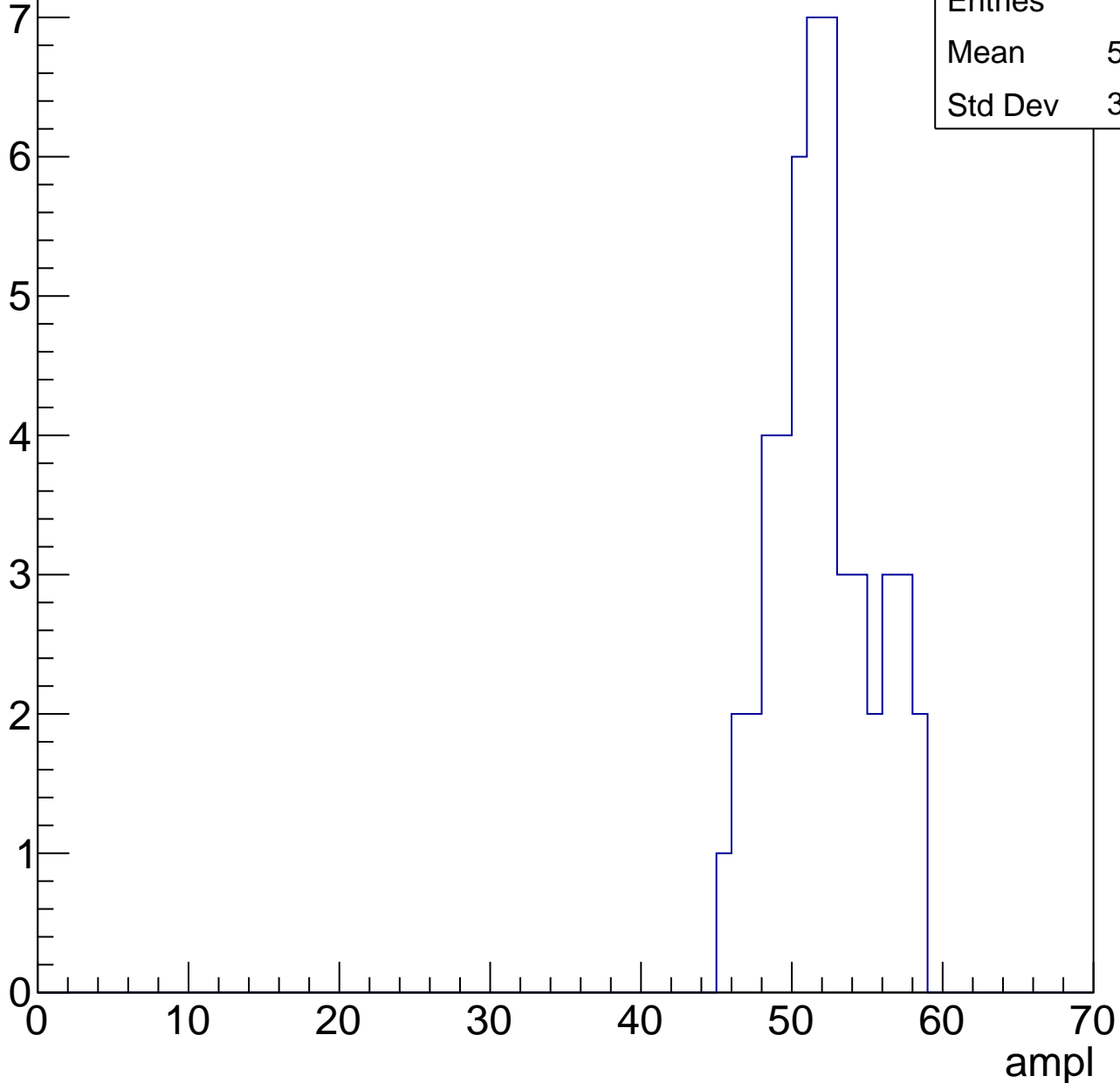


B1L100S, U13-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

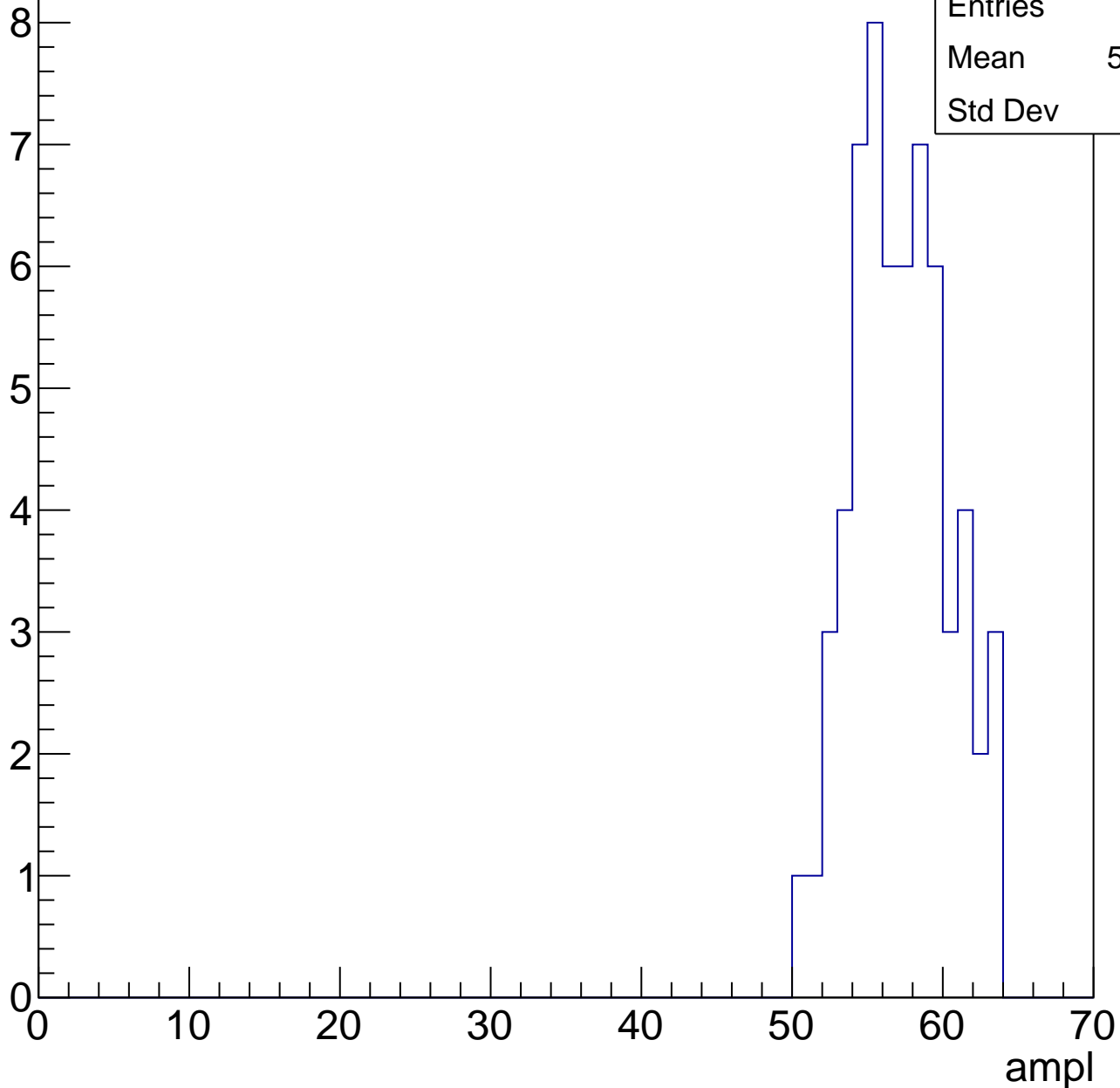
Entries	49
Mean	51.55
Std Dev	3.258



B1L100S, U13-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

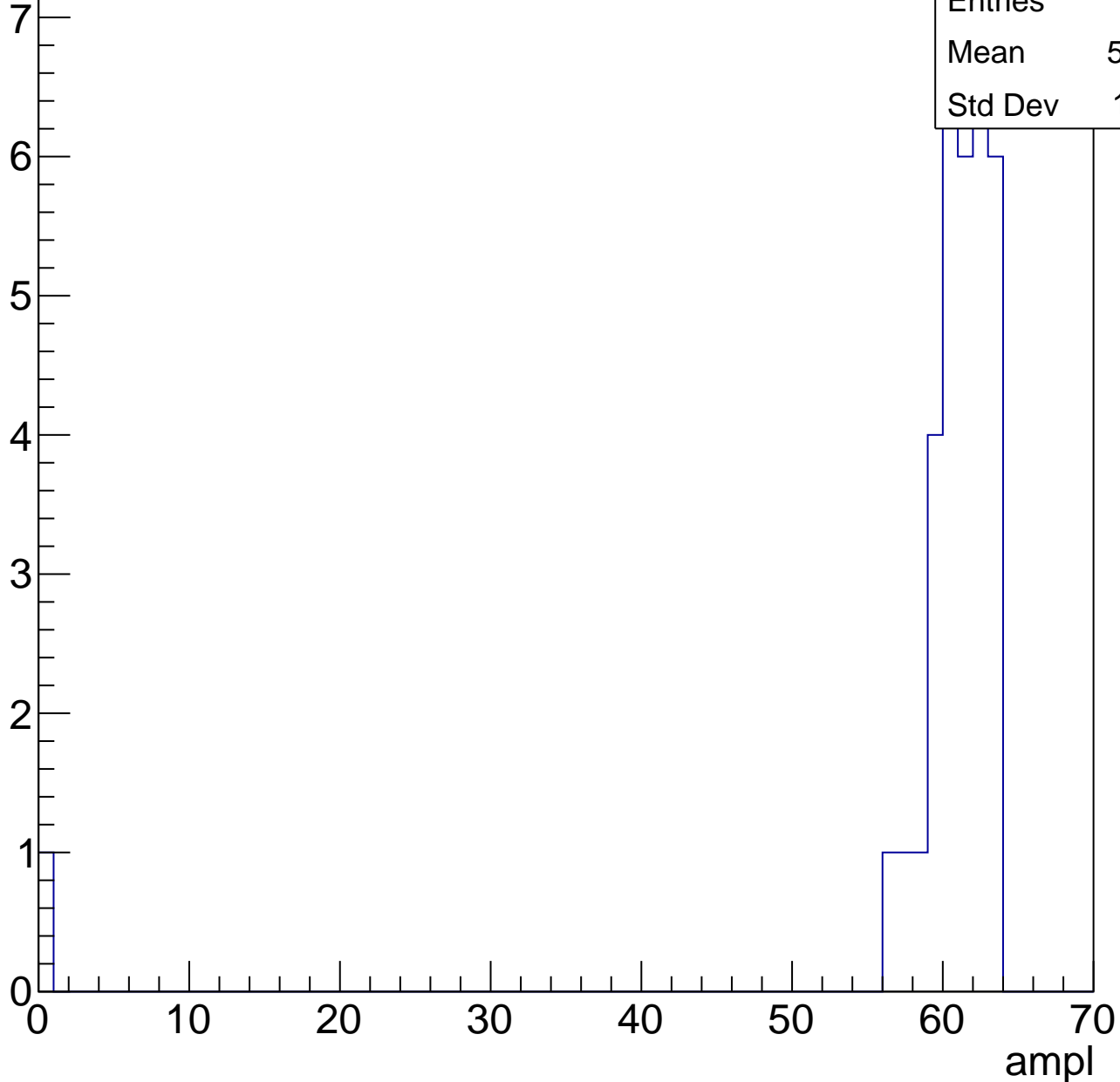
Entry



B1L100S, U13-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



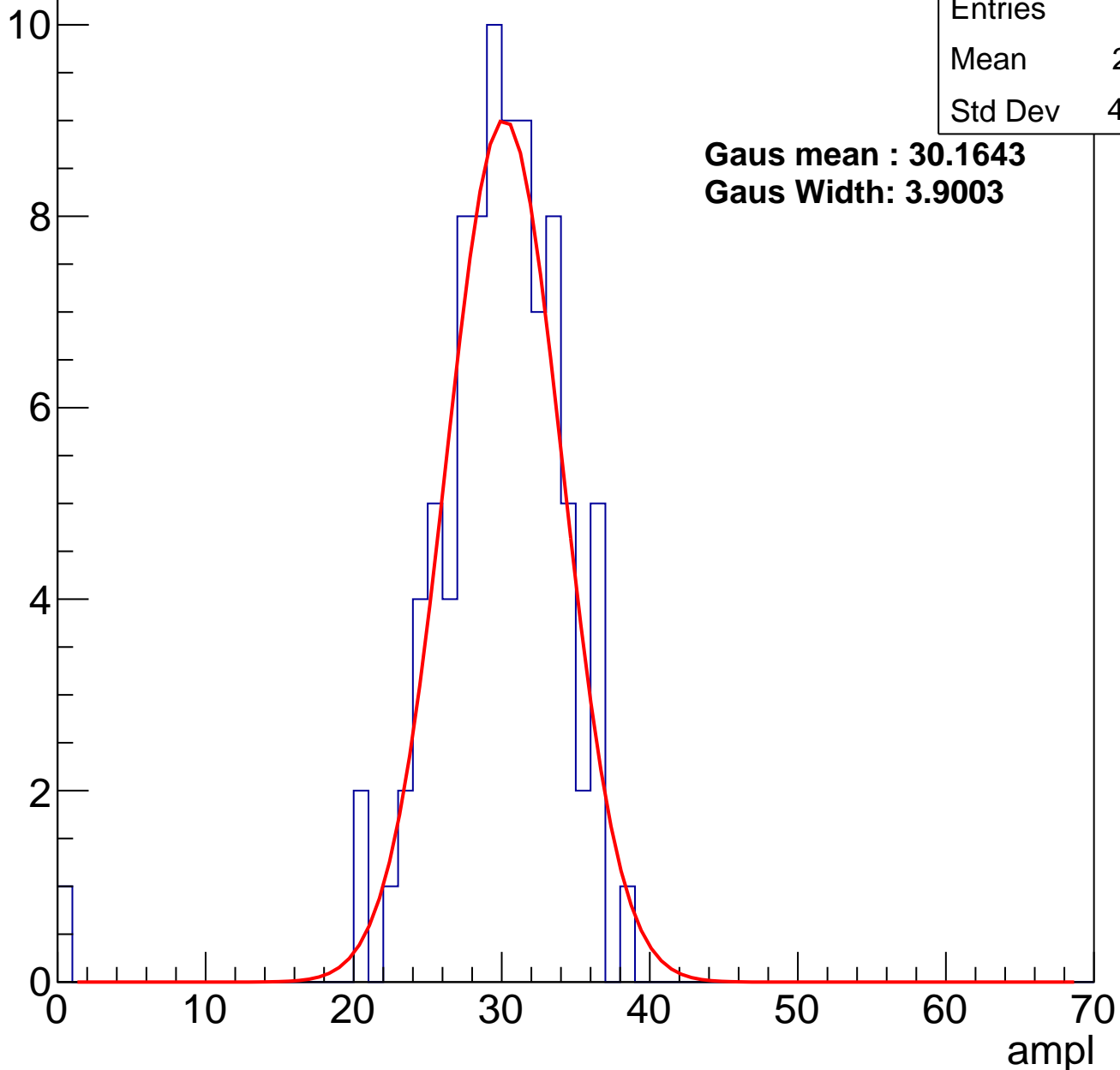
B1L100S, U13-ch99, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	29.21
Std Dev	4.844

Gaus mean : 30.1643
Gaus Width: 3.9003

Entry



B1L100S, U13-ch99, adc1

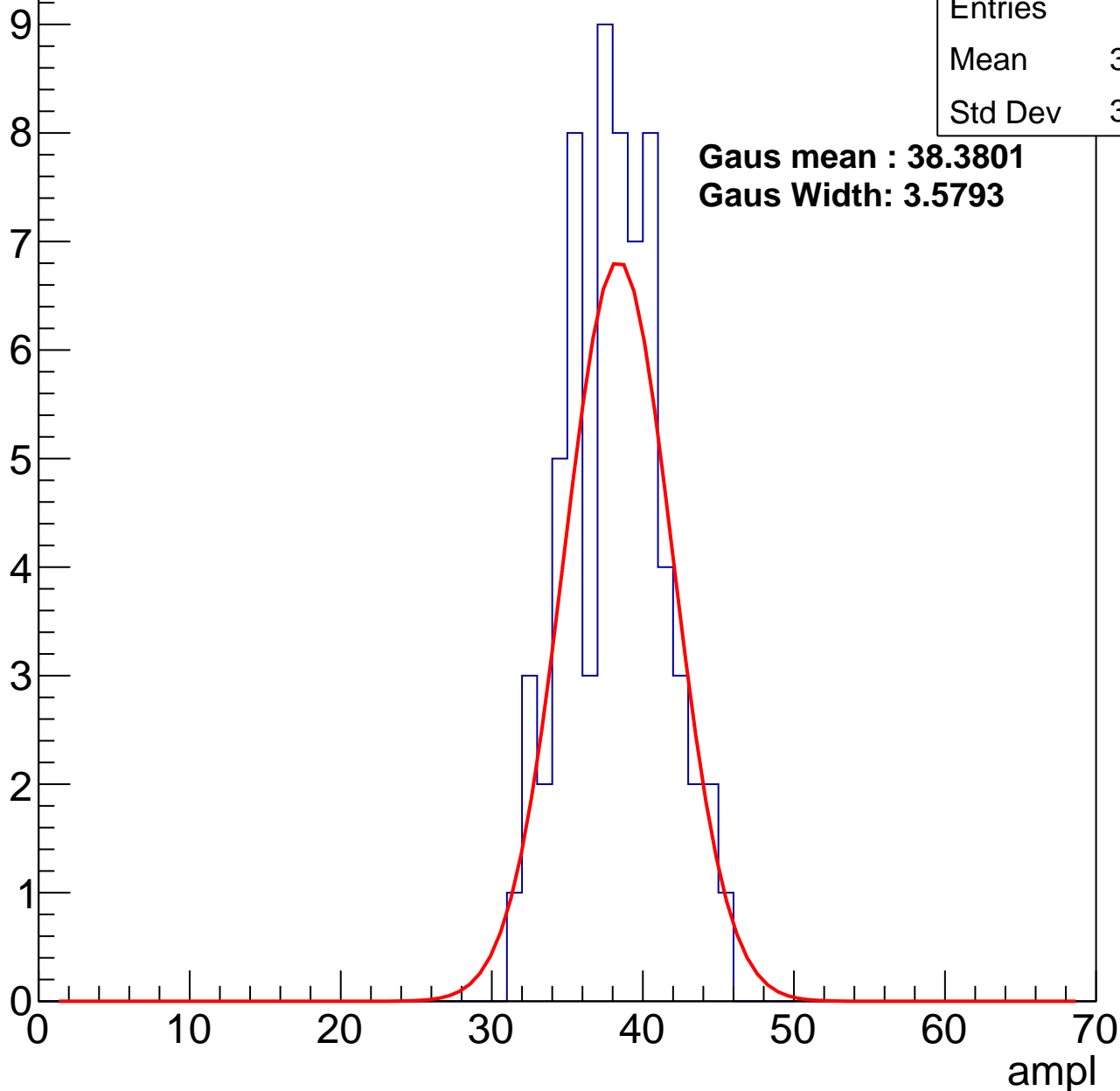
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.73
Std Dev	3.184

Gaus mean : 38.3801

Gaus Width: 3.5793



B1L100S, U13-ch99, adc2

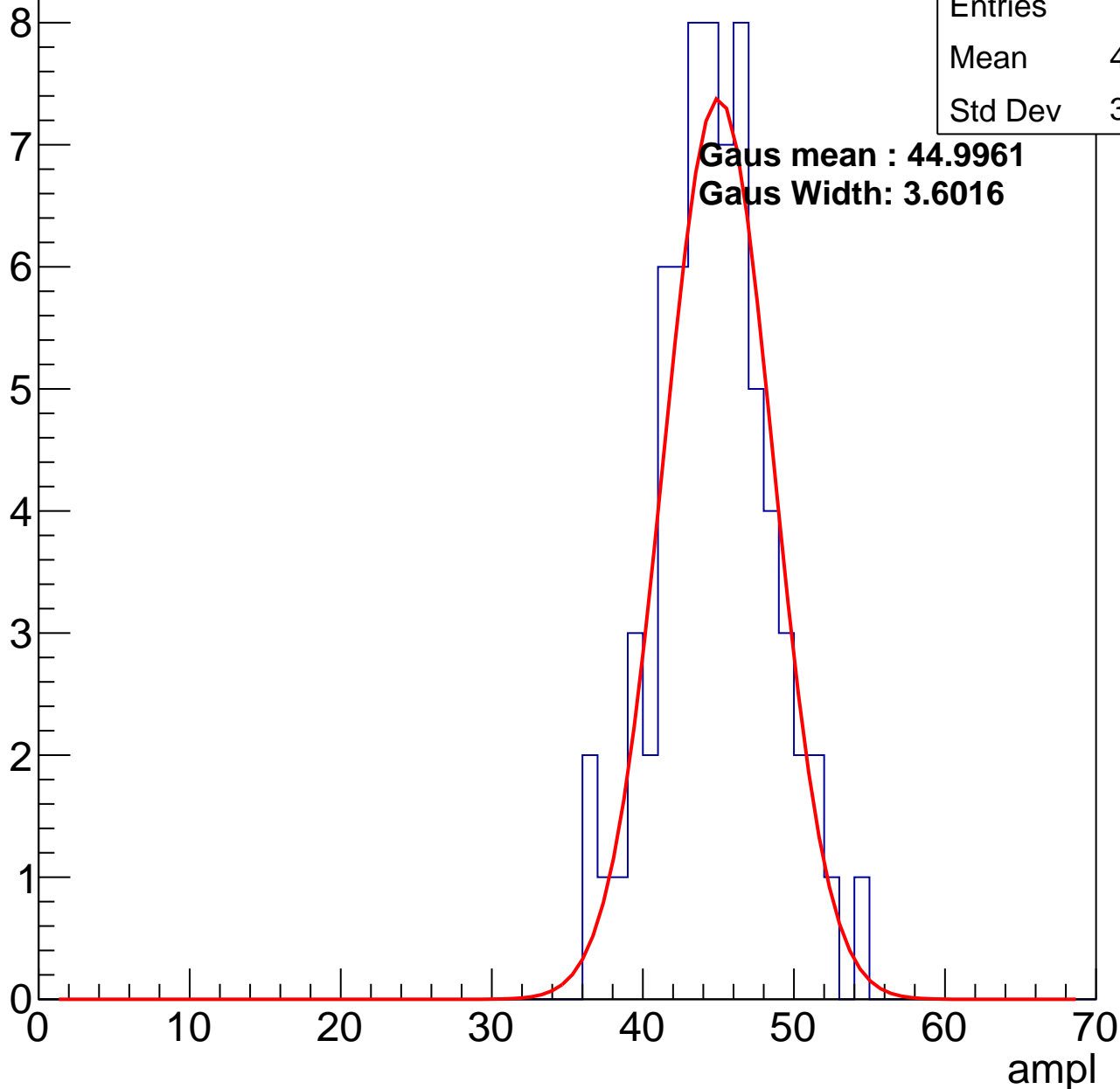
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.33
Std Dev	3.694

Gaus mean : 44.9961

Gaus Width: 3.6016

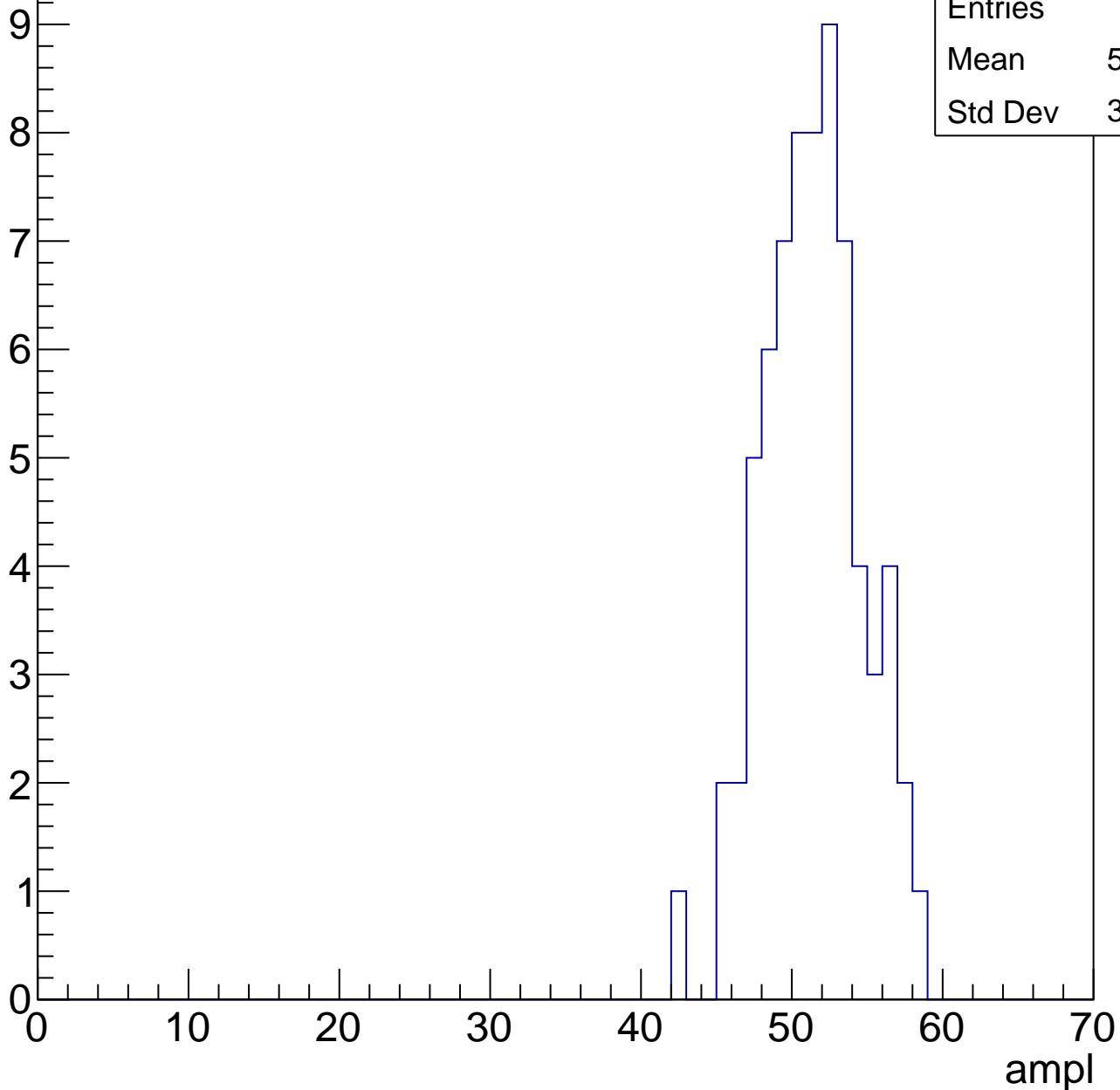


B1L100S, U13-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

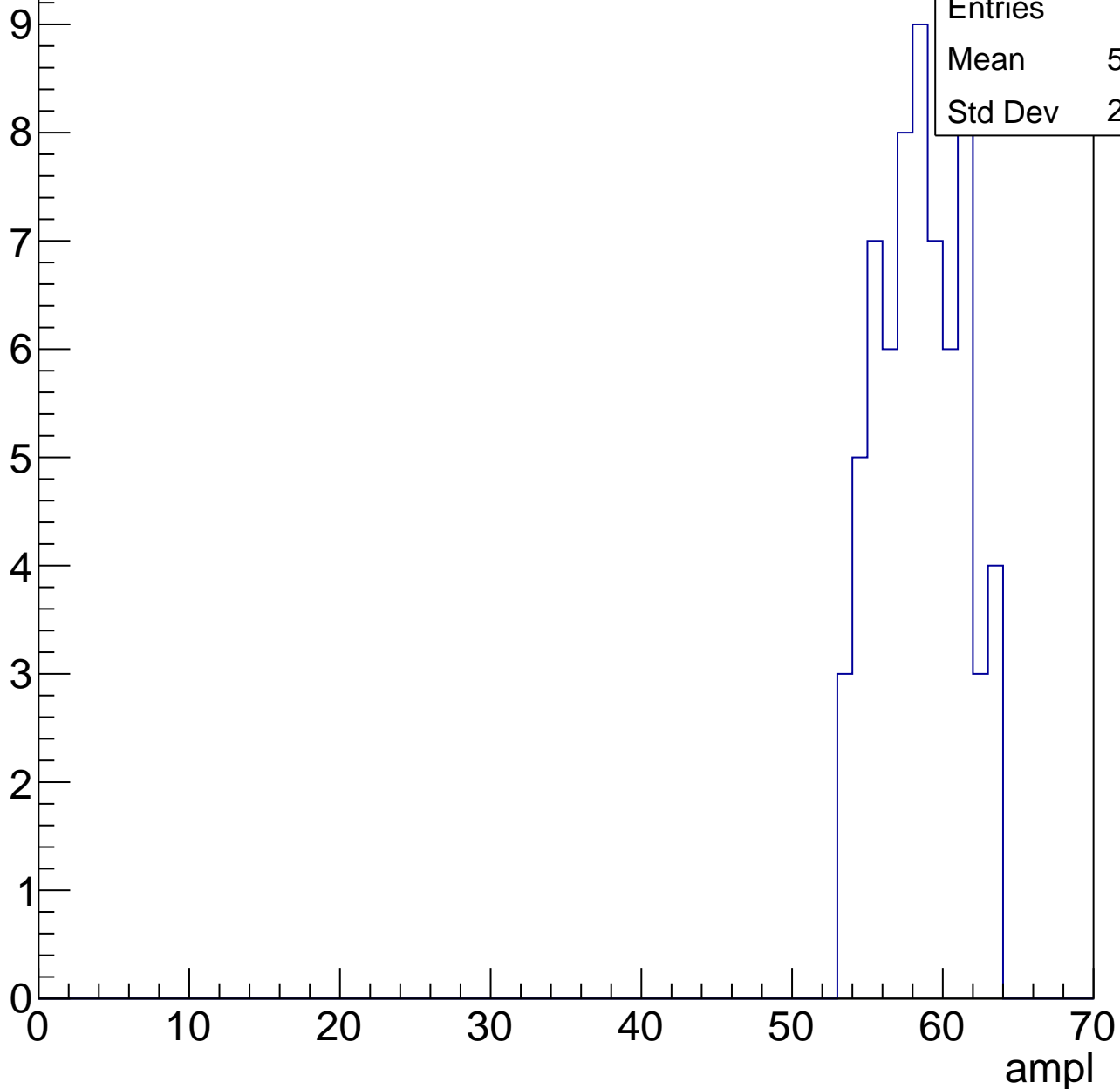
Entries	69
Mean	50.93
Std Dev	3.218



B1L100S, U13-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

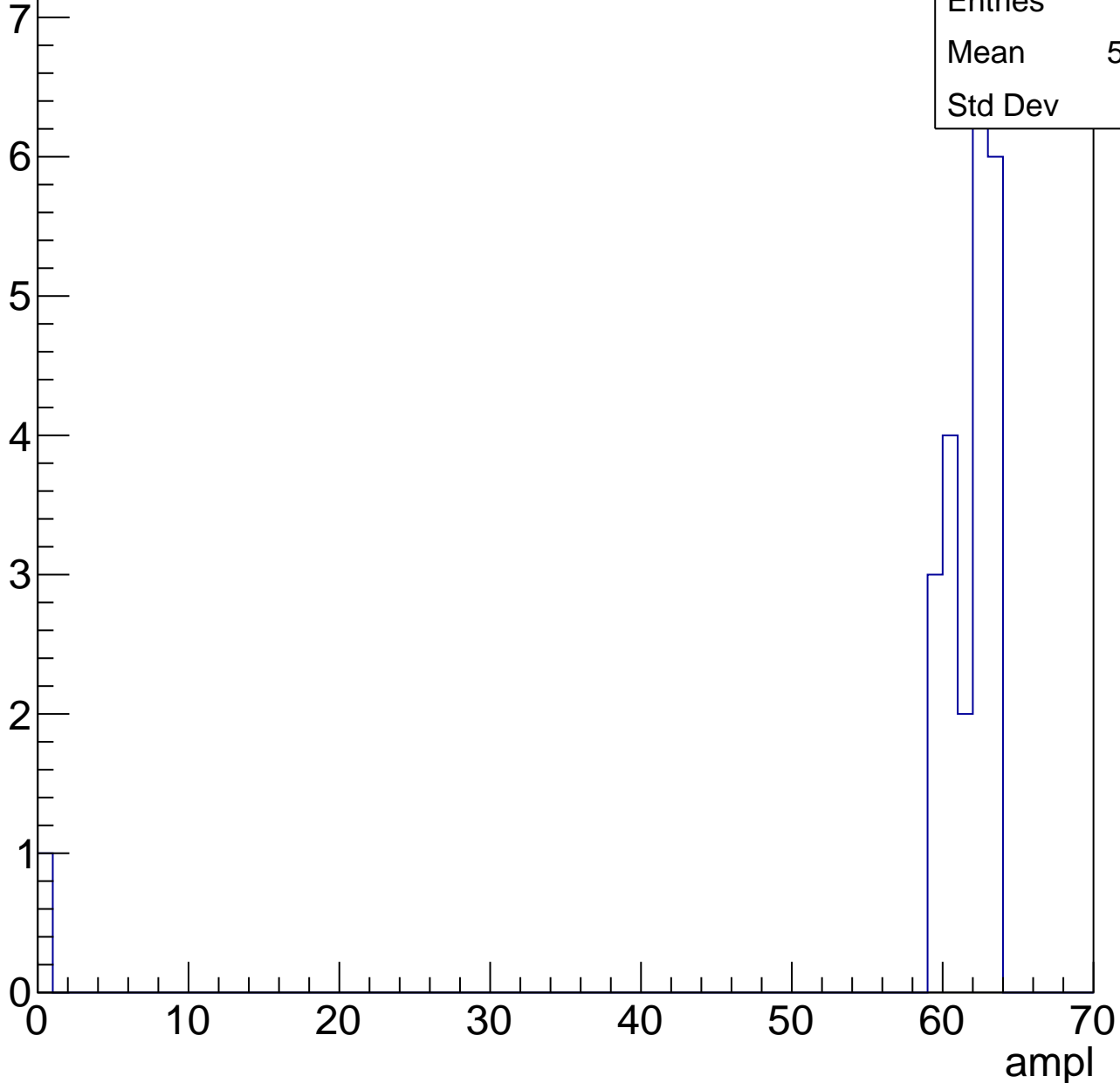


B1L100S, U13-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	58.74
Std Dev	12.6



B1L100S, U13-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch100, adc0

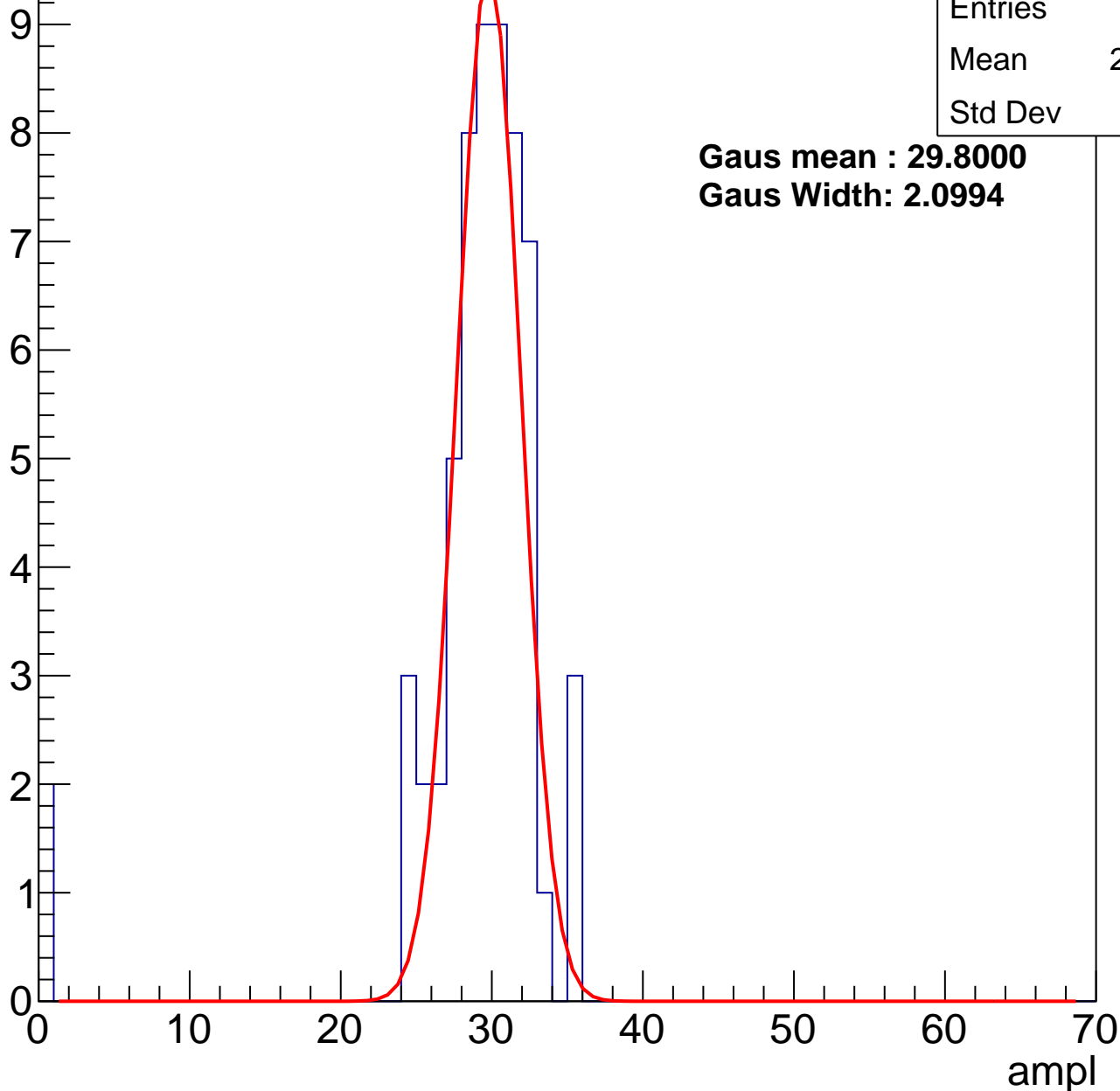
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.37
Std Dev	5.88

Gaus mean : 29.8000

Gaus Width: 2.0994



B1L100S, U13-ch100, adc1

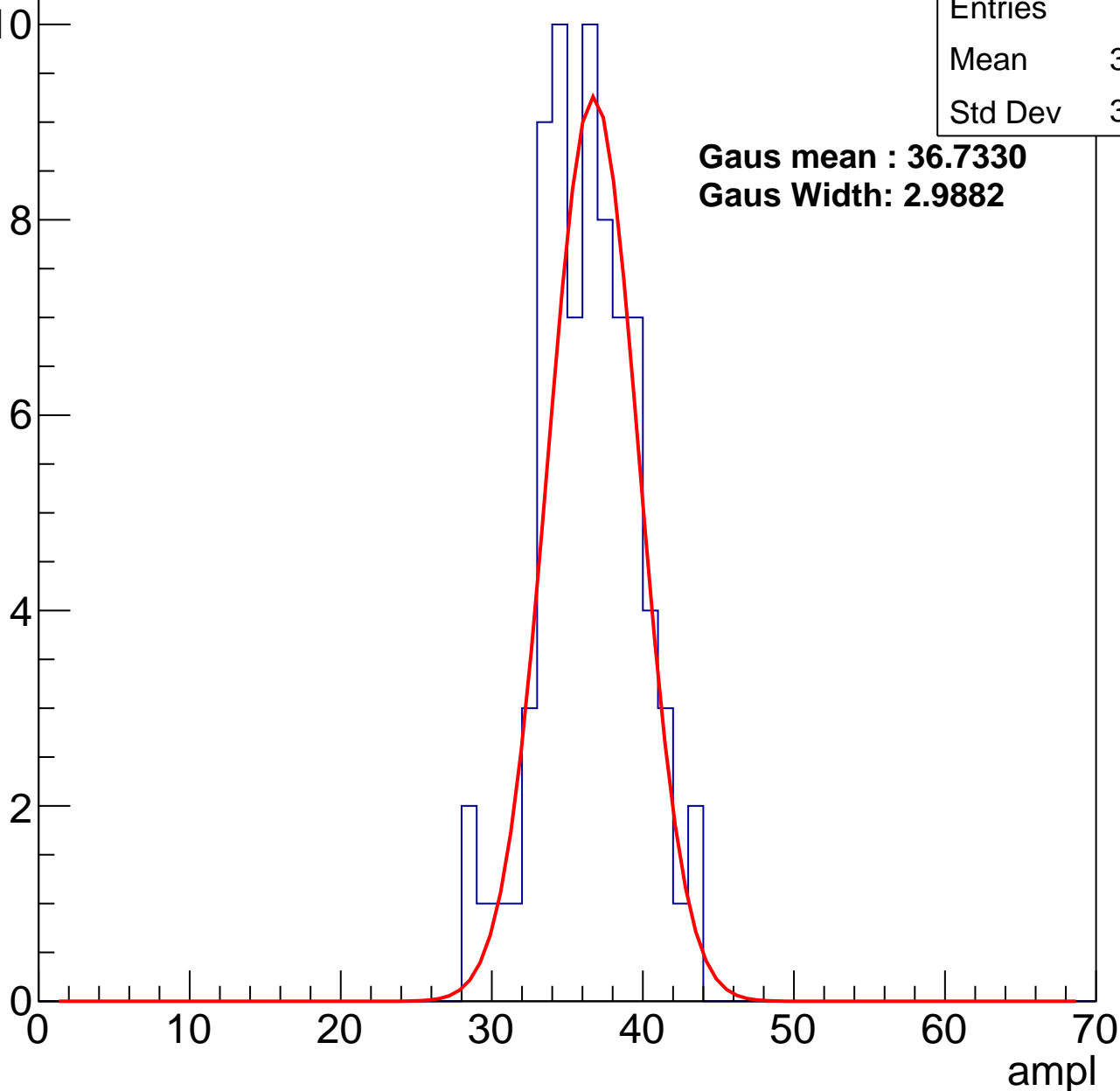
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	35.92
Std Dev	3.215

Gaus mean : 36.7330

Gaus Width: 2.9882



B1L100S, U13-ch100, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	42.98
Std Dev	3.272

Gaus mean : 42.9284

Gaus Width: 3.3240

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

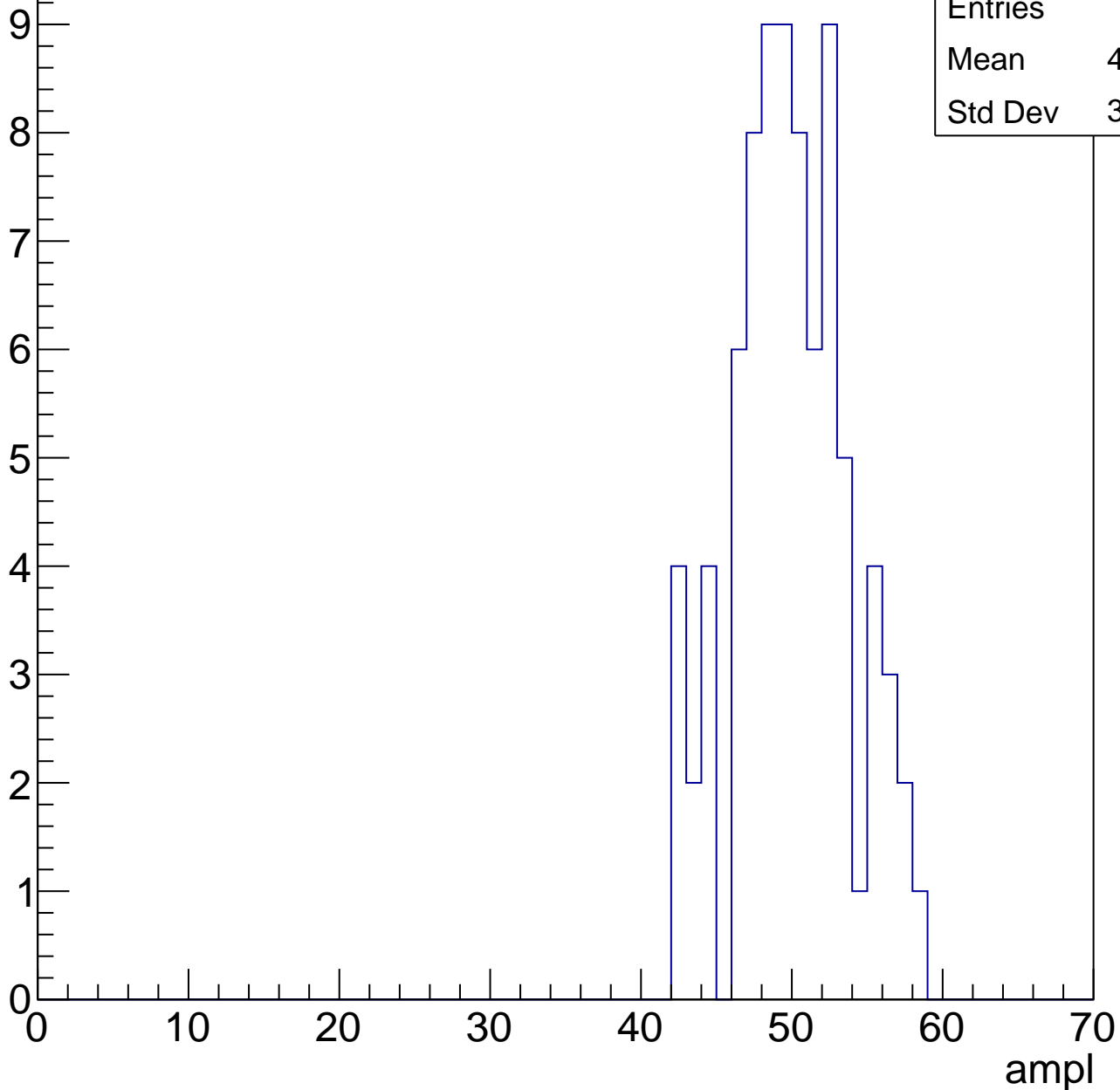
70

B1L100S, U13-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	49.48
Std Dev	3.788

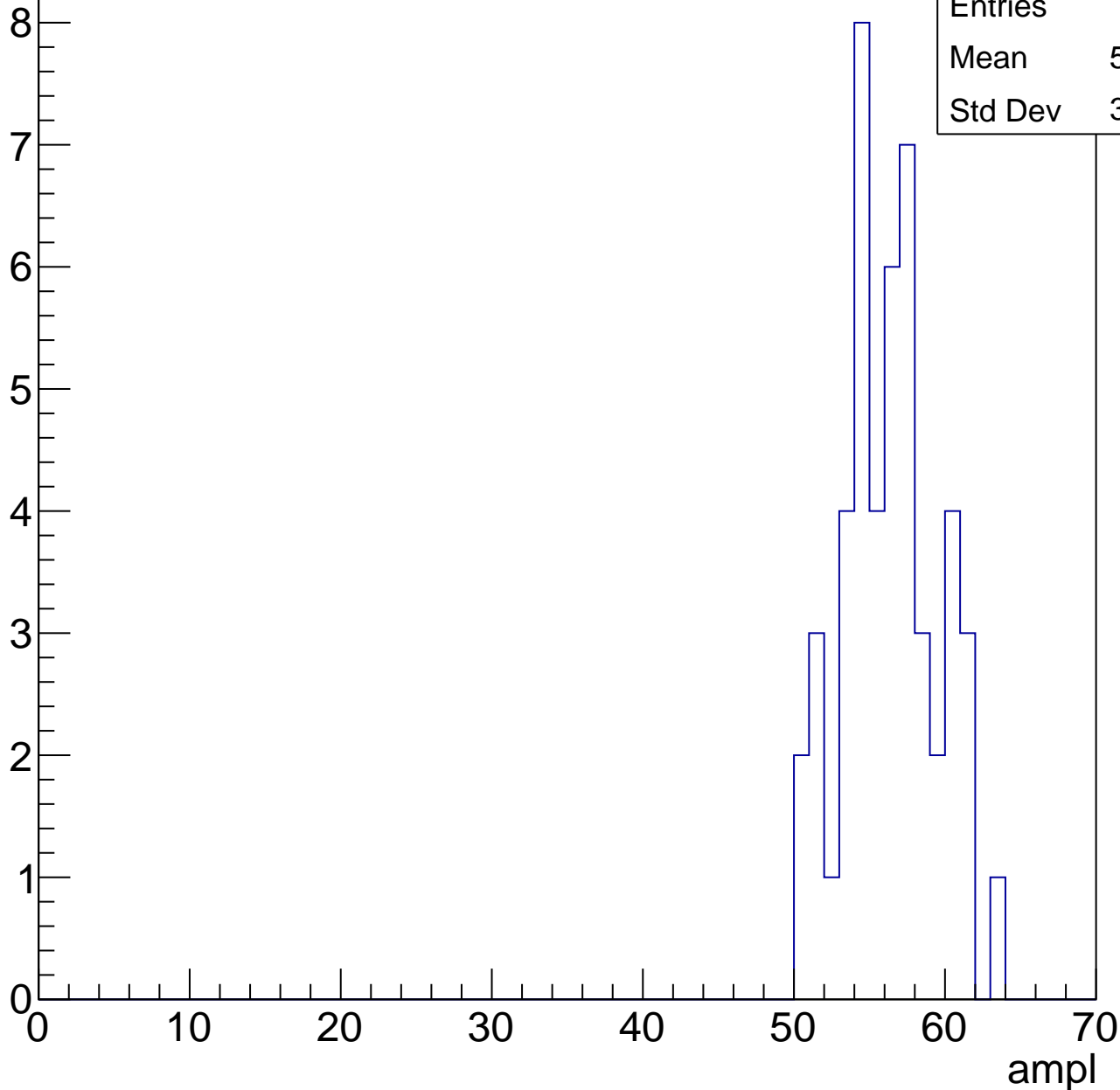


B1L100S, U13-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

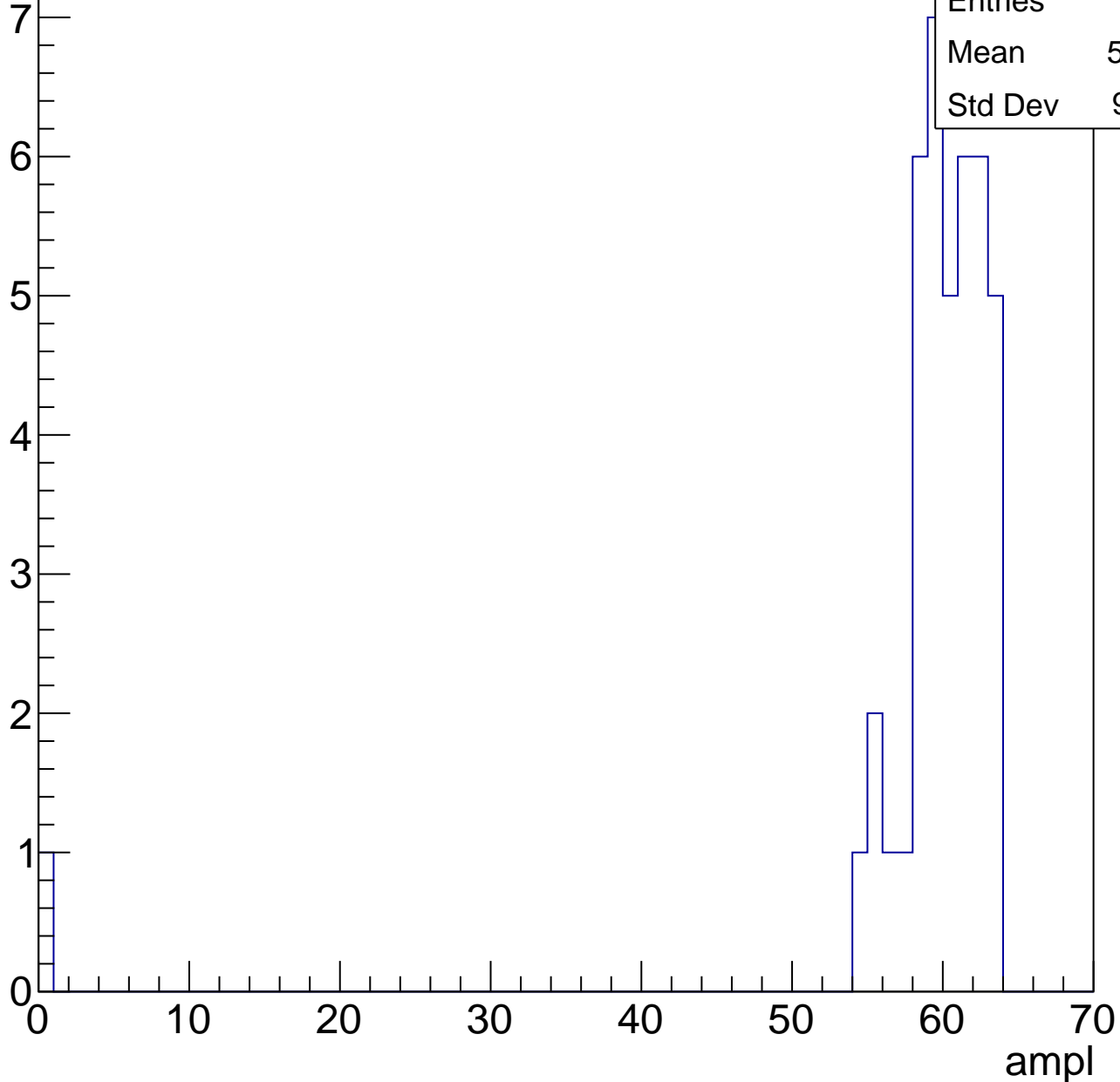
Entries	48
Mean	55.88
Std Dev	3.093



B1L100S, U13-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

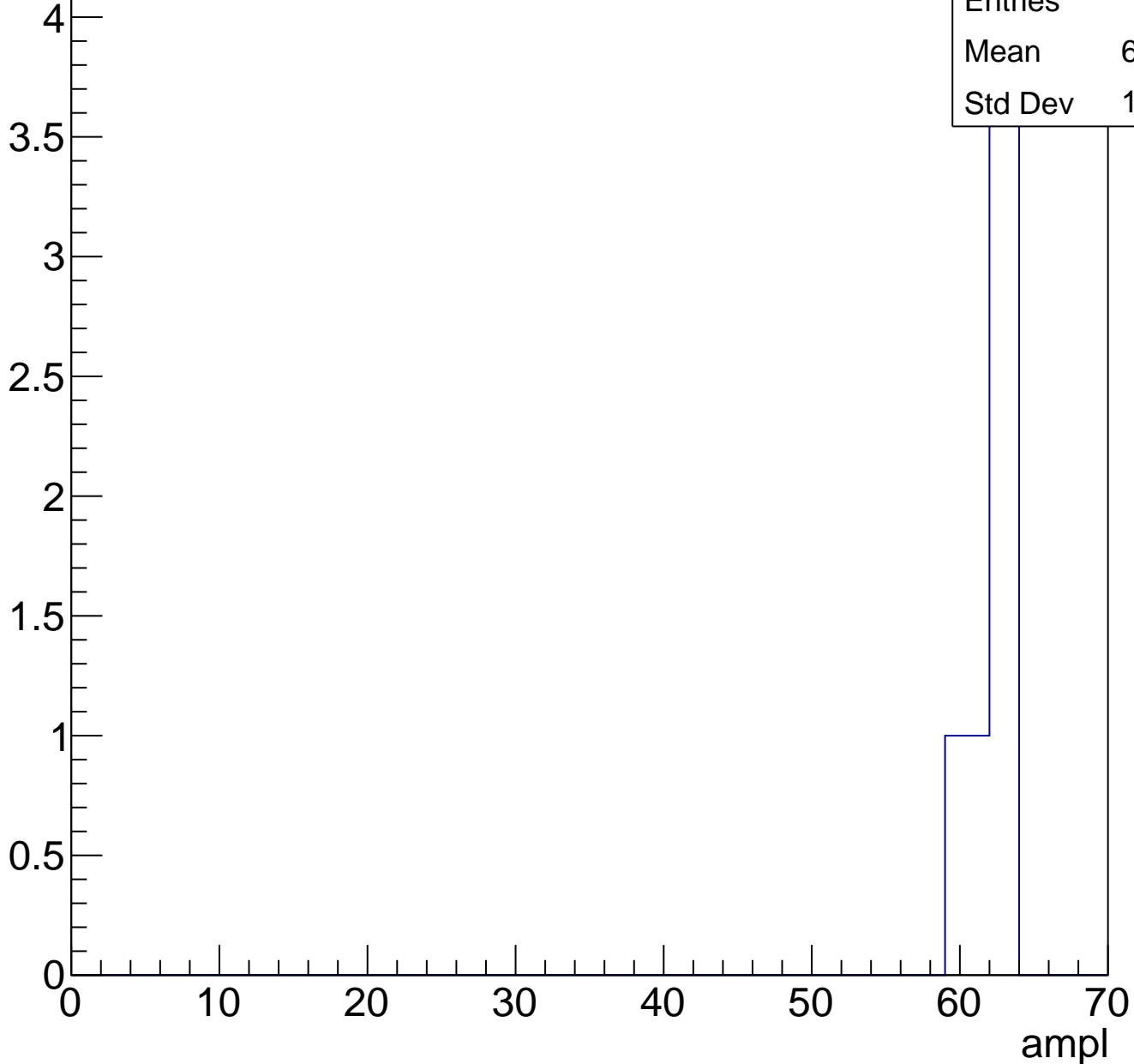
Entry



B1L100S, U13-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch101, adc0

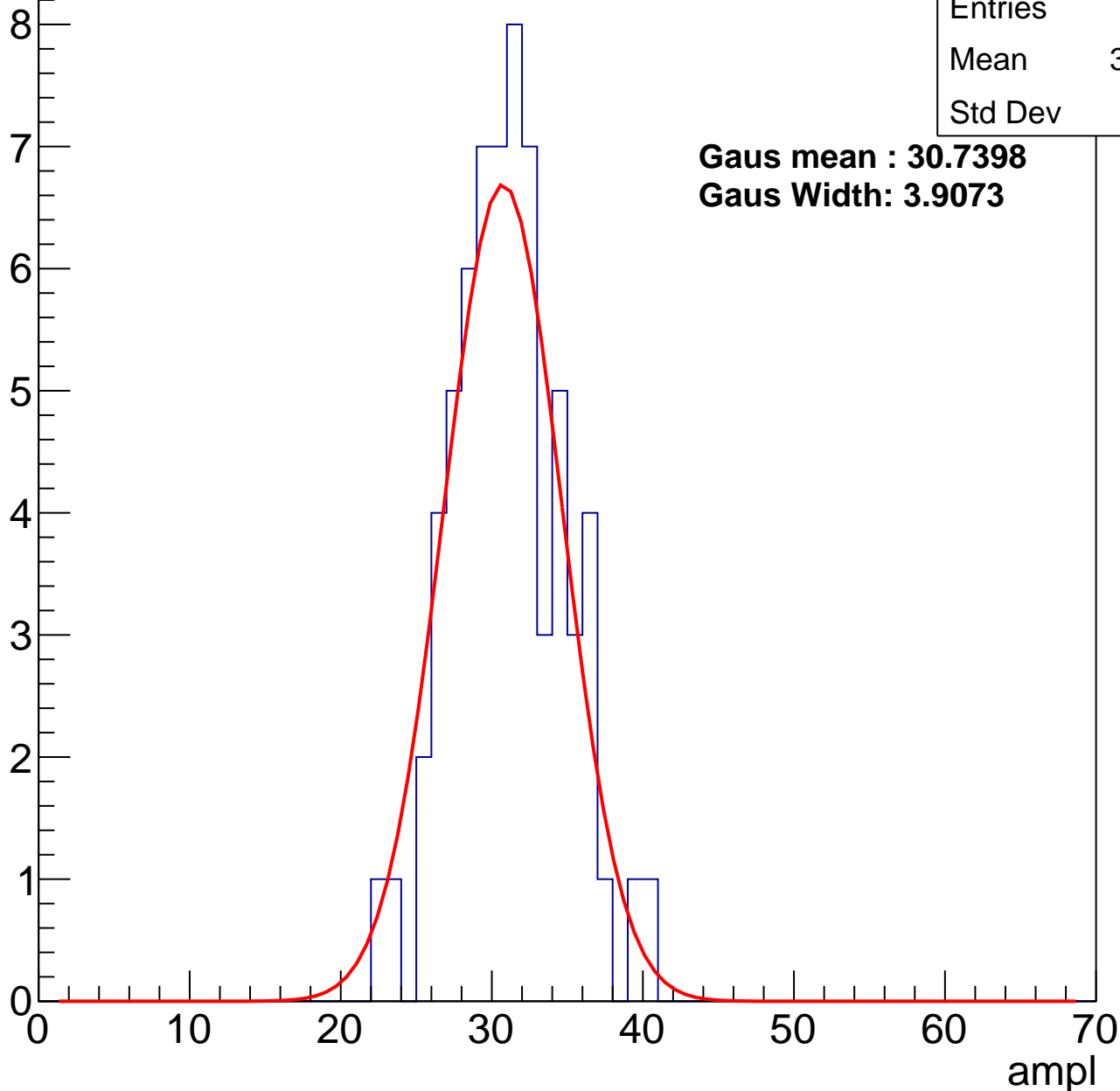
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.62
Std Dev	3.63

Gaus mean : 30.7398

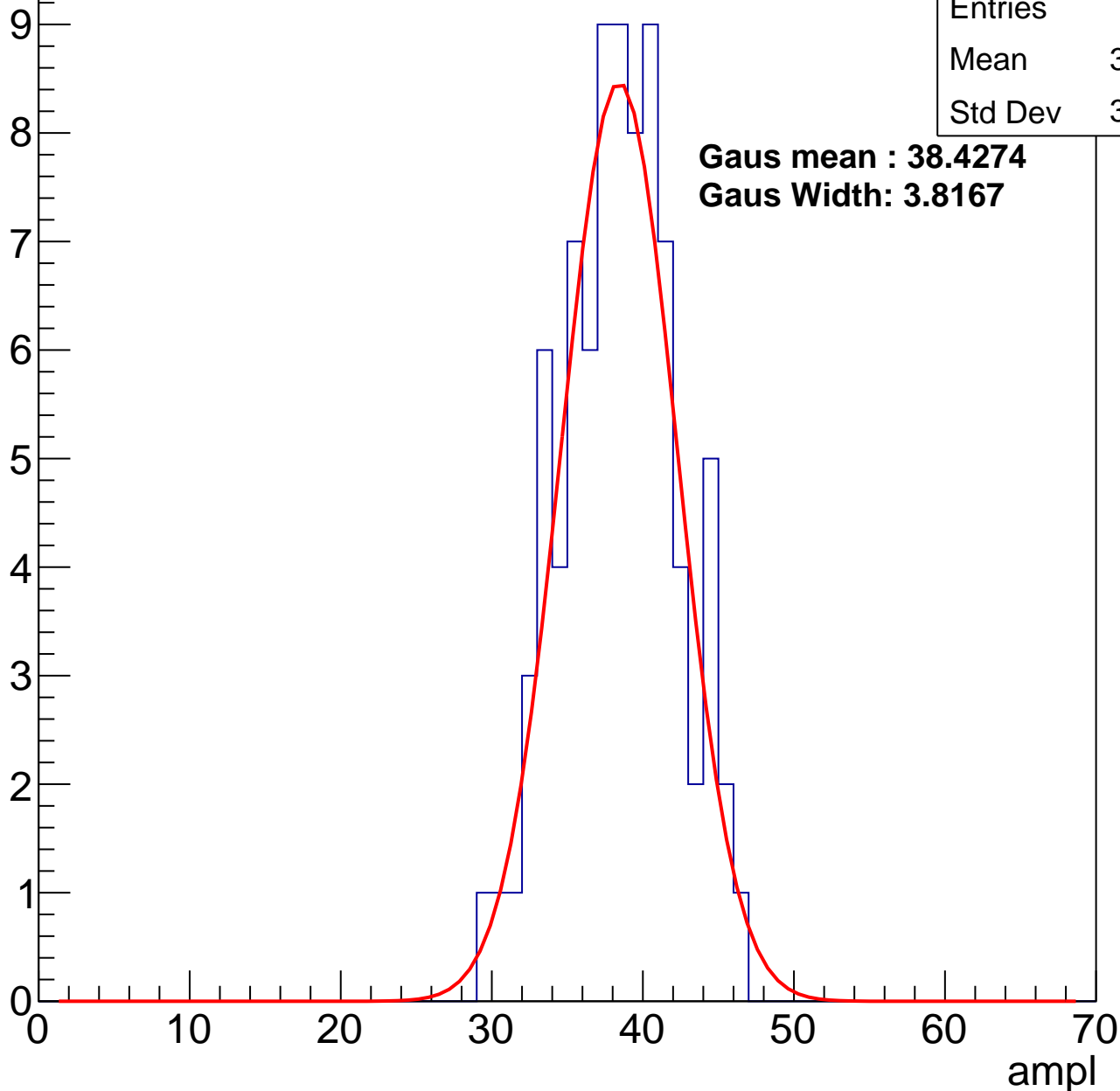
Gaus Width: 3.9073



B1L100S, U13-ch101, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch101, adc2

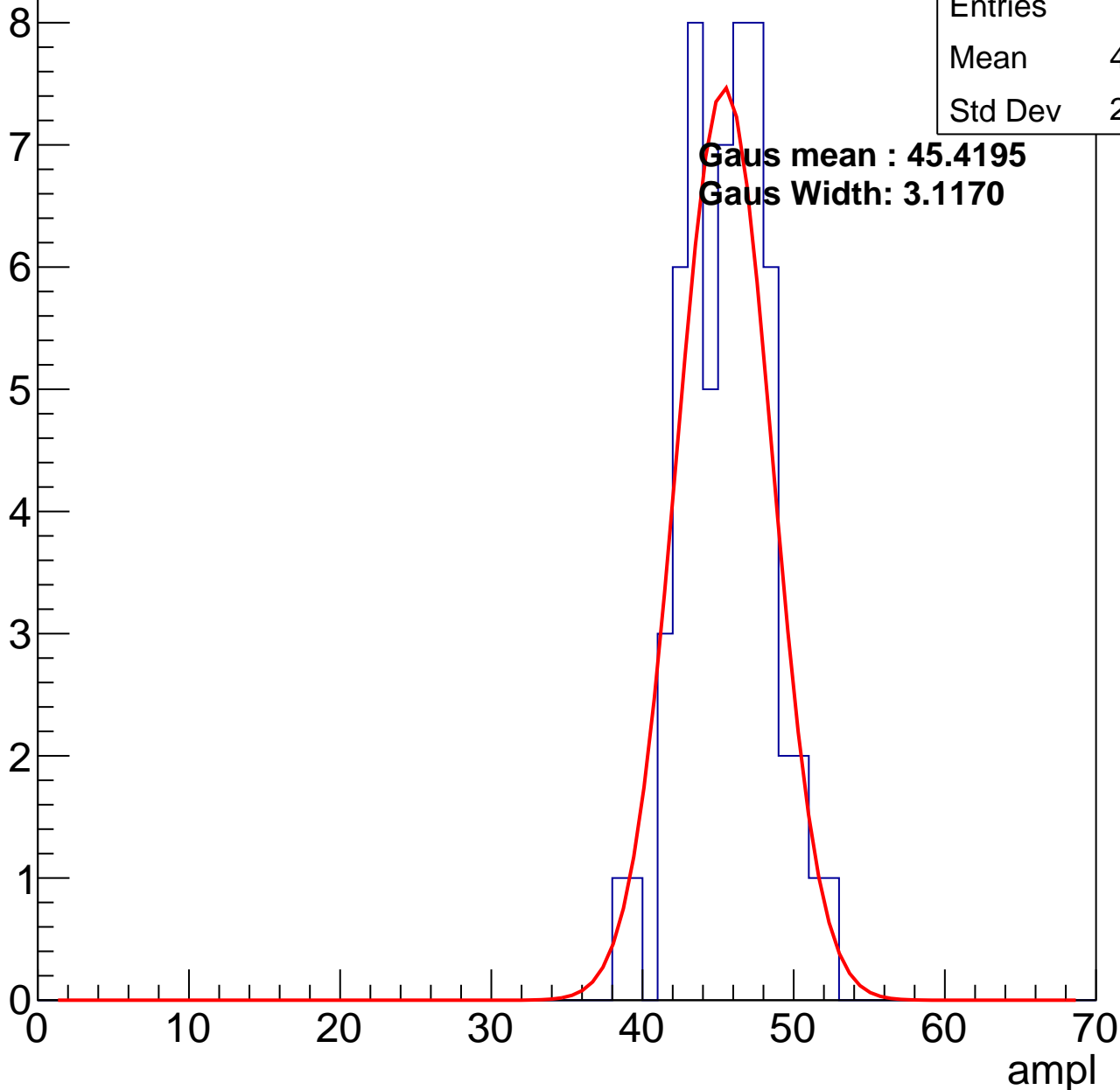
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	45.15
Std Dev	2.863

Gaus mean : 45.4195

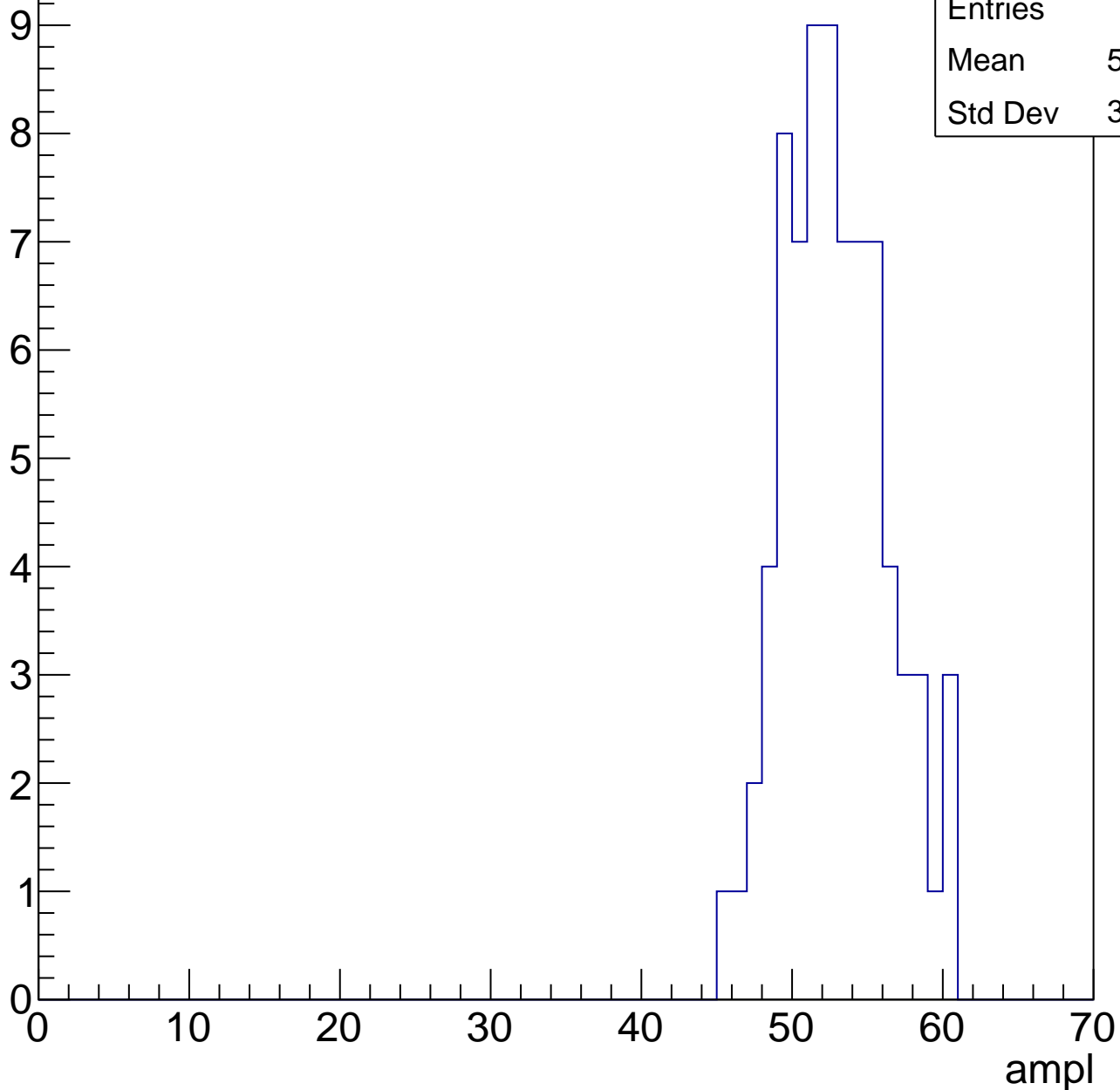
Gaus Width: 3.1170



B1L100S, U13-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



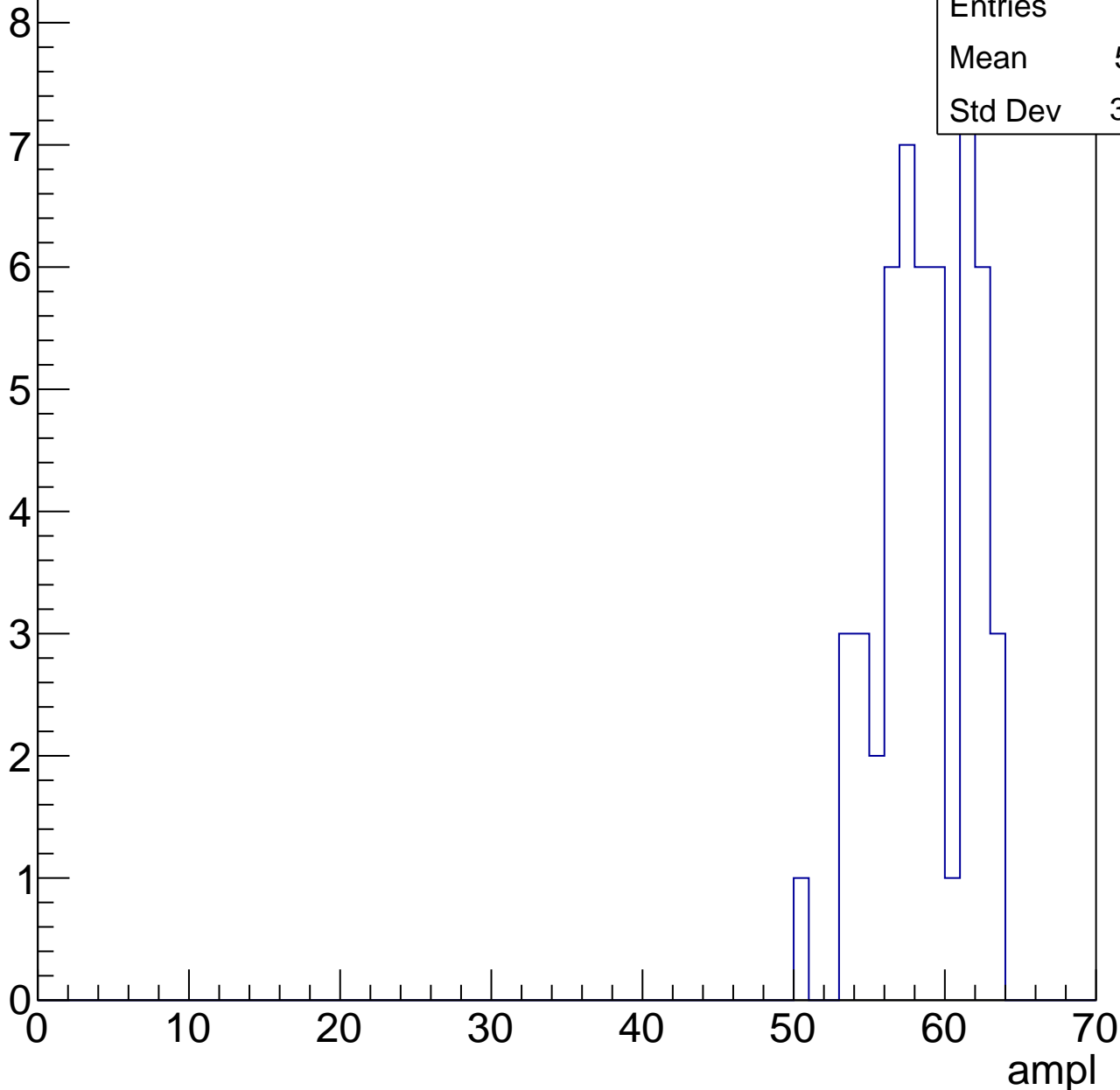
Entries	76
Mean	52.47
Std Dev	3.397

B1L100S, U13-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.21
Std Dev	3.059

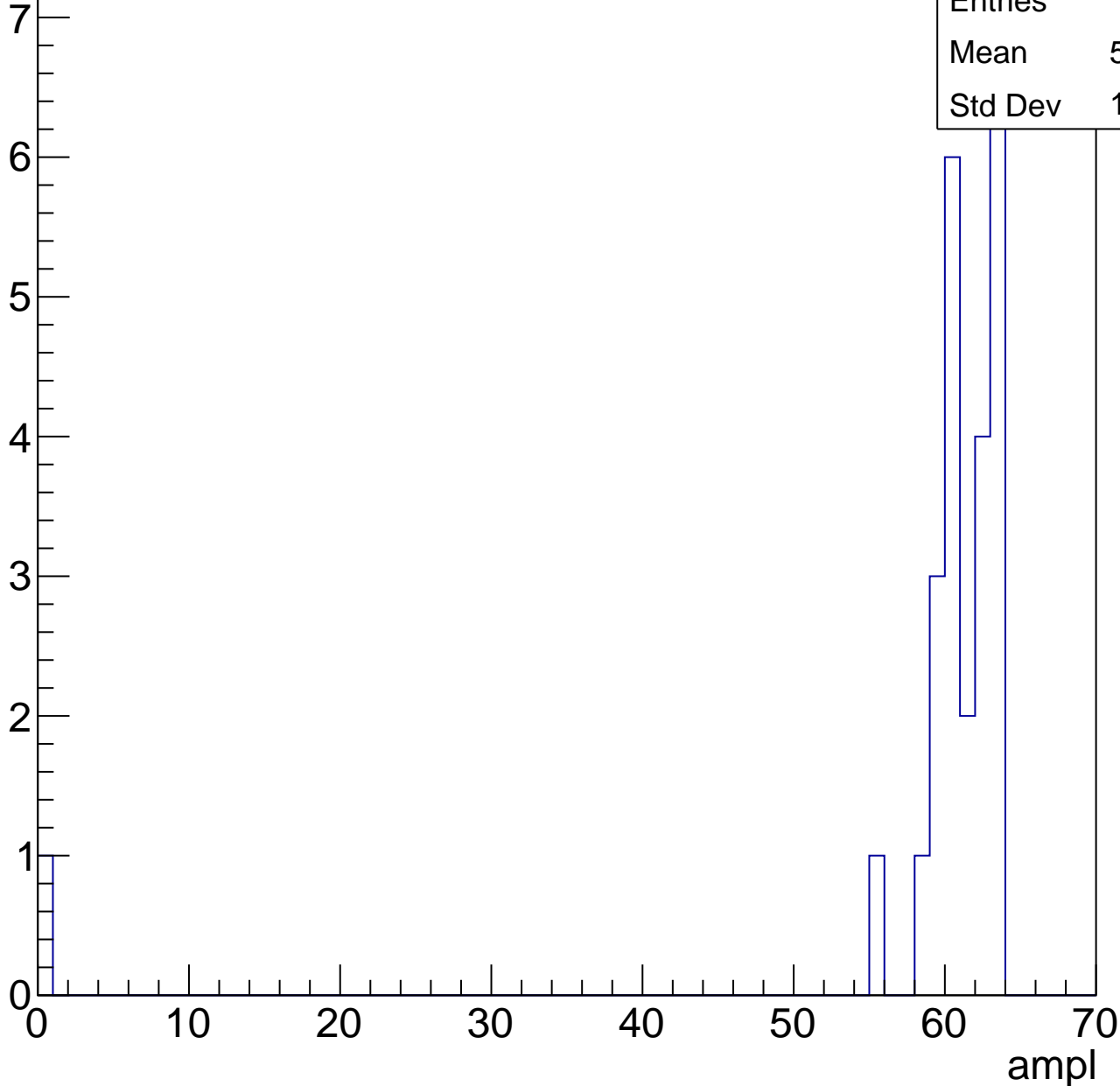


B1L100S, U13-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.44
Std Dev	12.09



B1L100S, U13-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L100S, U13-ch102, adc0

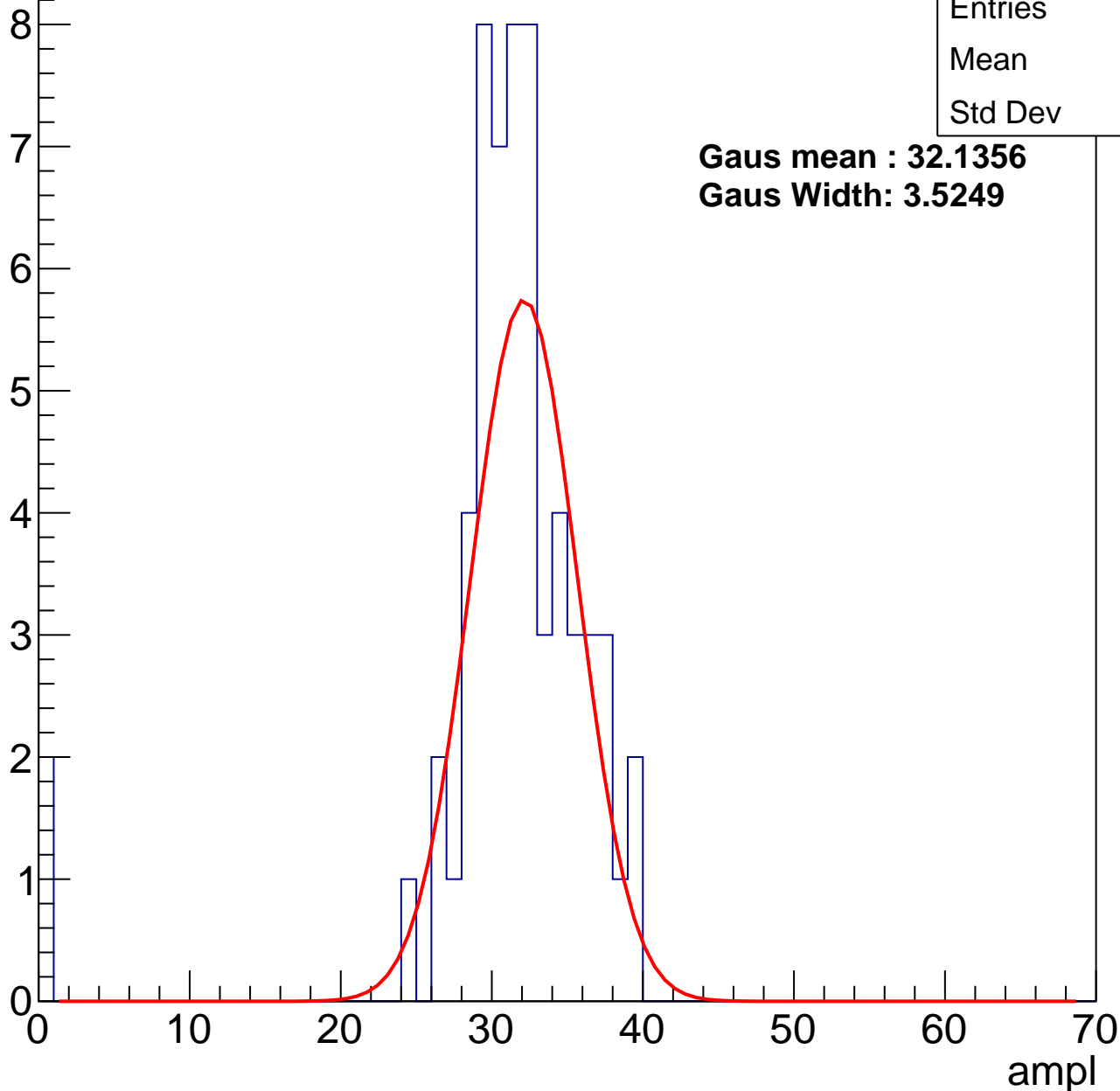
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	30.6
Std Dev	6.54

Gaus mean : 32.1356

Gaus Width: 3.5249



B1L100S, U13-ch102, adc1

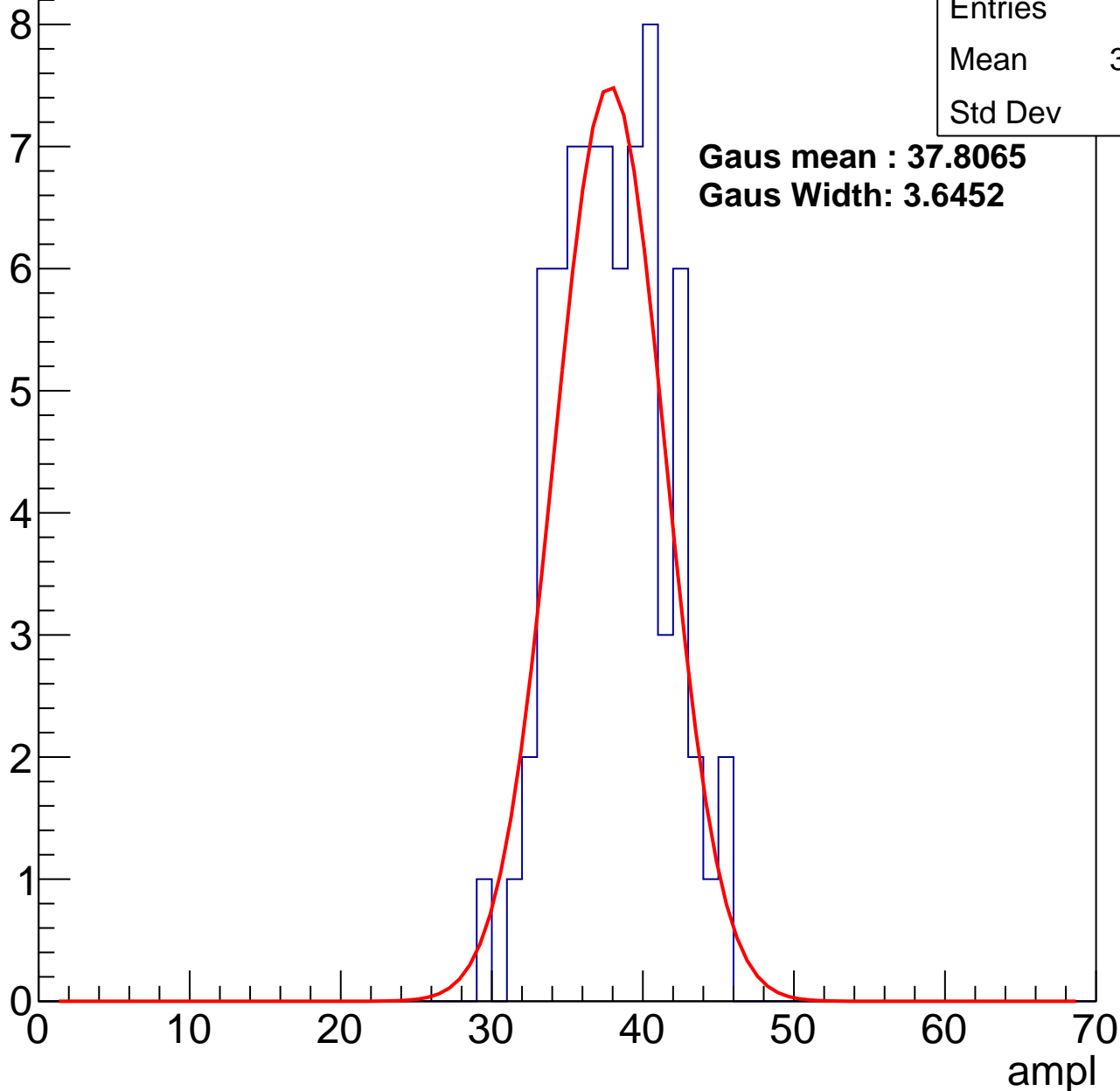
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.47
Std Dev	3.48

Gaus mean : 37.8065

Gaus Width: 3.6452



B1L100S, U13-ch102, adc2

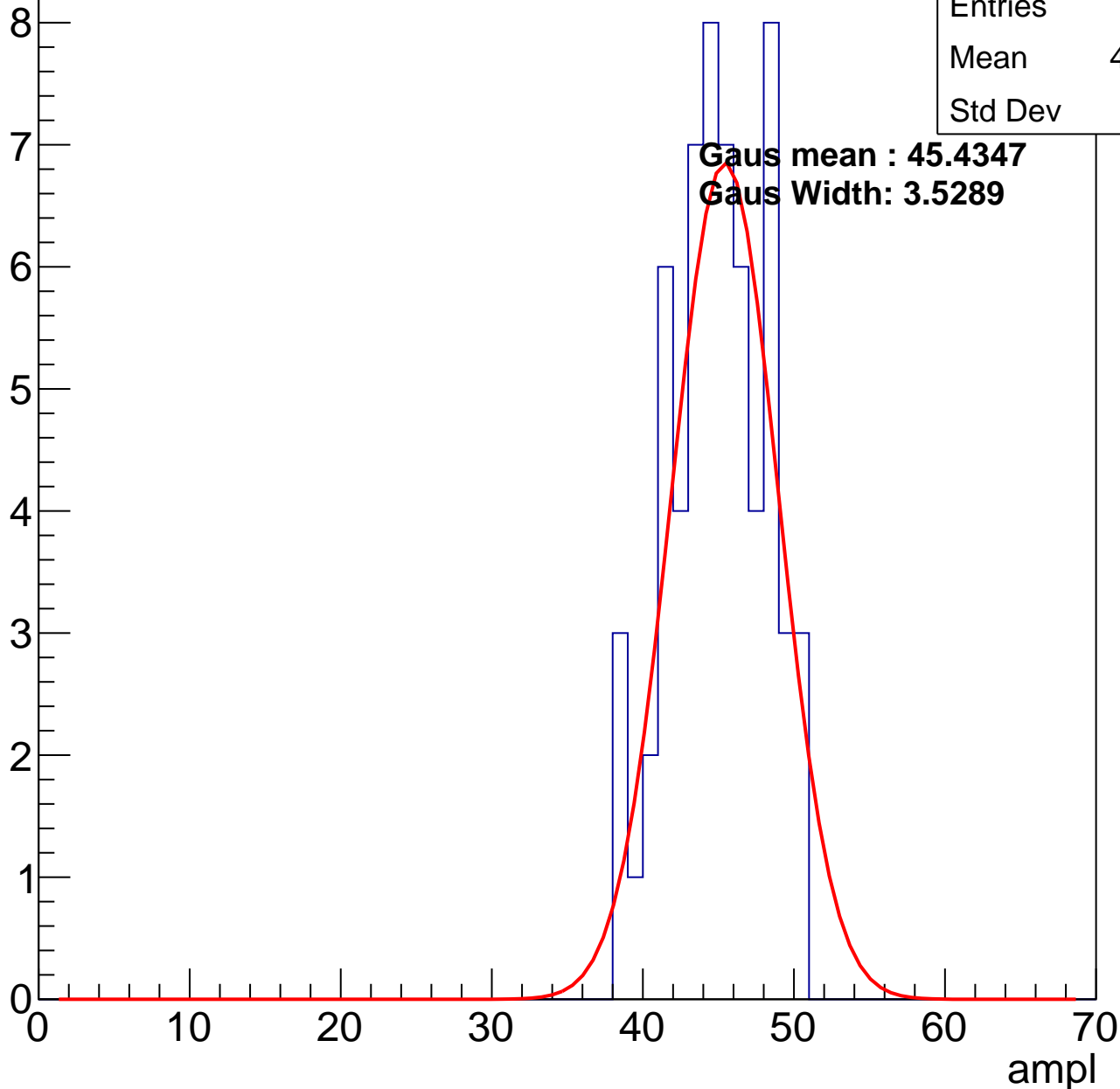
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.52
Std Dev	3.12

Gaus mean : 45.4347

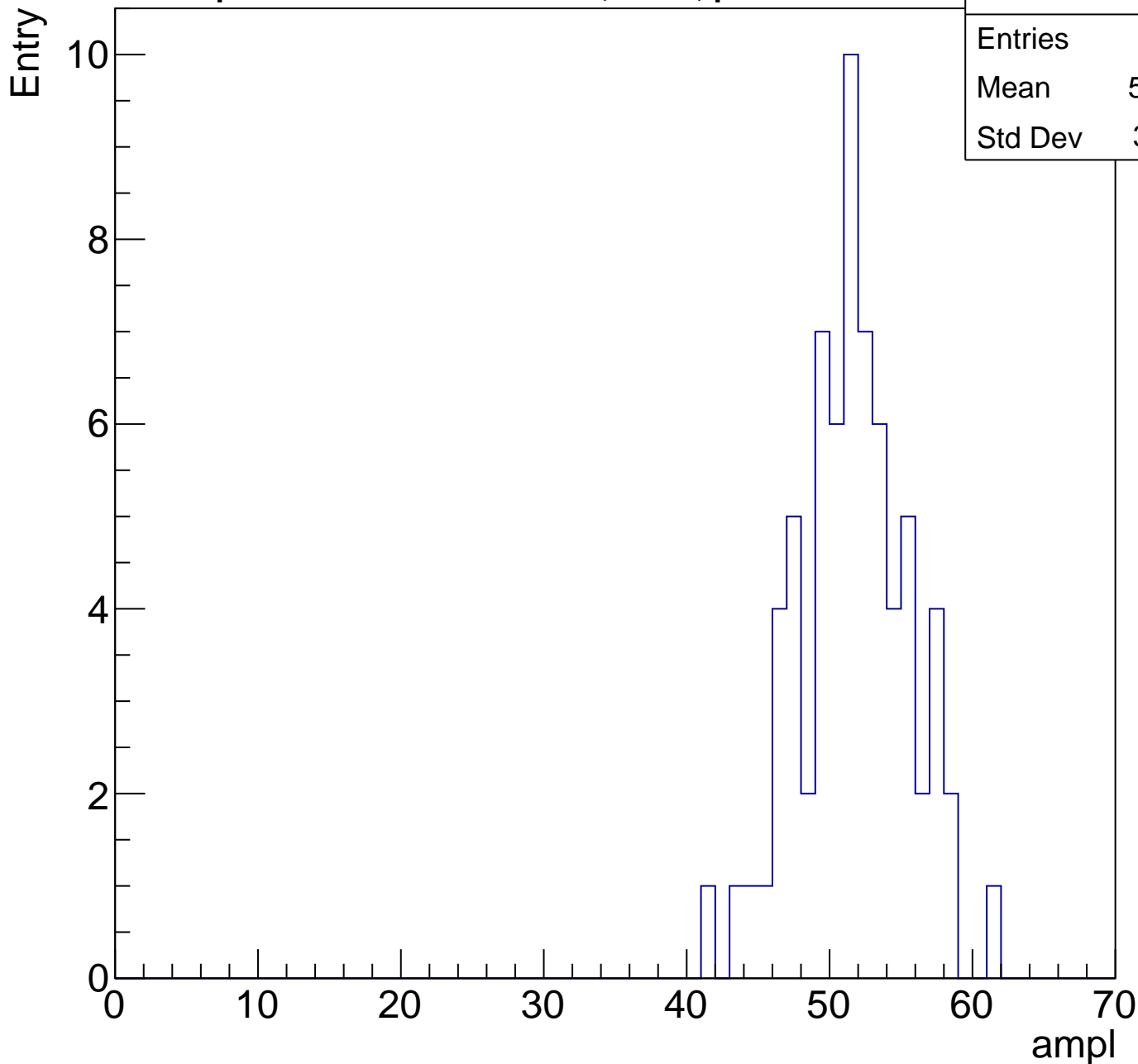
Gaus Width: 3.5289



B1L100S, U13-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	51.17
Std Dev	3.841

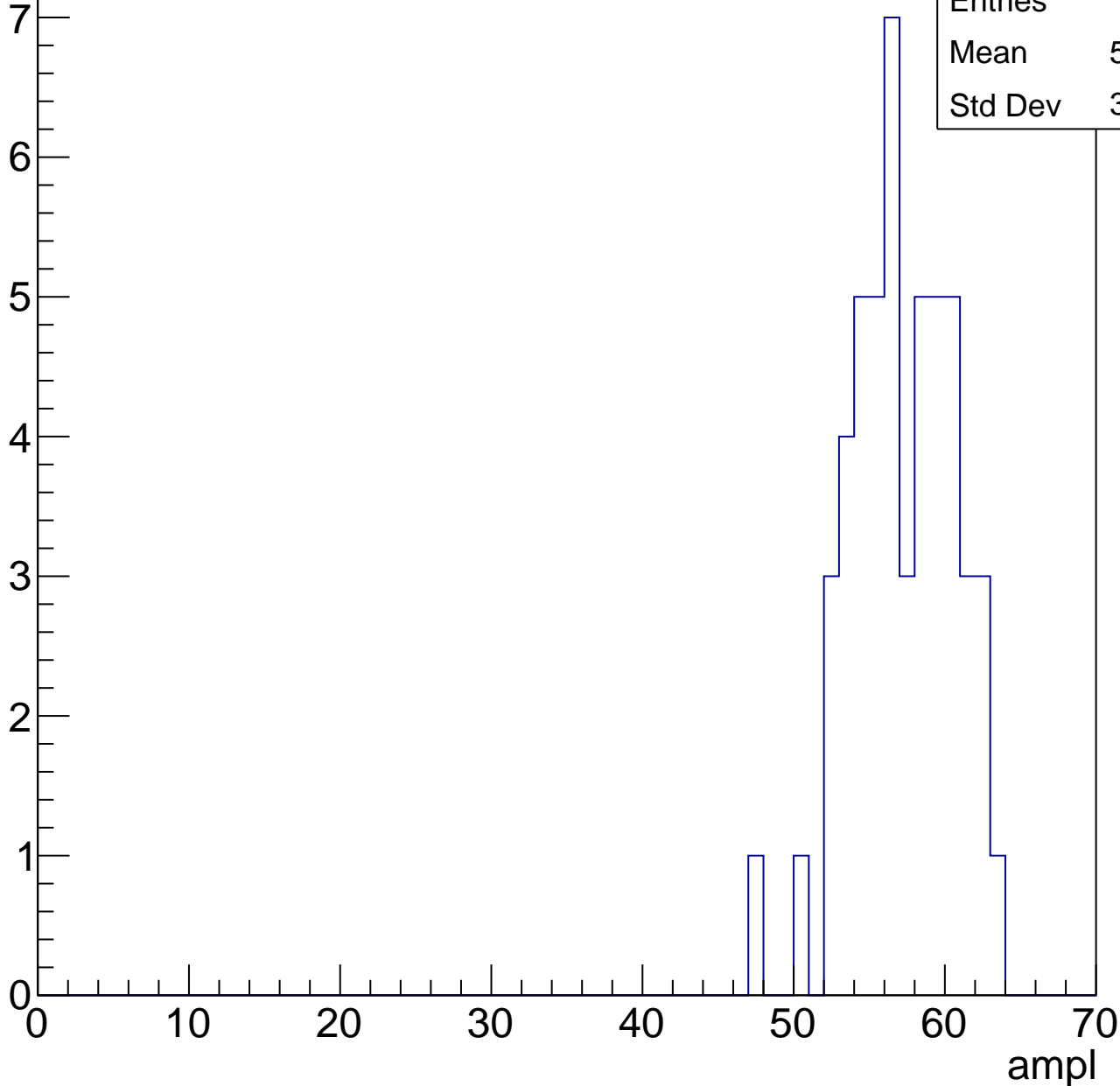


B1L100S, U13-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	56.67
Std Dev	3.382

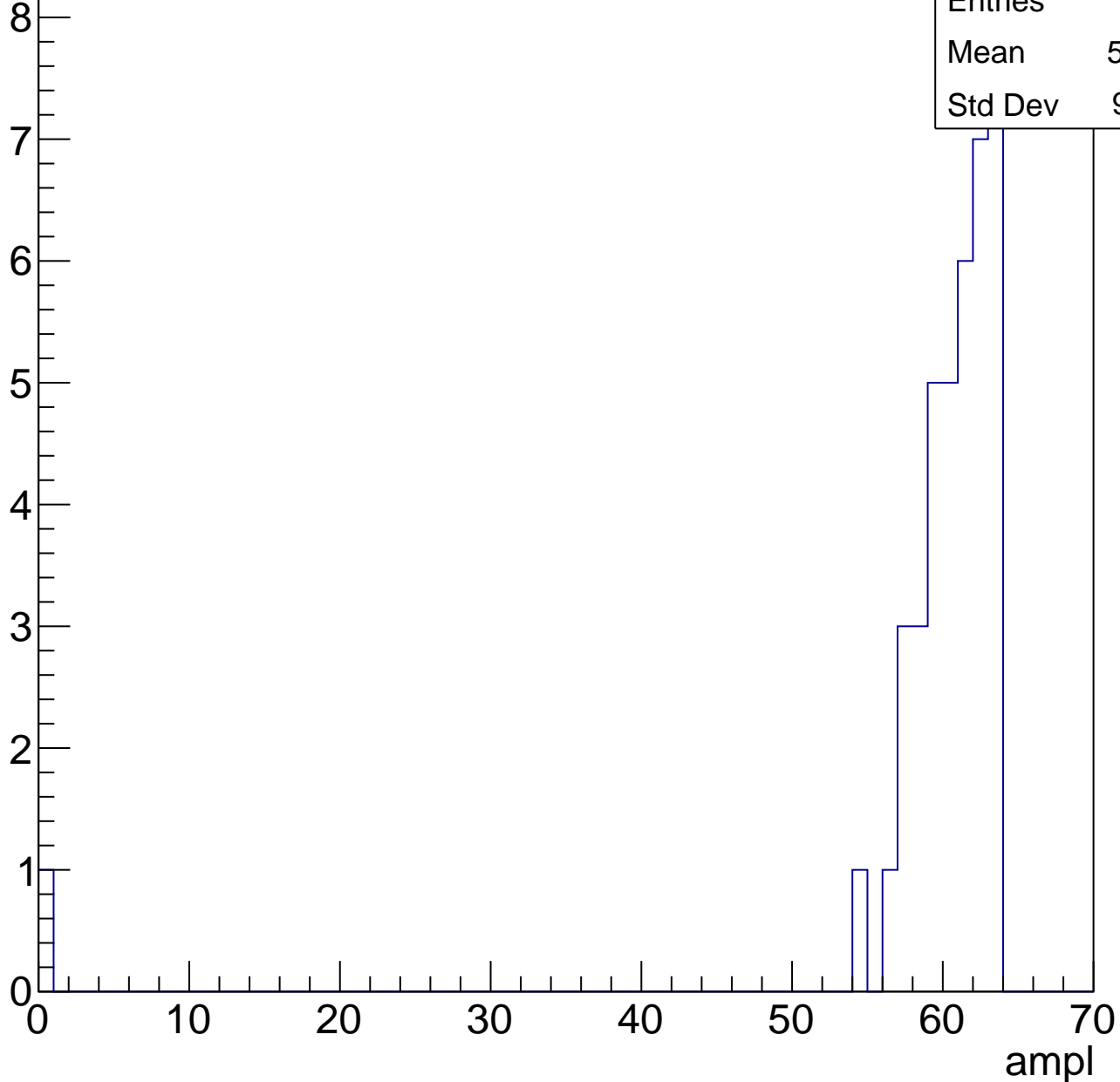


B1L100S, U13-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.85
Std Dev	9.681



B1L100S, U13-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch103, adc0

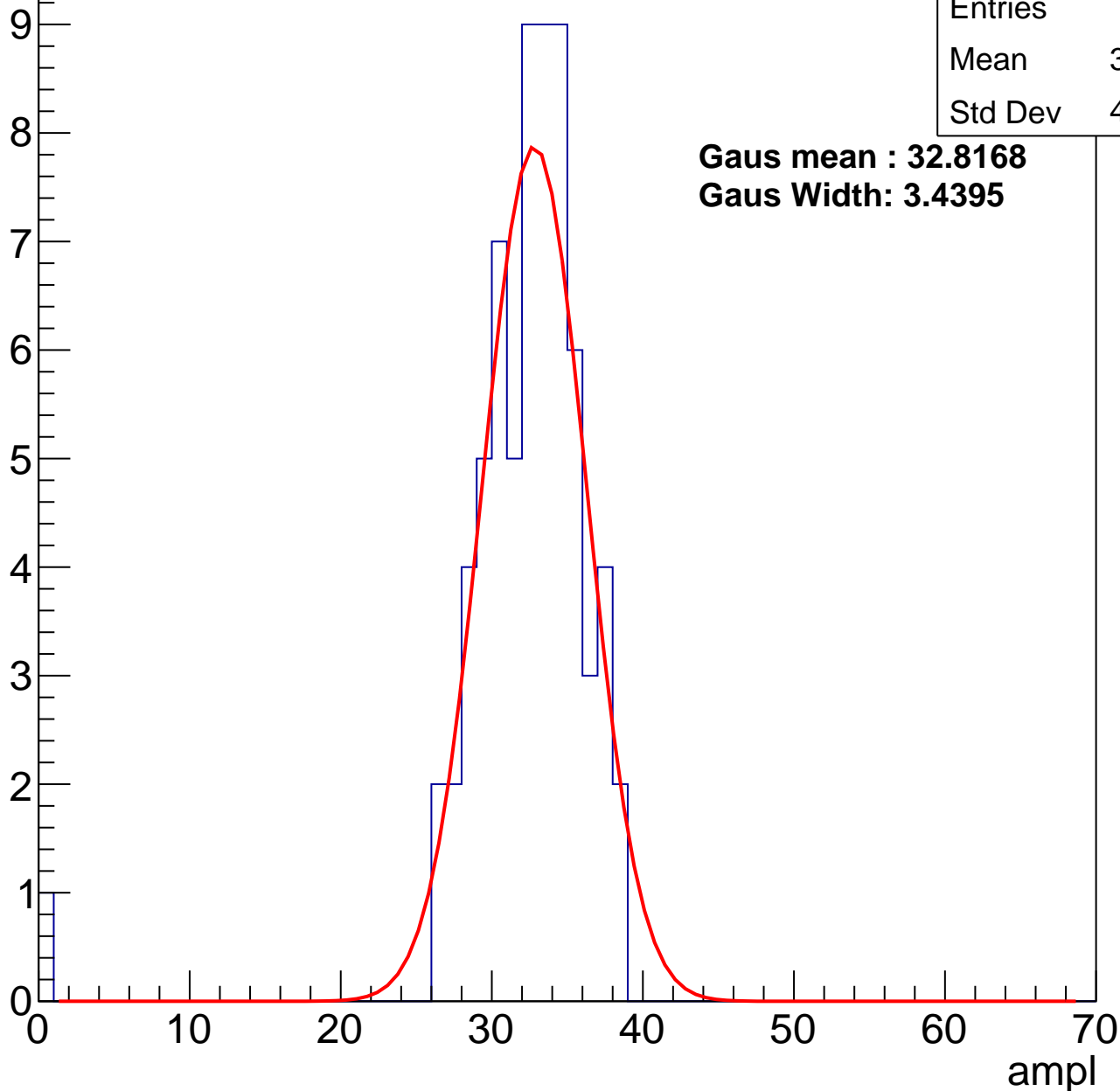
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	31.78
Std Dev	4.856

Gaus mean : 32.8168

Gaus Width: 3.4395



B1L100S, U13-ch103, adc1

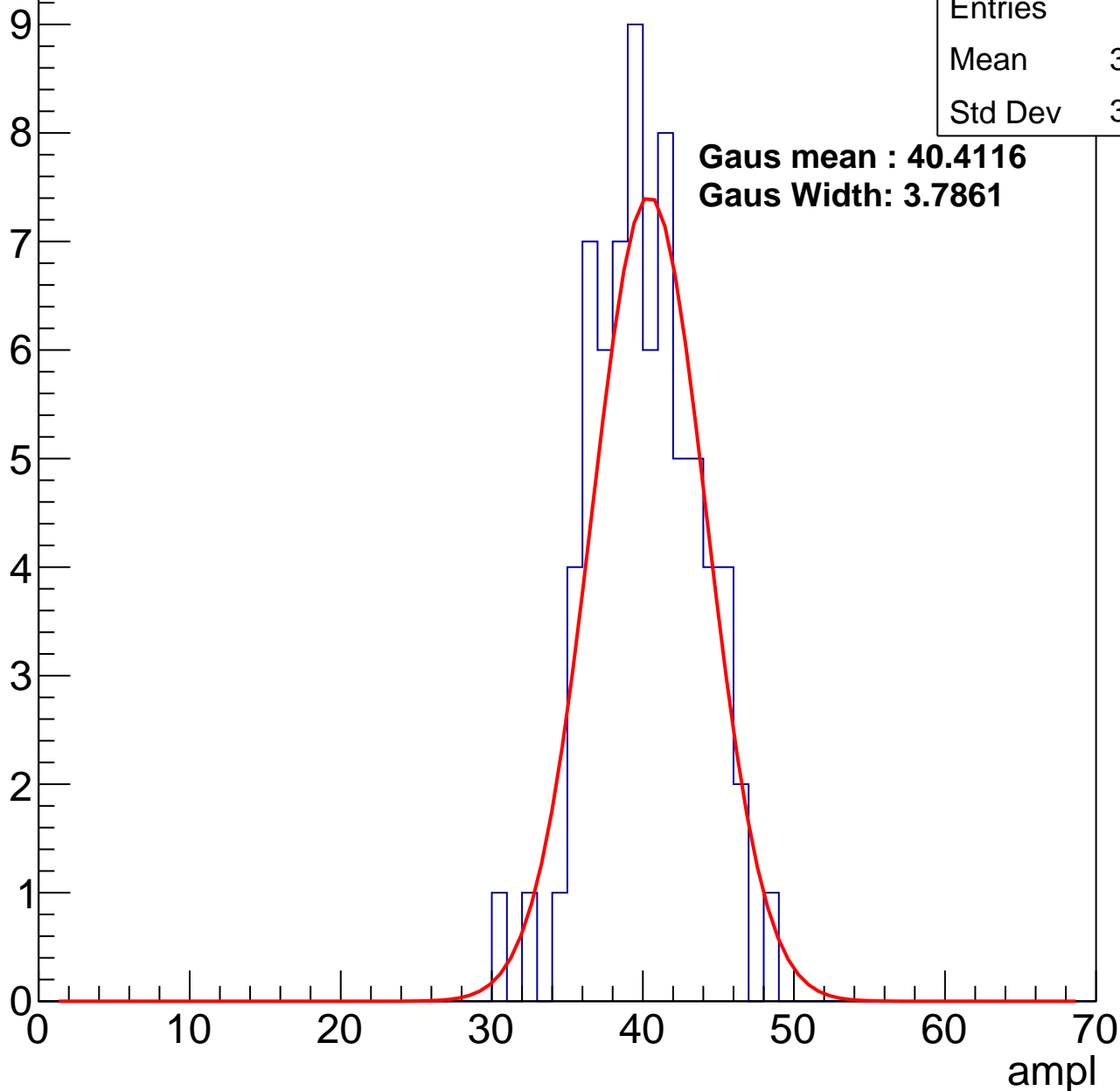
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	39.66
Std Dev	3.496

Gaus mean : 40.4116

Gaus Width: 3.7861



B1L100S, U13-ch103, adc2

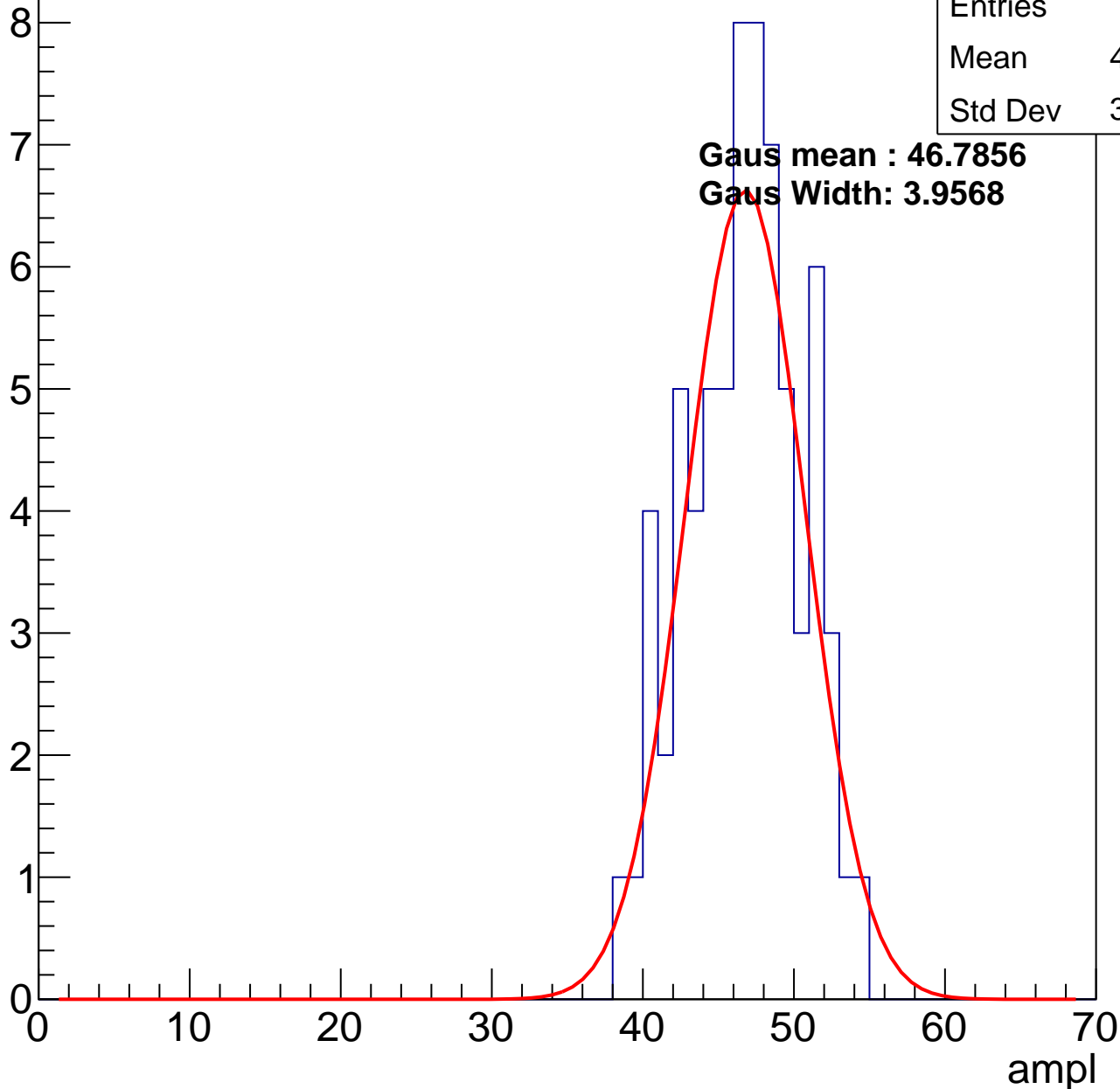
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	46.23
Std Dev	3.699

Gaus mean : 46.7856

Gaus Width: 3.9568

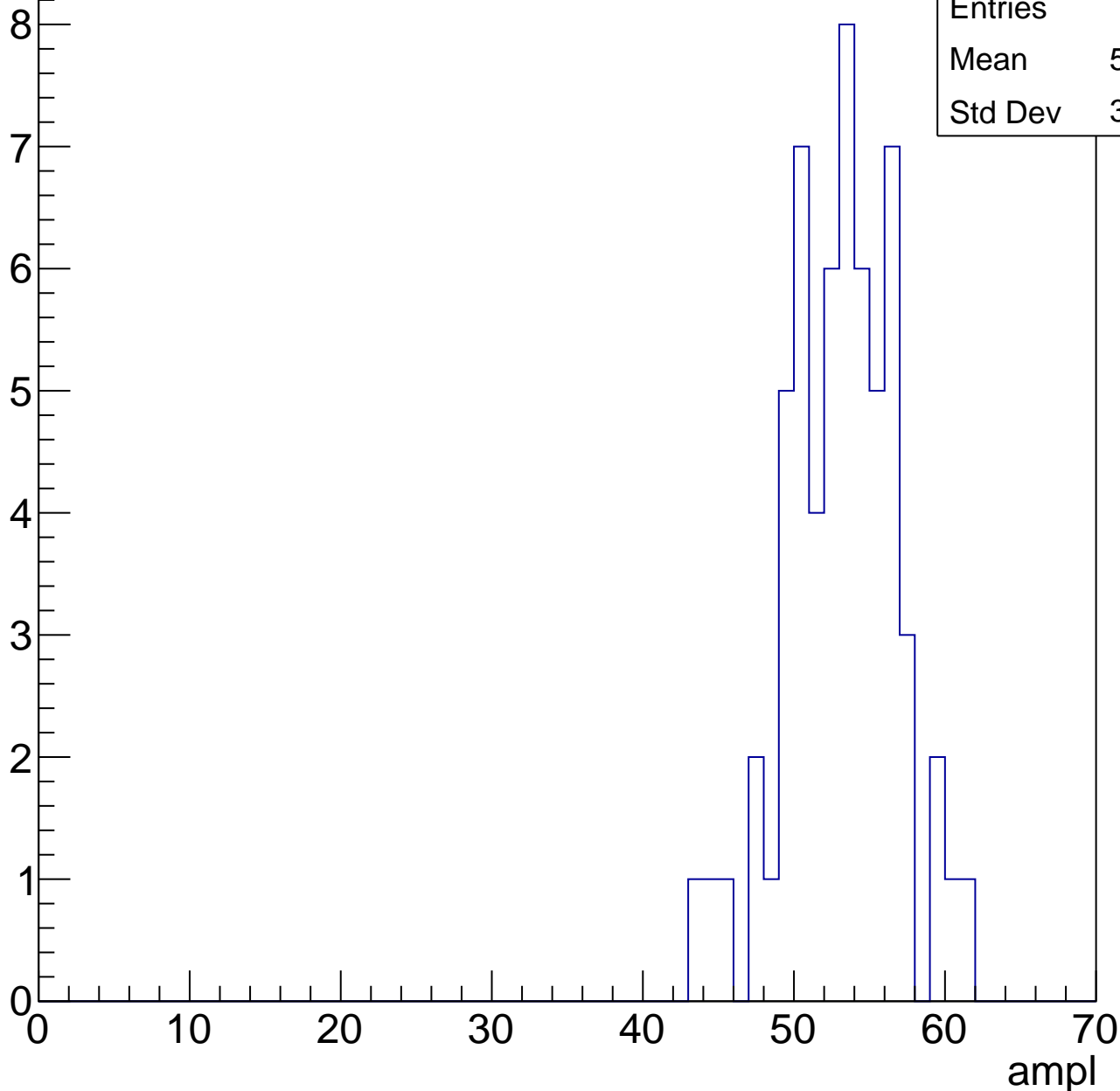


B1L100S, U13-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

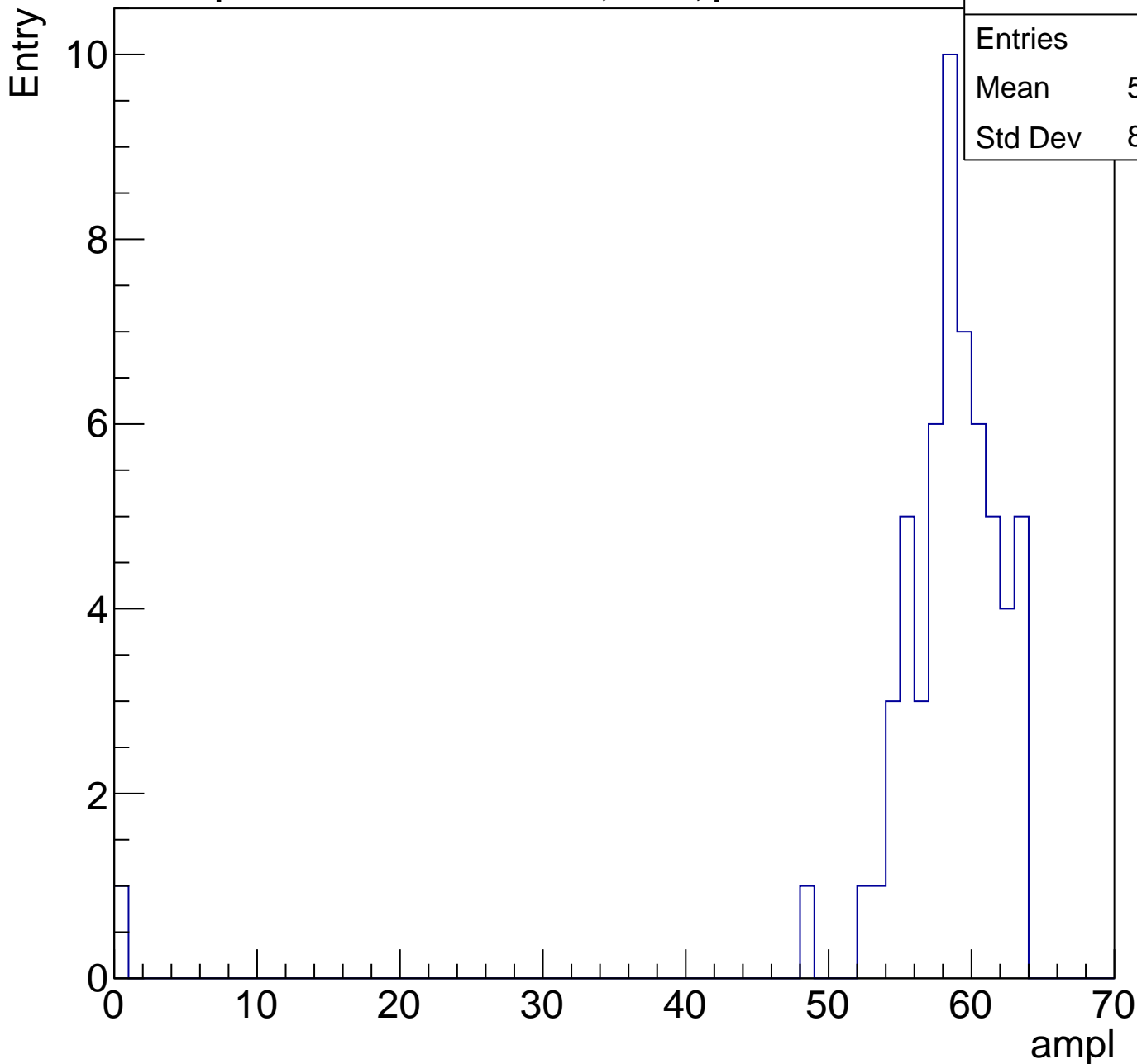
Entries	61
Mean	52.62
Std Dev	3.667



B1L100S, U13-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	57.26
Std Dev	8.168

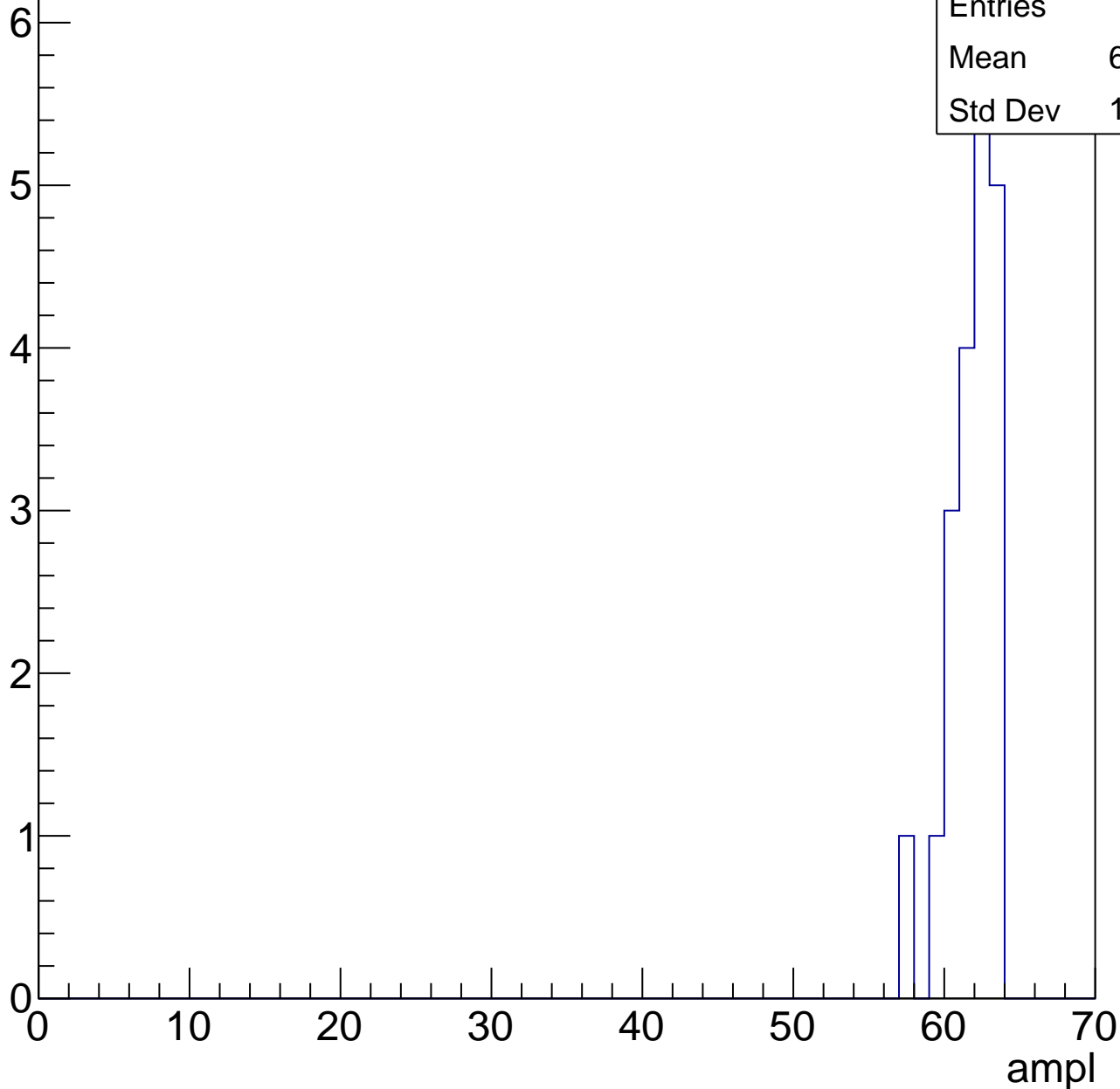


B1L100S, U13-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	61.35
Std Dev	1.526



B1L100S, U13-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch104, adc0

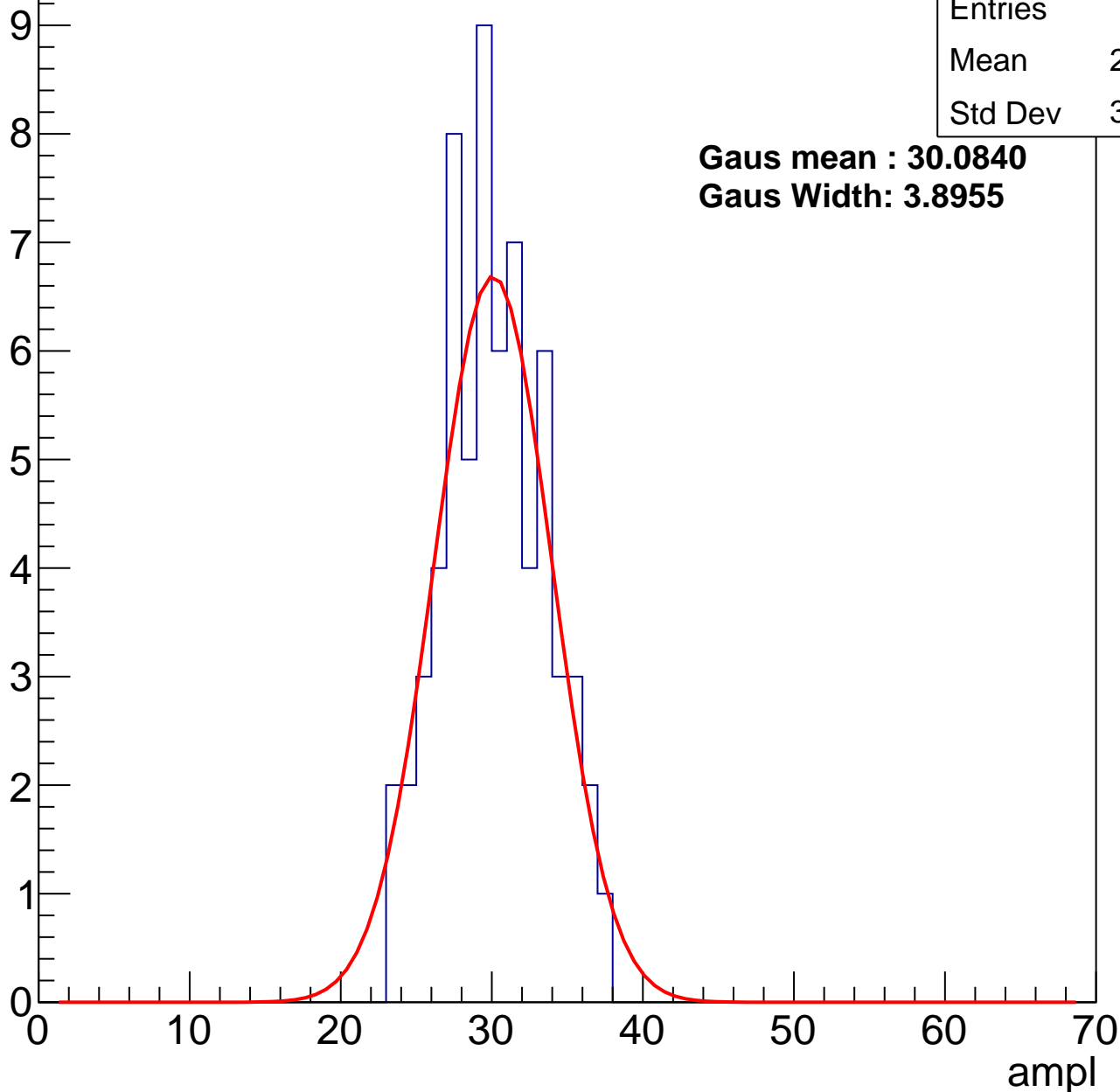
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.68
Std Dev	3.338

Gaus mean : 30.0840

Gaus Width: 3.8955



B1L100S, U13-ch104, adc1

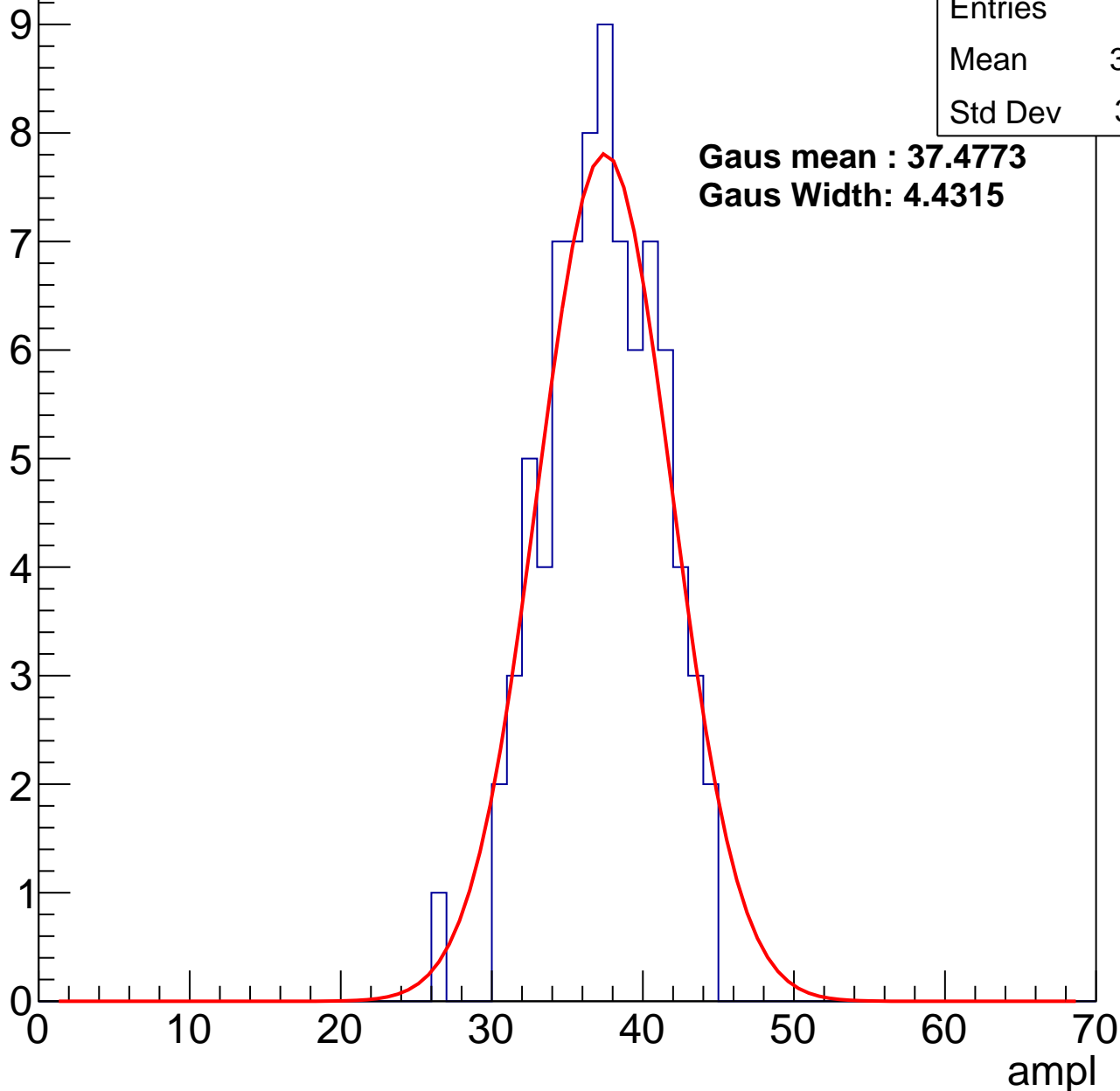
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.86
Std Dev	3.701

Gaus mean : 37.4773

Gaus Width: 4.4315



B1L100S, U13-ch104, adc2

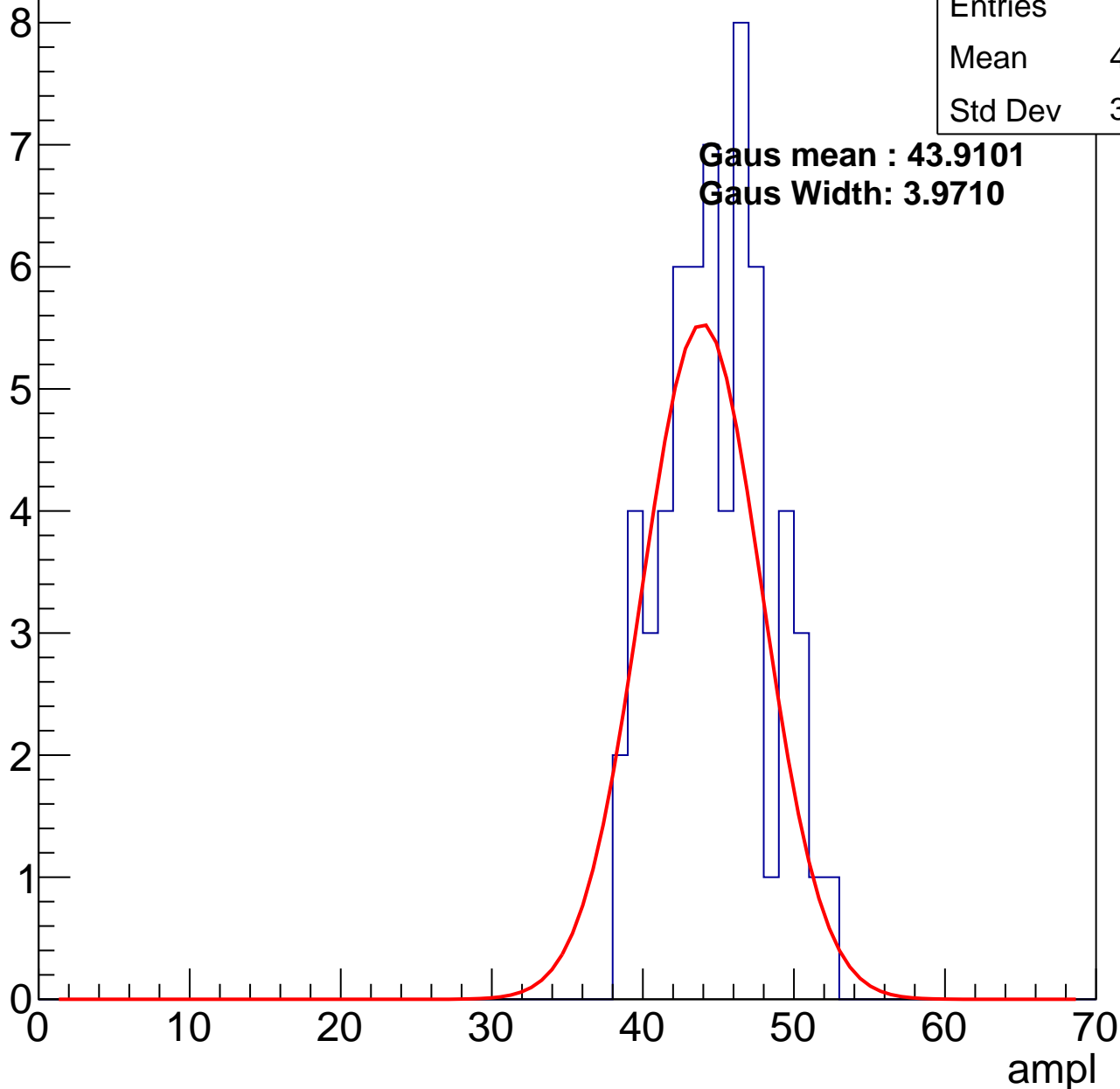
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.35
Std Dev	3.429

Gaus mean : 43.9101

Gaus Width: 3.9710

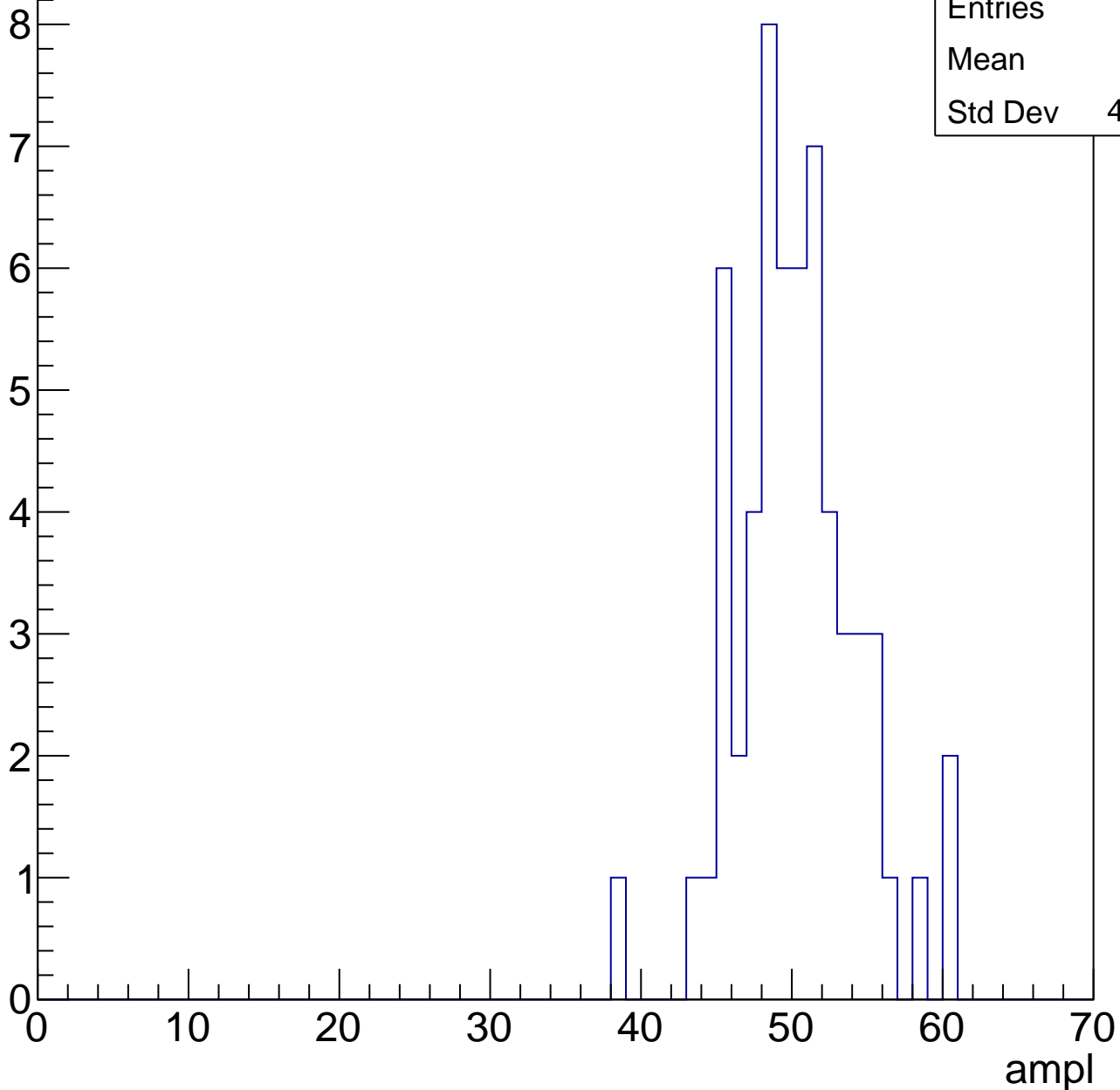


B1L100S, U13-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.8
Std Dev	4.024

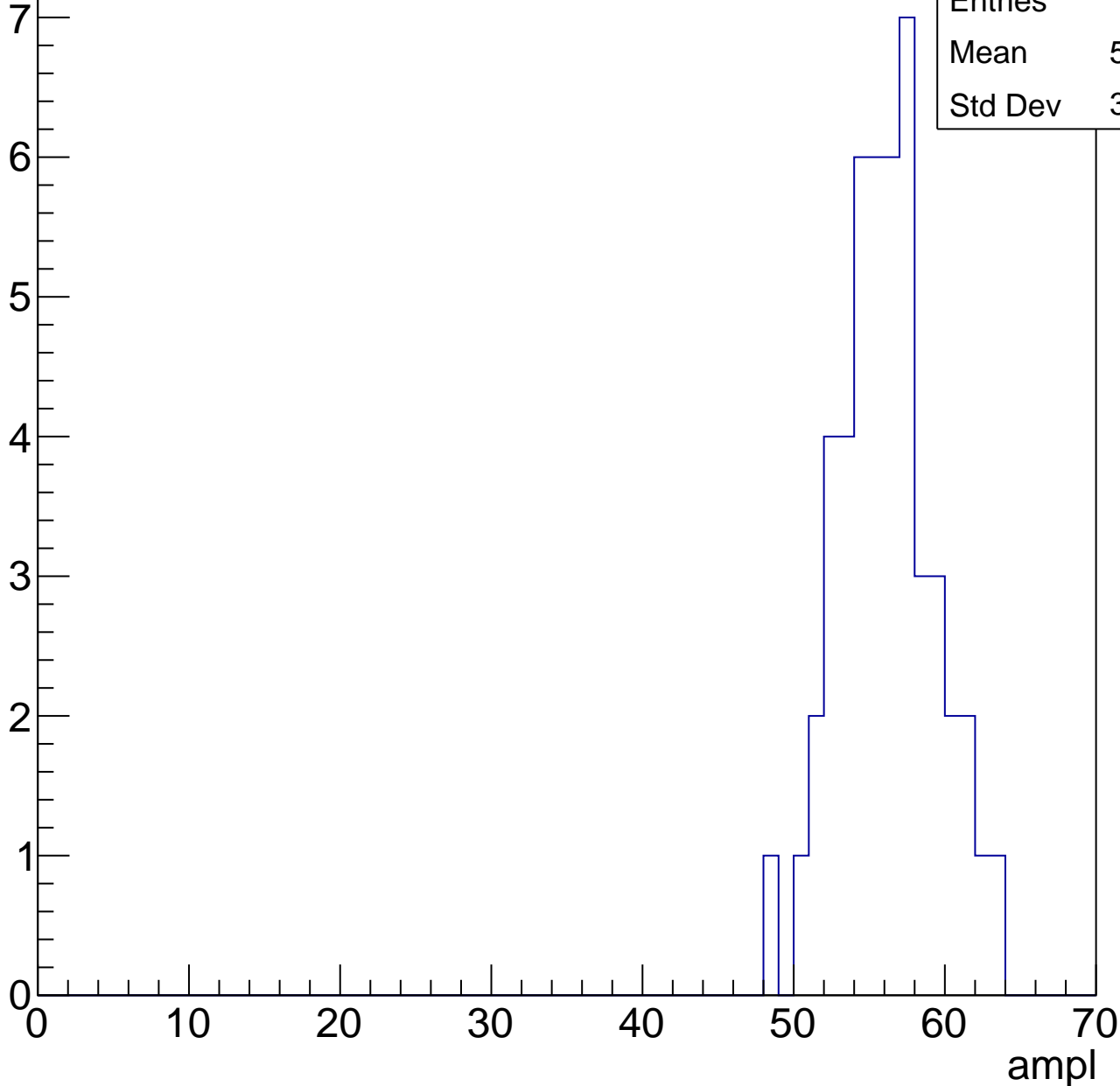


B1L100S, U13-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	55.65
Std Dev	3.153

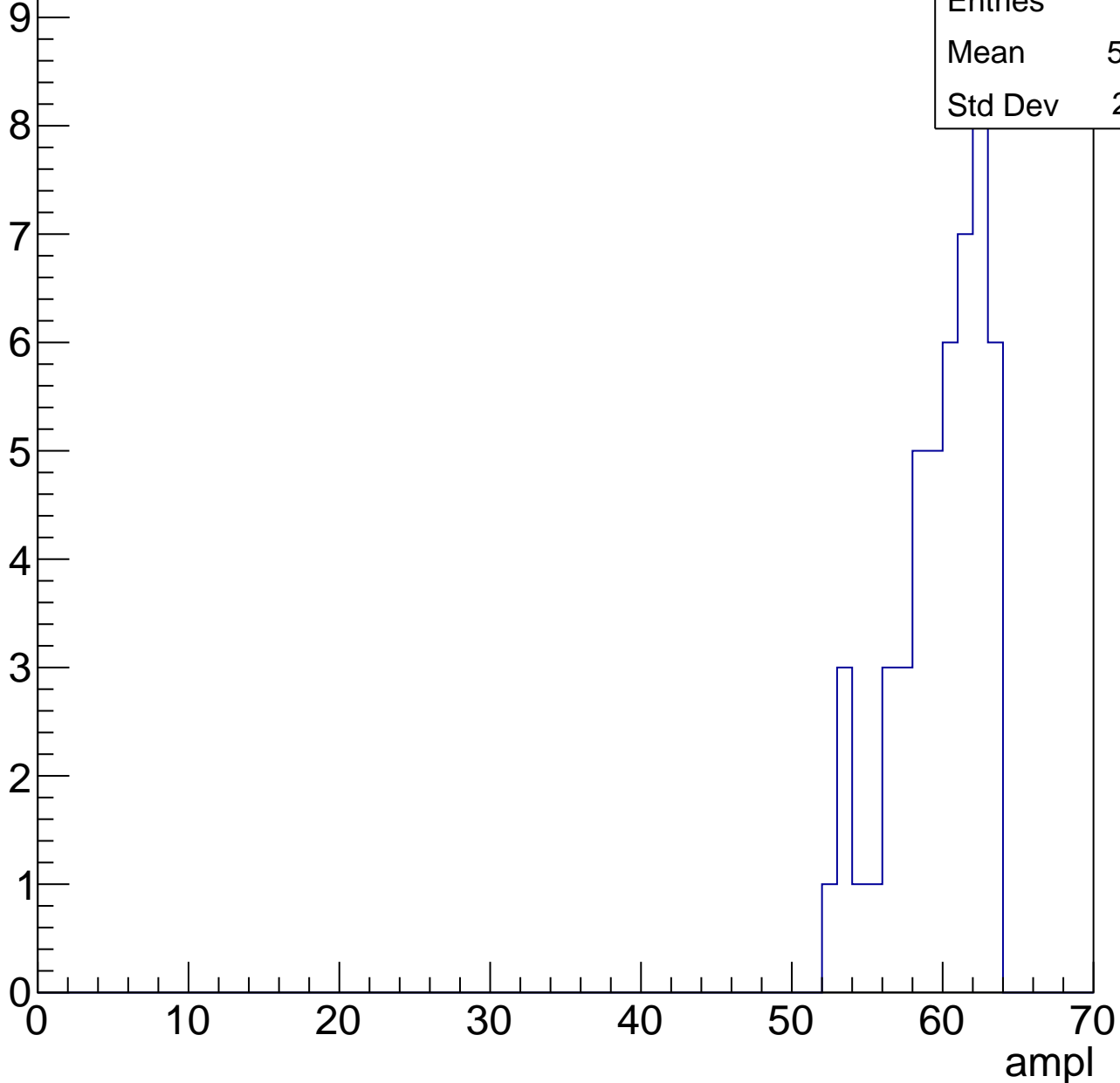


B1L100S, U13-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

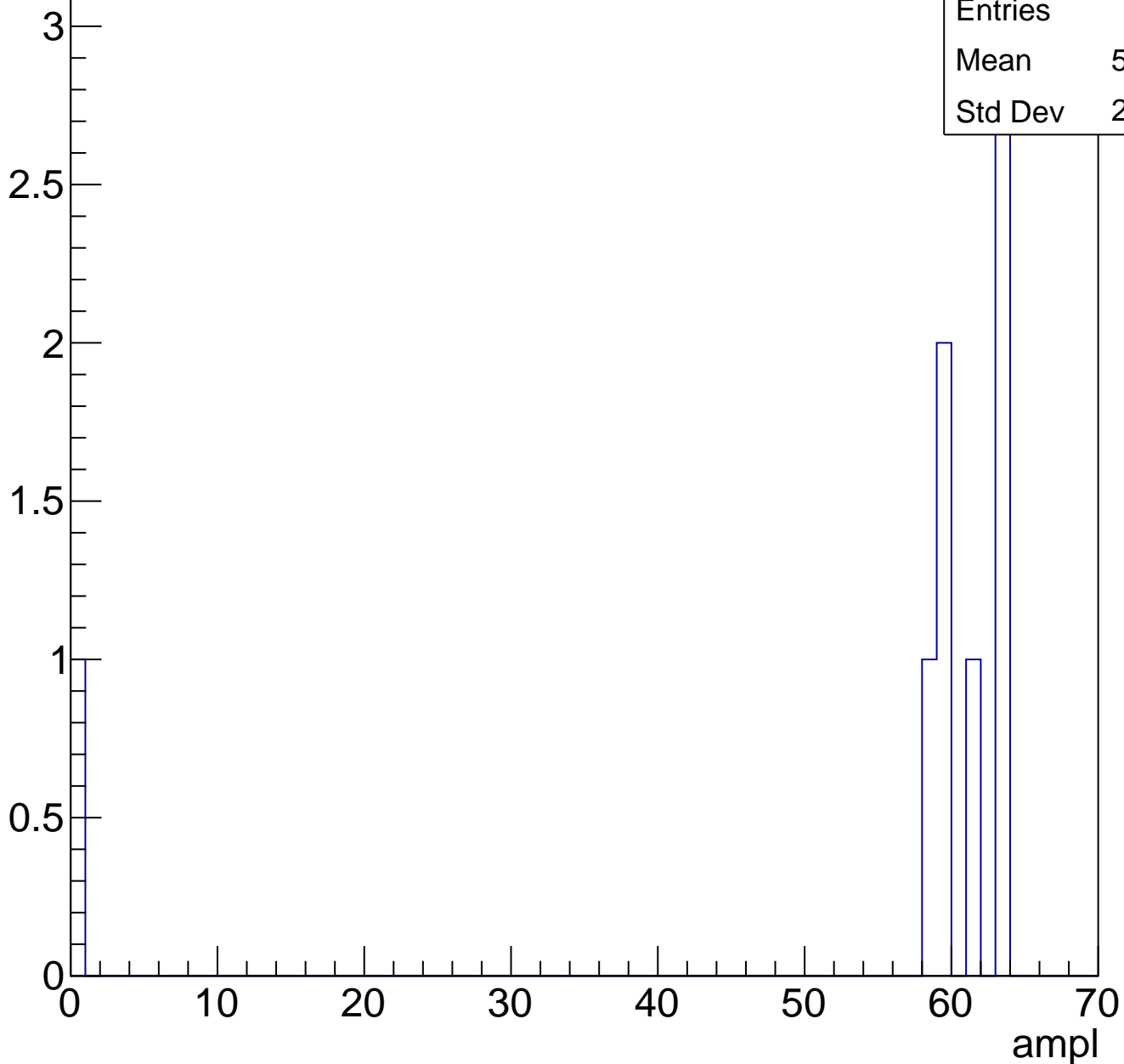
Entries	50
Mean	59.34
Std Dev	2.991



B1L100S, U13-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch105, adc0

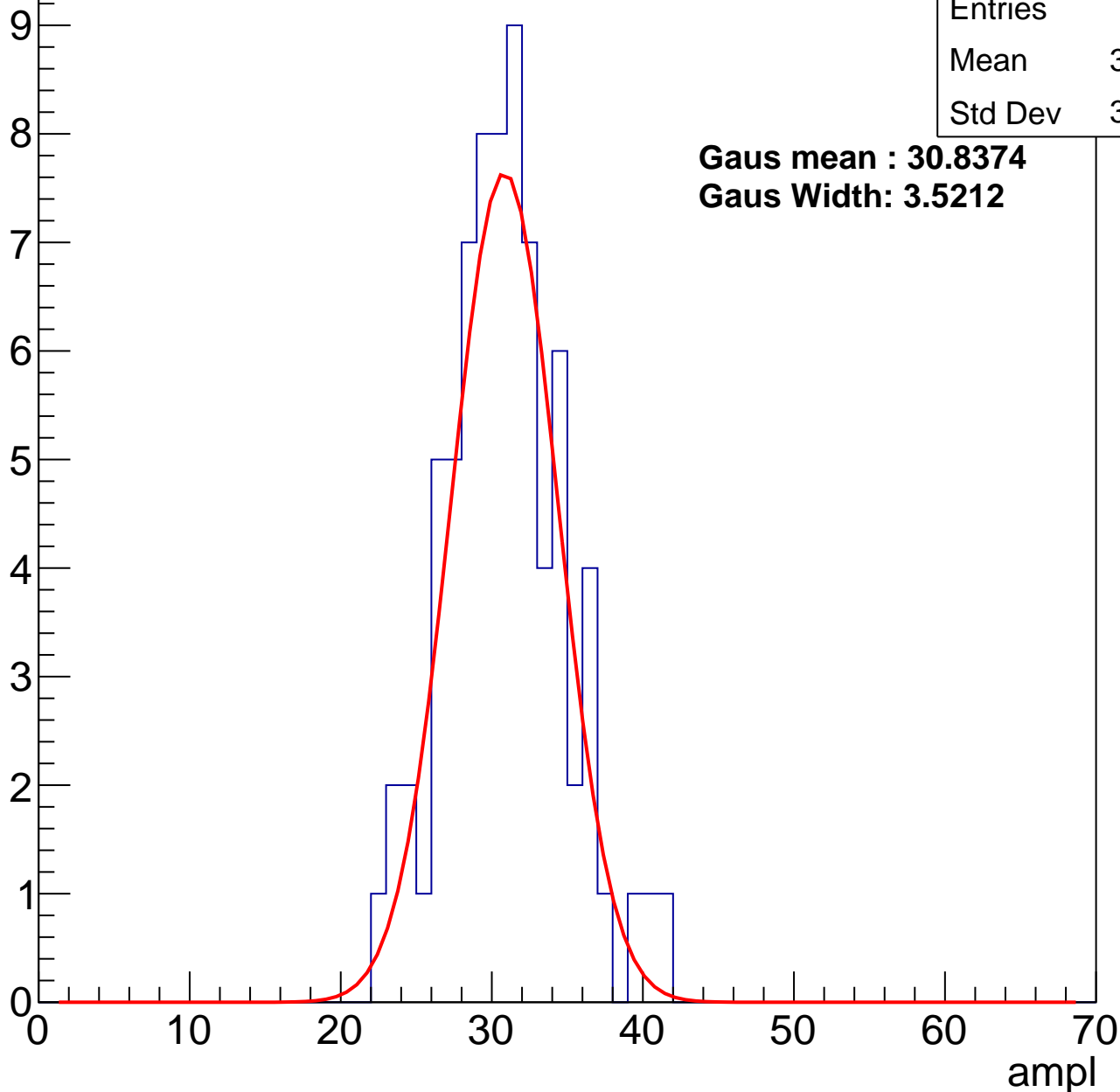
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.45
Std Dev	3.862

Gaus mean : 30.8374

Gaus Width: 3.5212



B1L100S, U13-ch105, adc1

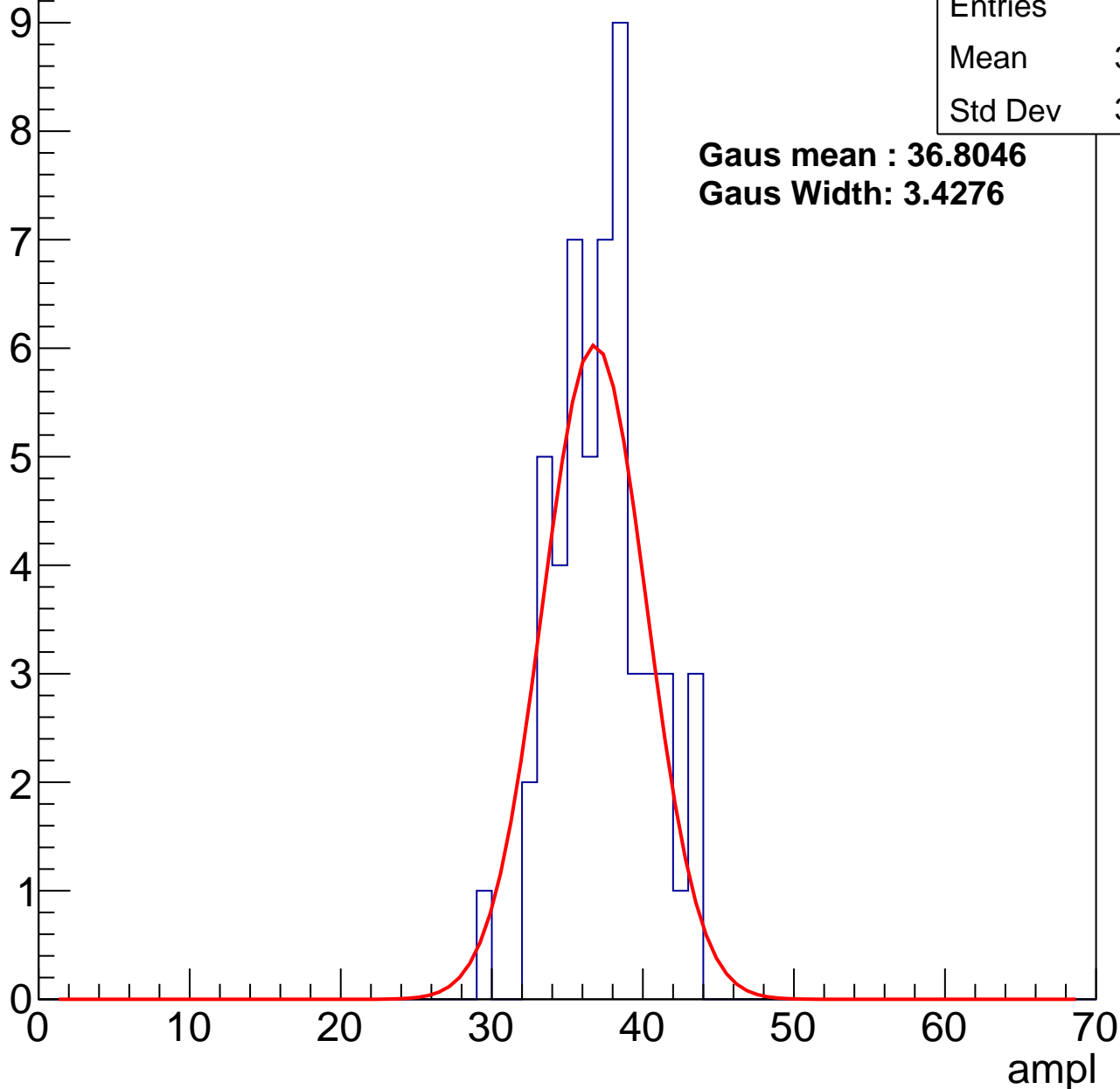
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	36.81
Std Dev	3.041

Gaus mean : 36.8046

Gaus Width: 3.4276



B1L100S, U13-ch105, adc2

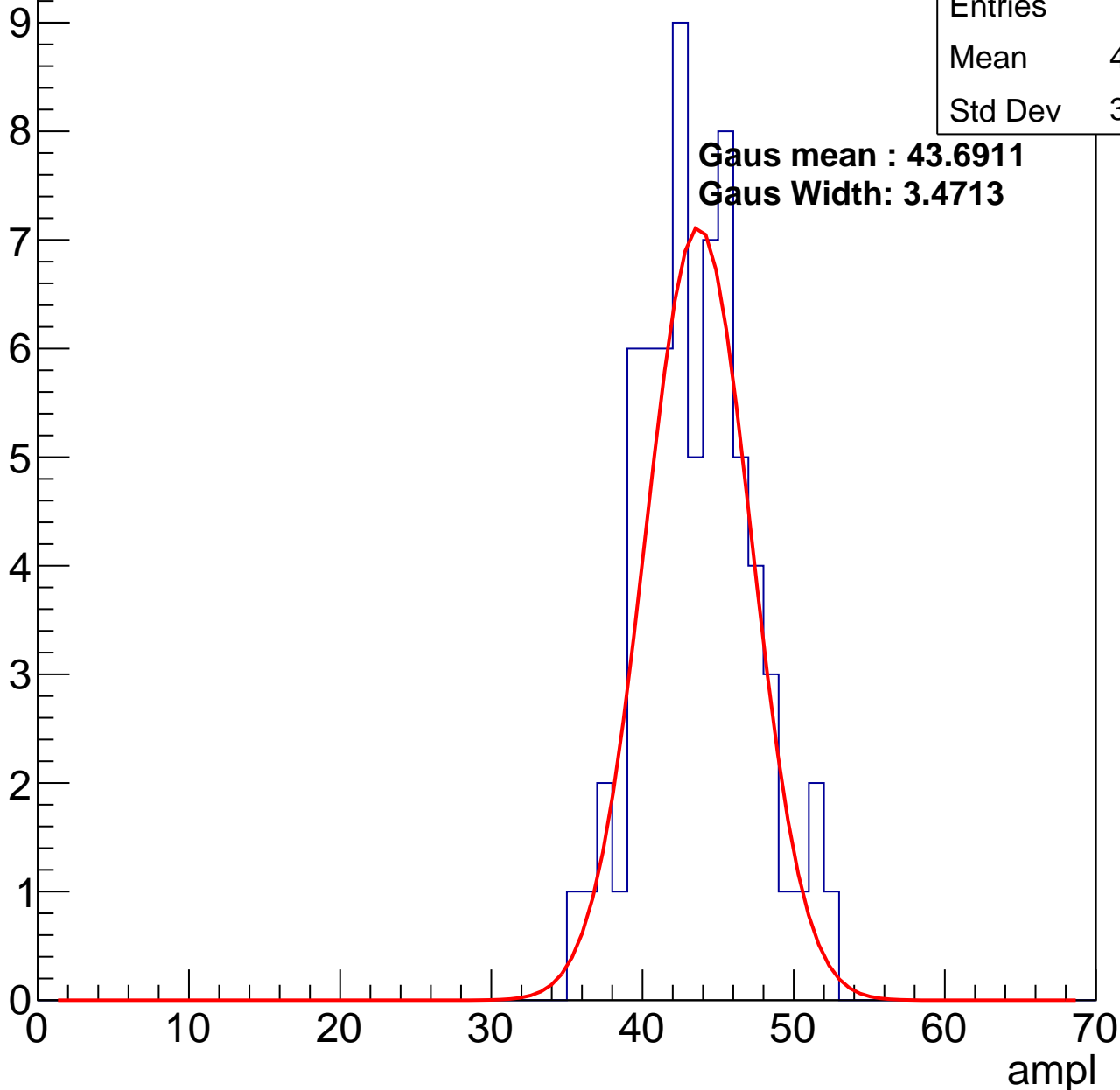
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.17
Std Dev	3.627

Gaus mean : 43.6911

Gaus Width: 3.4713

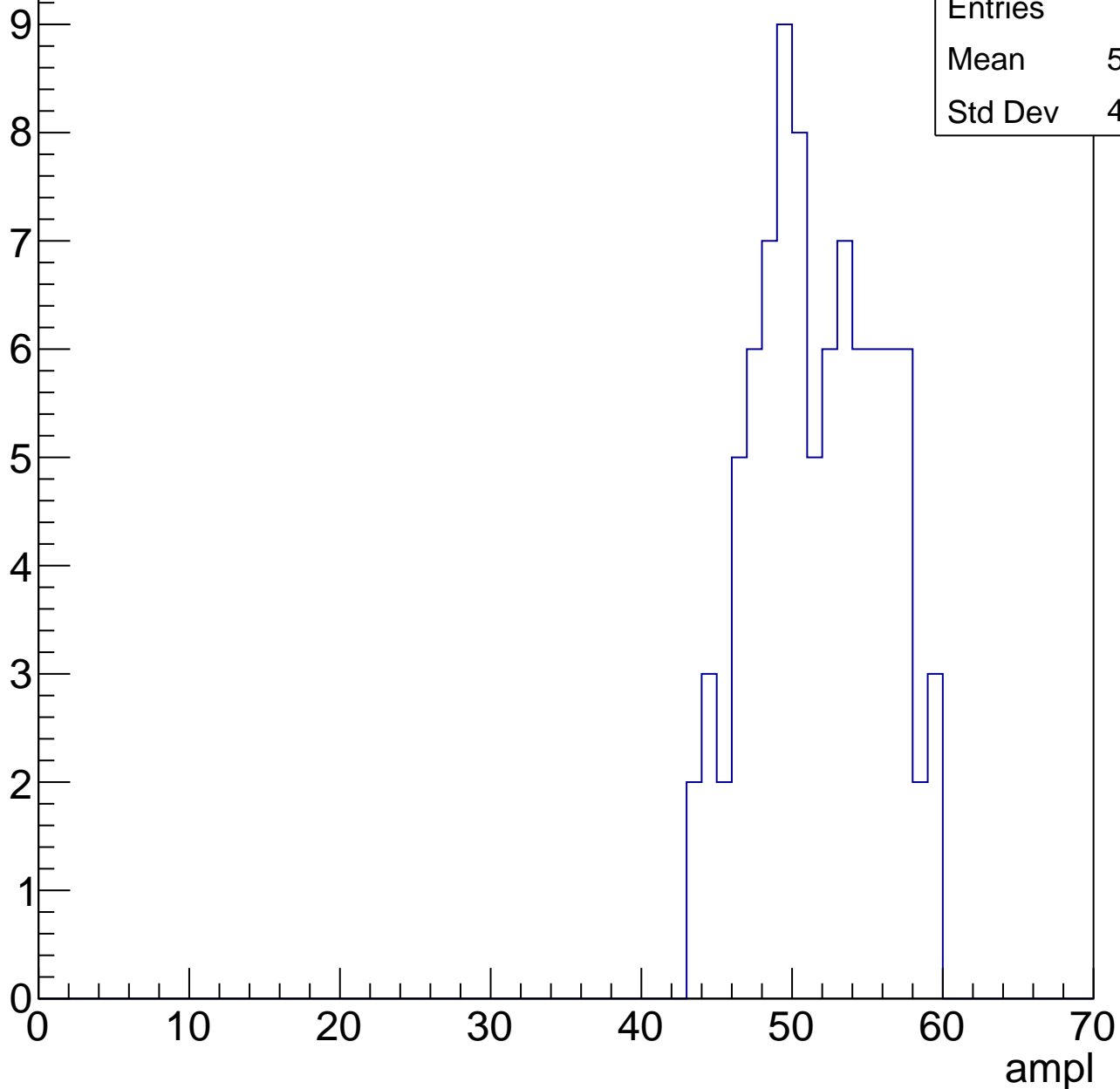


B1L100S, U13-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

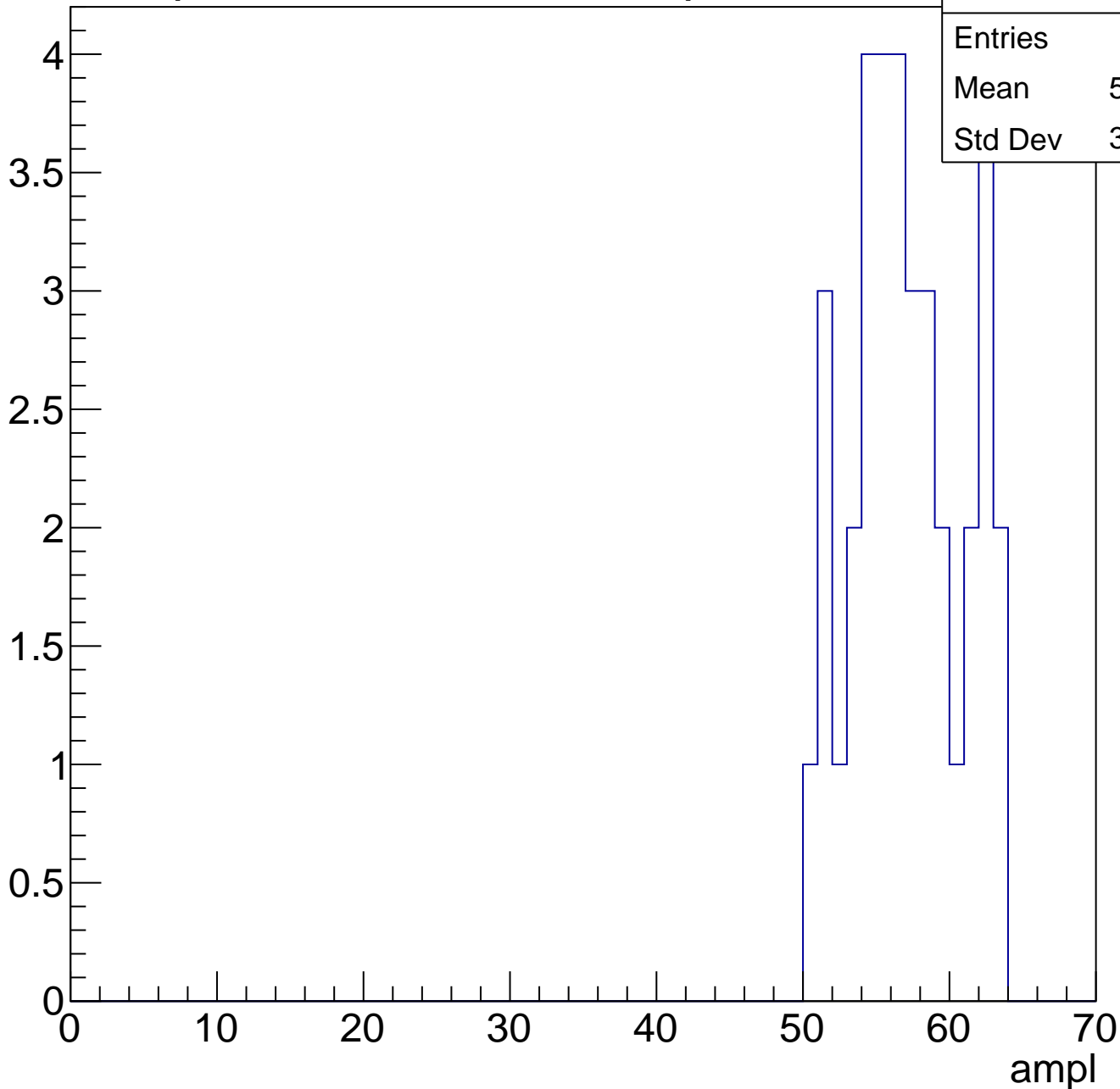
Entries	89
Mean	51.24
Std Dev	4.119



B1L100S, U13-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

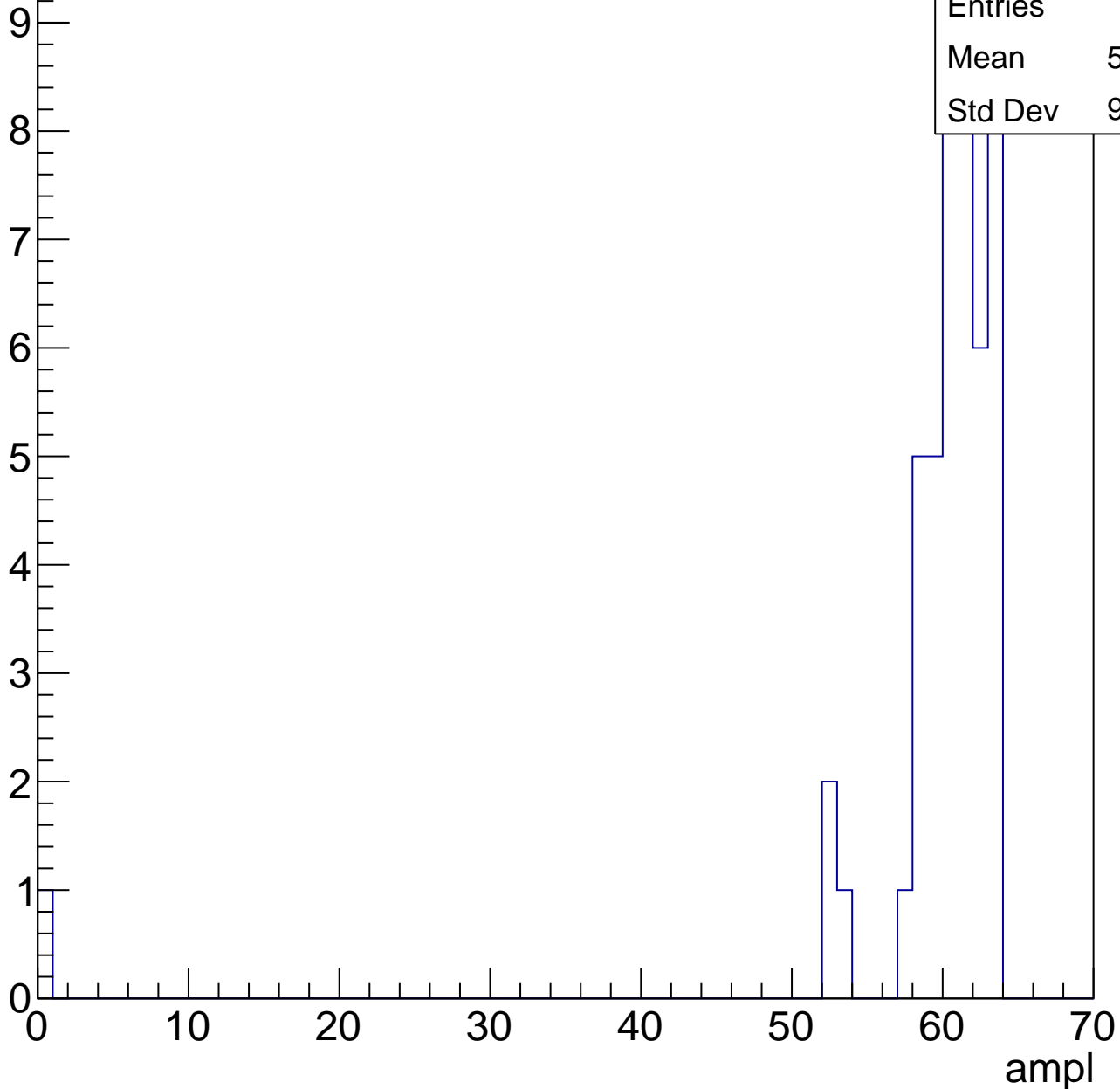


B1L100S, U13-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.76
Std Dev	9.142



B1L100S, U13-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch106, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	28.96
Std Dev	6.752

Gaus mean : 30.6638

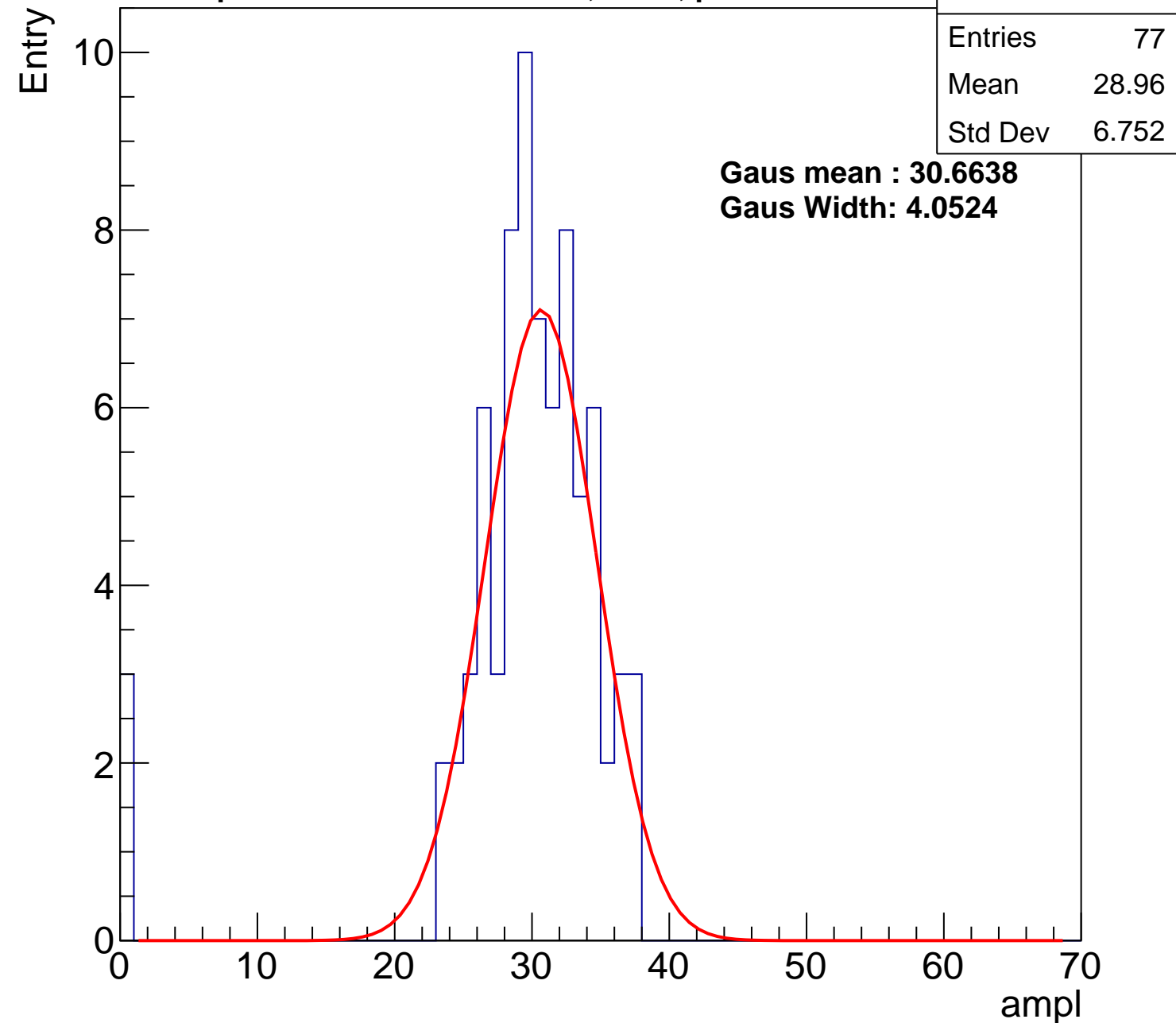
Gaus Width: 4.0524

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch106, adc1

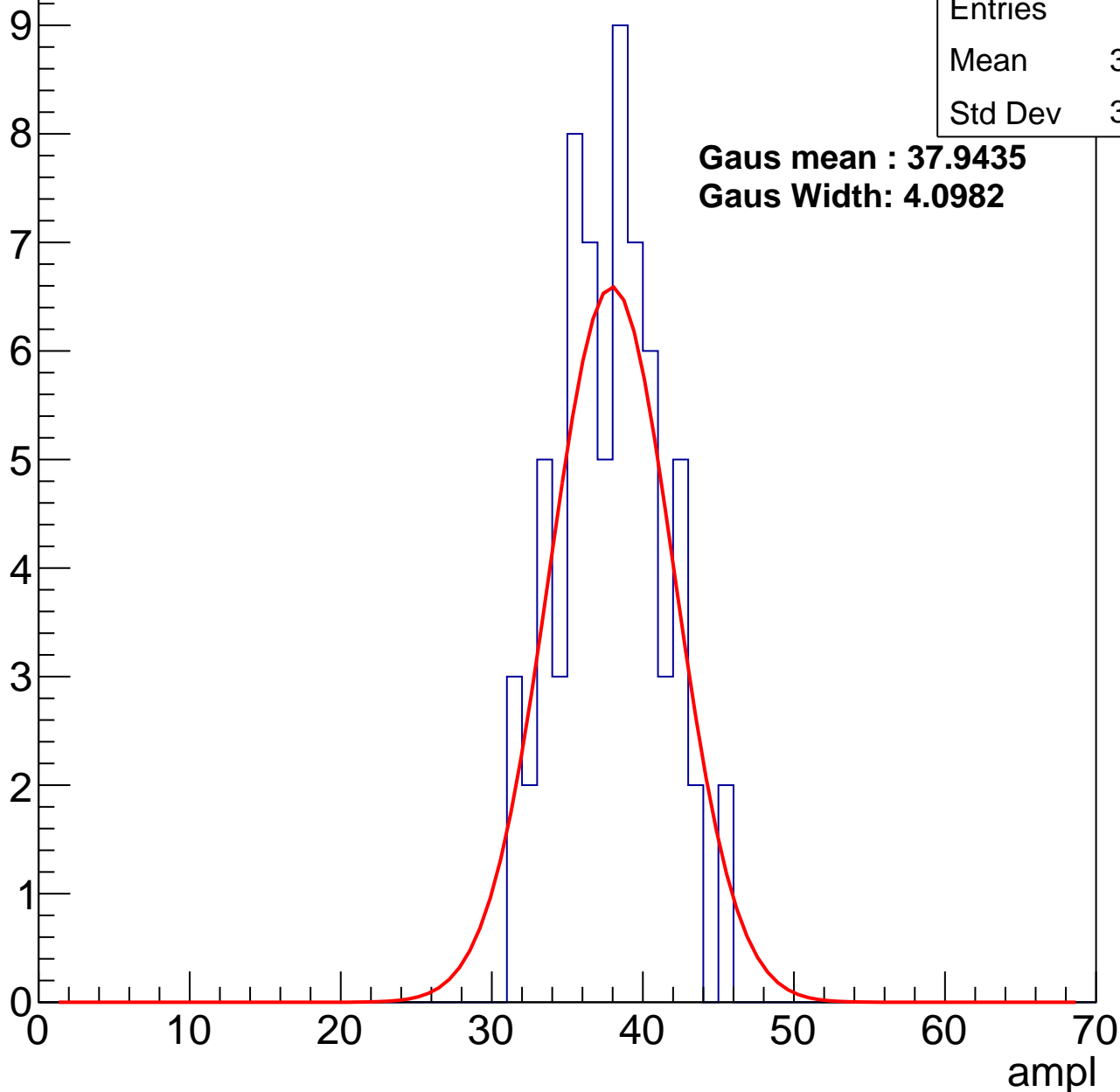
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.39
Std Dev	3.363

Gaus mean : 37.9435

Gaus Width: 4.0982



B1L100S, U13-ch106, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	43.89
Std Dev	3.425

Gaus mean : 44.6503

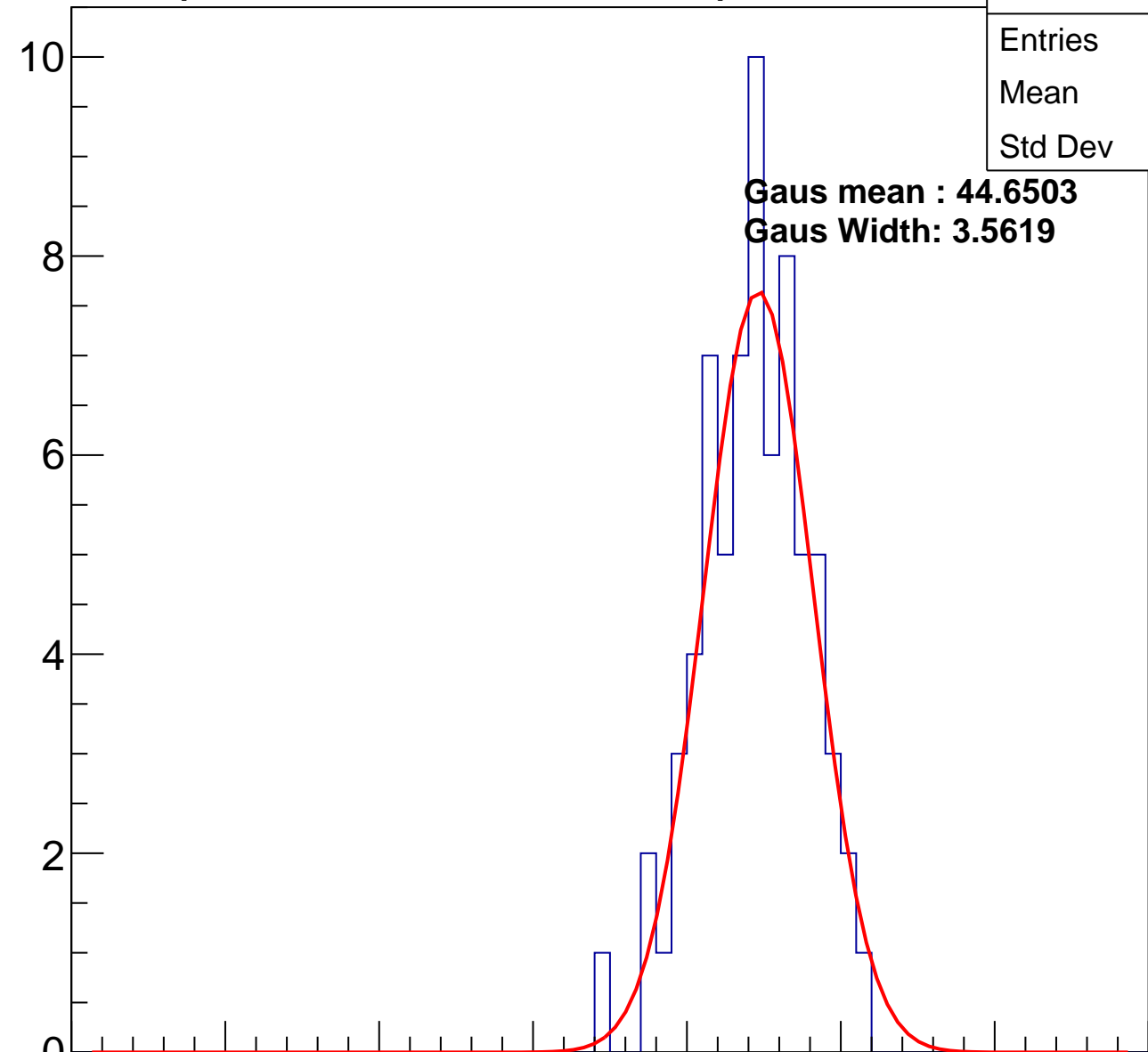
Gaus Width: 3.5619

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

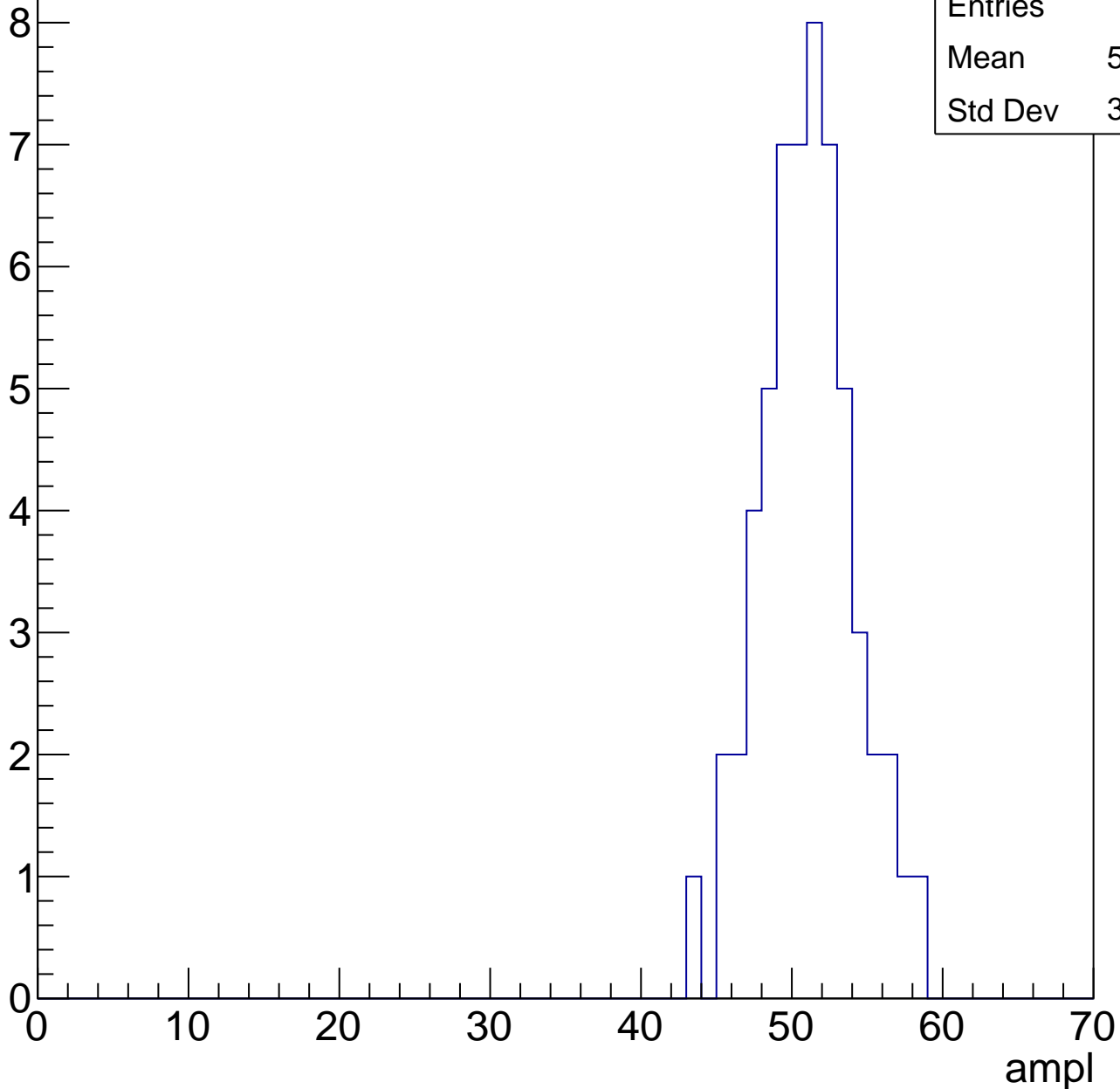


B1L100S, U13-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

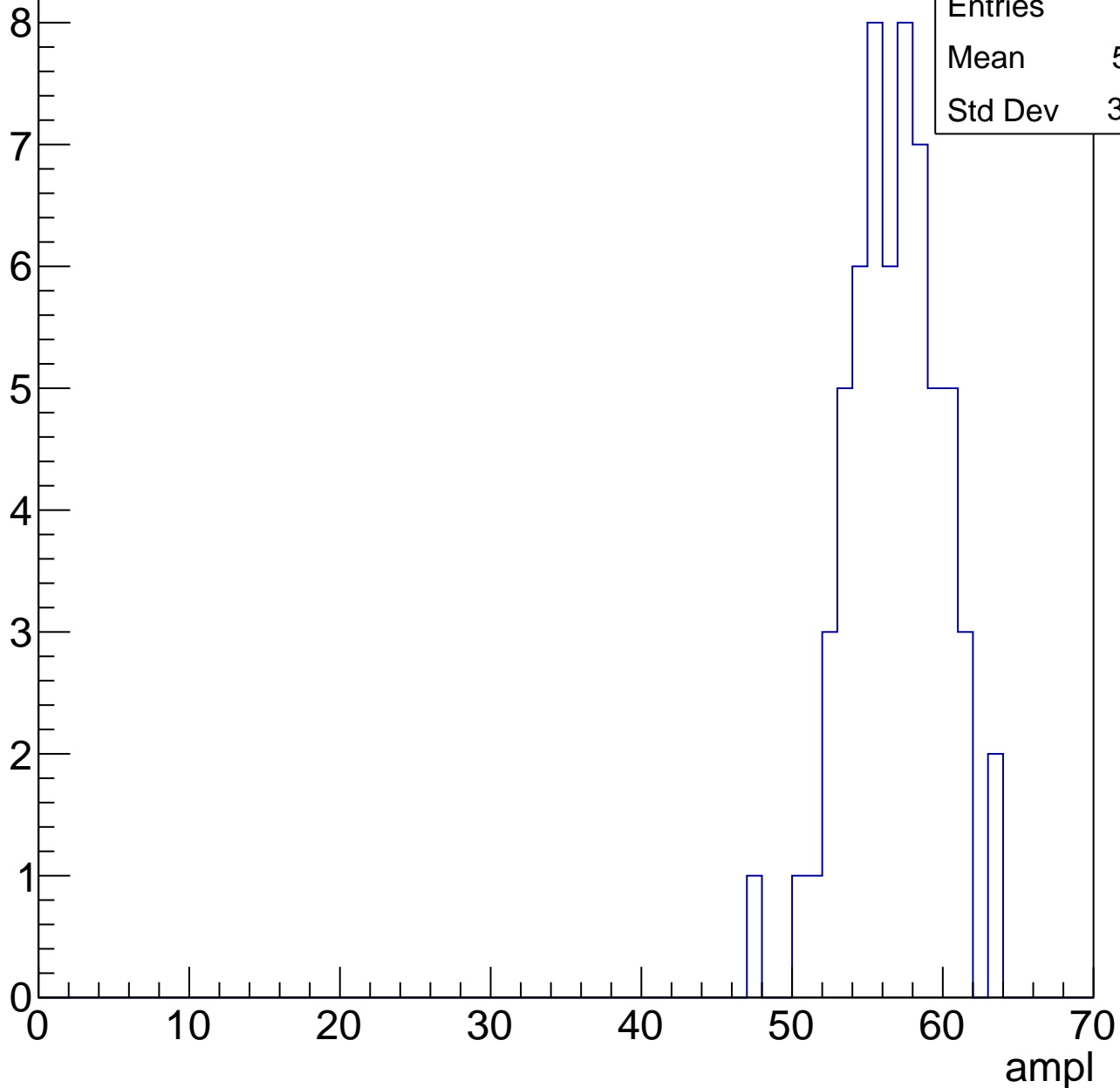
Entries	57
Mean	50.56
Std Dev	3.078



B1L100S, U13-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



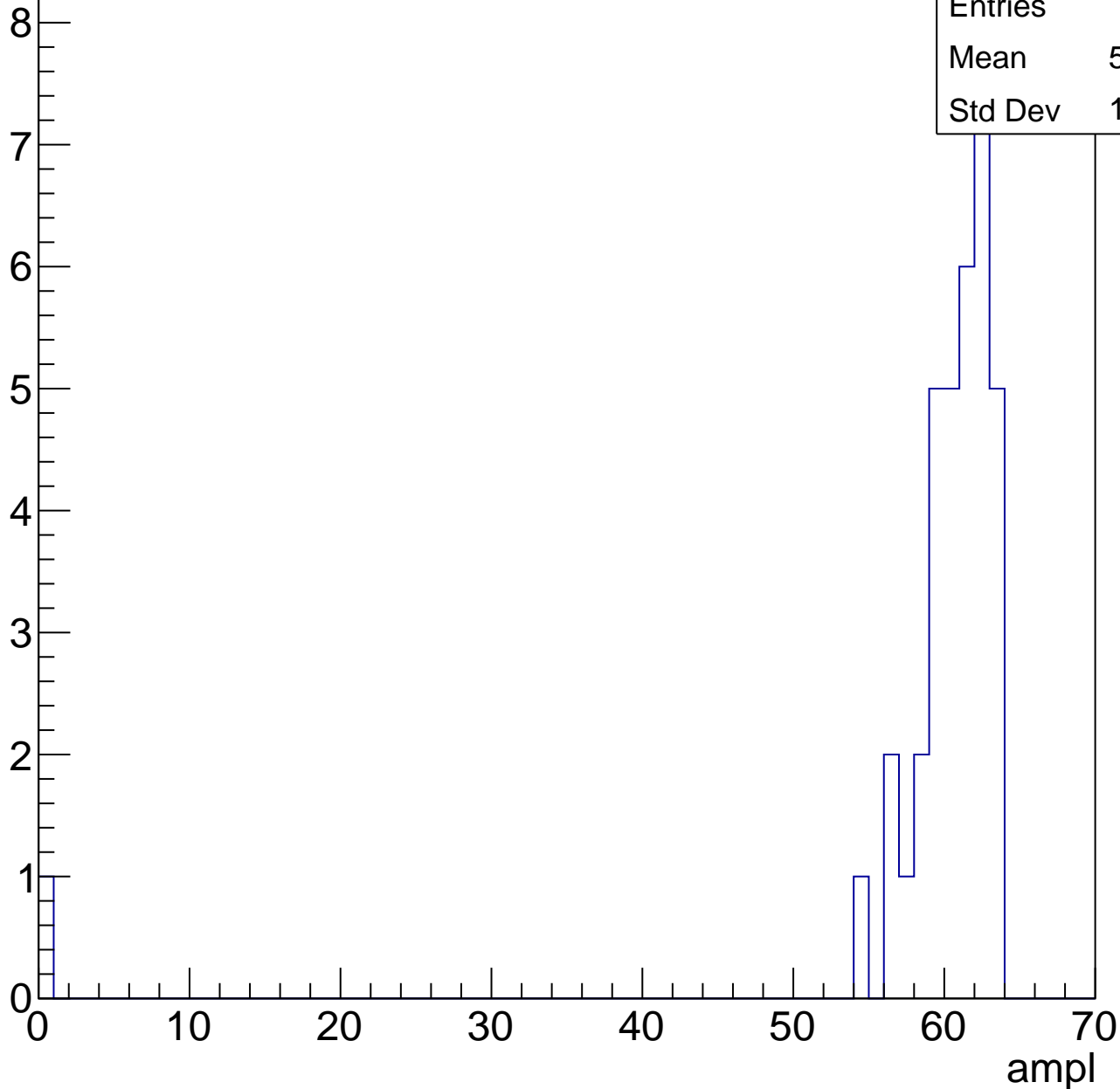
Entries	61
Mean	56.31
Std Dev	3.123

B1L100S, U13-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.64
Std Dev	10.15



B1L100S, U13-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch107, adc0

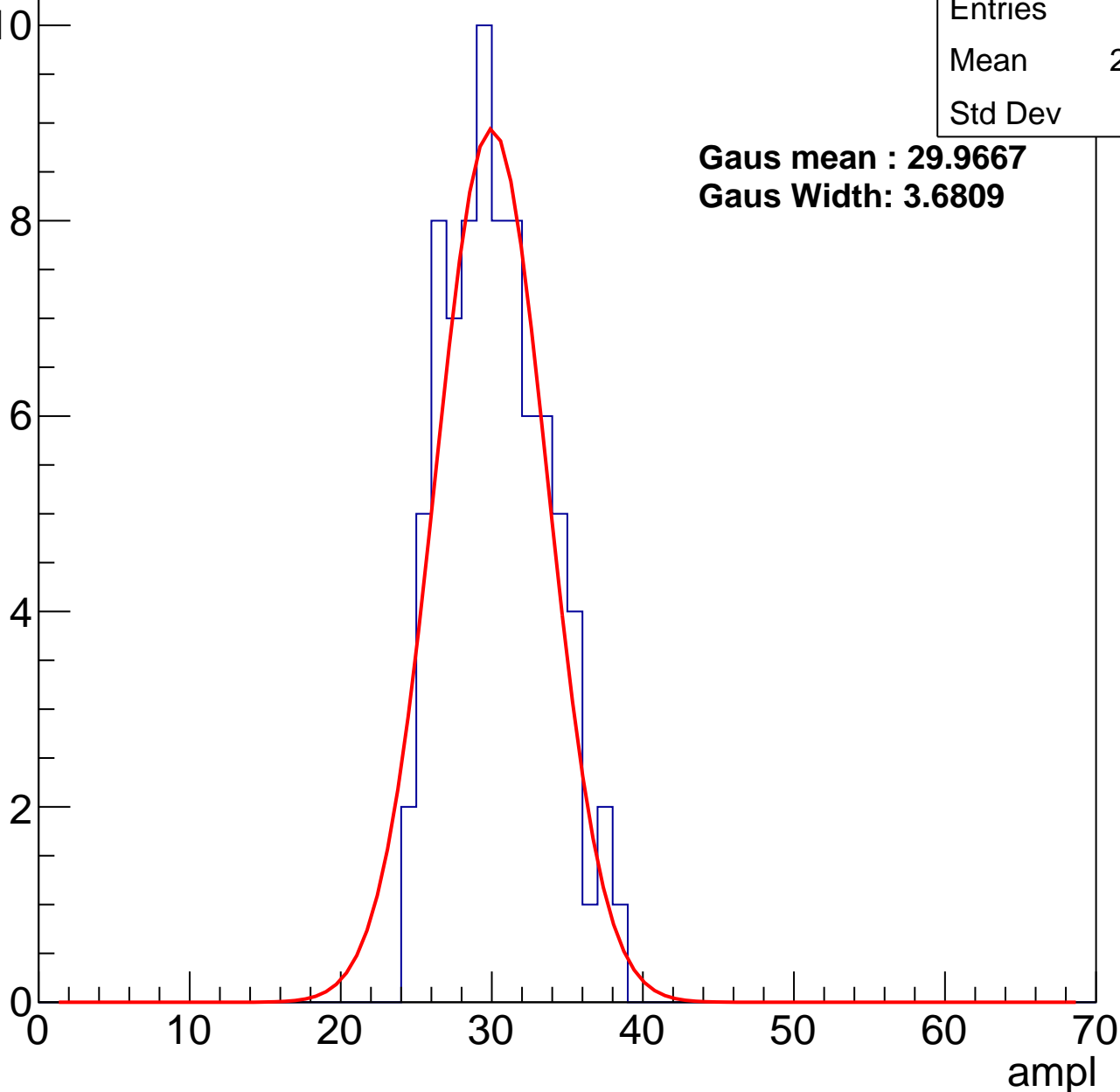
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	29.88
Std Dev	3.32

Gaus mean : 29.9667

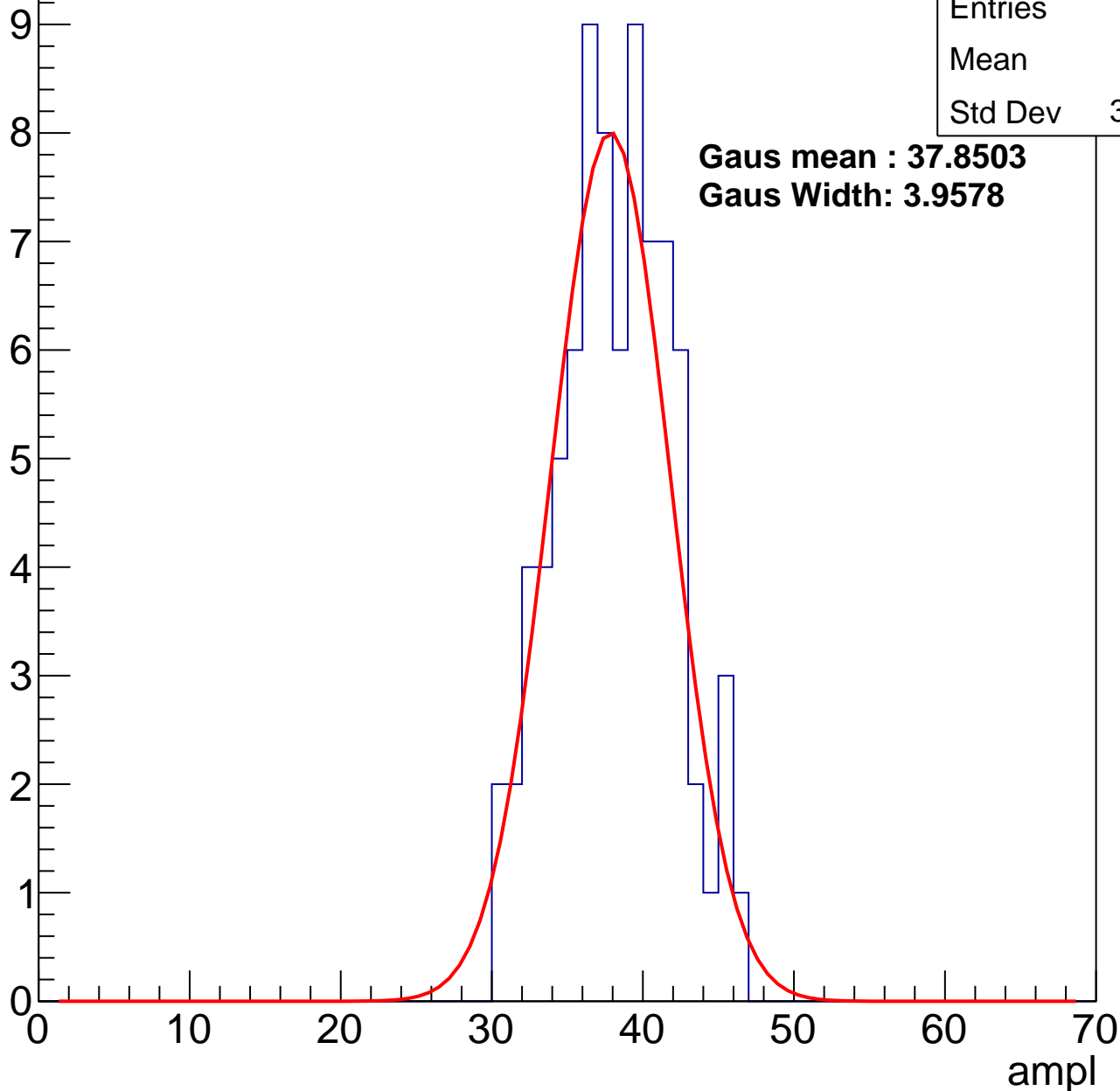
Gaus Width: 3.6809



B1L100S, U13-ch107, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch107, adc2

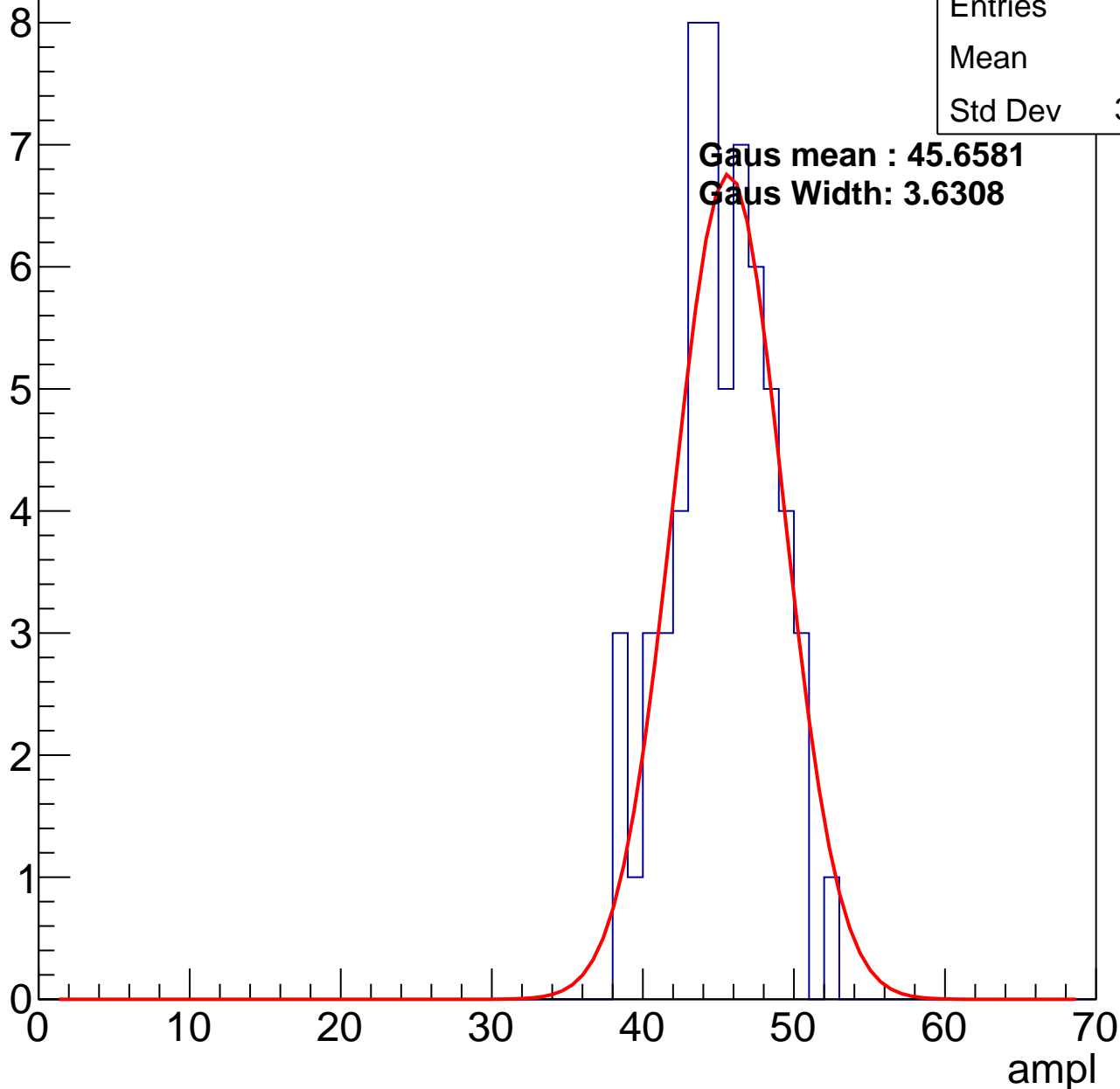
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.7
Std Dev	3.241

Gaus mean : 45.6581

Gaus Width: 3.6308

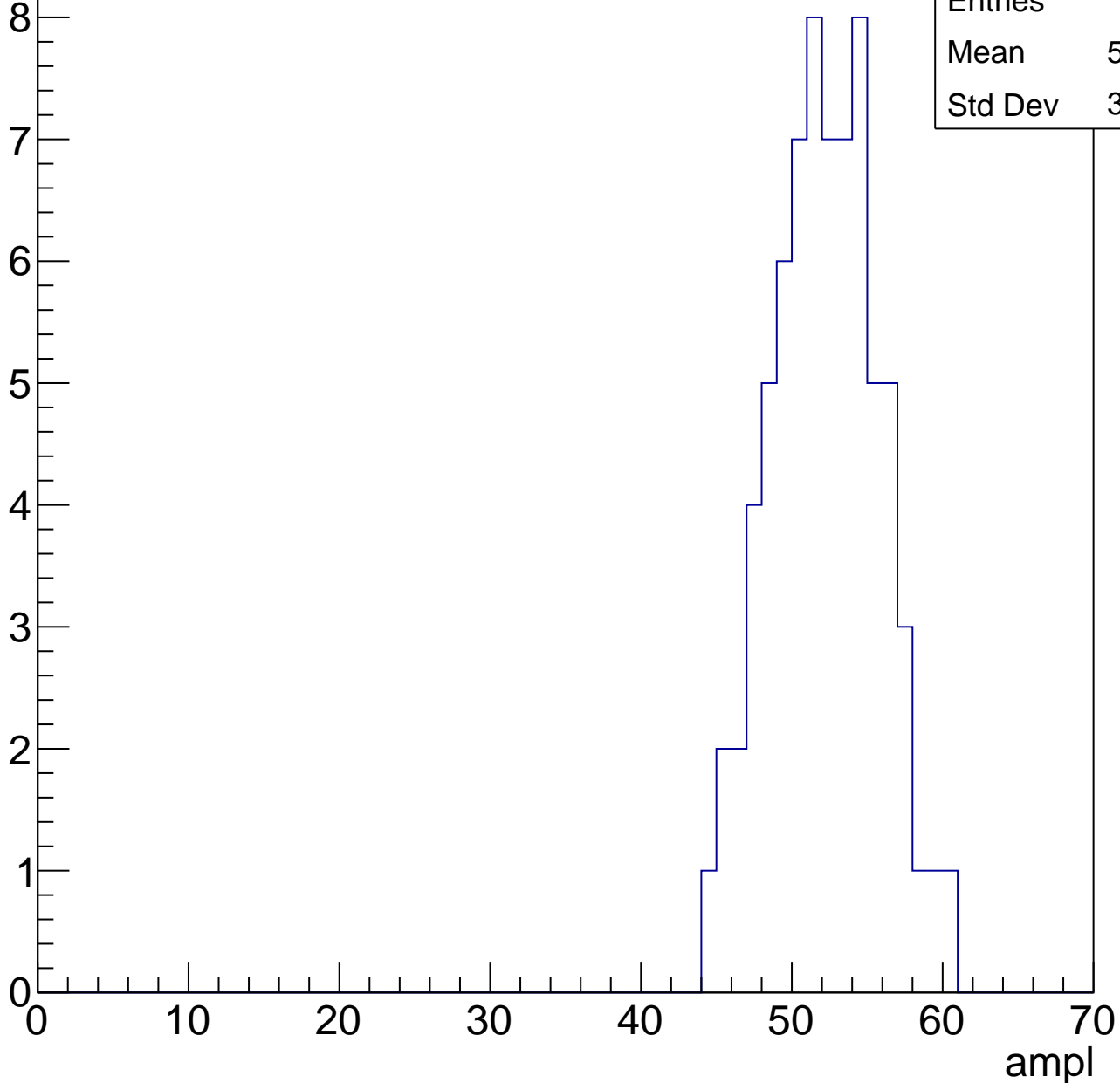


B1L100S, U13-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

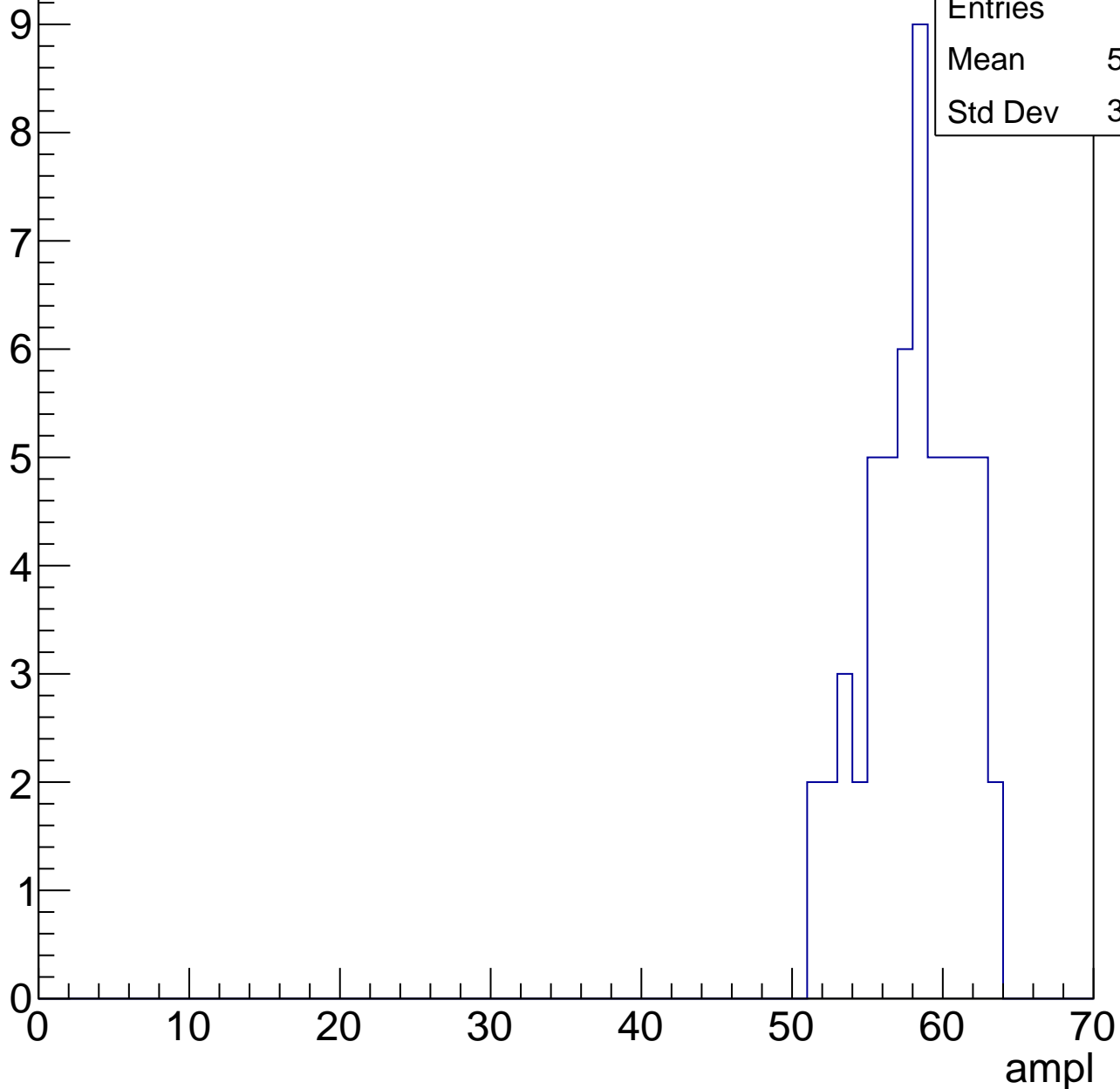
Entries	73
Mean	51.73
Std Dev	3.485



B1L100S, U13-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

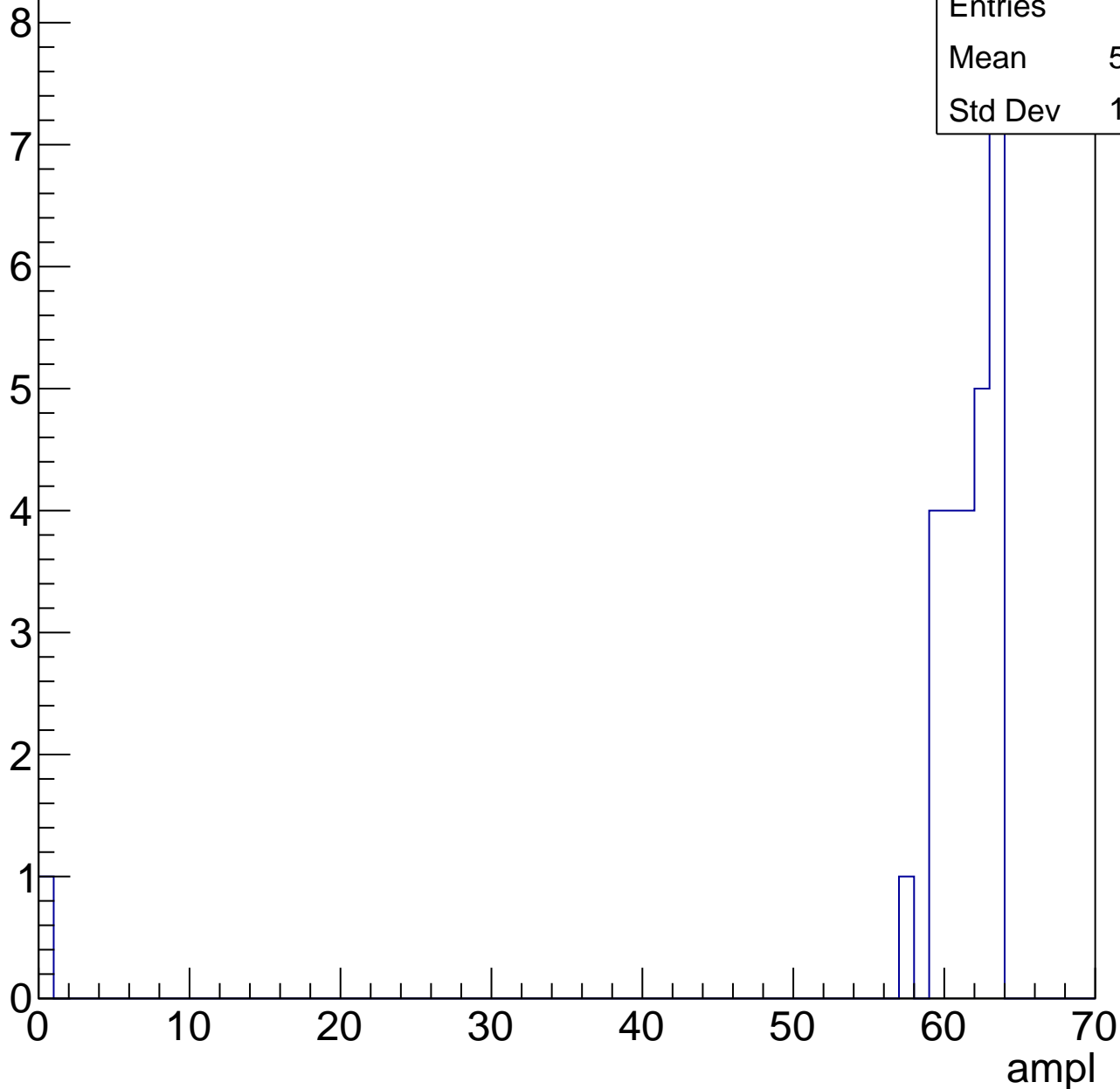


B1L100S, U13-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.93
Std Dev	11.67



B1L100S, U13-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch108, adc0

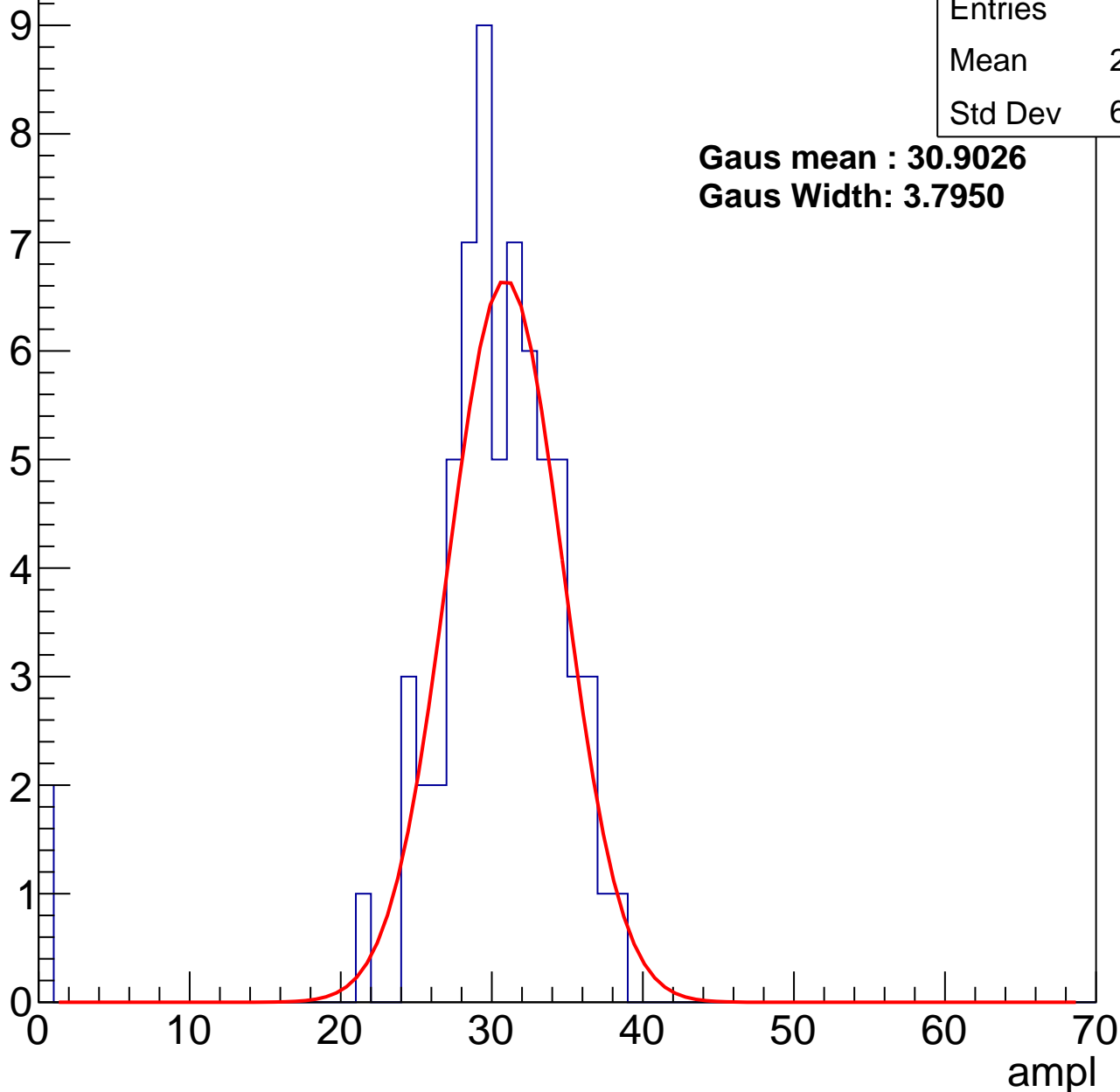
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.39
Std Dev	6.205

Gaus mean : 30.9026

Gaus Width: 3.7950



B1L100S, U13-ch108, adc1

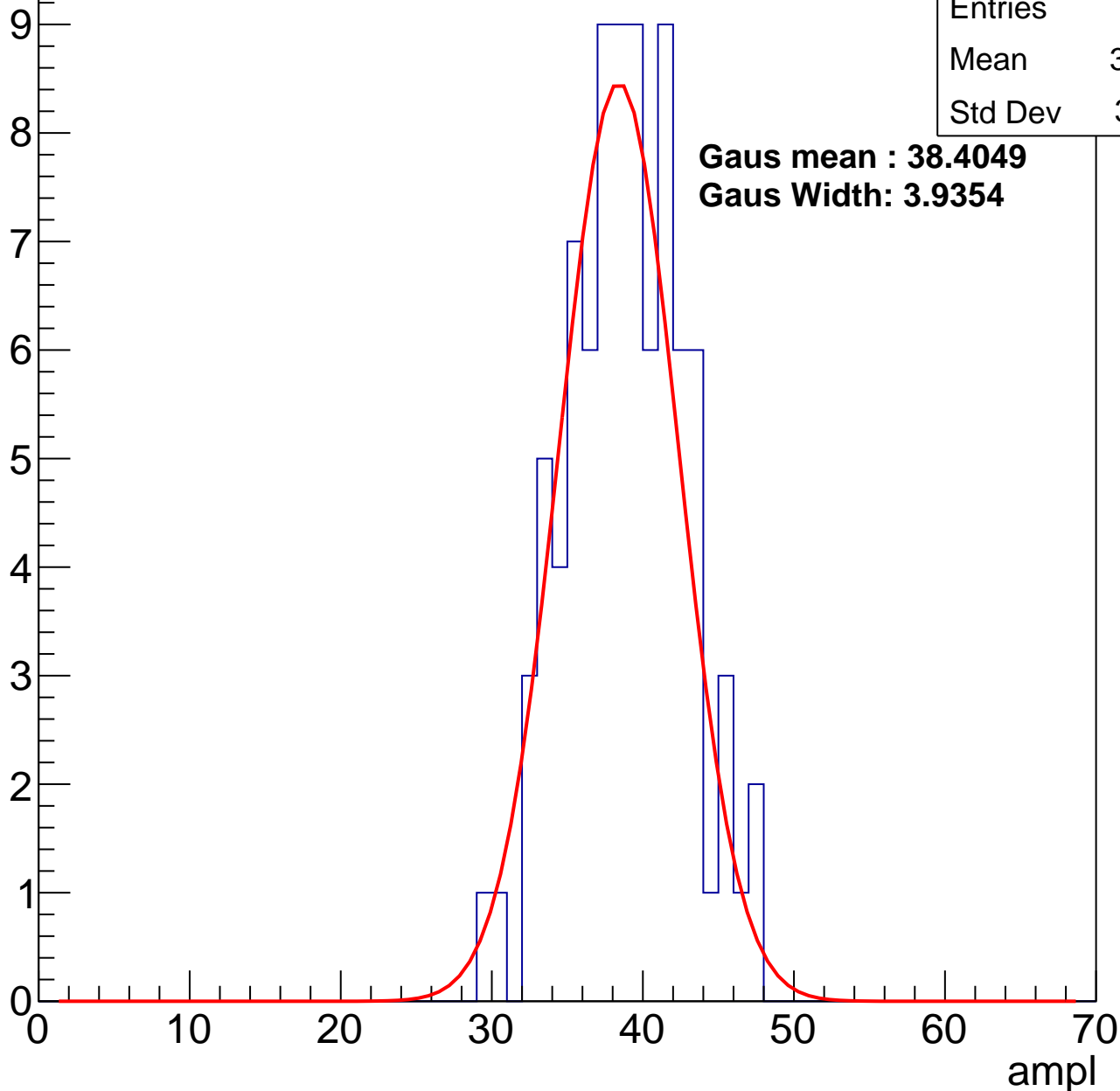
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	38.42
Std Dev	3.831

Gaus mean : 38.4049

Gaus Width: 3.9354



B1L100S, U13-ch108, adc2

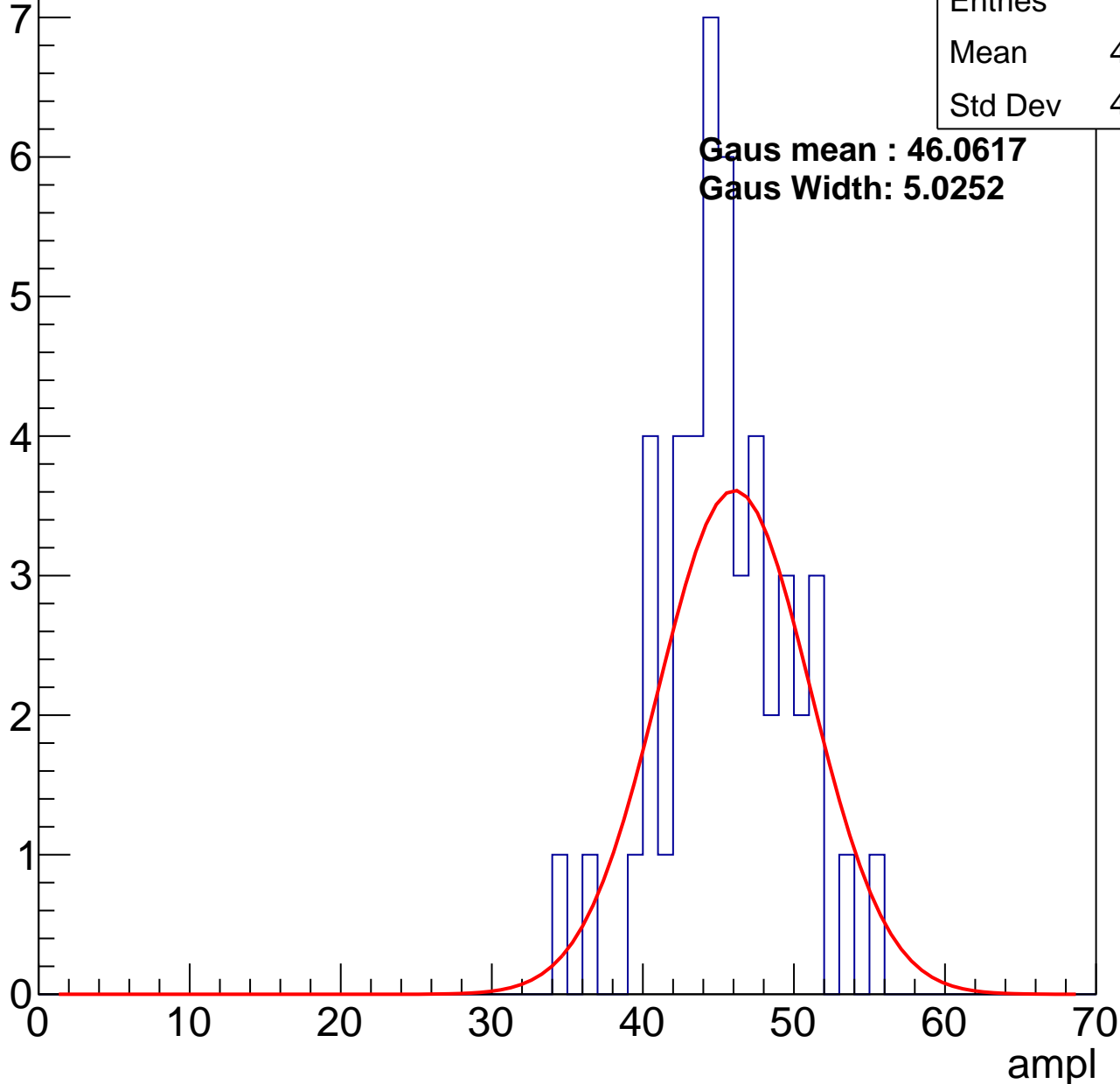
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	44.96
Std Dev	4.133

Gaus mean : 46.0617

Gaus Width: 5.0252

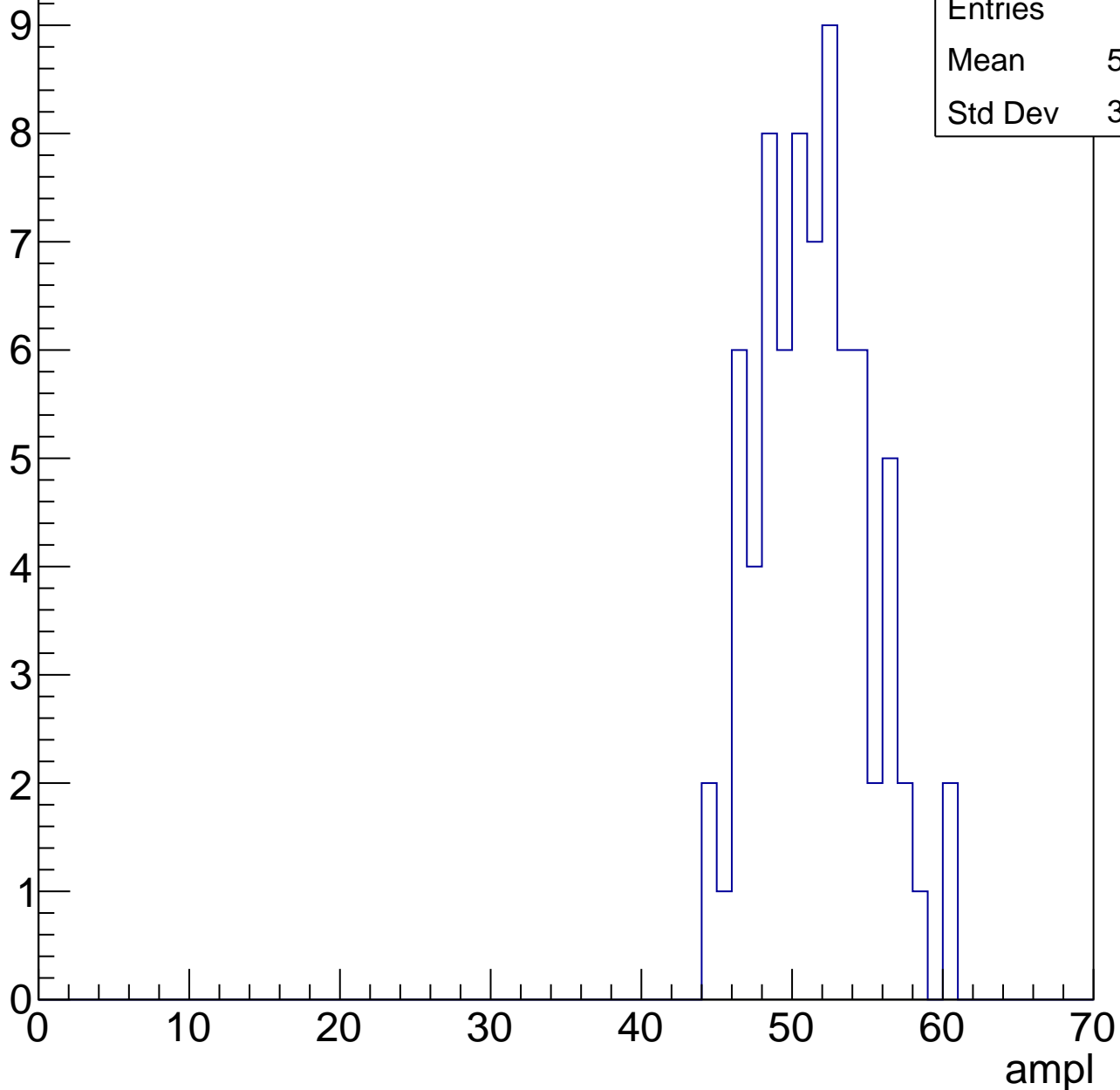


B1L100S, U13-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	50.99
Std Dev	3.613

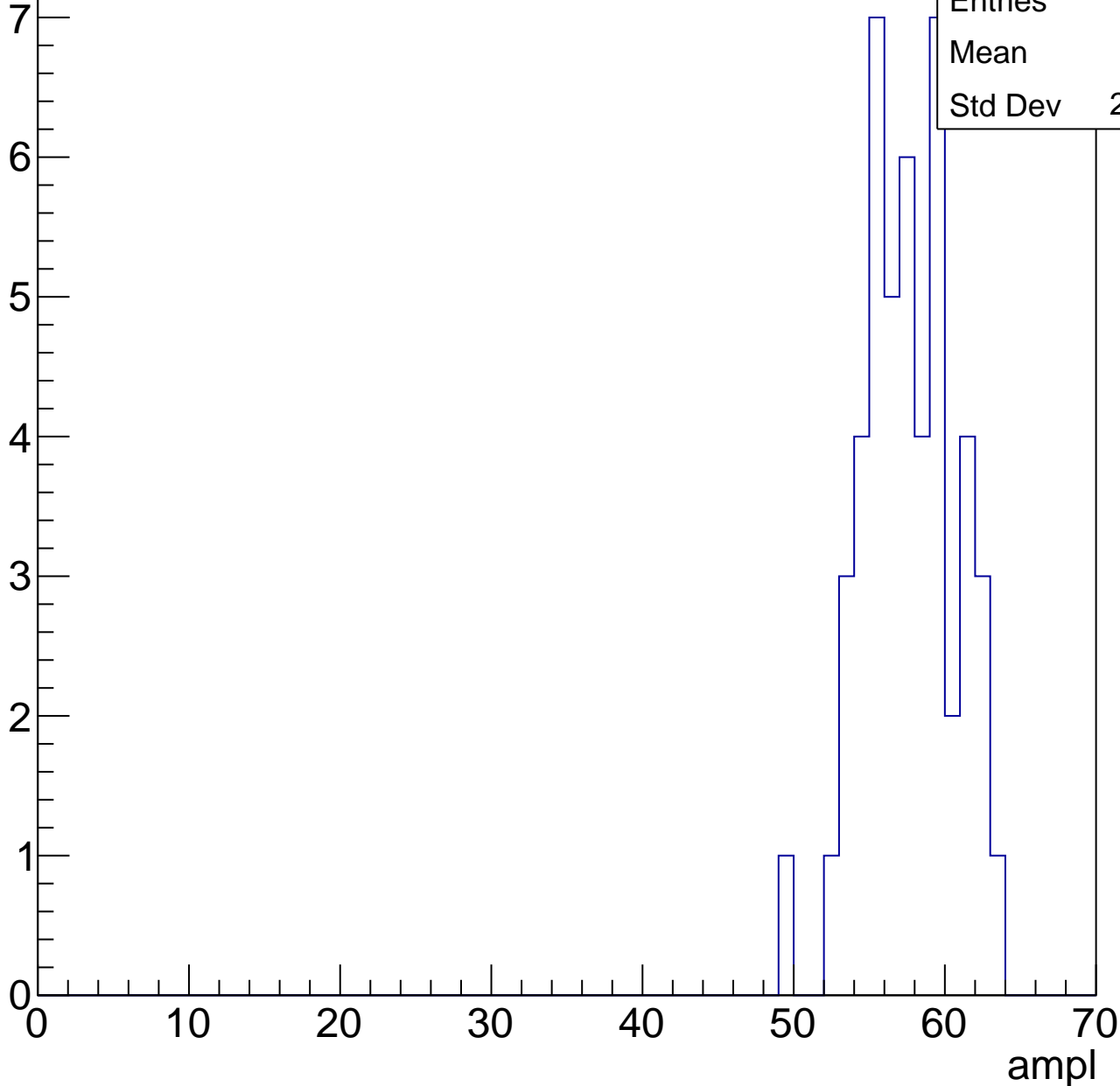


B1L100S, U13-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

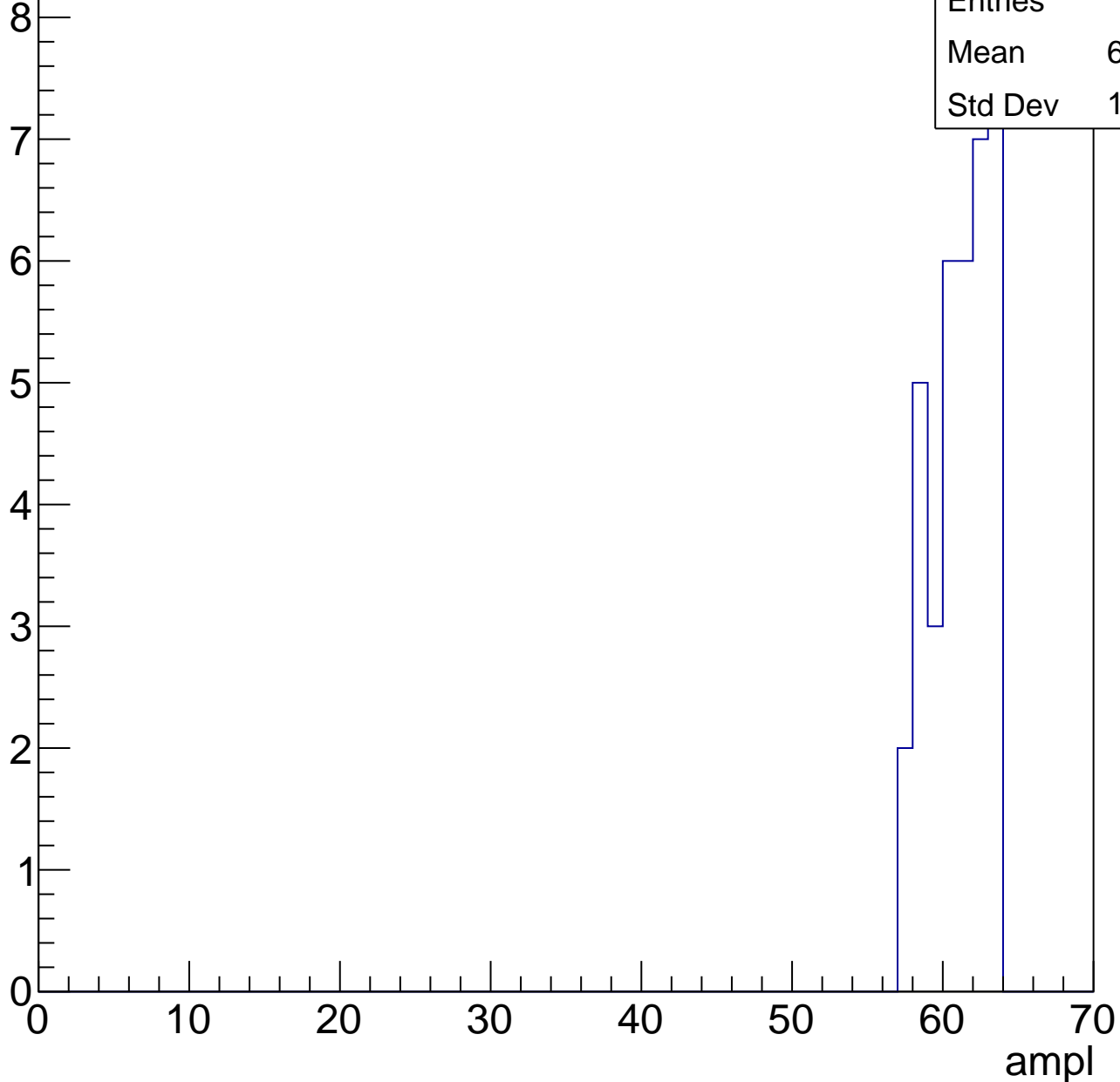
Entries	48
Mean	57.1
Std Dev	2.995



B1L100S, U13-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch109, adc0

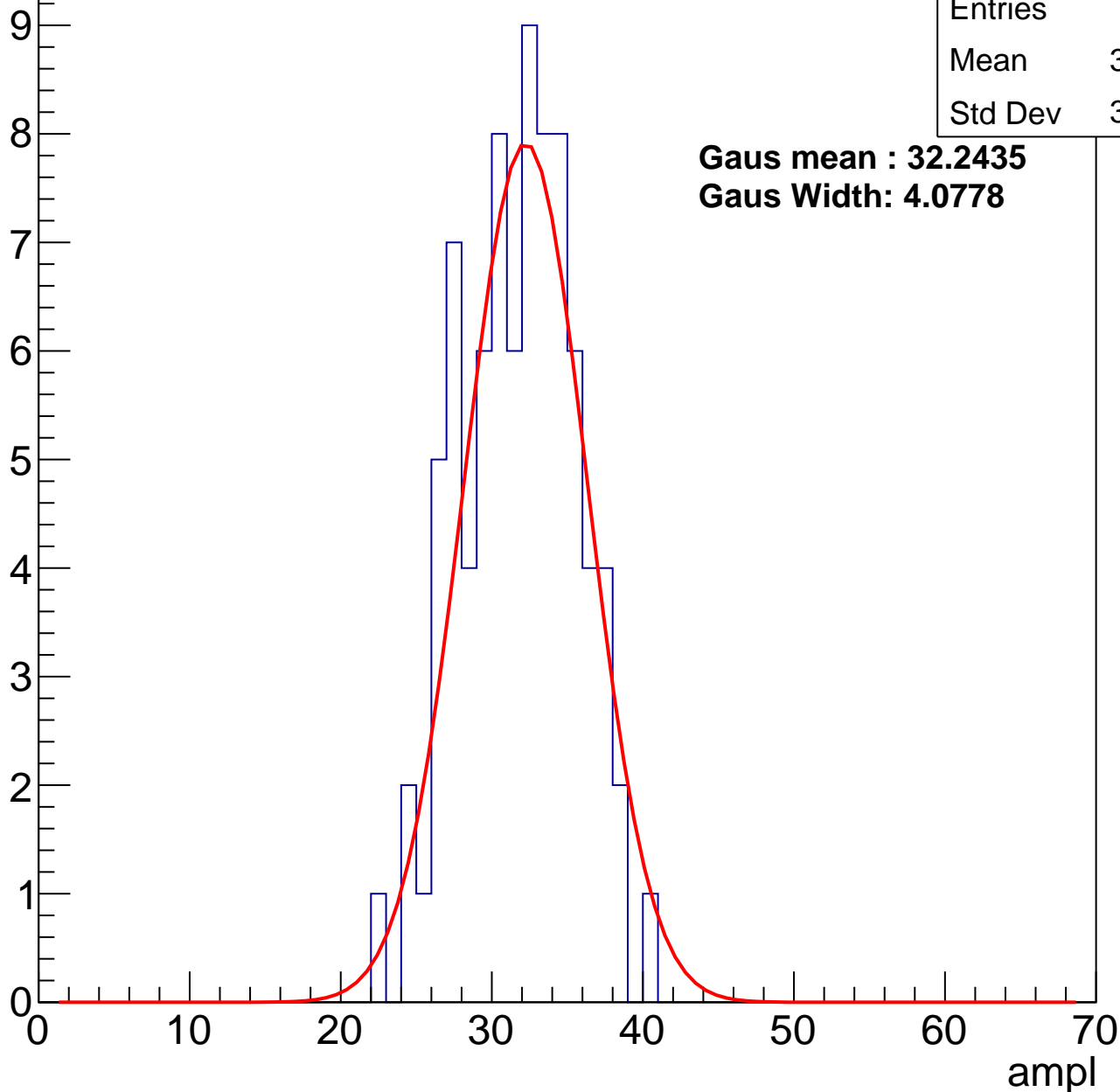
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	31.32
Std Dev	3.735

Gaus mean : 32.2435

Gaus Width: 4.0778



B1L100S, U13-ch109, adc1

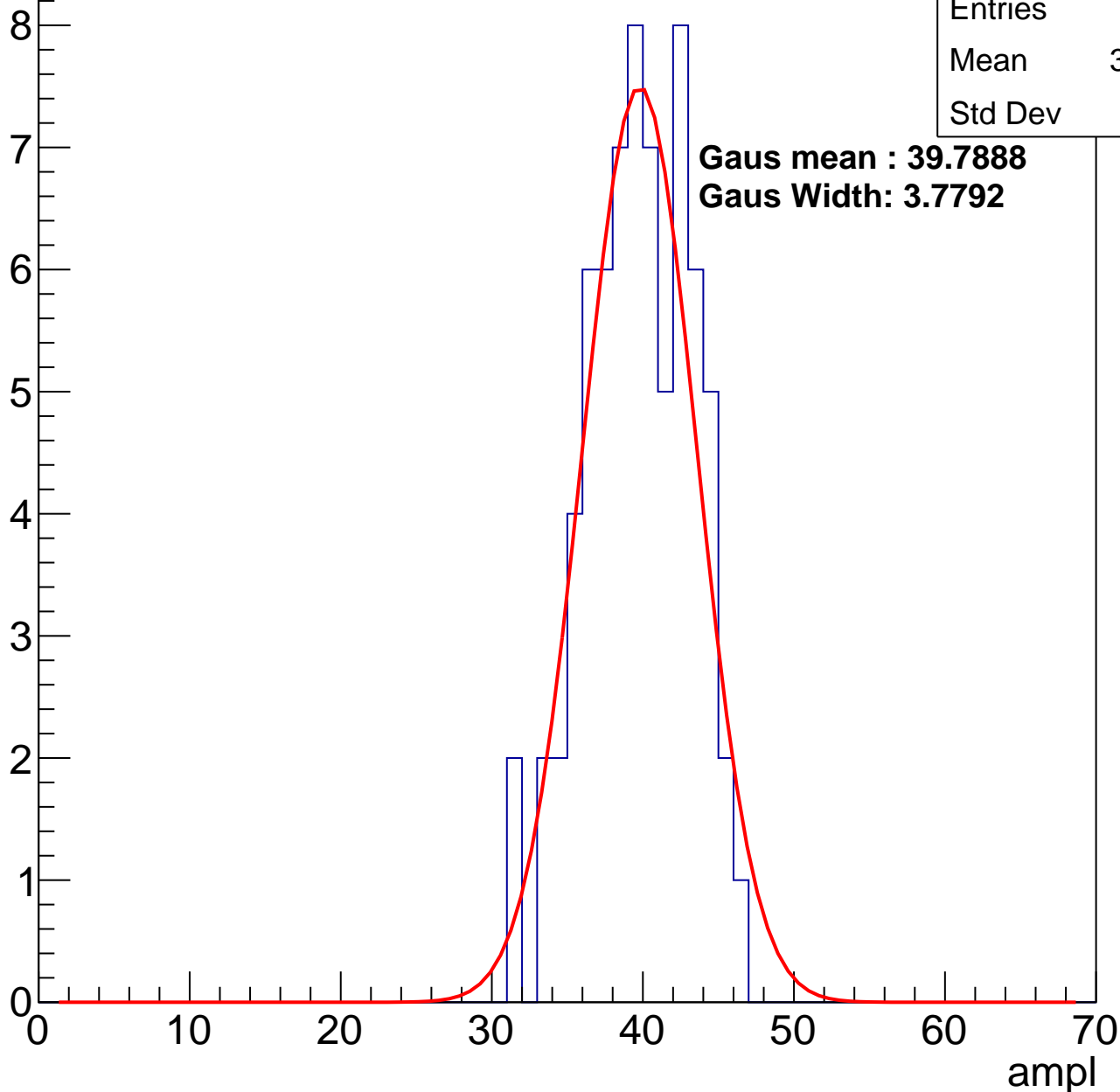
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	39.25
Std Dev	3.43

Gaus mean : 39.7888

Gaus Width: 3.7792



B1L100S, U13-ch109, adc2

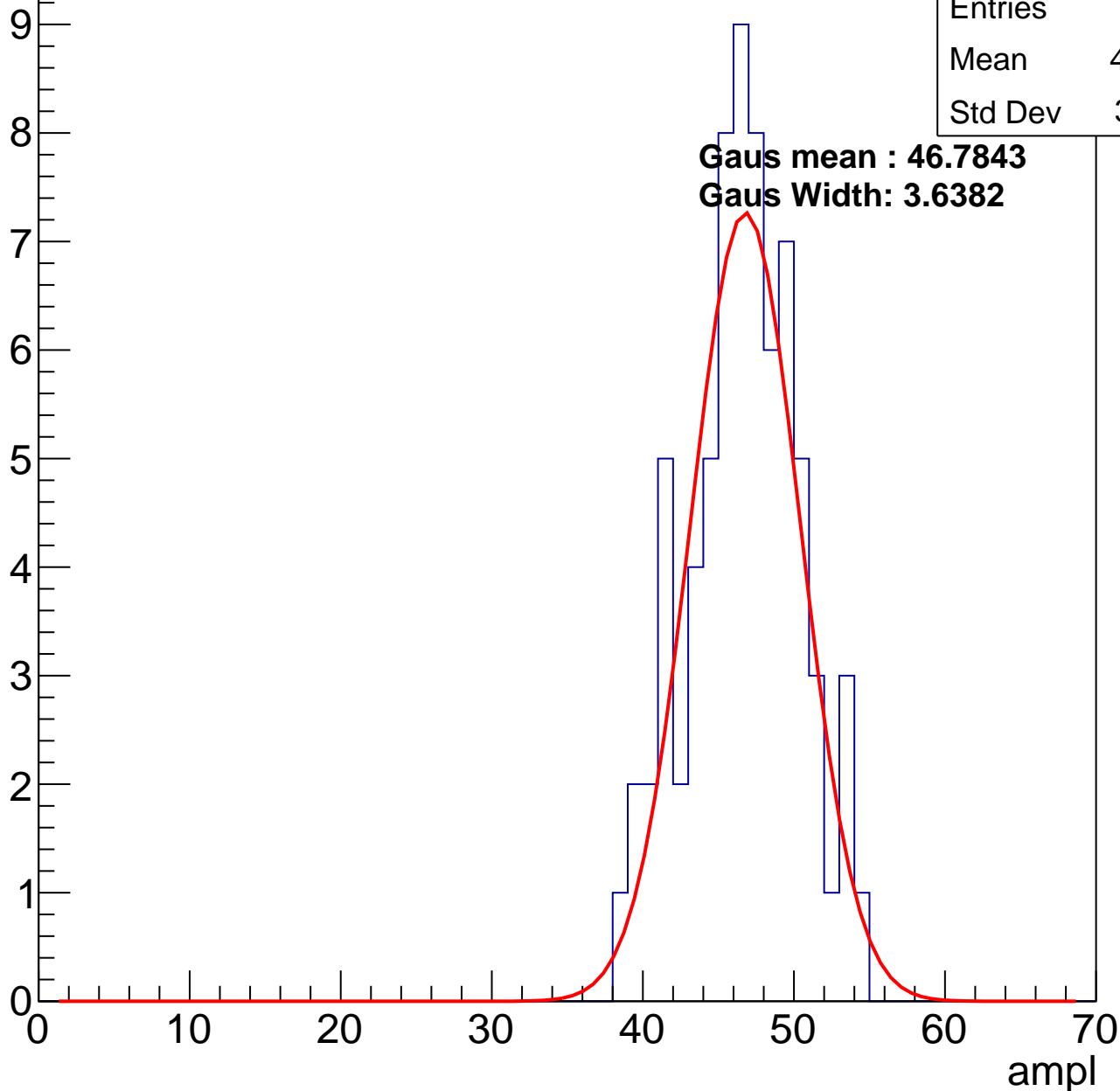
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	46.19
Std Dev	3.631

Gaus mean : 46.7843

Gaus Width: 3.6382

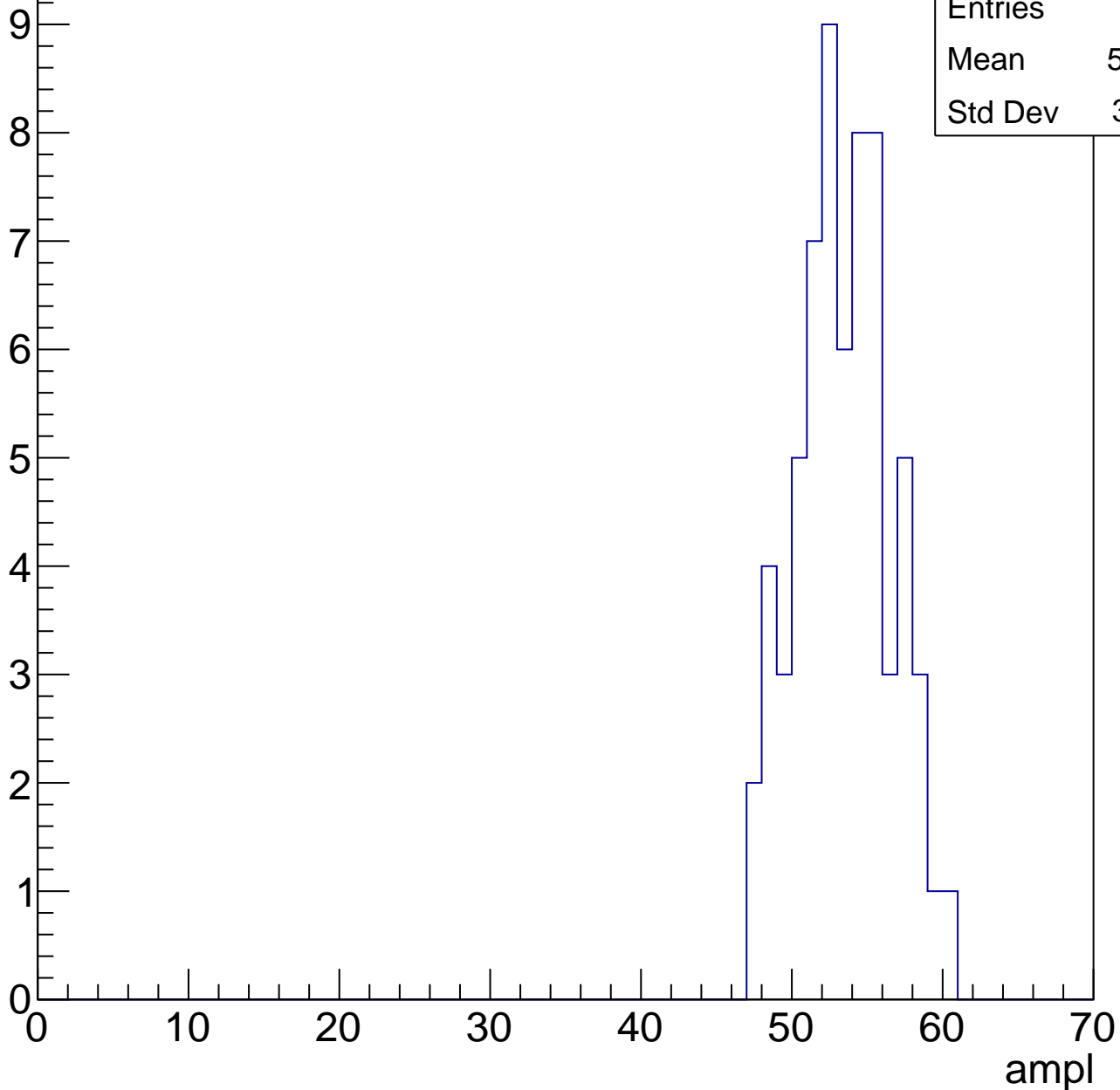


B1L100S, U13-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	52.98
Std Dev	3.061



B1L100S, U13-ch109, adc4

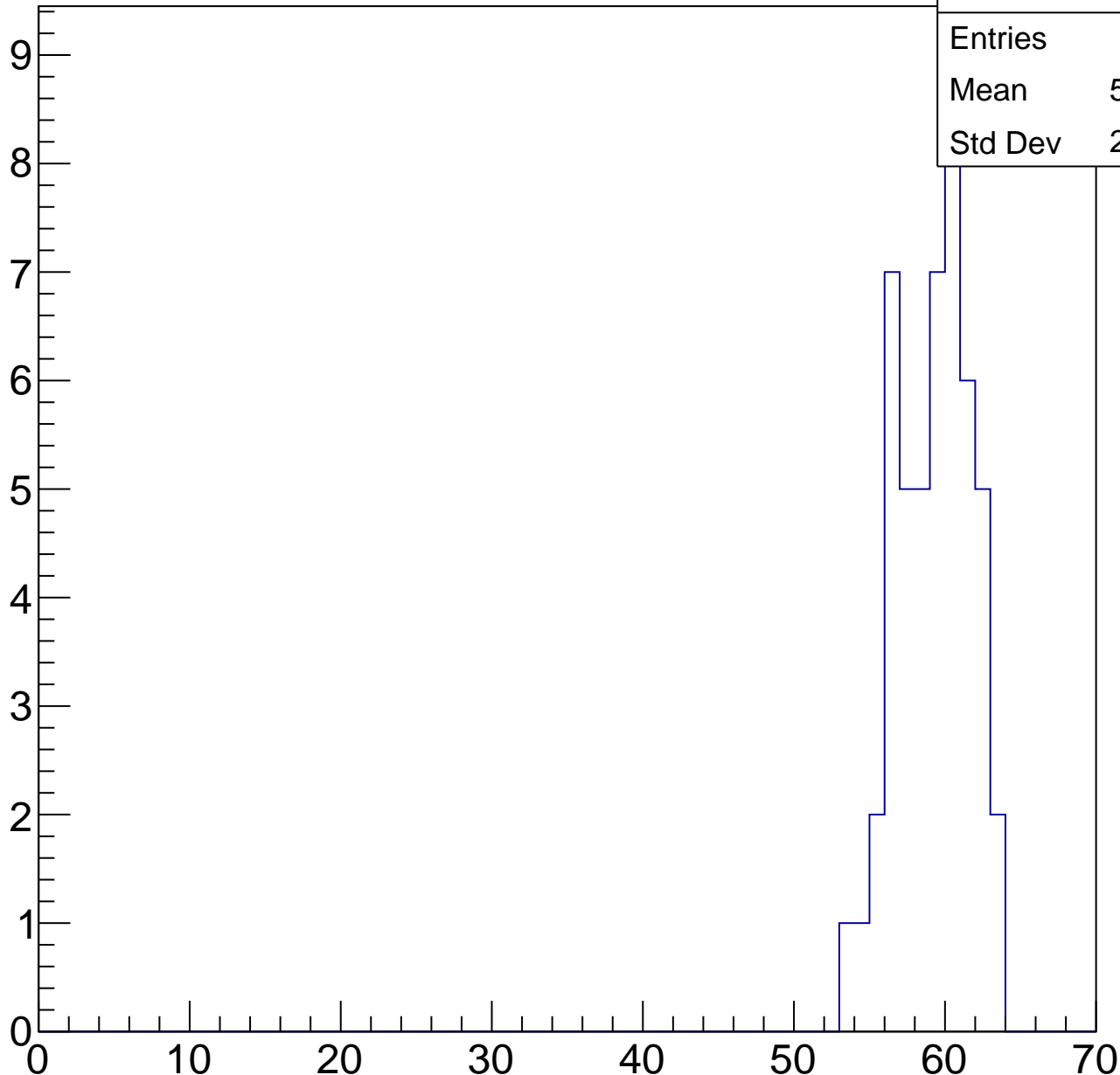
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.78
Std Dev	2.402

ampl

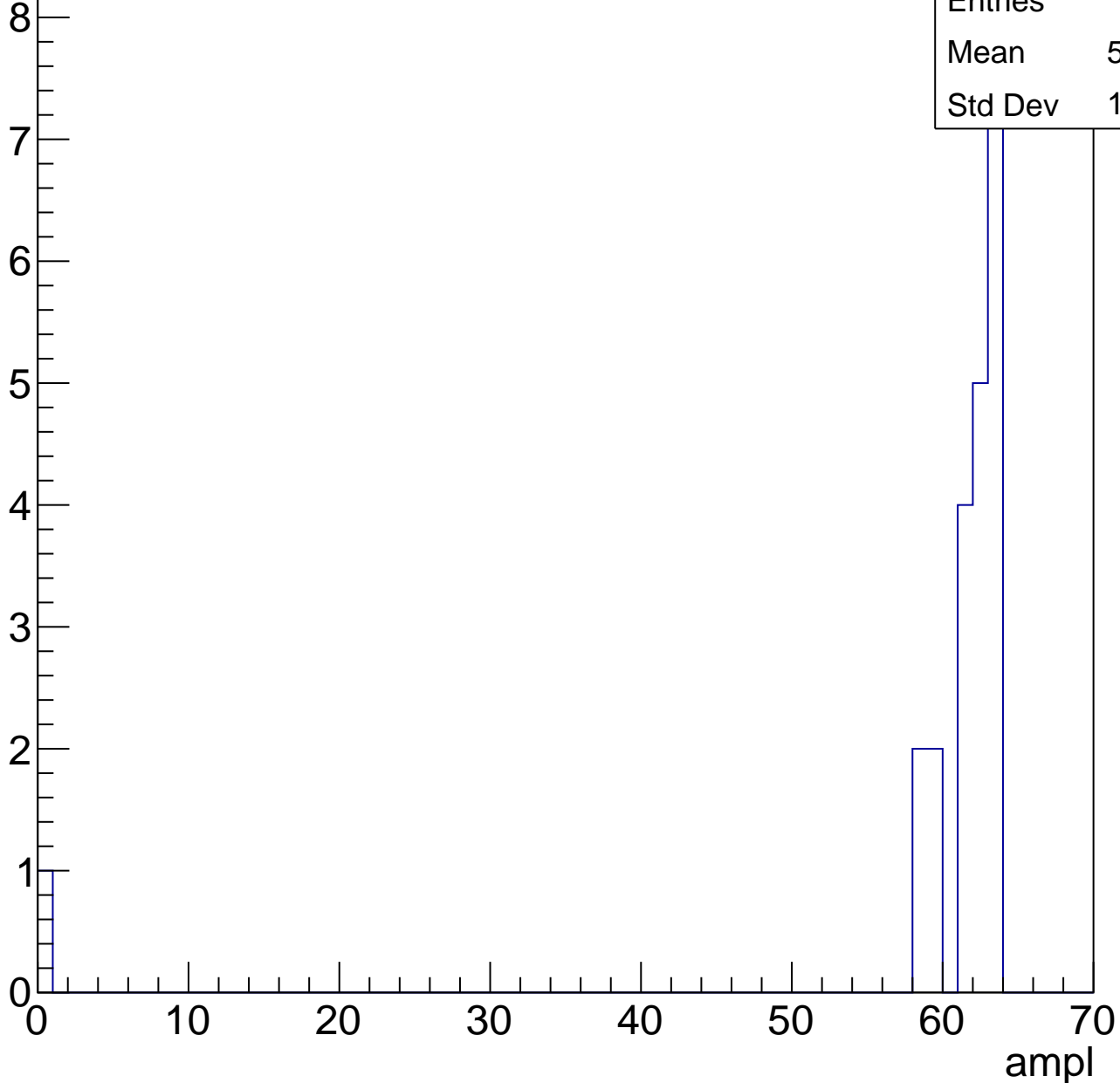


B1L100S, U13-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.73
Std Dev	12.92



B1L100S, U13-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch110, adc0

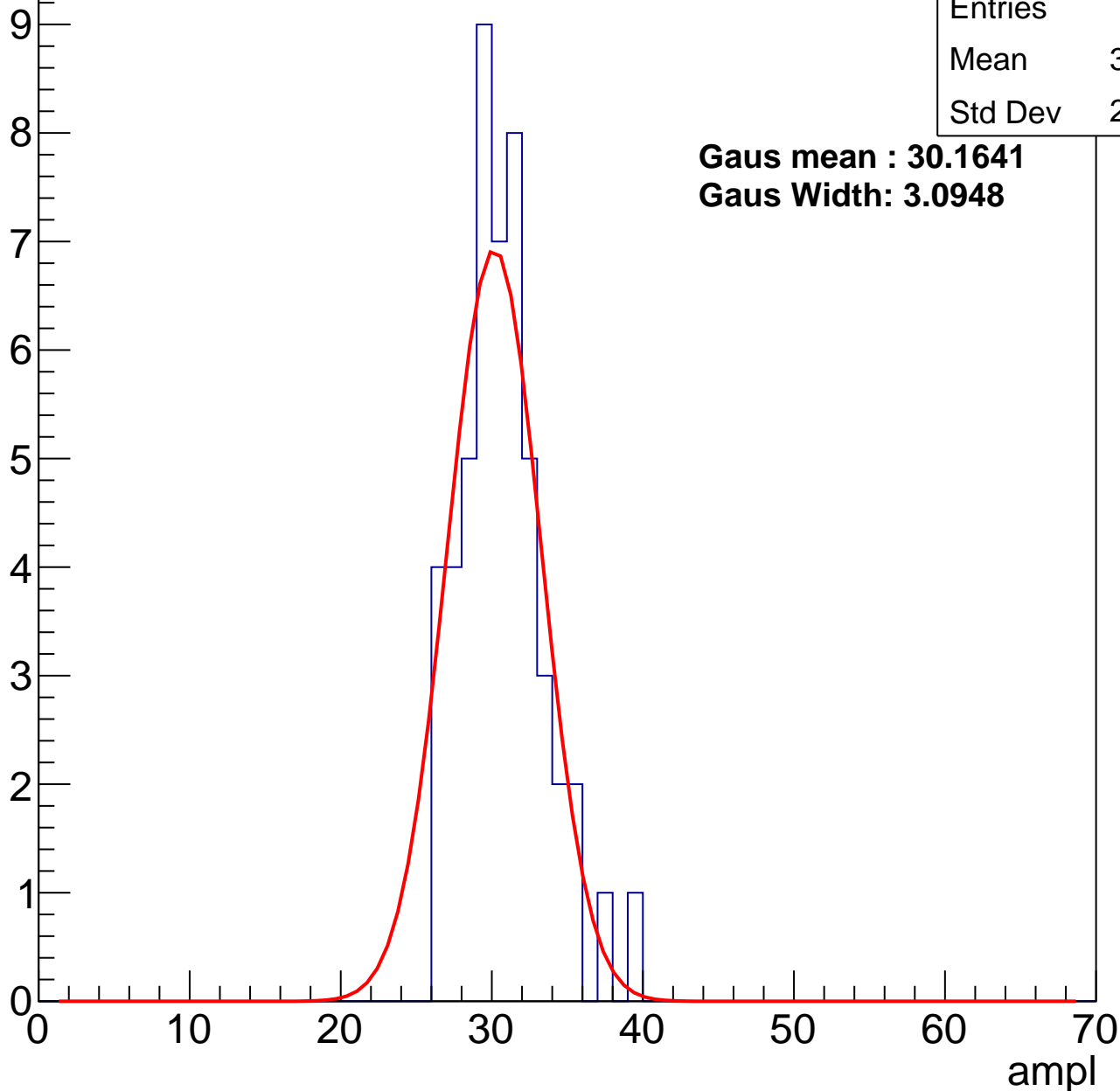
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	30.27
Std Dev	2.773

Gaus mean : 30.1641

Gaus Width: 3.0948



B1L100S, U13-ch110, adc1

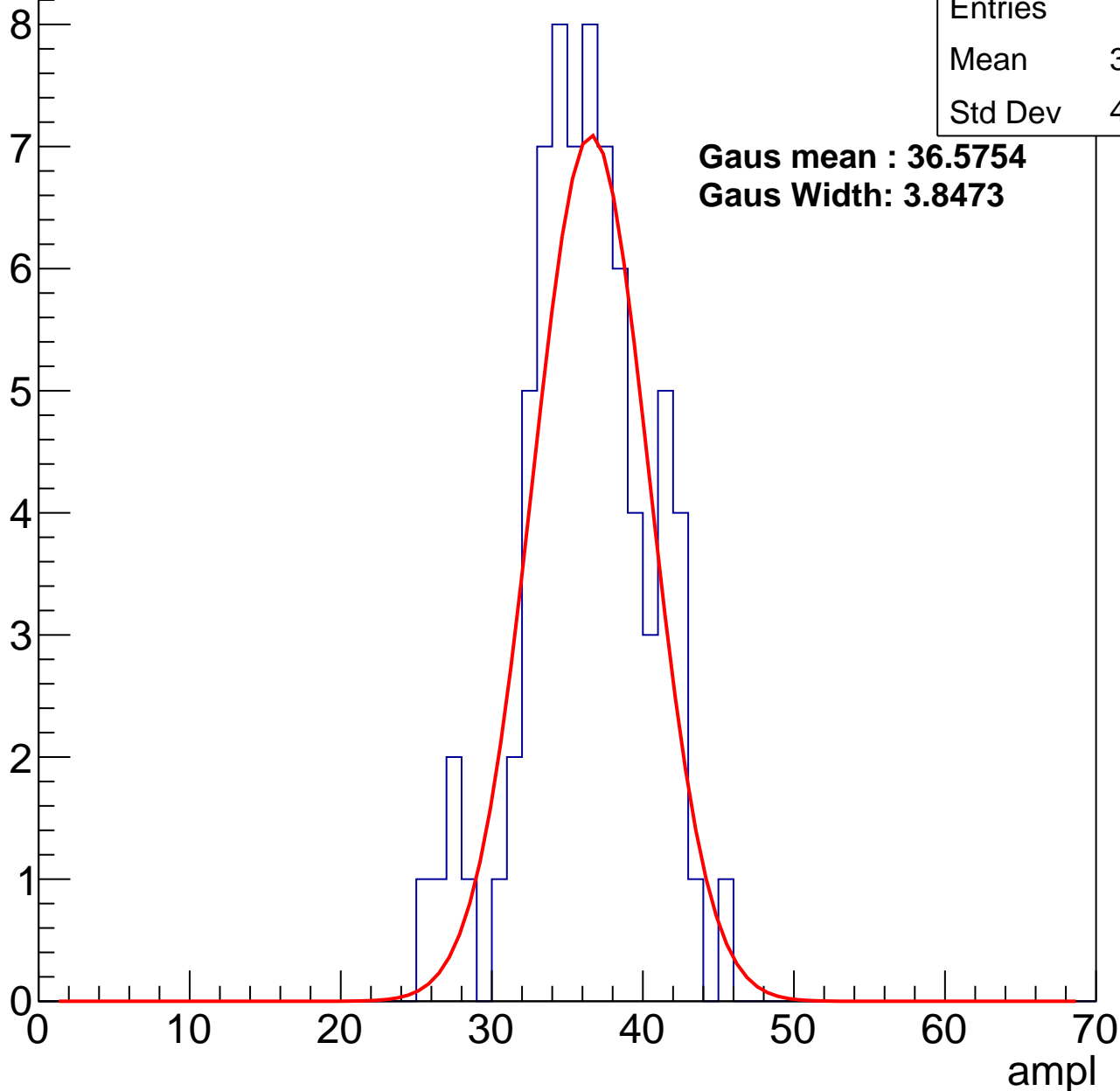
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.74
Std Dev	4.067

Gaus mean : 36.5754

Gaus Width: 3.8473



B1L100S, U13-ch110, adc2

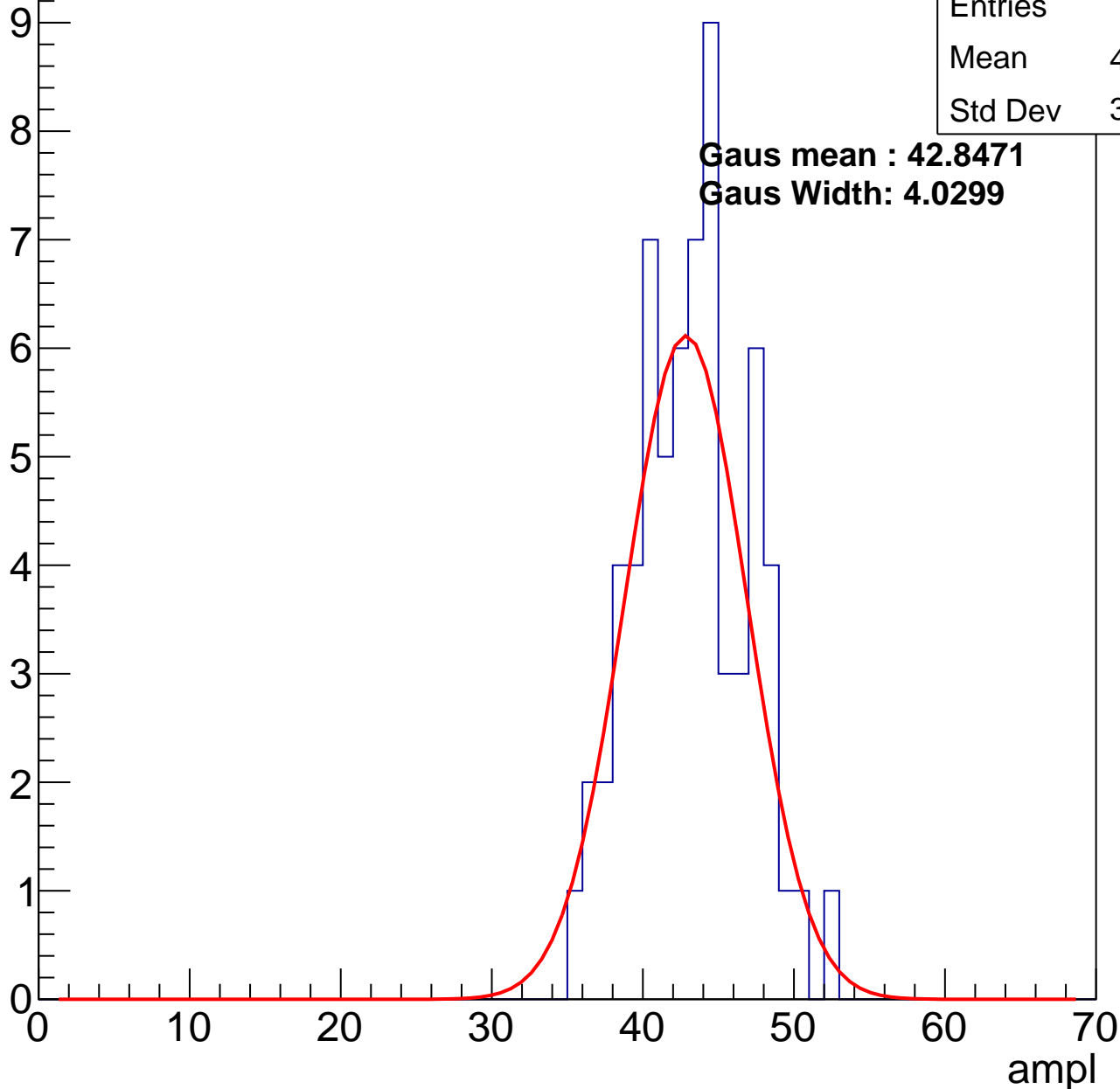
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.74
Std Dev	3.678

Gaus mean : 42.8471

Gaus Width: 4.0299

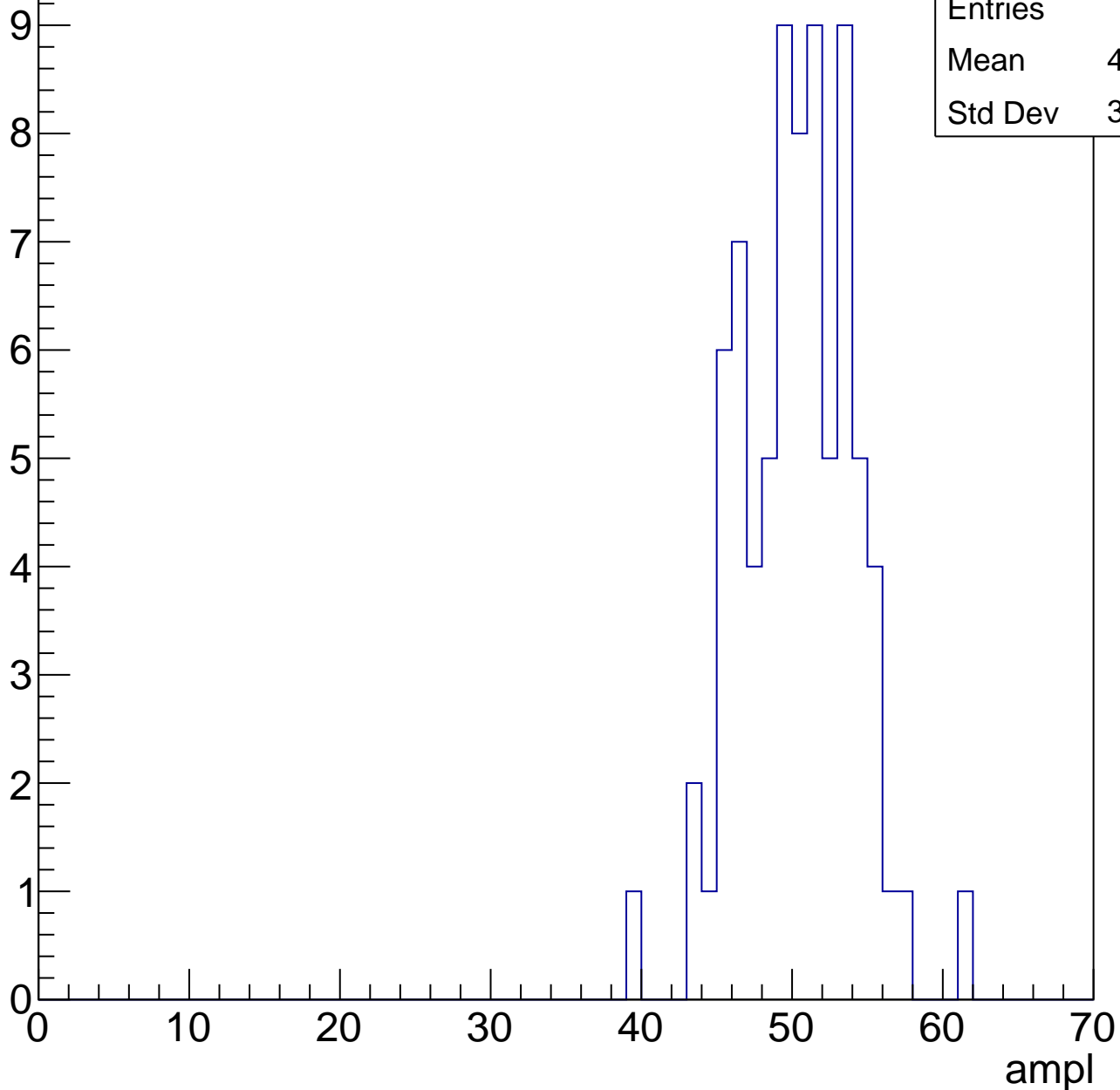


B1L100S, U13-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	49.87
Std Dev	3.715

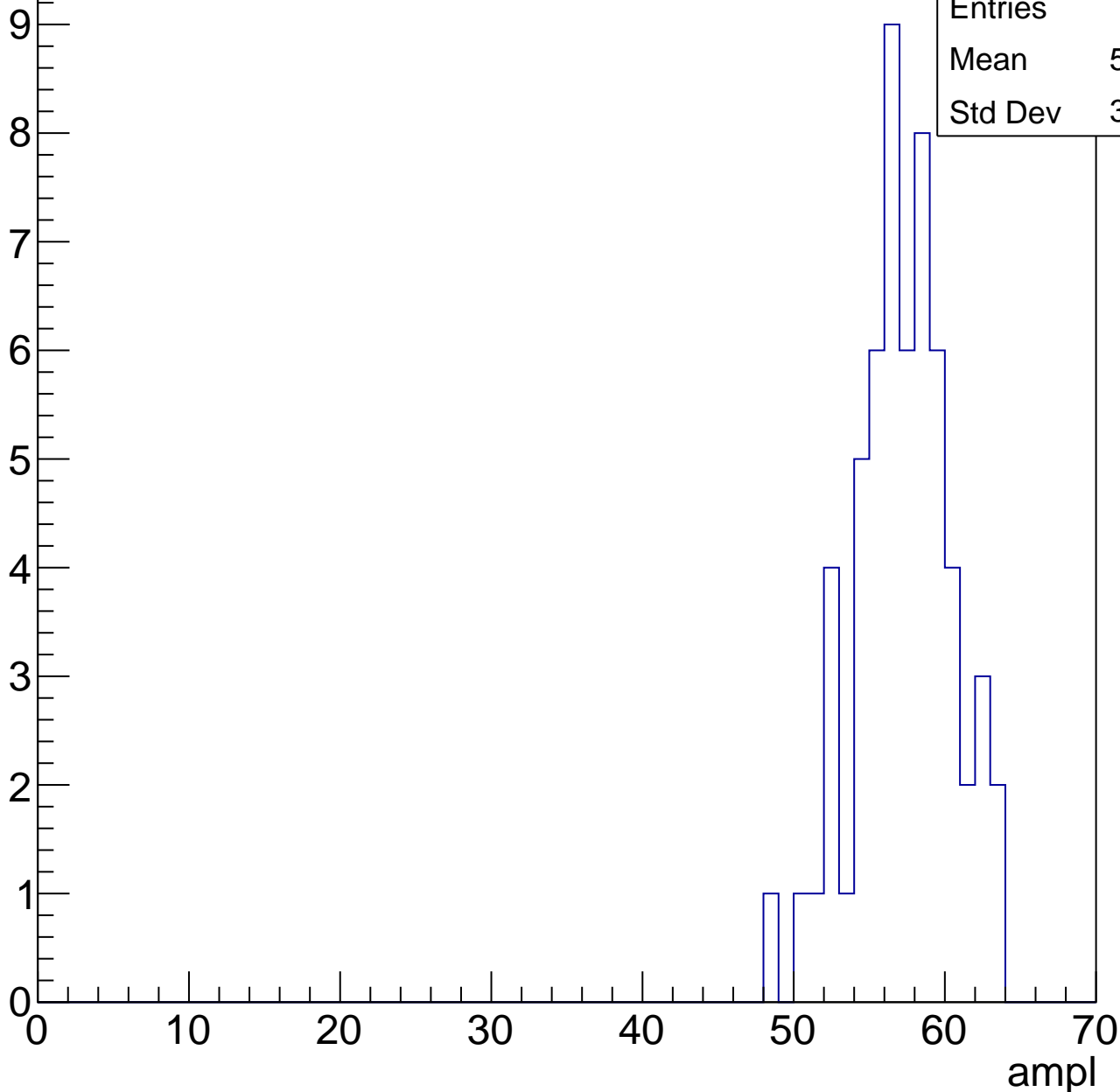


B1L100S, U13-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.75
Std Dev	3.197

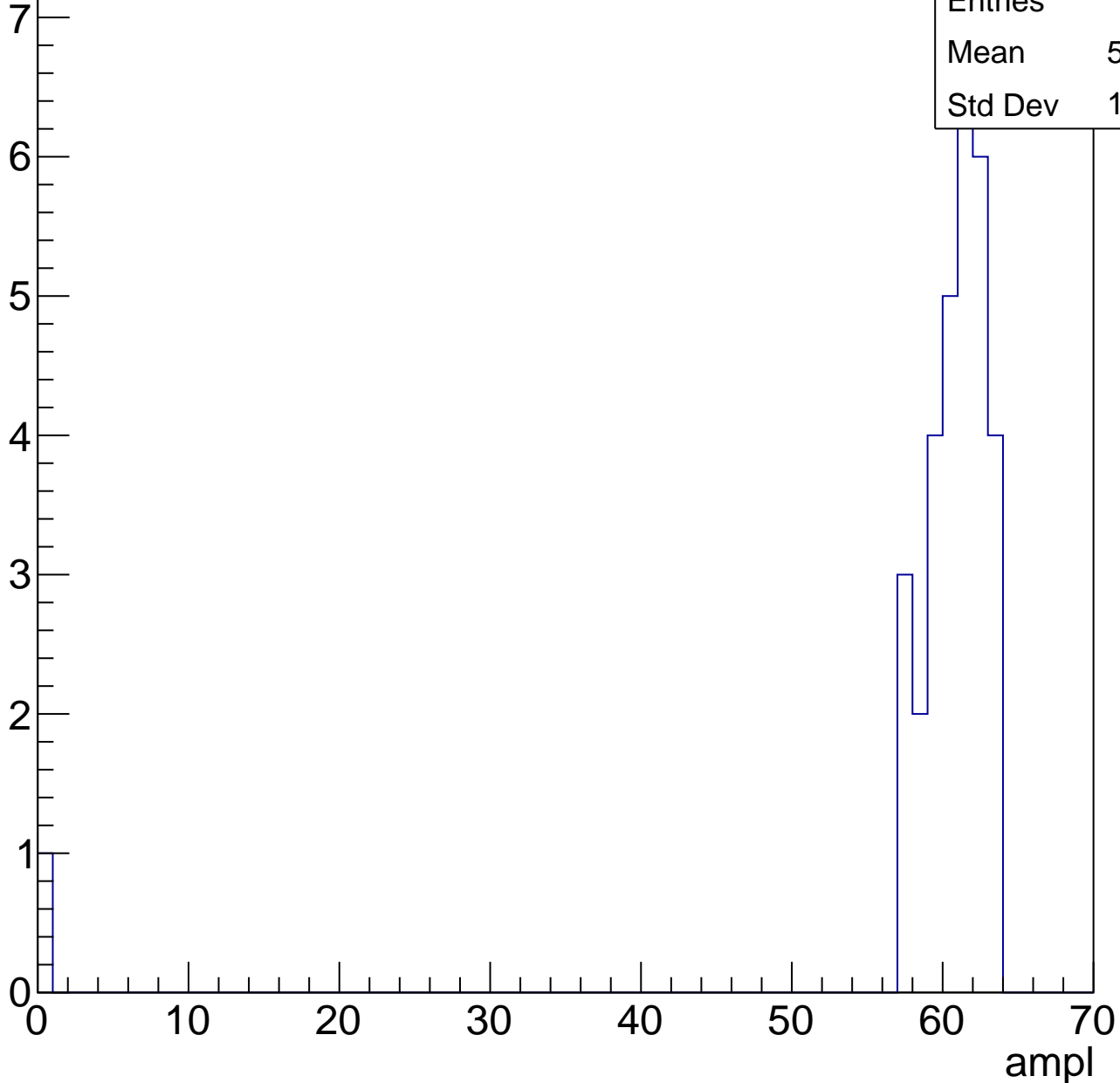


B1L100S, U13-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

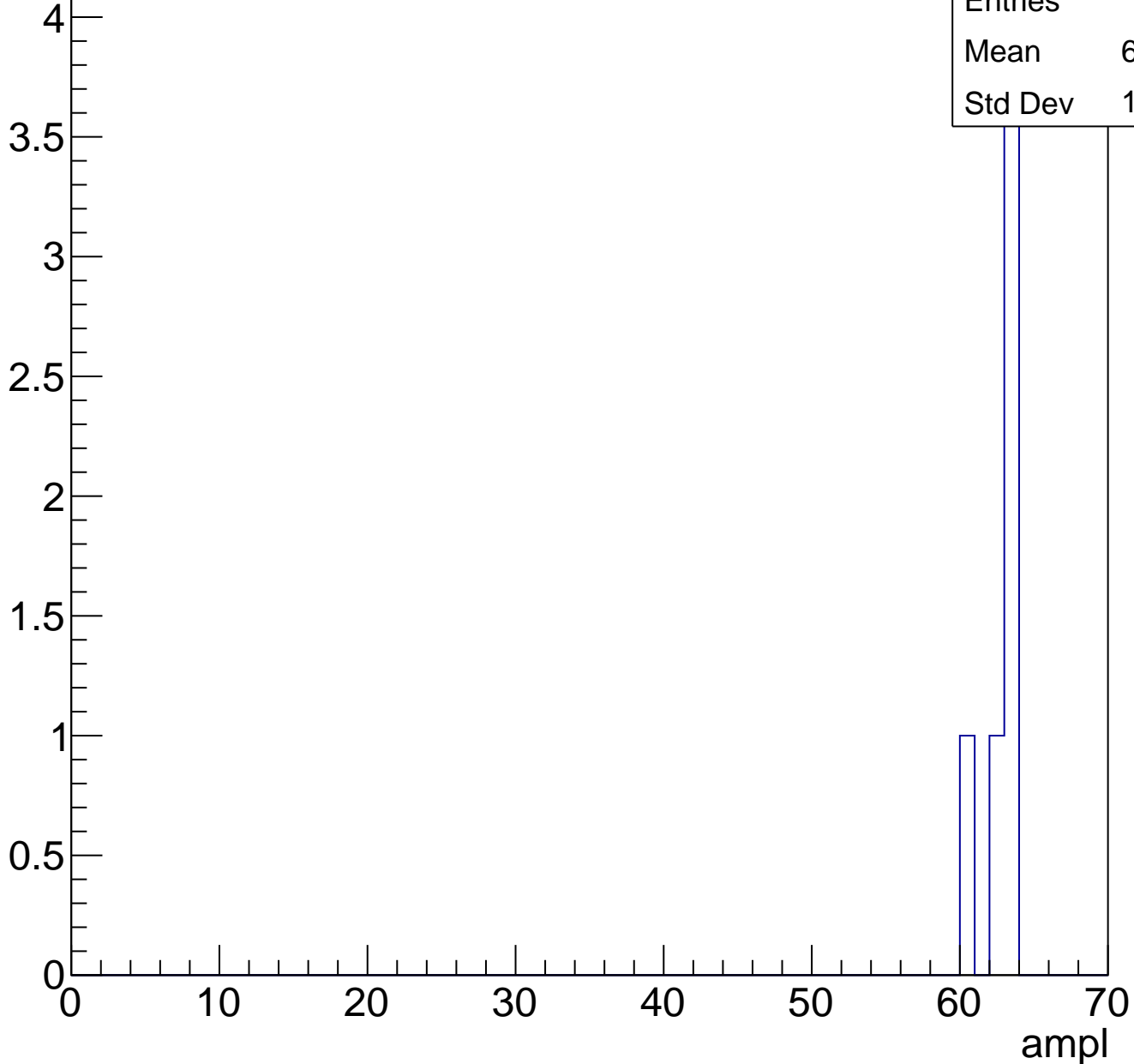
Entries	32
Mean	58.56
Std Dev	10.67



B1L100S, U13-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch111, adc0

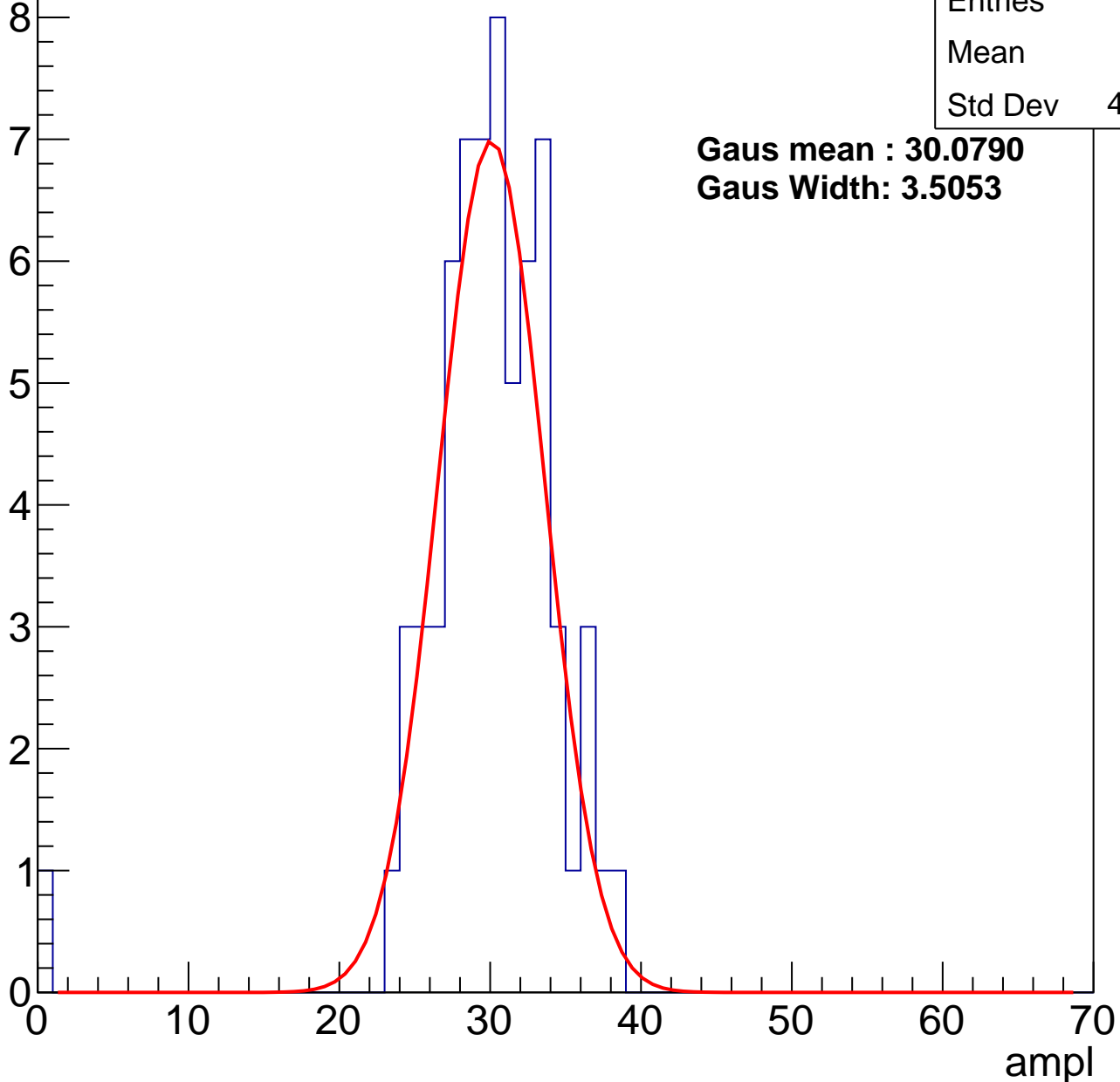
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.5
Std Dev	4.983

Gaus mean : 30.0790

Gaus Width: 3.5053



B1L100S, U13-ch111, adc1

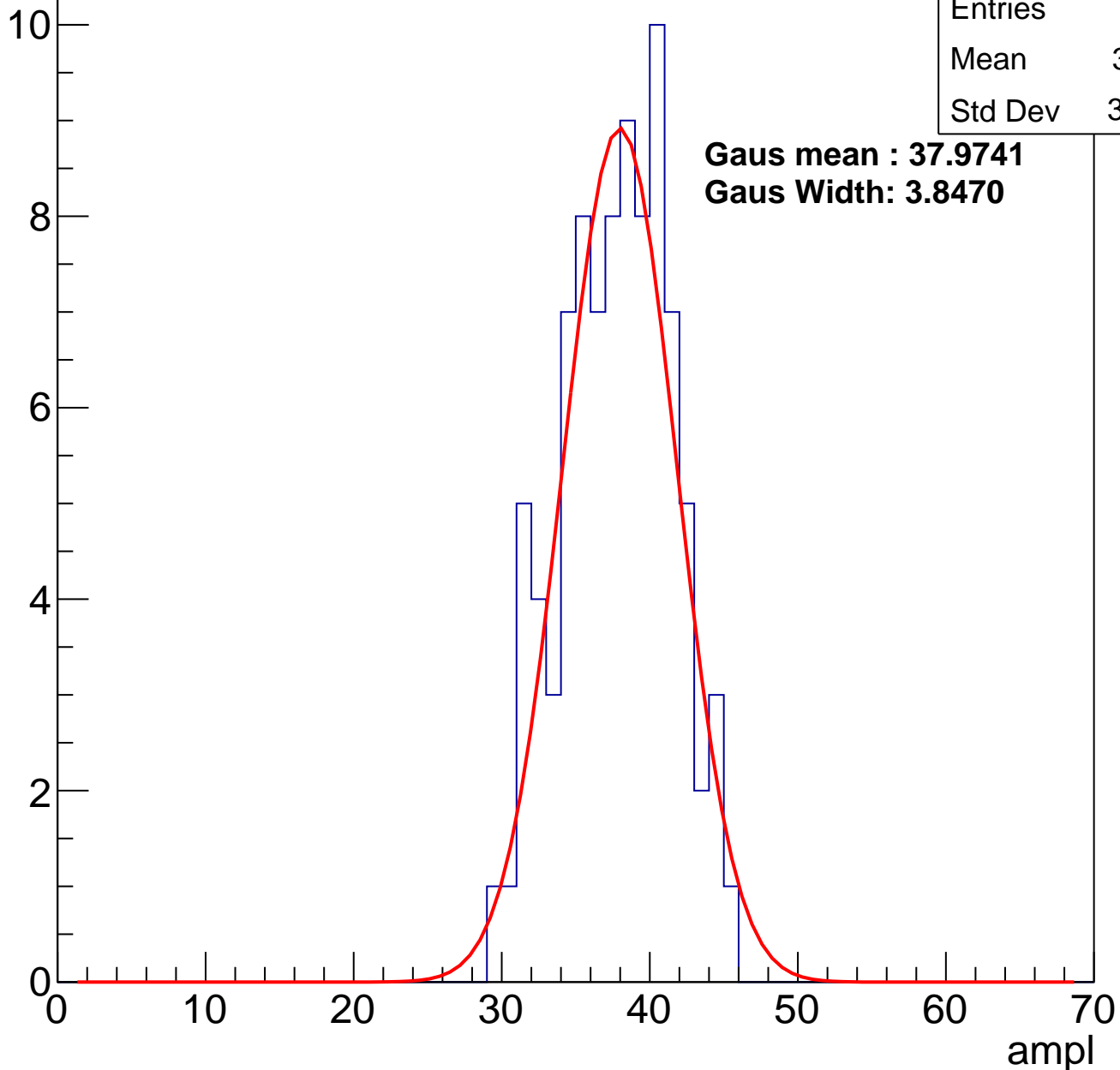
calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	37.31
Std Dev	3.649

Gaus mean : 37.9741

Gaus Width: 3.8470

Entry



B1L100S, U13-ch111, adc2

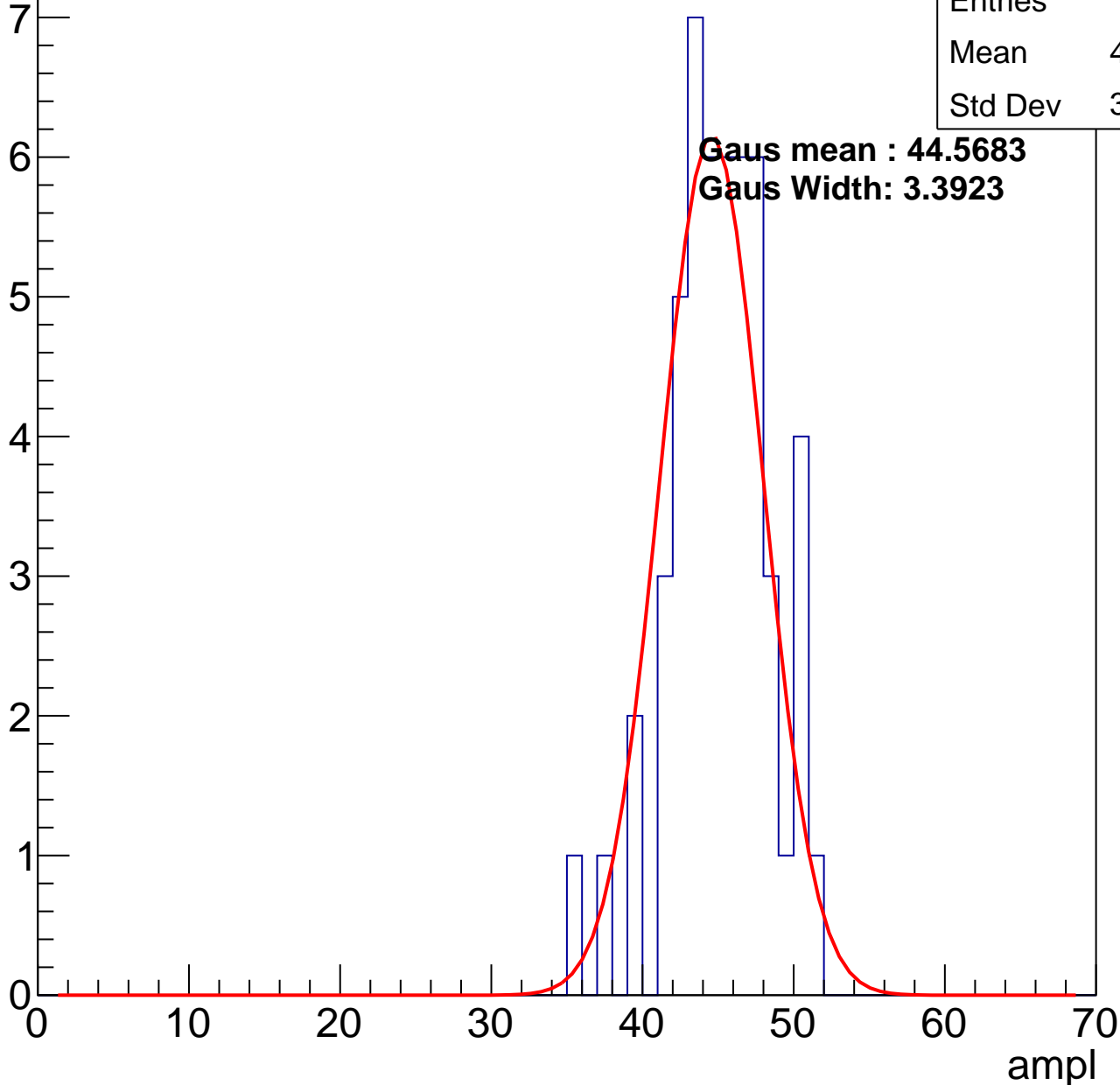
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	44.62
Std Dev	3.294

Gaus mean : 44.5683

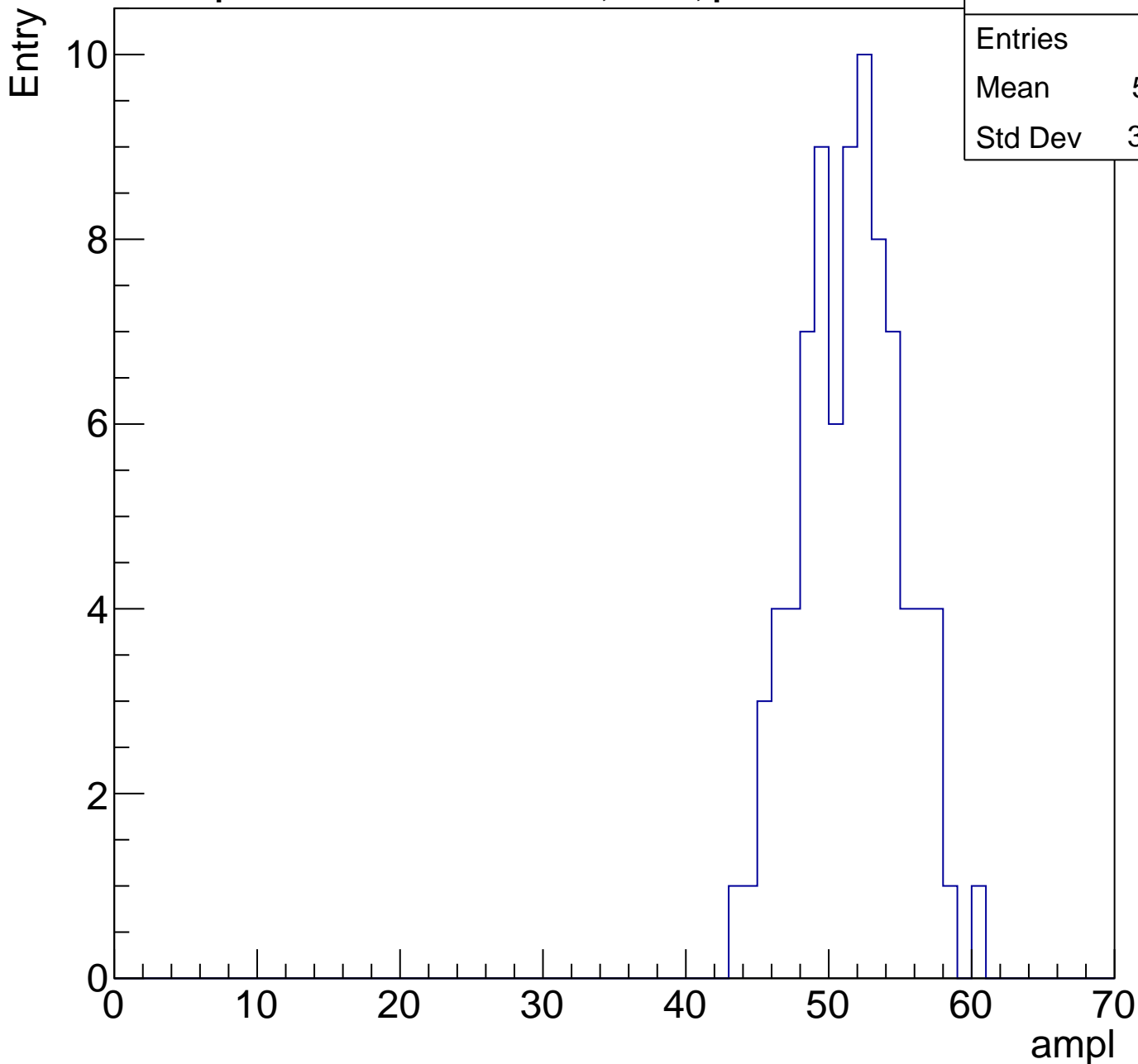
Gaus Width: 3.3923



B1L100S, U13-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	51.11
Std Dev	3.526

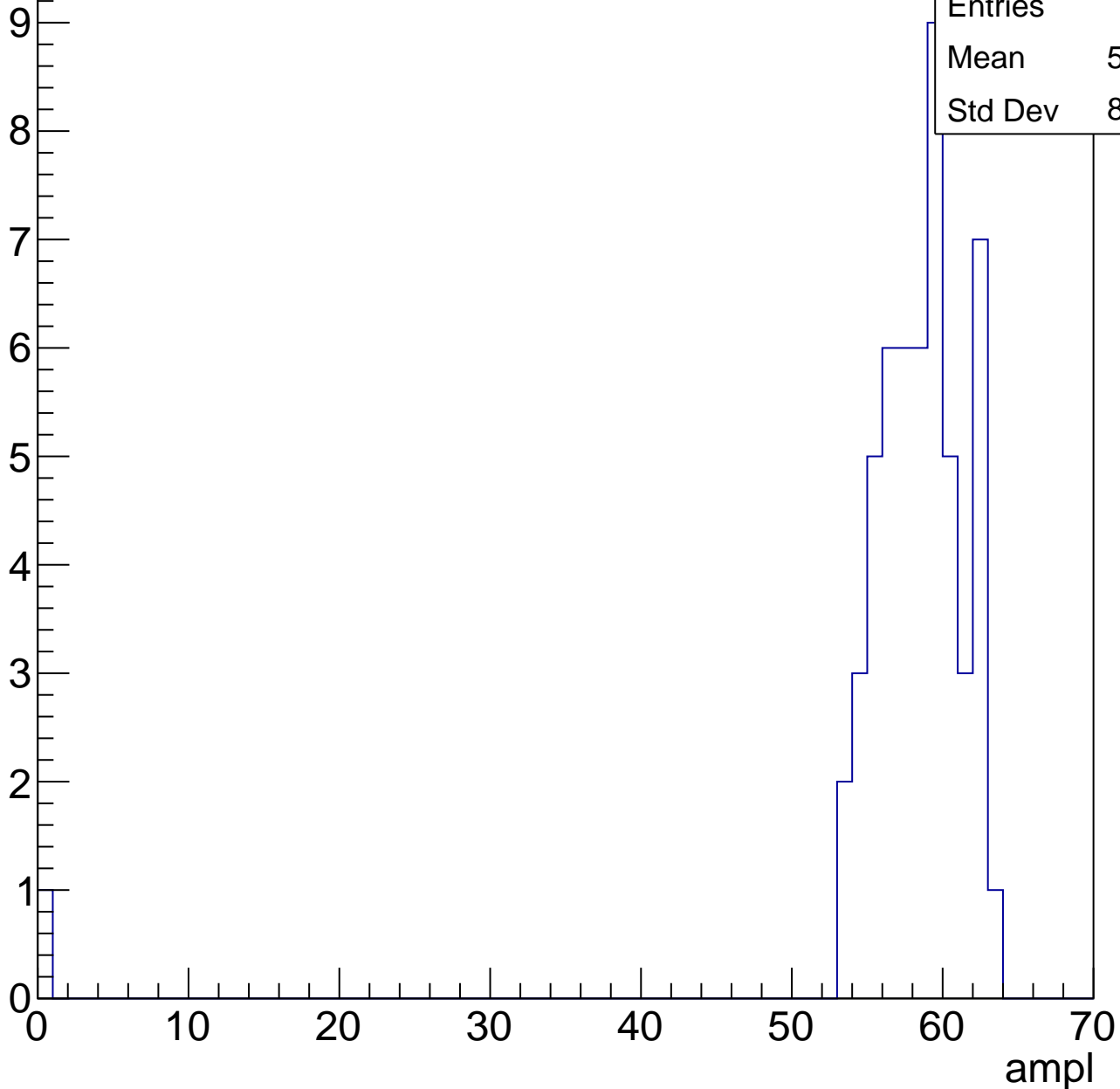


B1L100S, U13-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.04
Std Dev	8.255

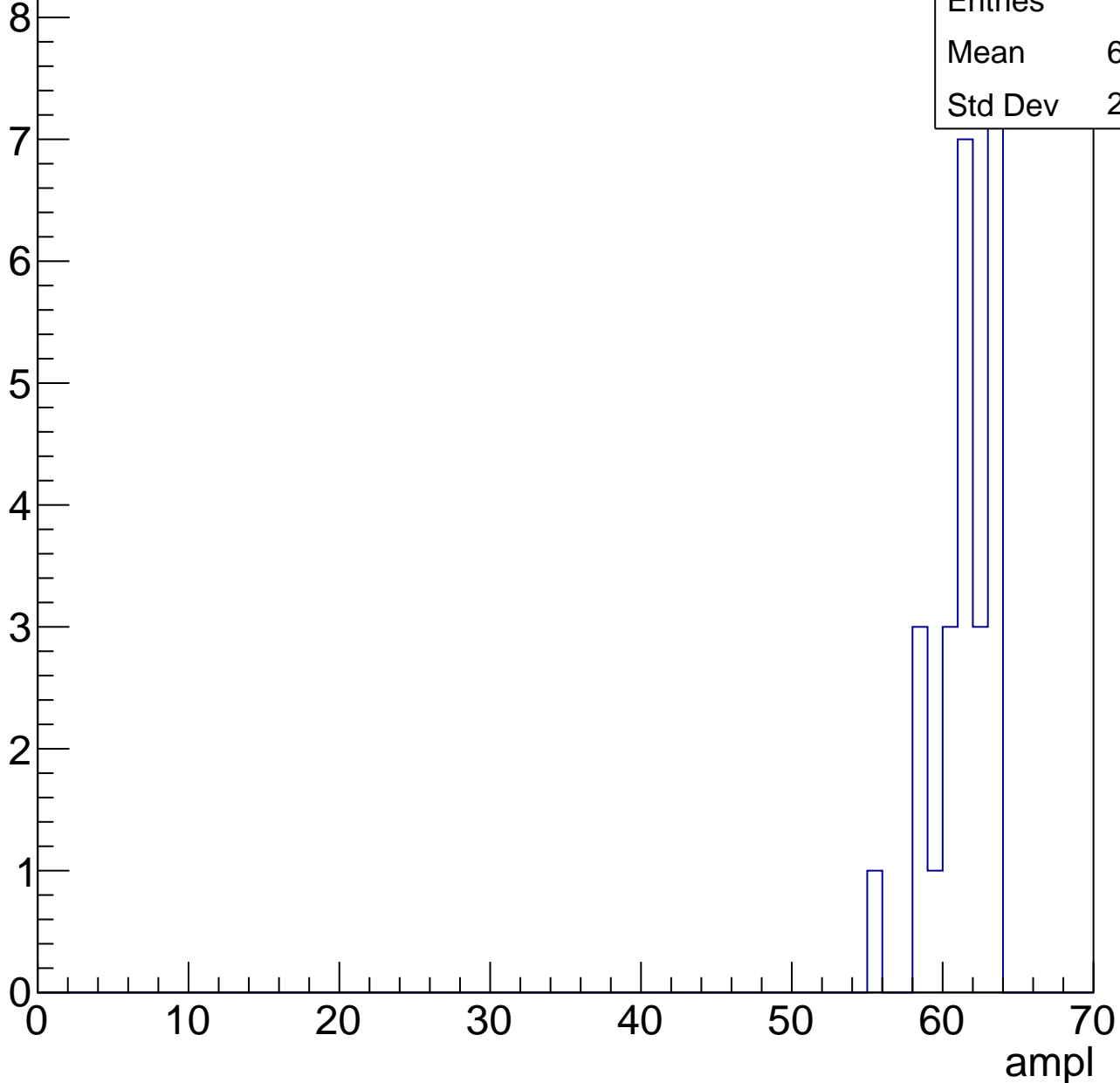


B1L100S, U13-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	60.96
Std Dev	2.009



B1L100S, U13-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch112, adc0

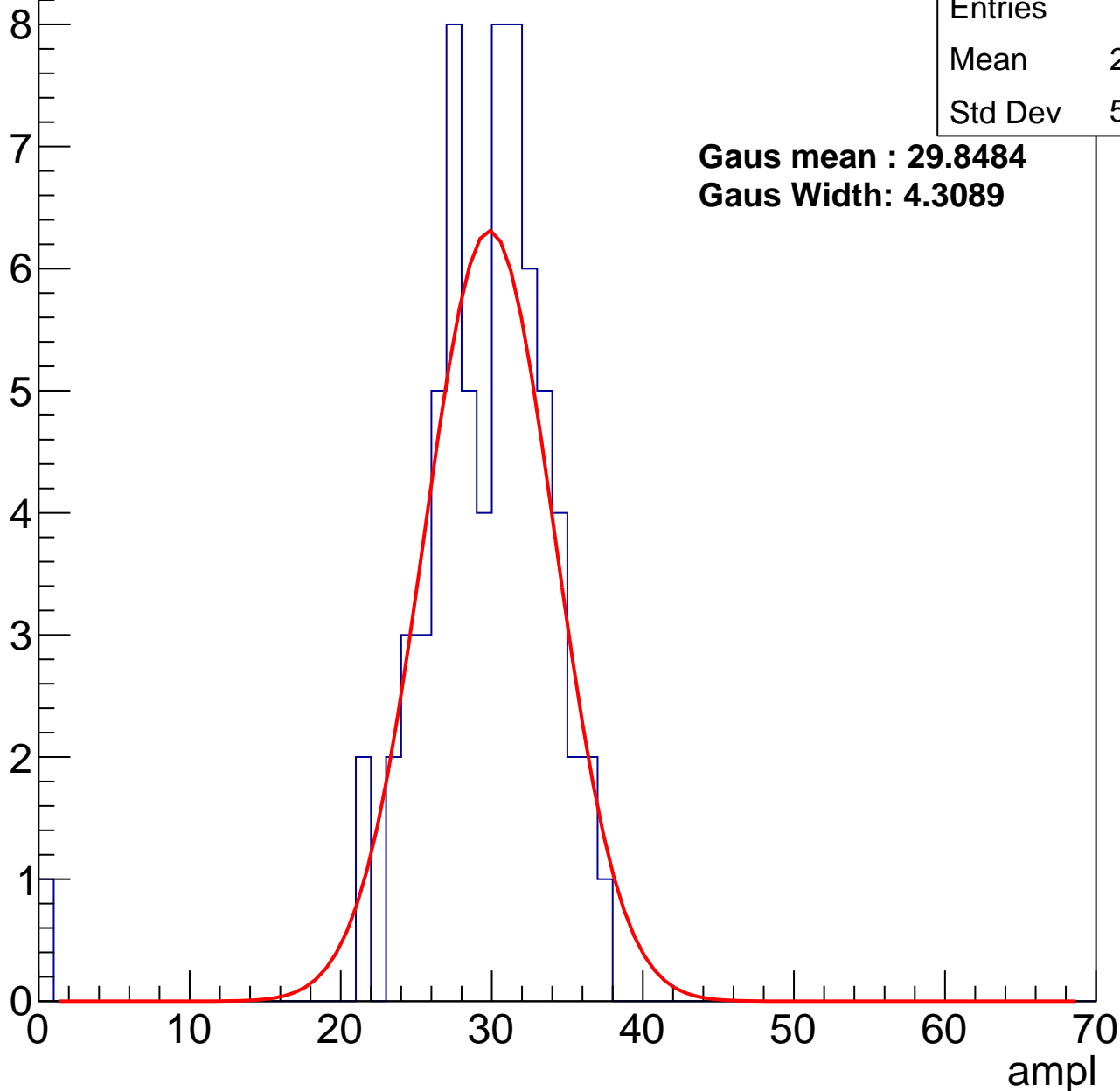
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.94
Std Dev	5.042

Gaus mean : 29.8484

Gaus Width: 4.3089



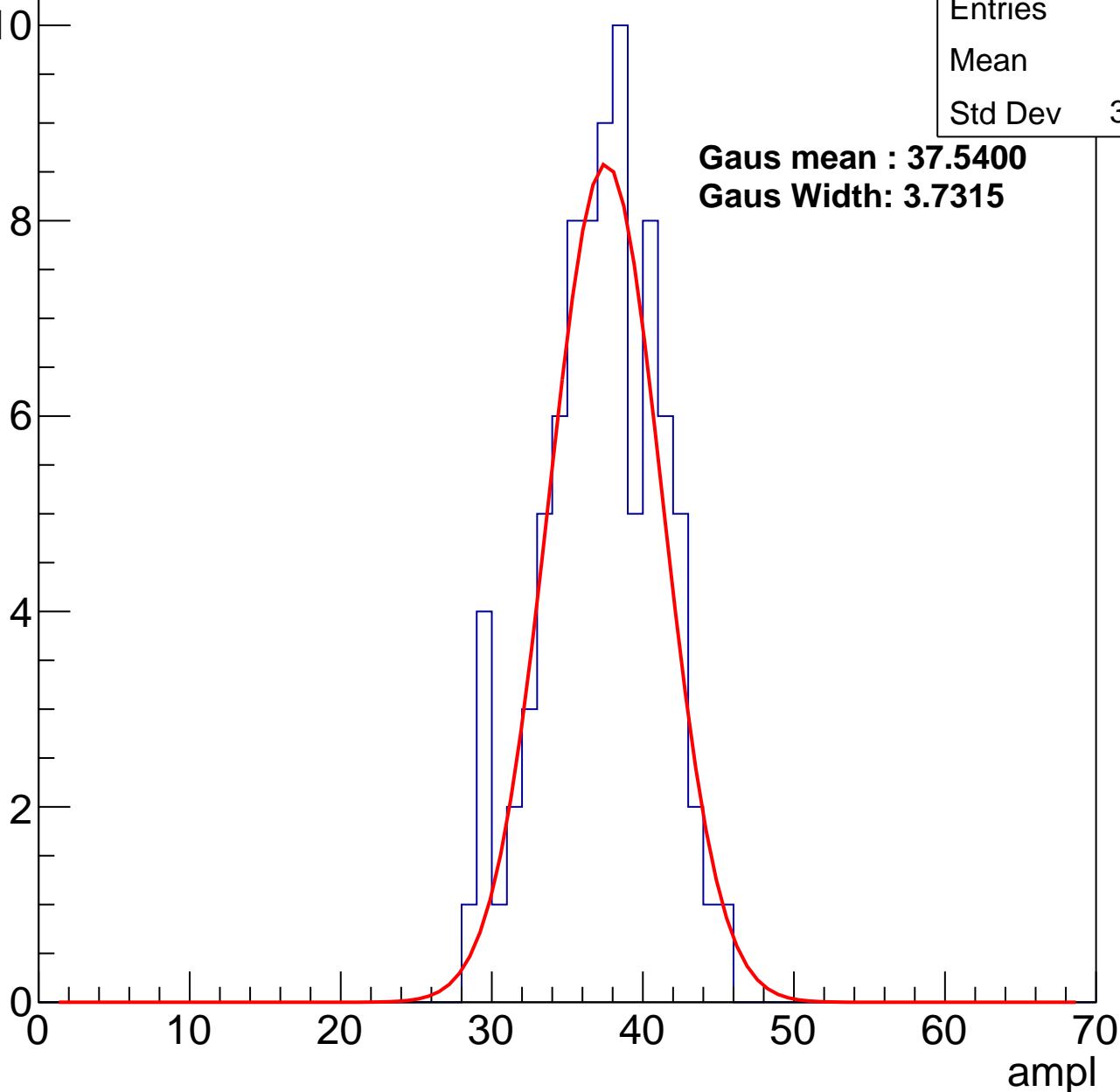
B1L100S, U13-ch112, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	36.8
Std Dev	3.775

Gaus mean : 37.5400
Gaus Width: 3.7315



B1L100S, U13-ch112, adc2

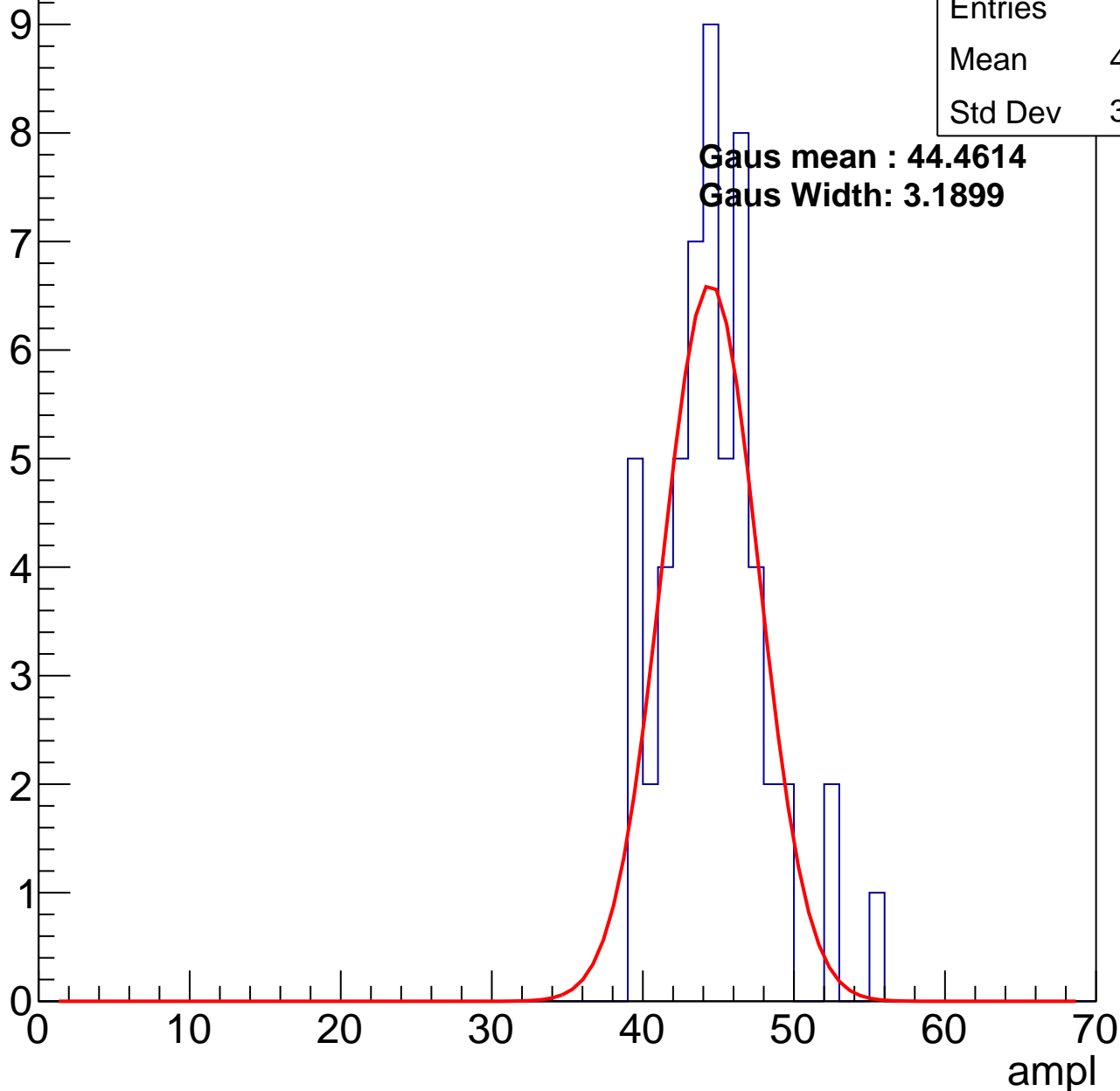
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.29
Std Dev	3.326

Gaus mean : 44.4614

Gaus Width: 3.1899



B1L100S, U13-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

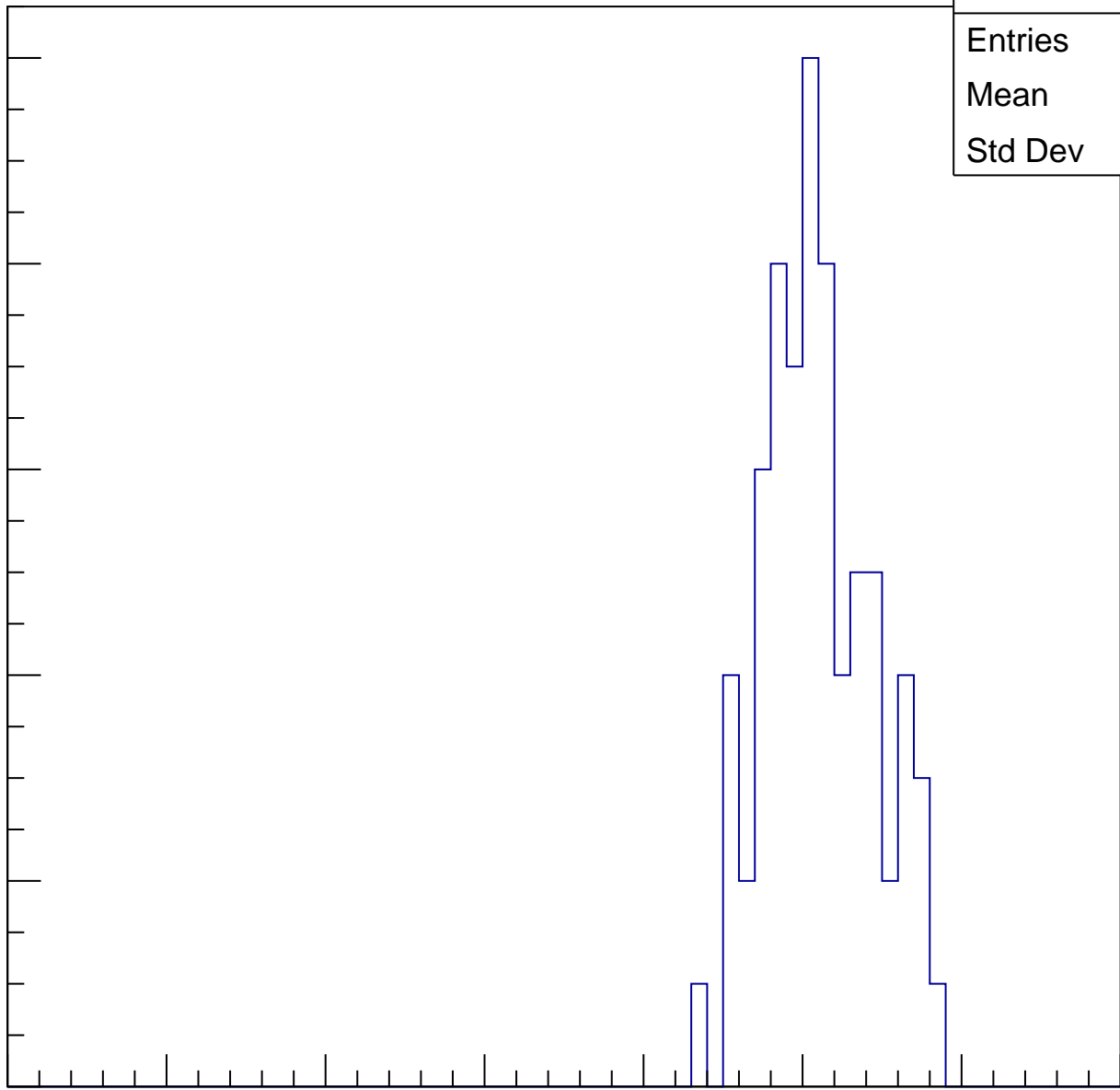
Entries	70
Mean	50.54
Std Dev	3.396

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

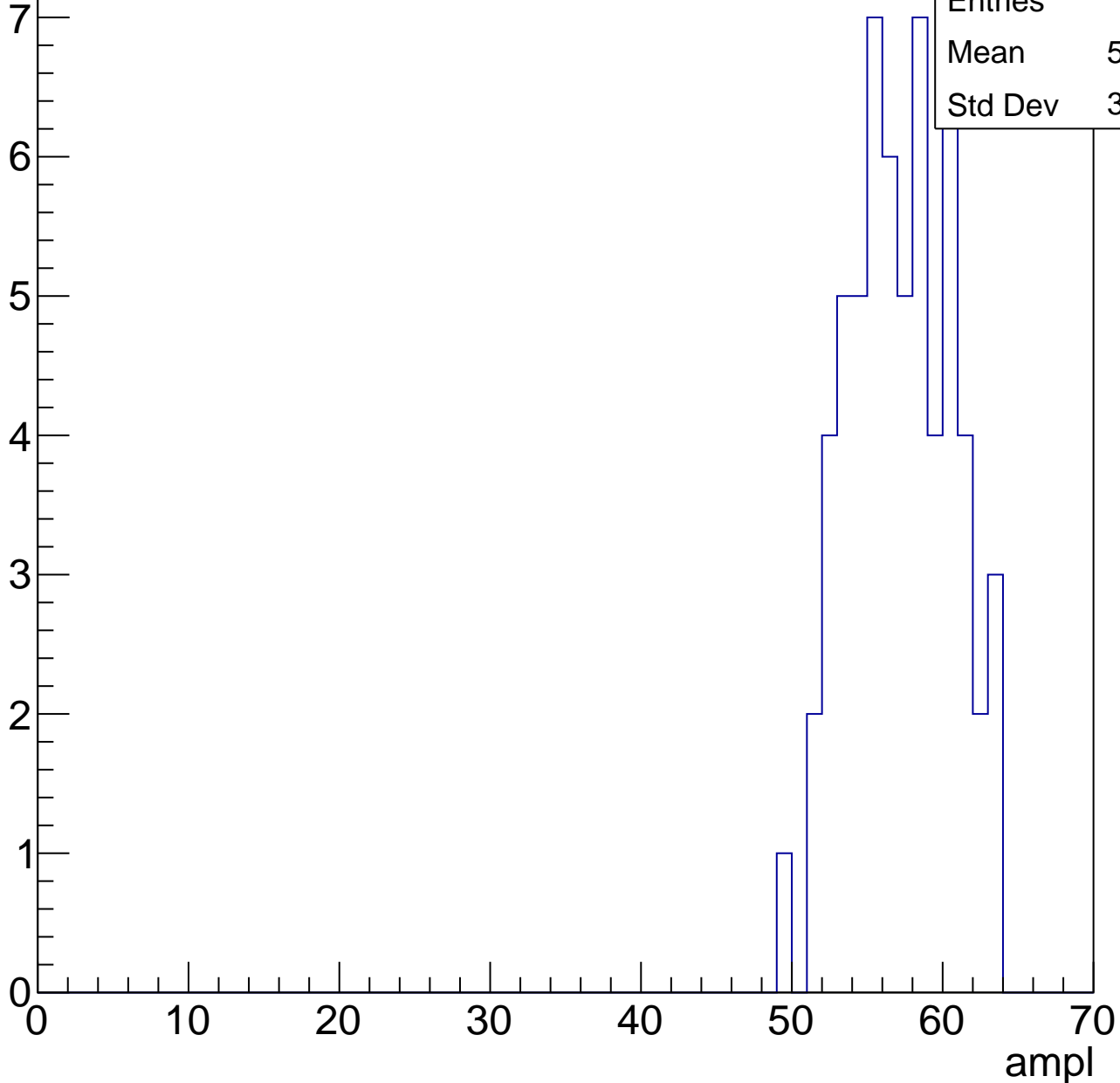


B1L100S, U13-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	56.76
Std Dev	3.359

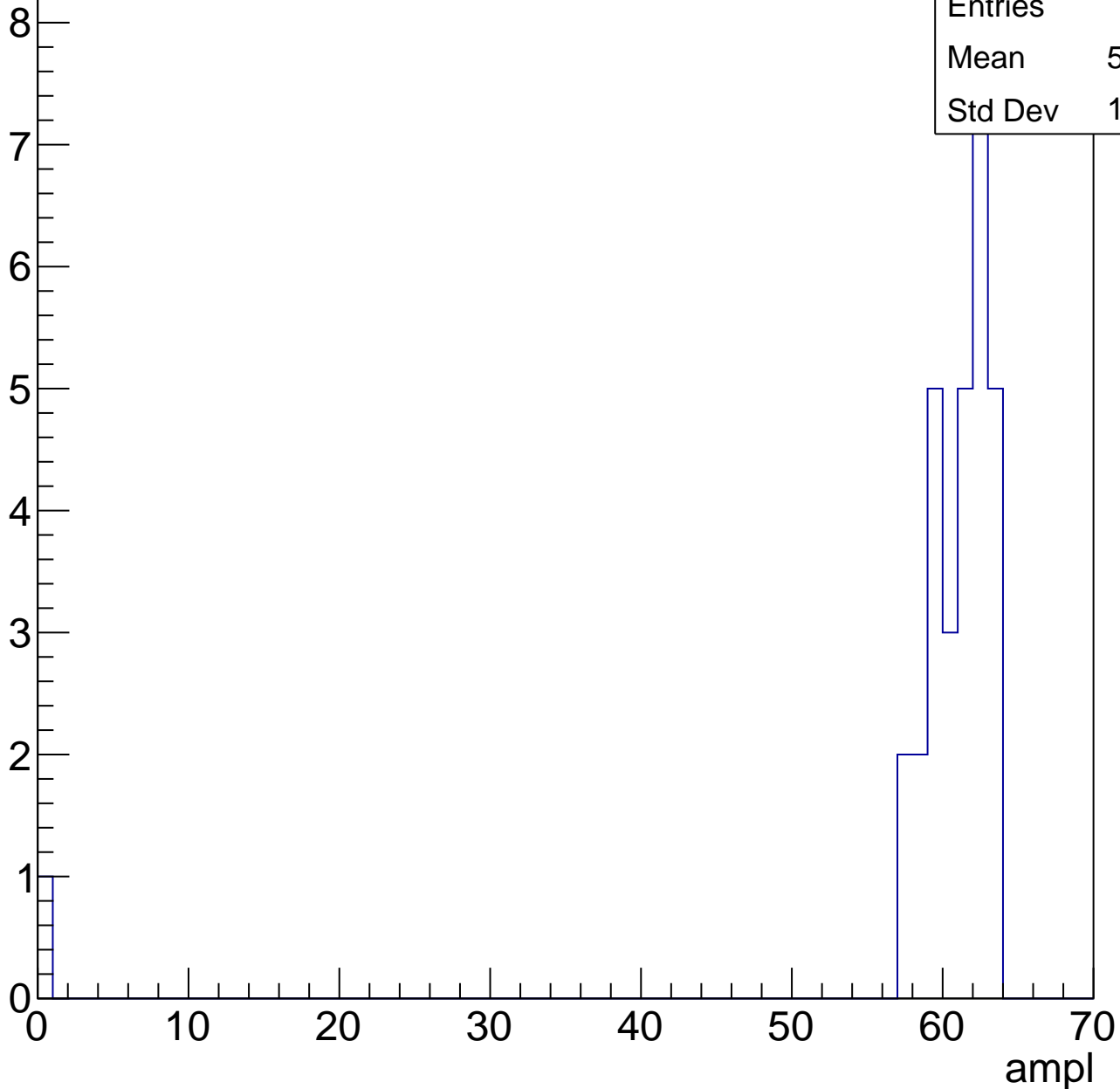


B1L100S, U13-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

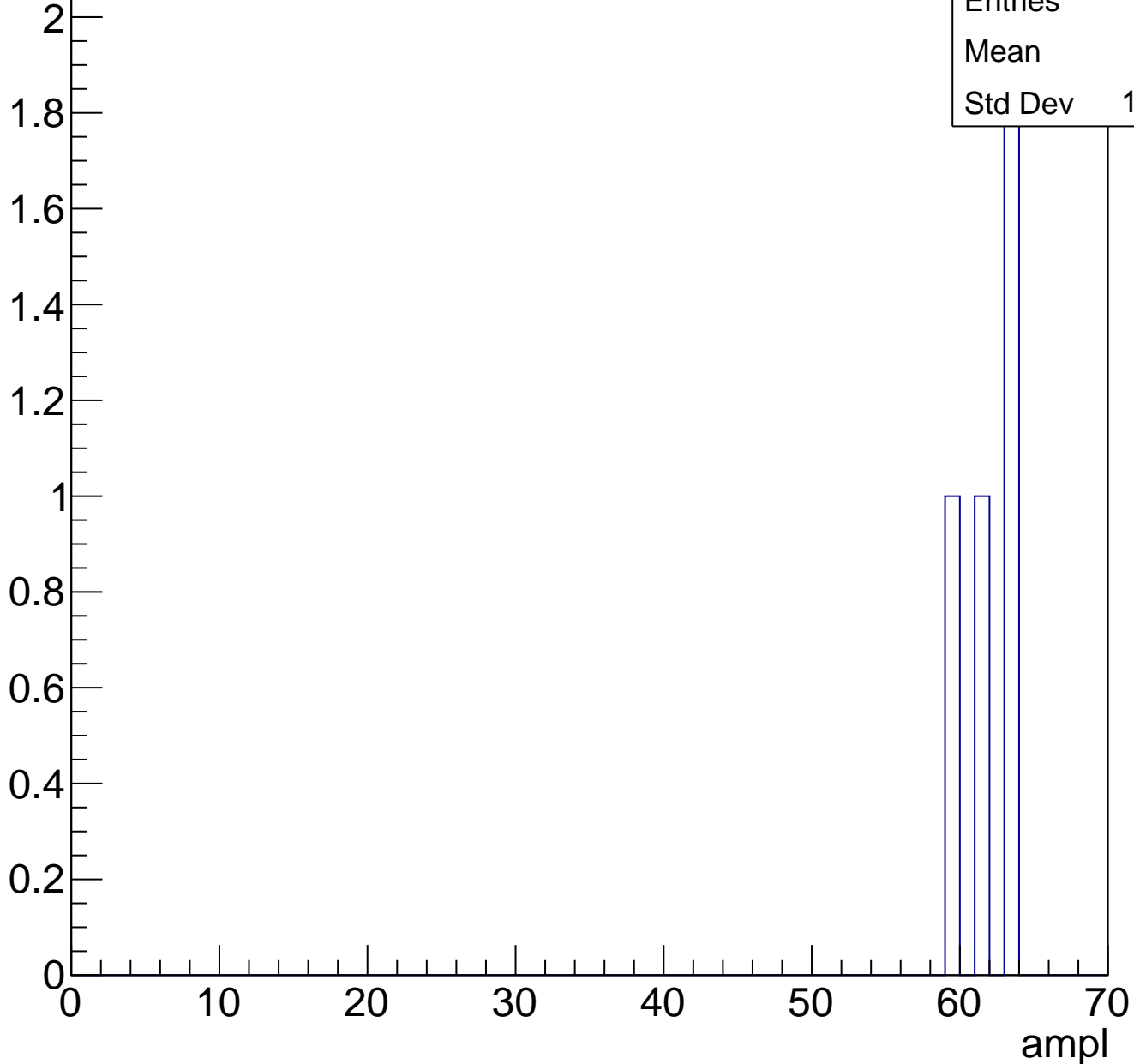
Entries	31
Mean	58.74
Std Dev	10.87



B1L100S, U13-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch113, adc0

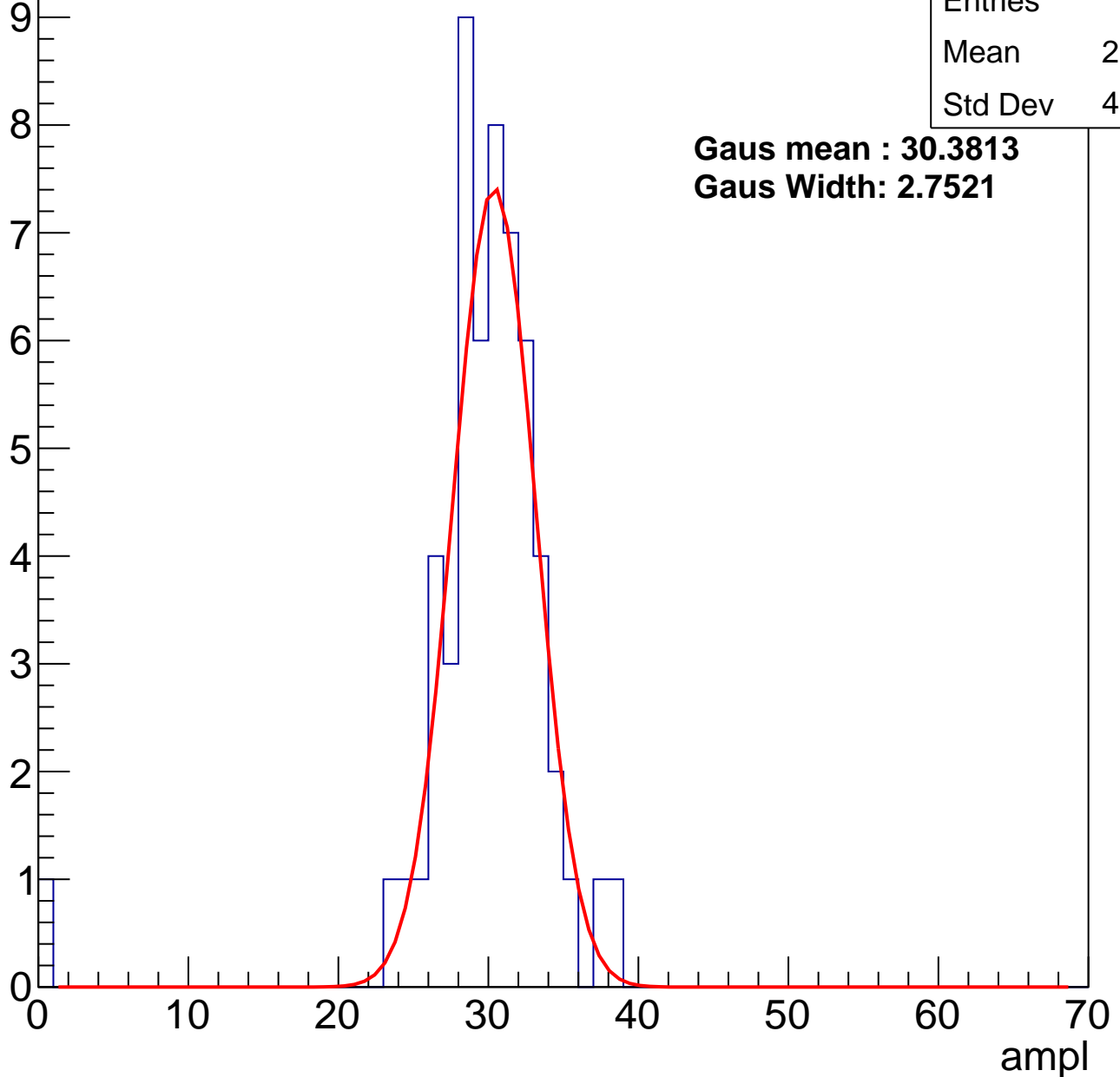
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.32
Std Dev	4.918

Gaus mean : 30.3813

Gaus Width: 2.7521



B1L100S, U13-ch113, adc1

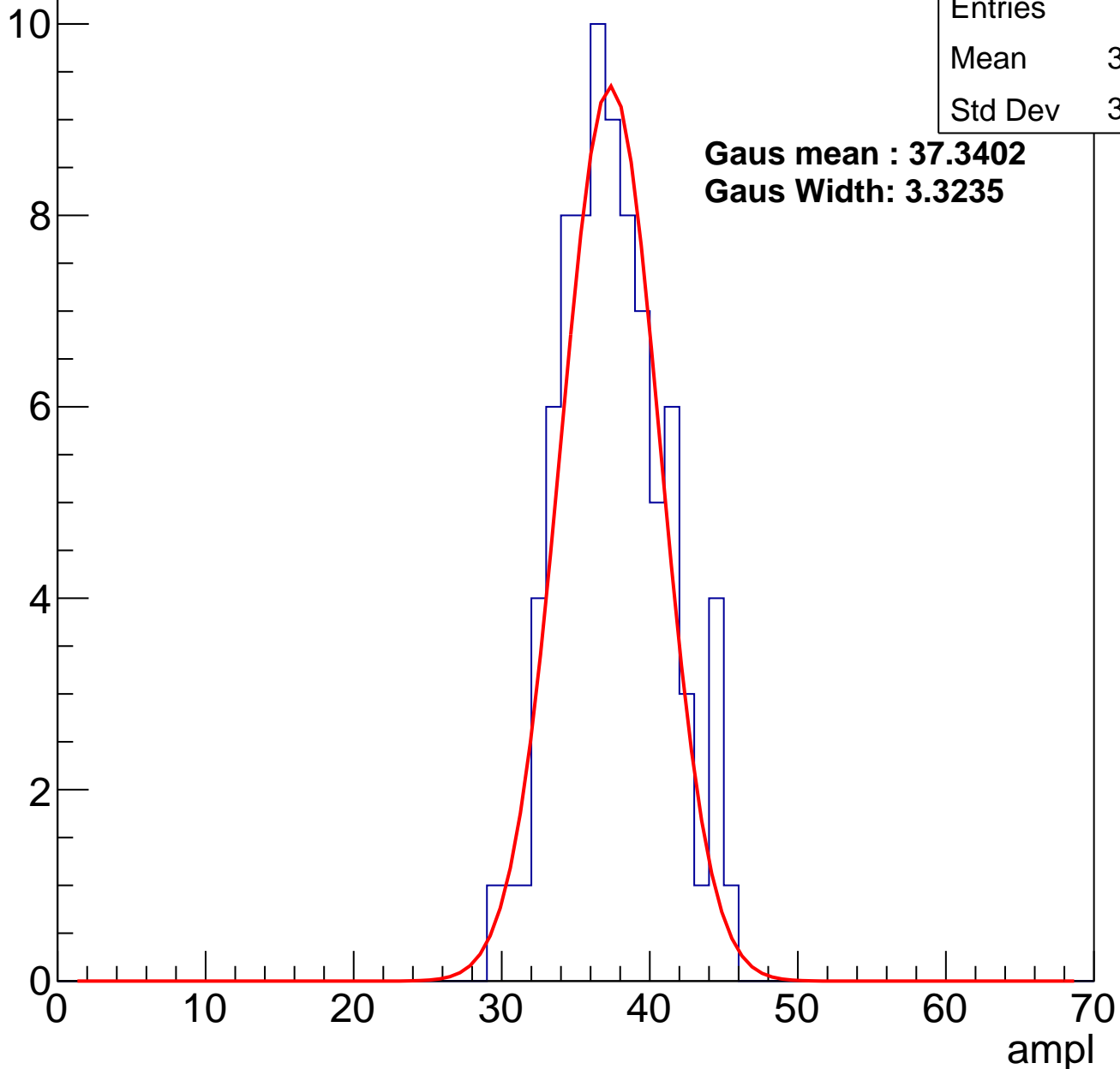
calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	37.04
Std Dev	3.483

Gaus mean : 37.3402

Gaus Width: 3.3235

Entry



B1L100S, U13-ch113, adc2

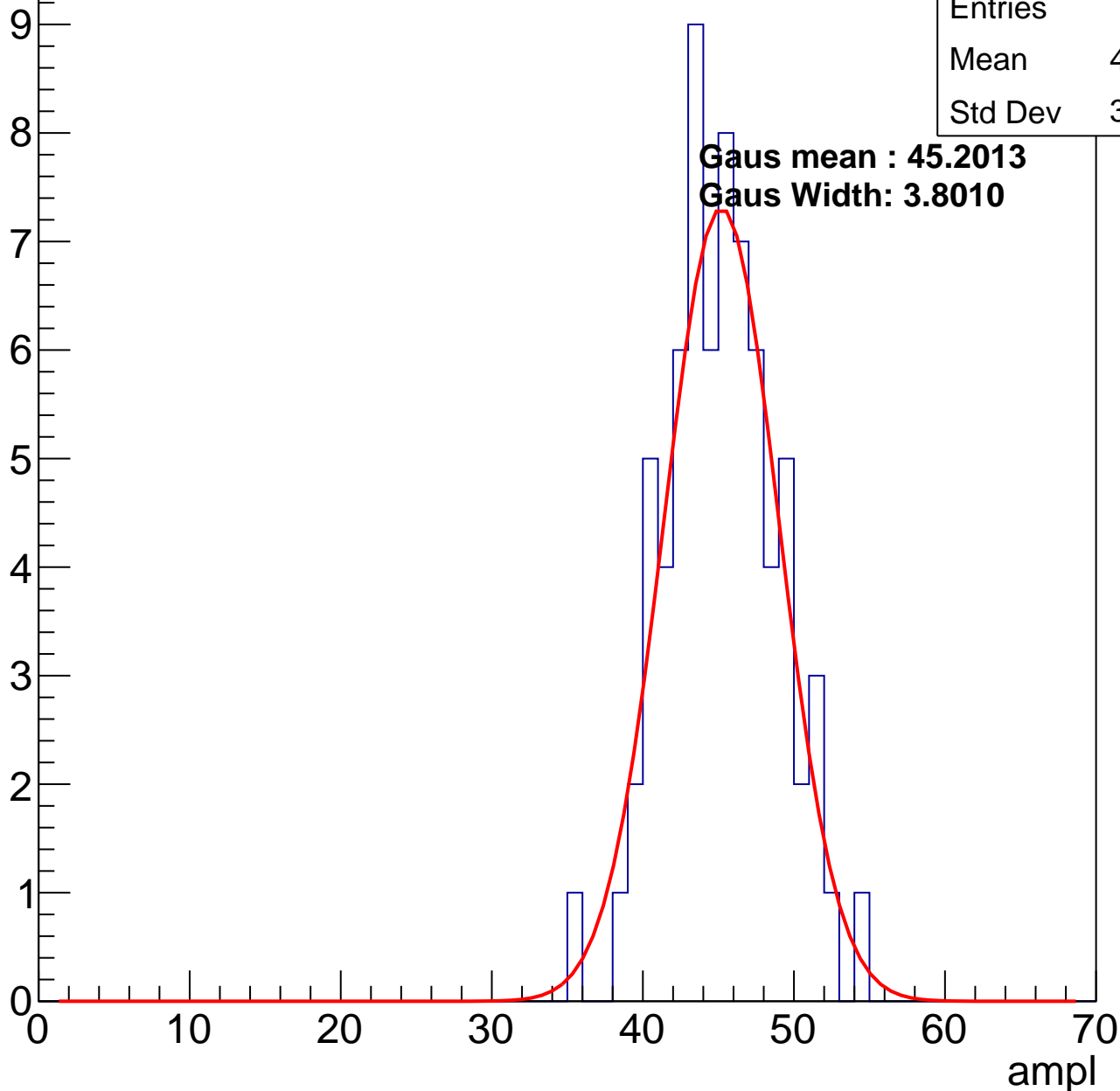
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.76
Std Dev	3.633

Gaus mean : 45.2013

Gaus Width: 3.8010

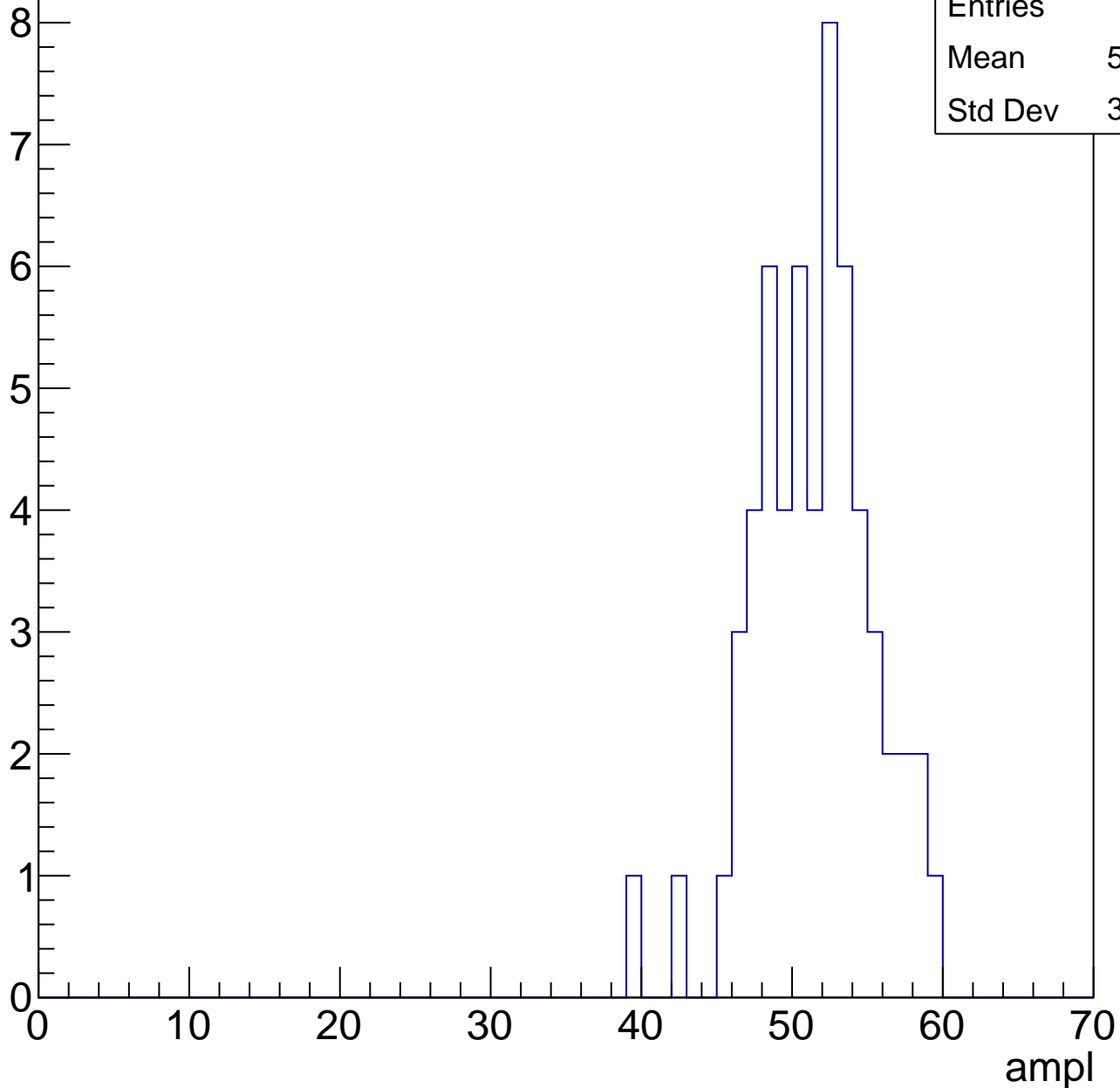


B1L100S, U13-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

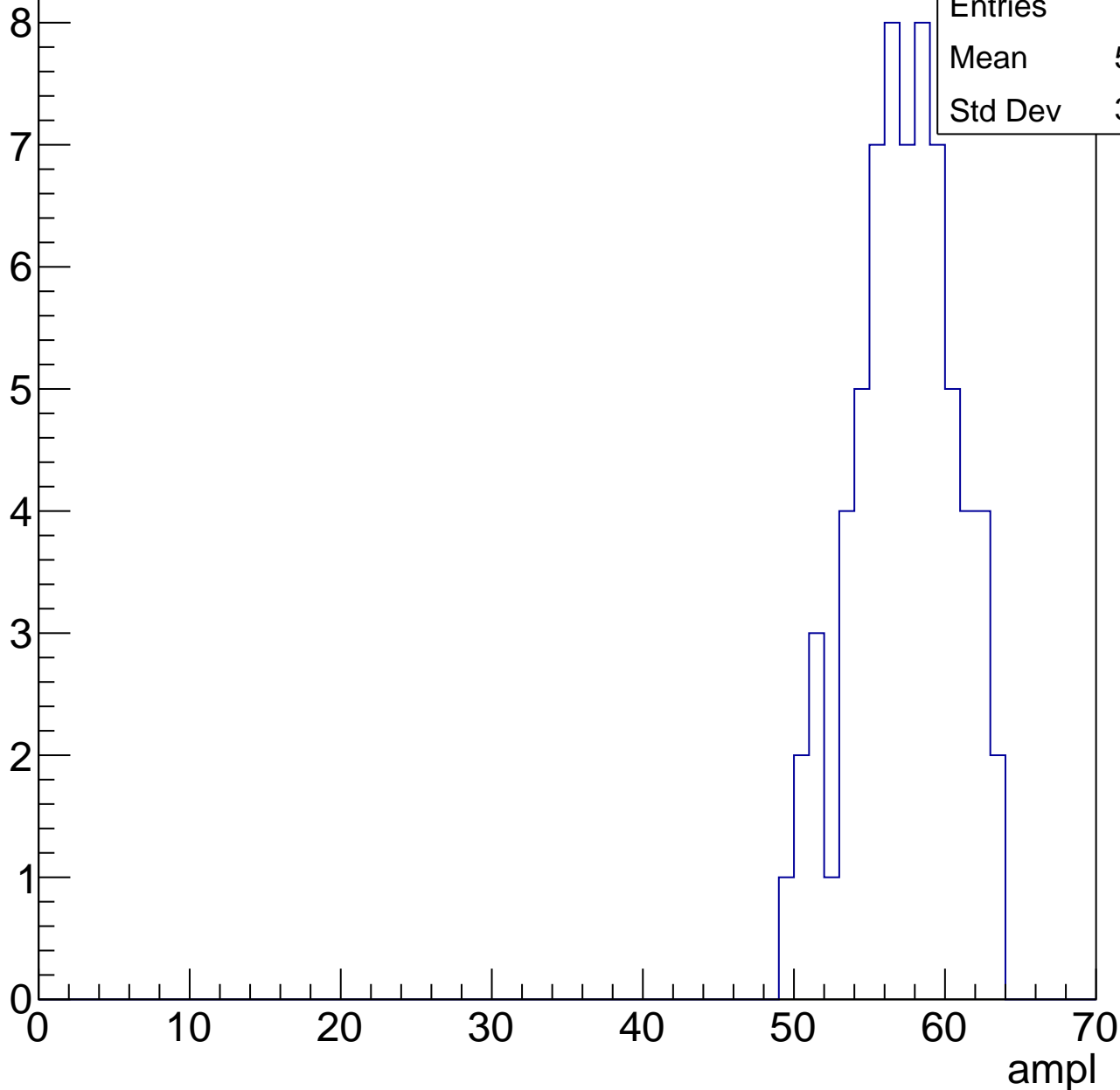
Entries	58
Mean	50.97
Std Dev	3.886



B1L100S, U13-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

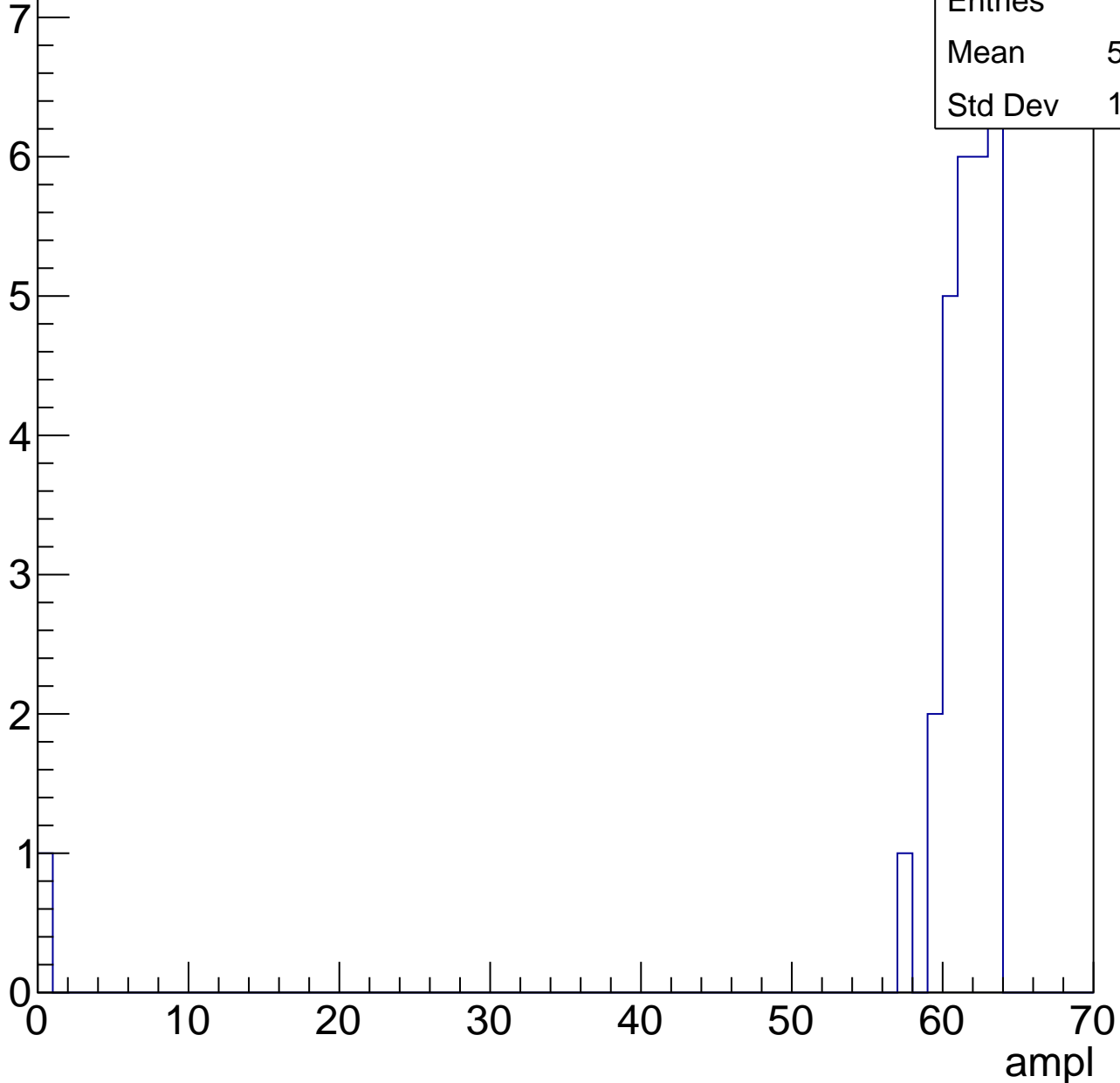


B1L100S, U13-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	59.07
Std Dev	11.46



B1L100S, U13-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch114, adc0

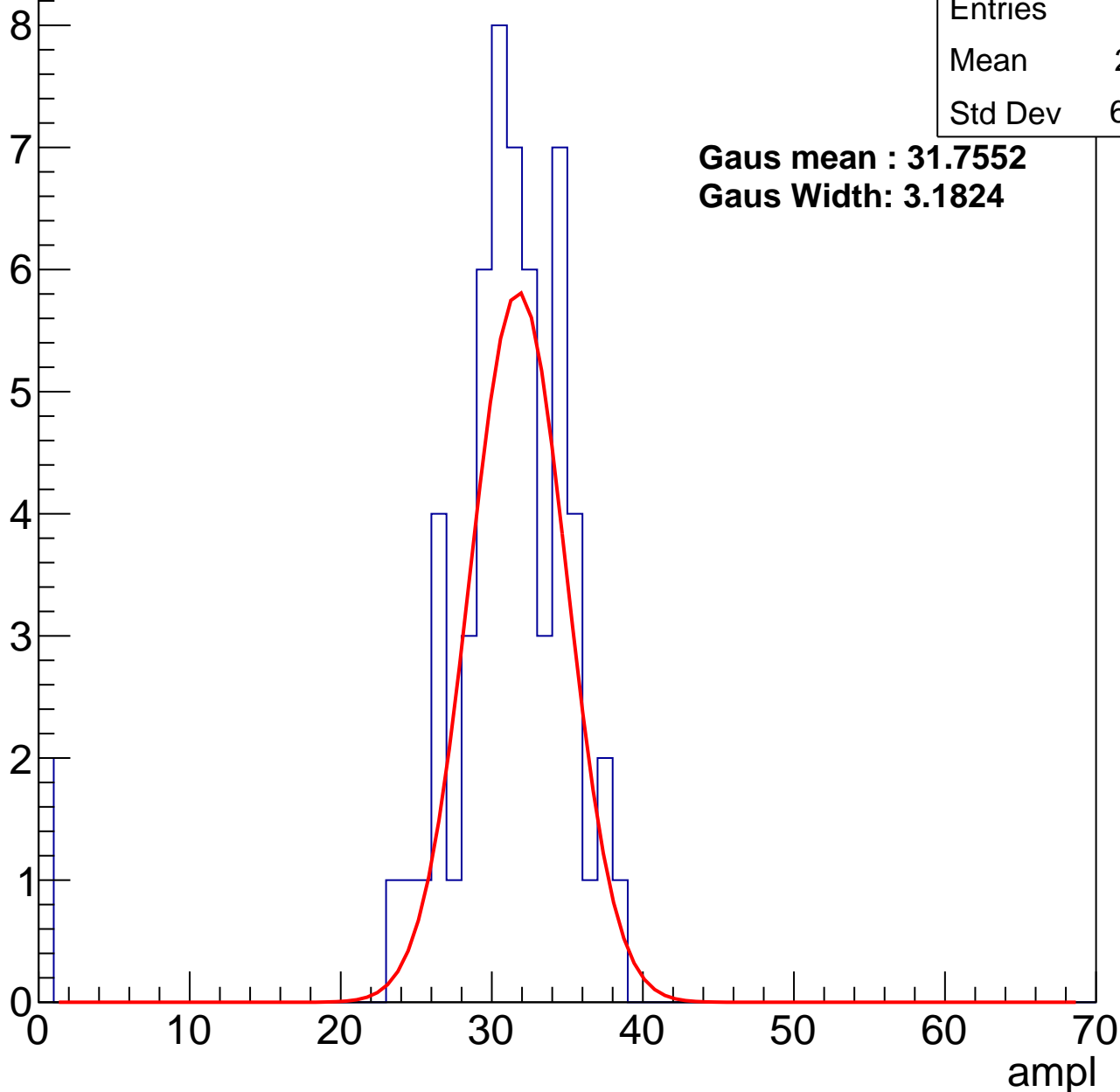
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.91
Std Dev	6.524

Gaus mean : 31.7552

Gaus Width: 3.1824



B1L100S, U13-ch114, adc1

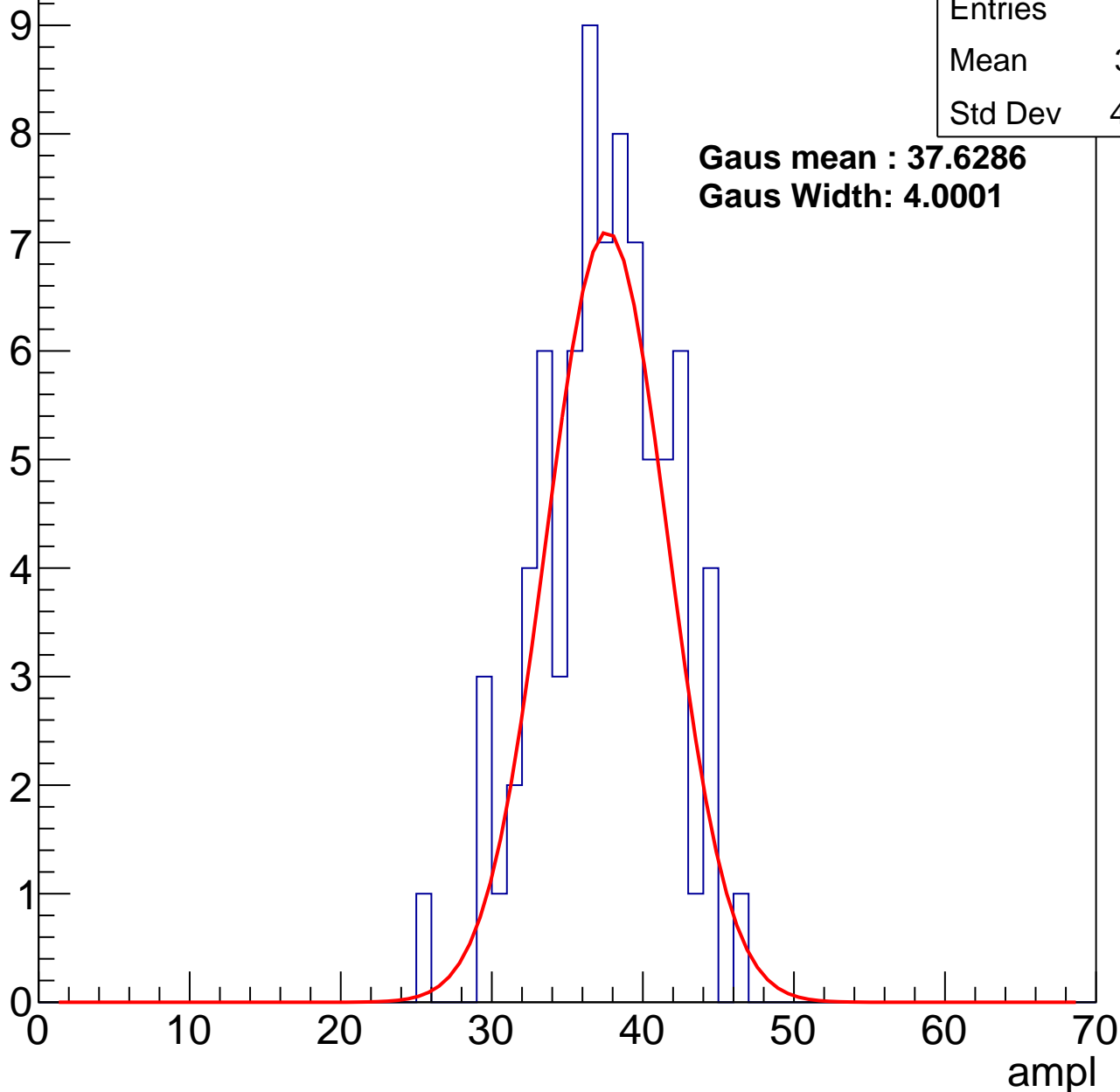
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37.01
Std Dev	4.114

Gaus mean : 37.6286

Gaus Width: 4.0001



B1L100S, U13-ch114, adc2

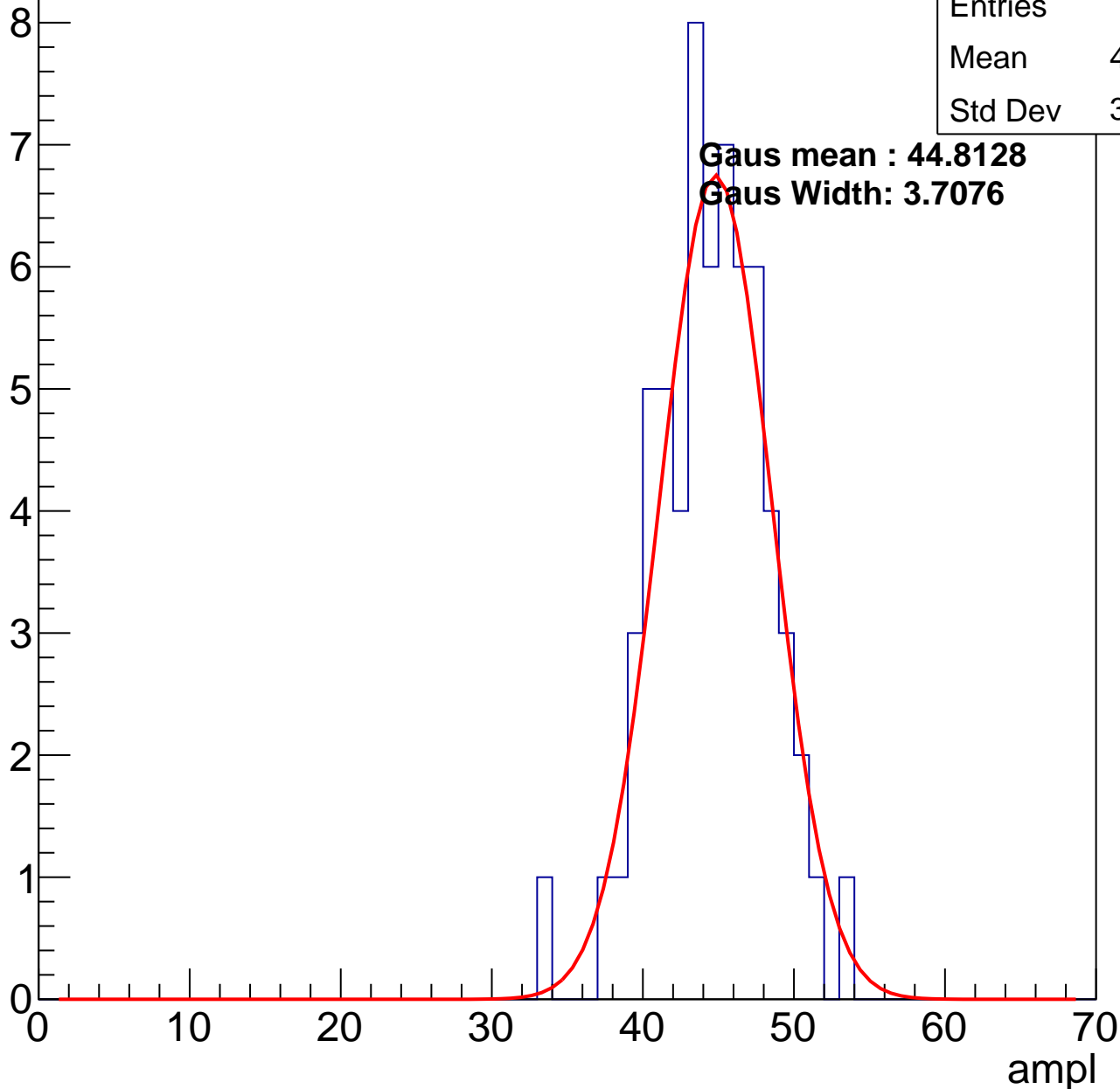
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.09
Std Dev	3.656

Gaus mean : 44.8128

Gaus Width: 3.7076

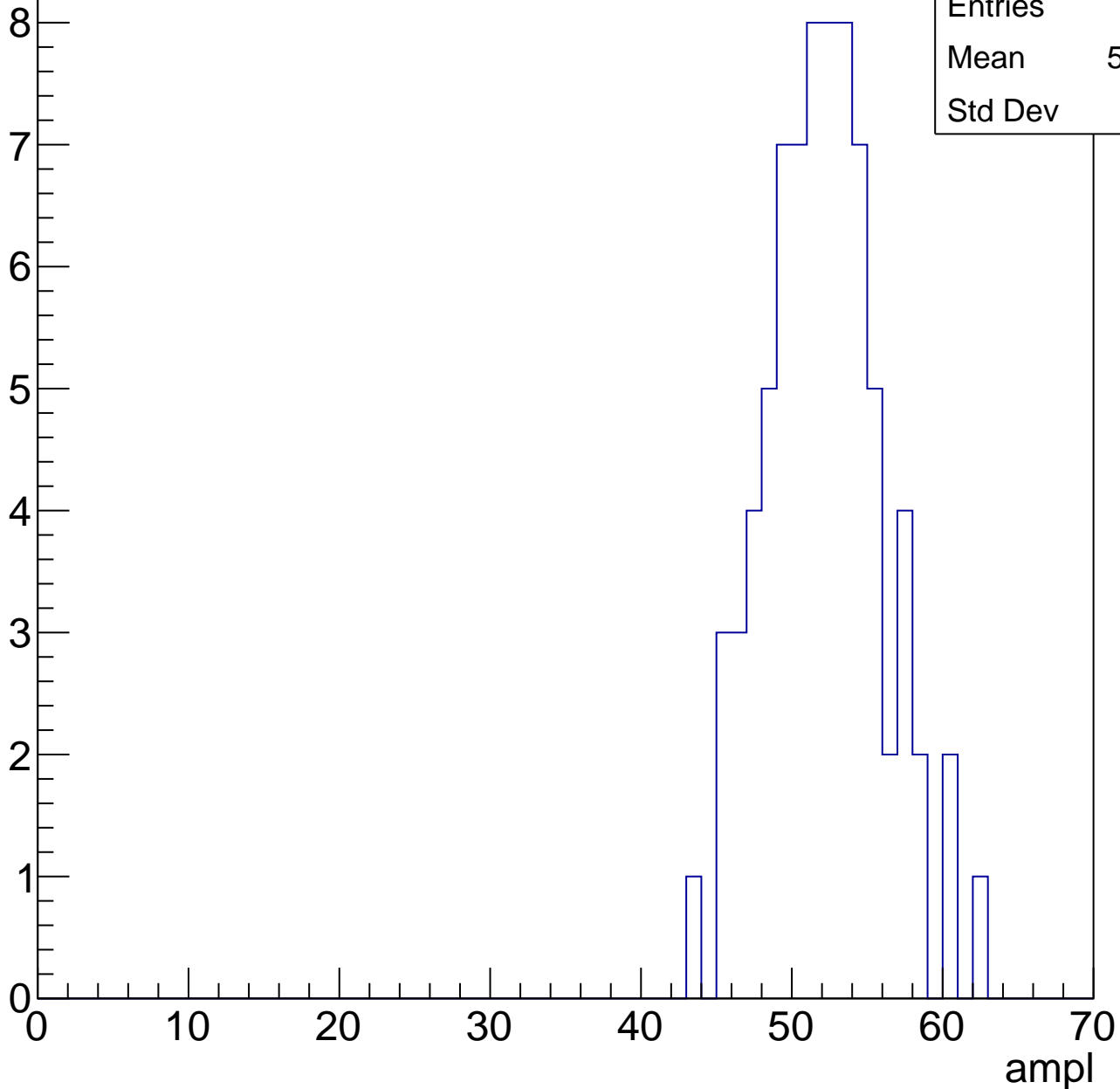


B1L100S, U13-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	51.64
Std Dev	3.81

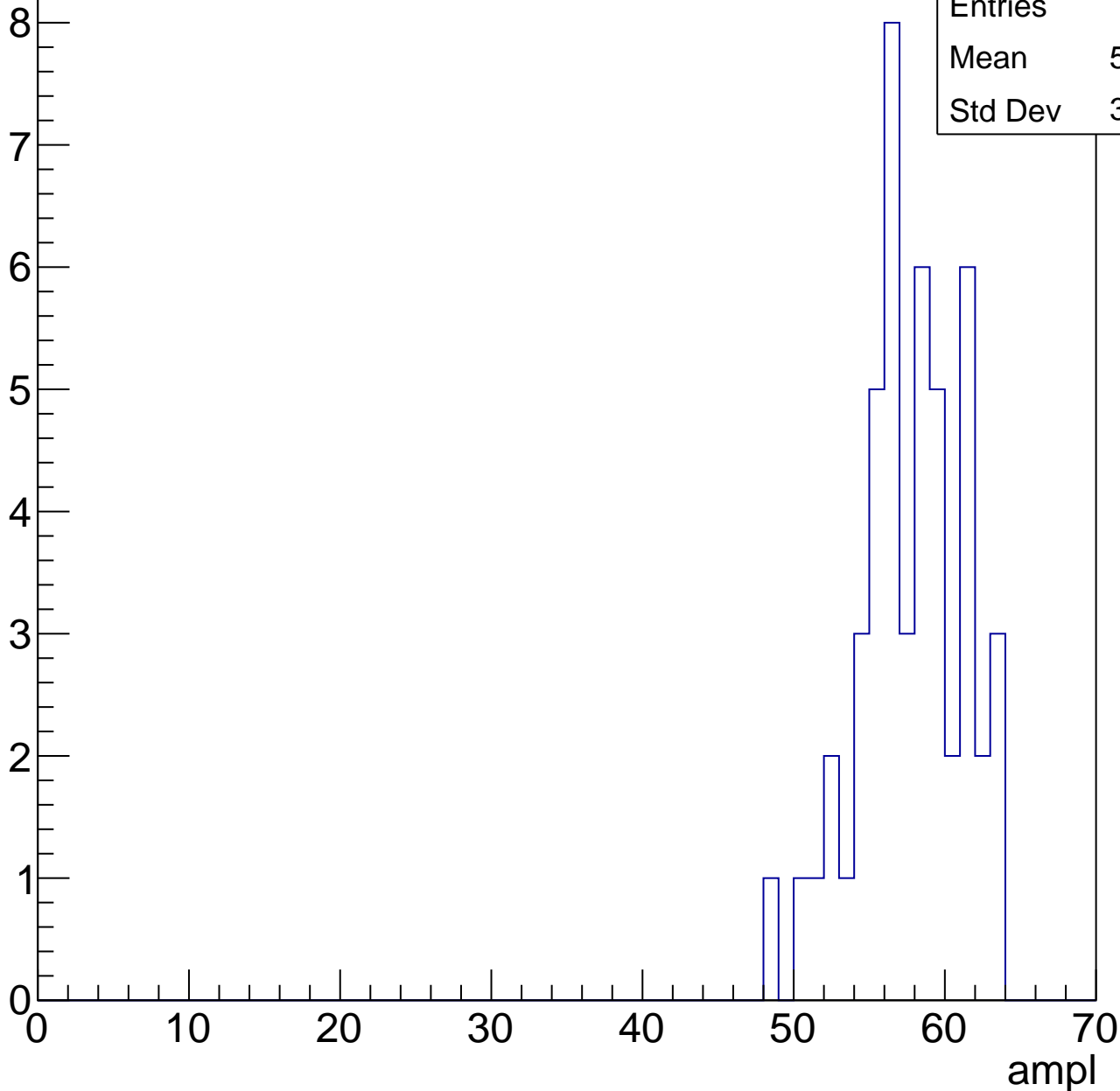


B1L100S, U13-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	57.22
Std Dev	3.448

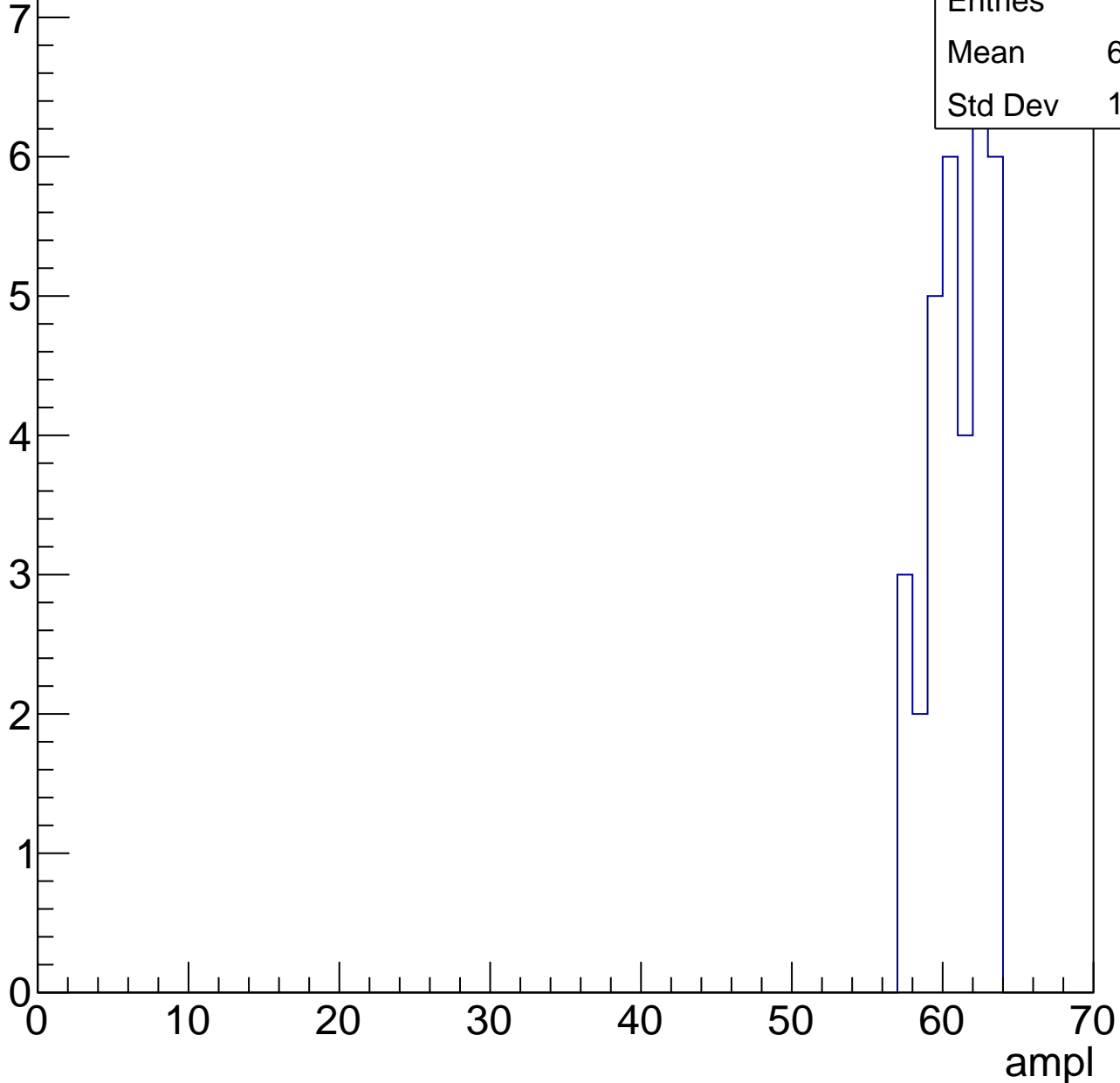


B1L100S, U13-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	60.55
Std Dev	1.876



B1L100S, U13-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch115, adc0

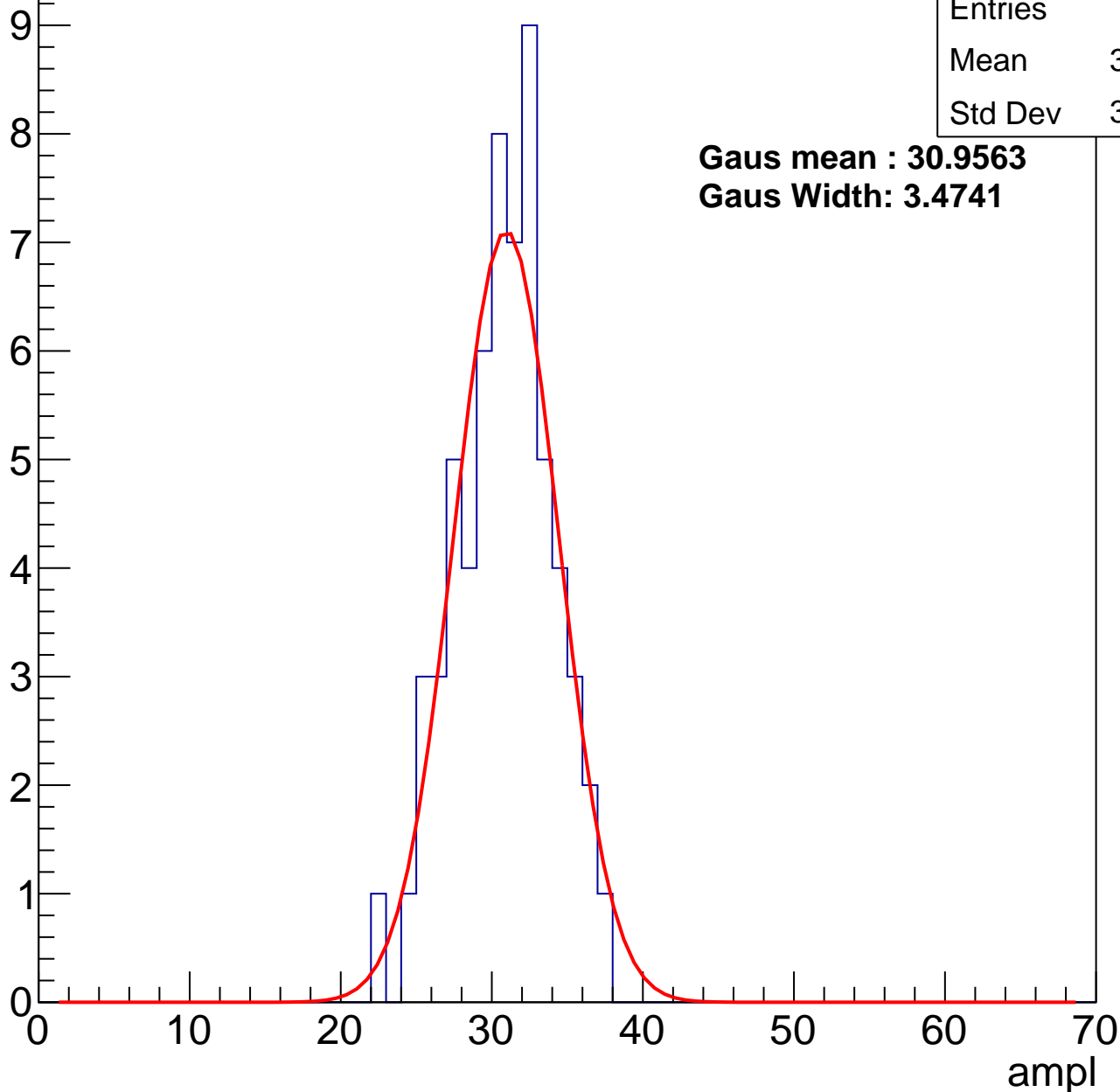
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.32
Std Dev	3.192

Gaus mean : 30.9563

Gaus Width: 3.4741



B1L100S, U13-ch115, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	38.16
Std Dev	3.846

Gaus mean : 38.0543

Gaus Width: 3.2506

10

8

6

4

2

0

0

10

20

30

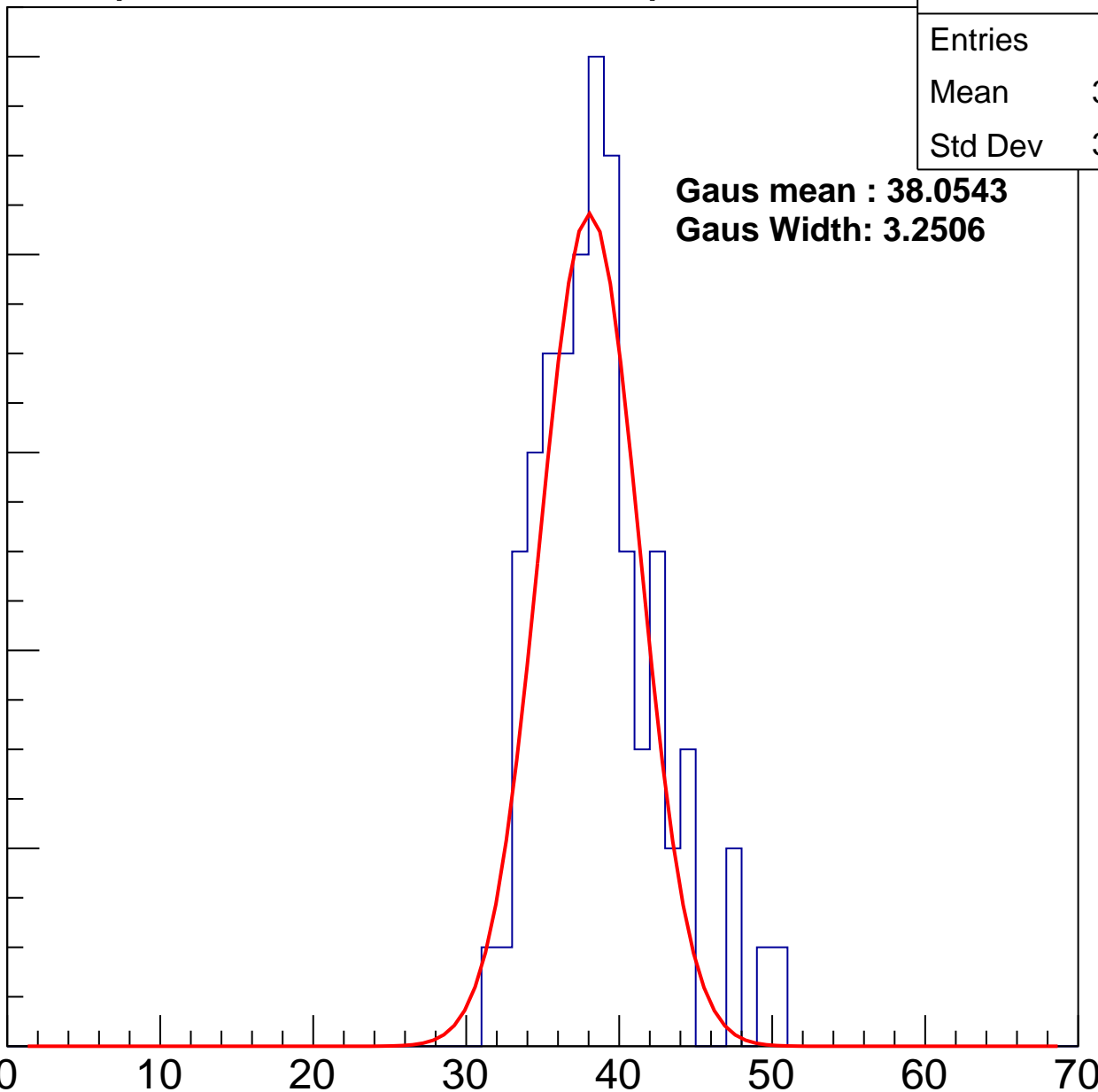
40

50

60

70

ampl



B1L100S, U13-ch115, adc2

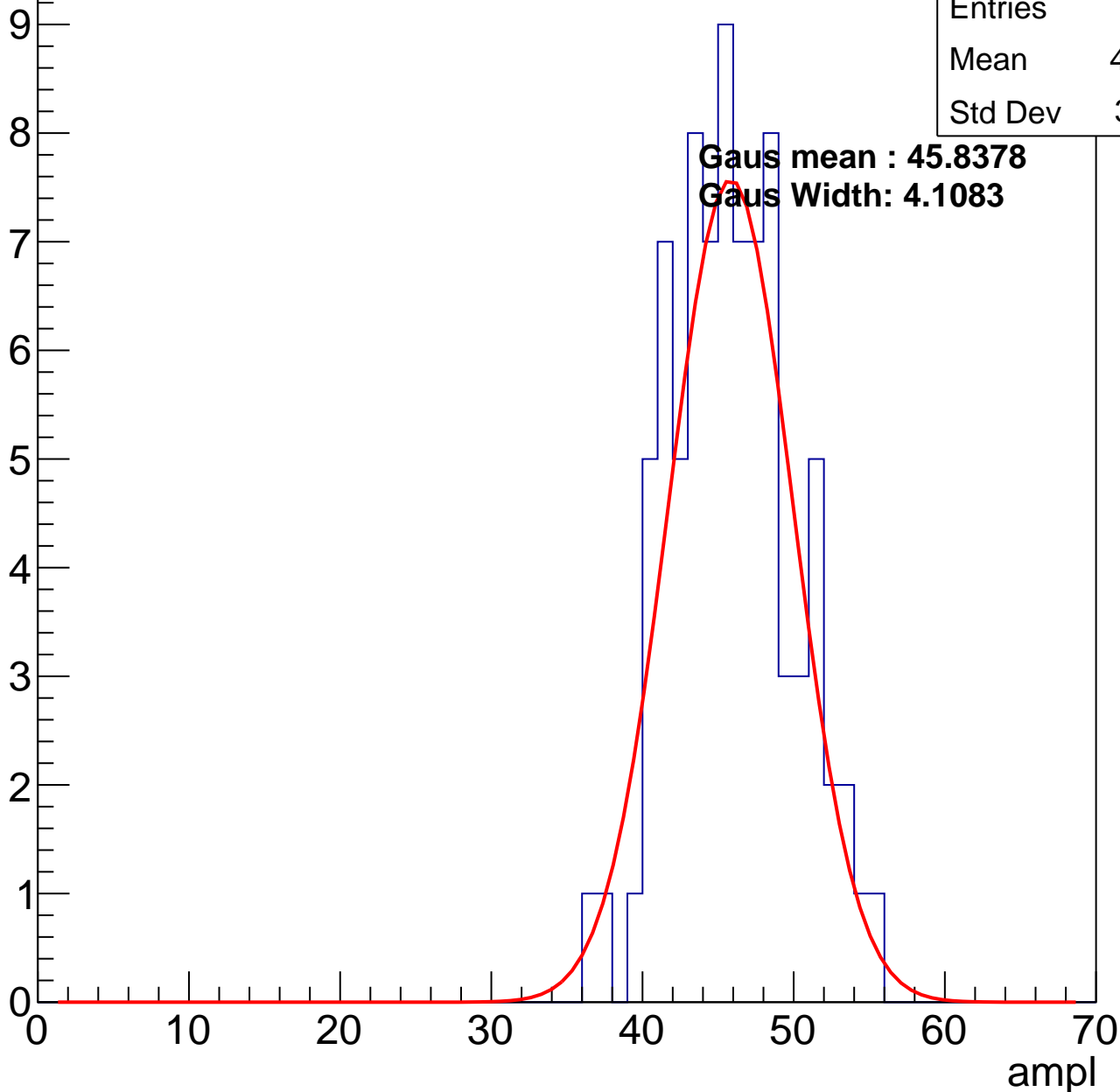
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	45.45
Std Dev	3.961

Gaus mean : 45.8378

Gaus Width: 4.1083

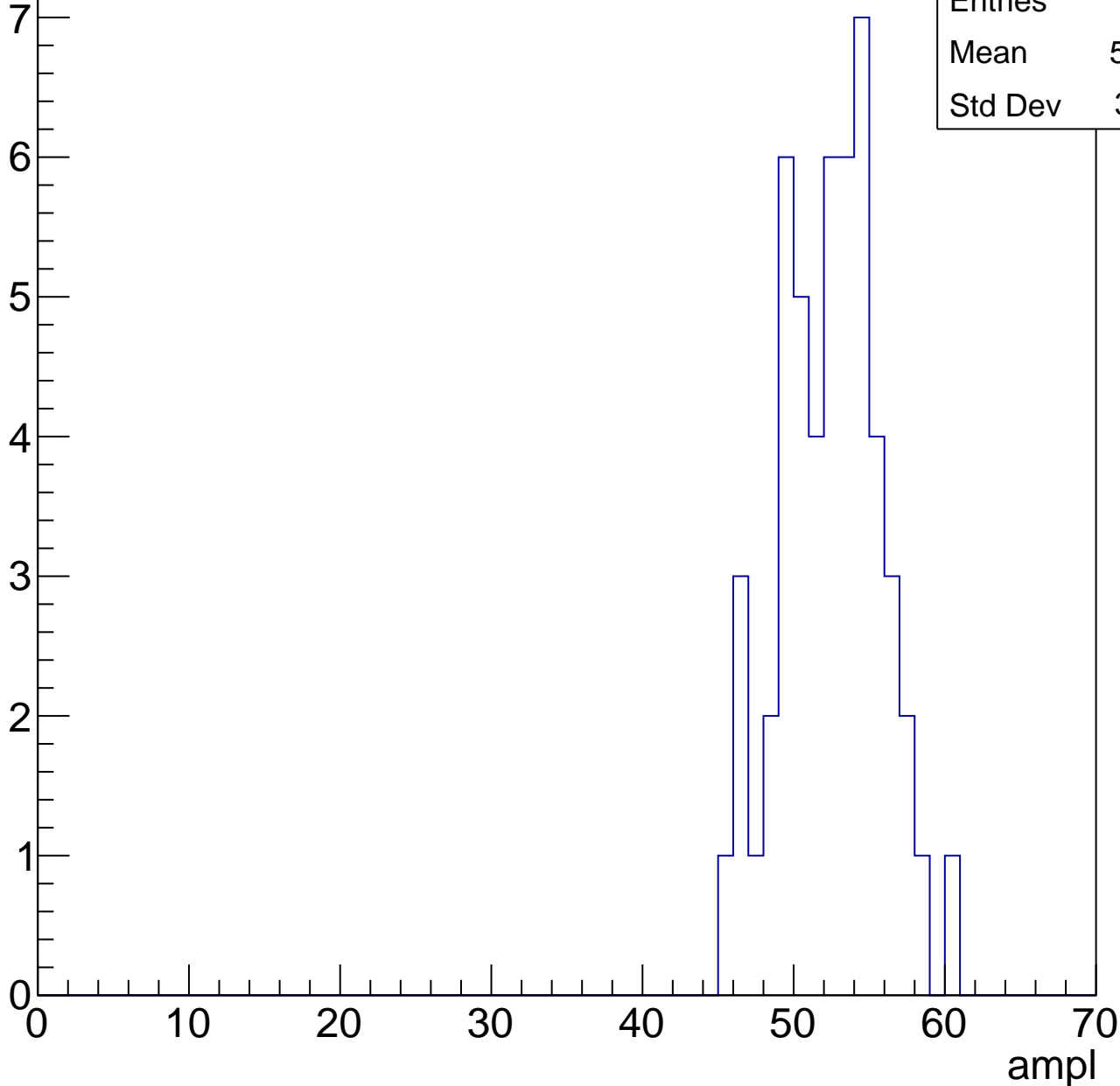


B1L100S, U13-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	51.96
Std Dev	3.281

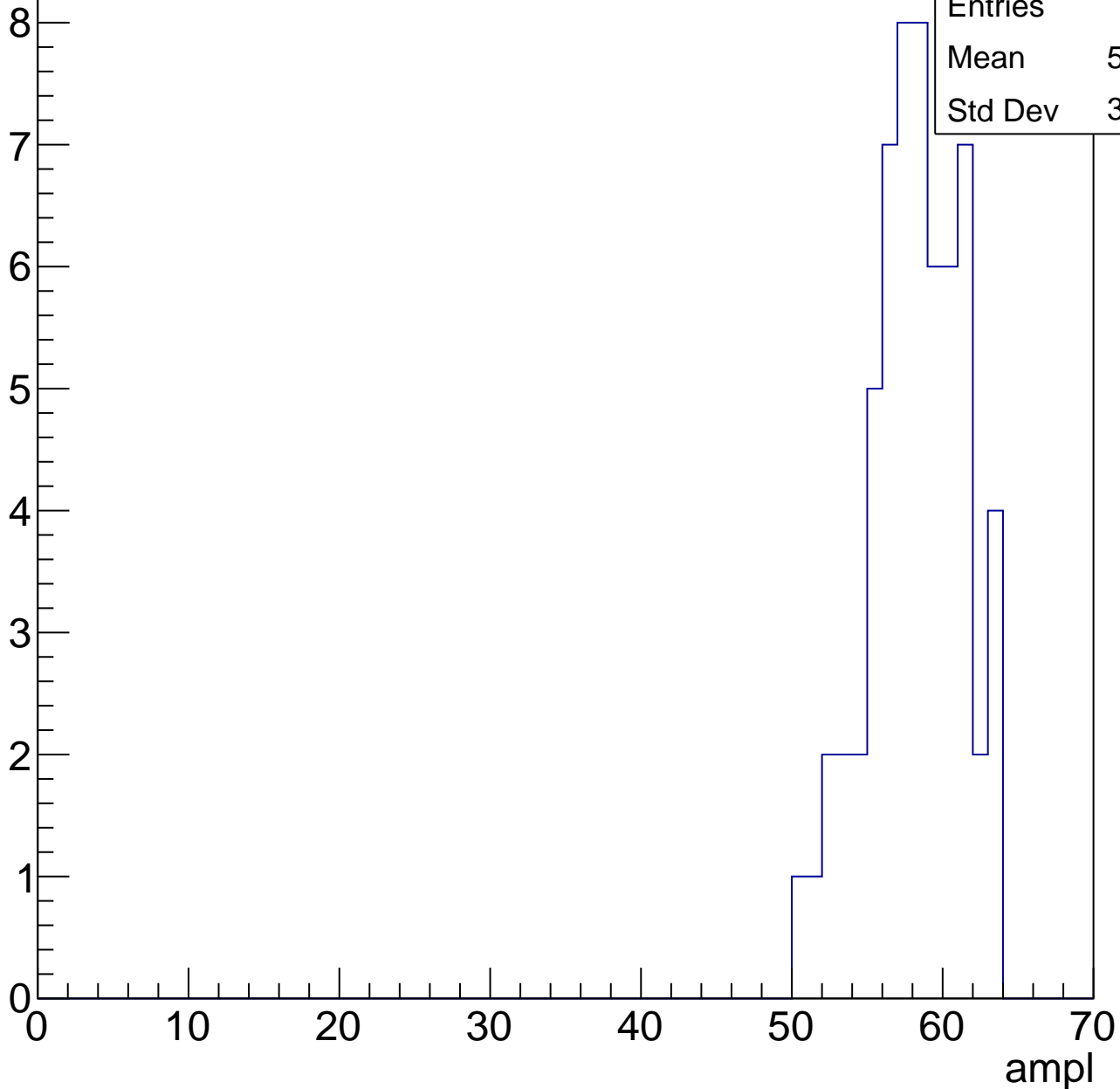


B1L100S, U13-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.75
Std Dev	3.055

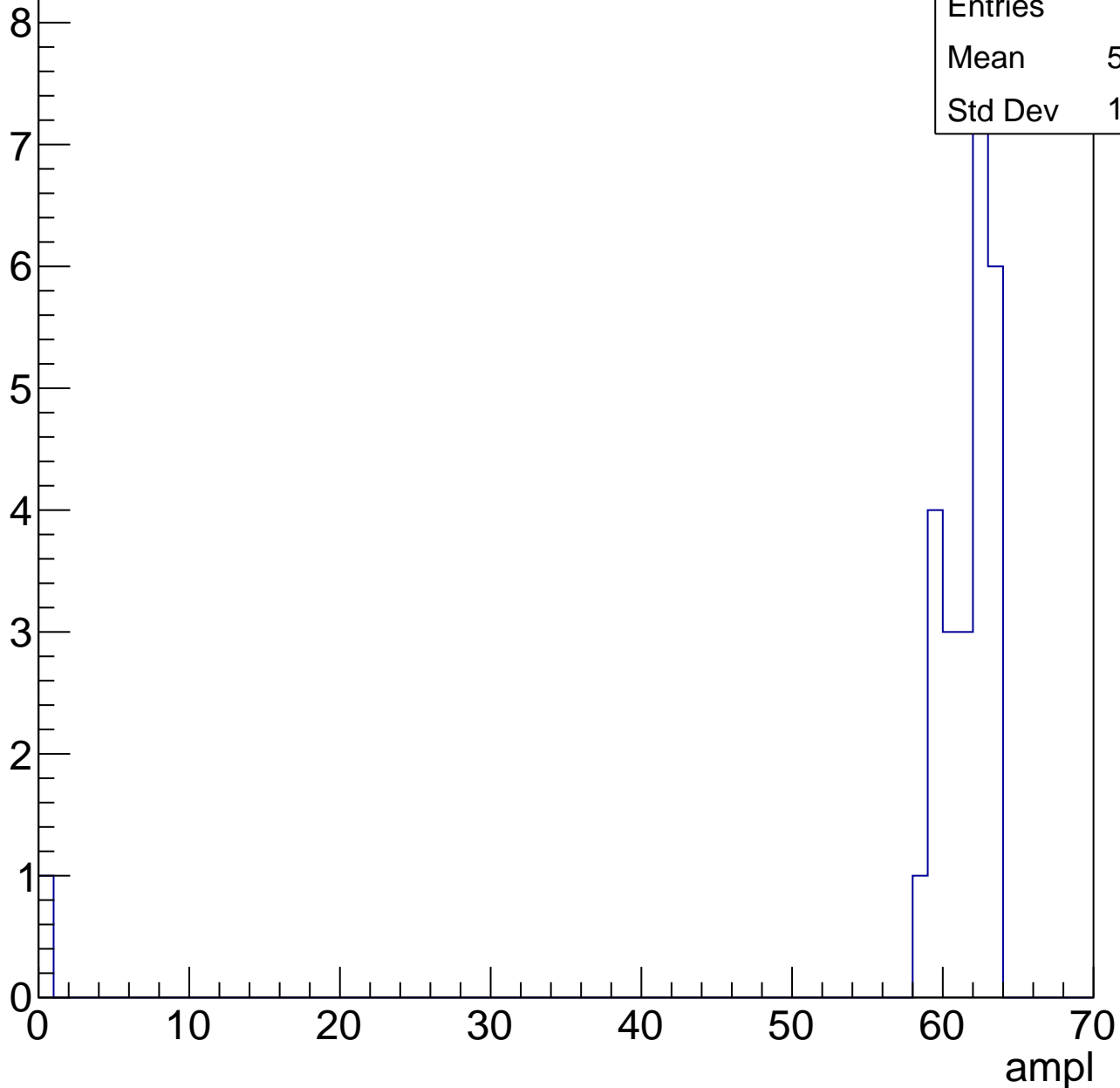


B1L100S, U13-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.88
Std Dev	11.87



B1L100S, U13-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch116, adc0

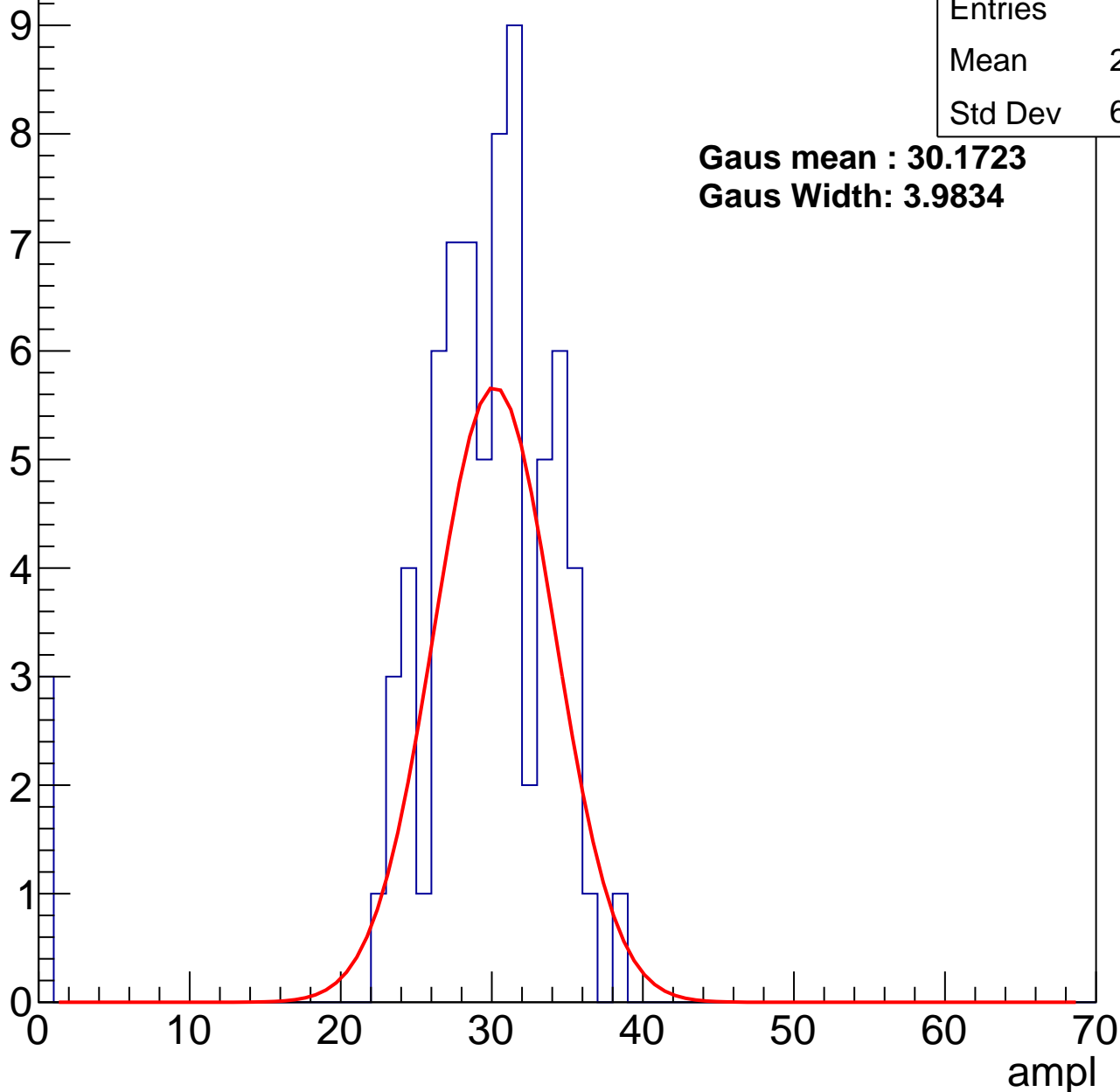
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.27
Std Dev	6.837

Gaus mean : 30.1723

Gaus Width: 3.9834



B1L100S, U13-ch116, adc1

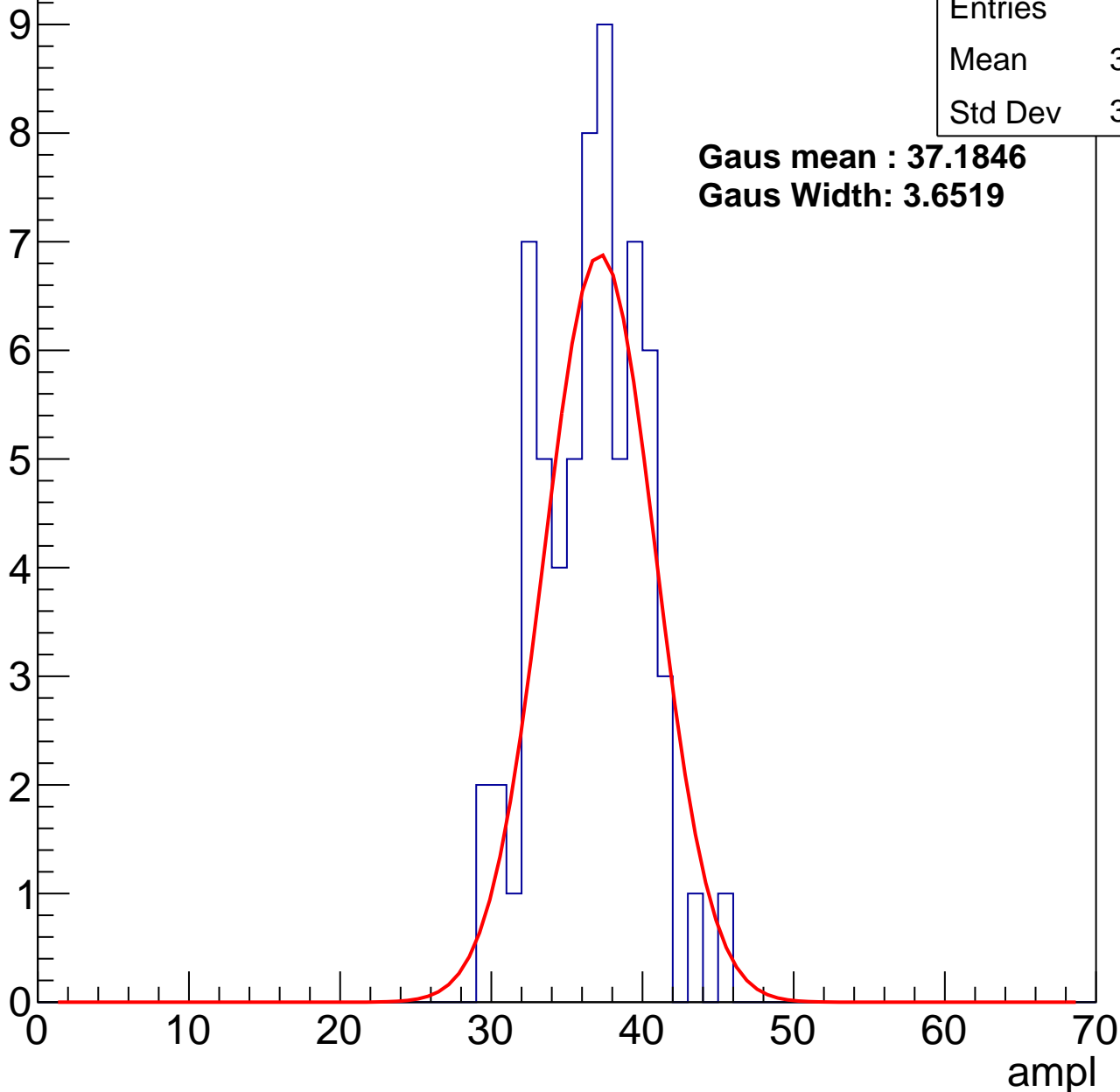
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.12
Std Dev	3.405

Gaus mean : 37.1846

Gaus Width: 3.6519



B1L100S, U13-ch116, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	43.66
Std Dev	3.532

Gaus mean : 44.4057

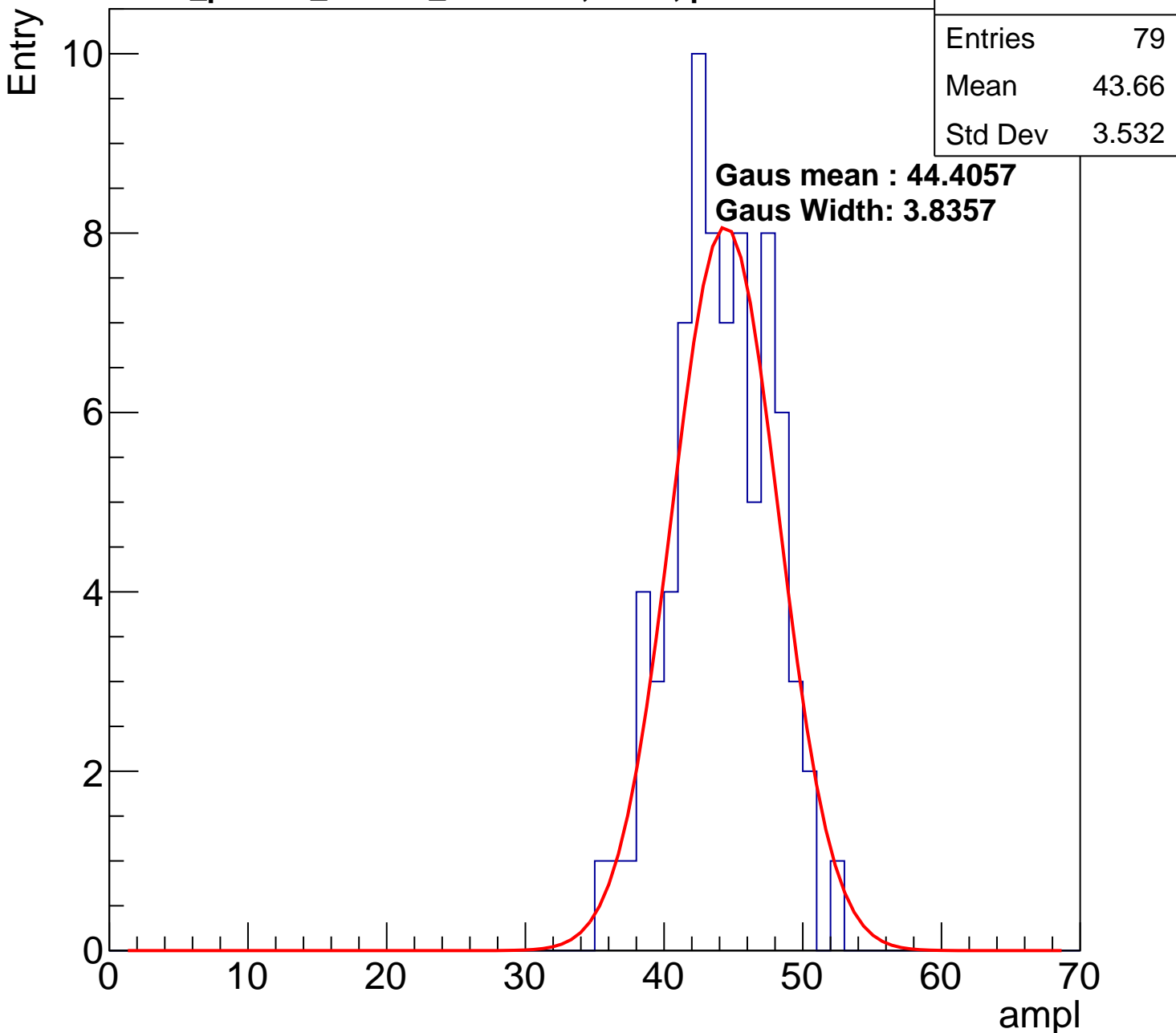
Gaus Width: 3.8357

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

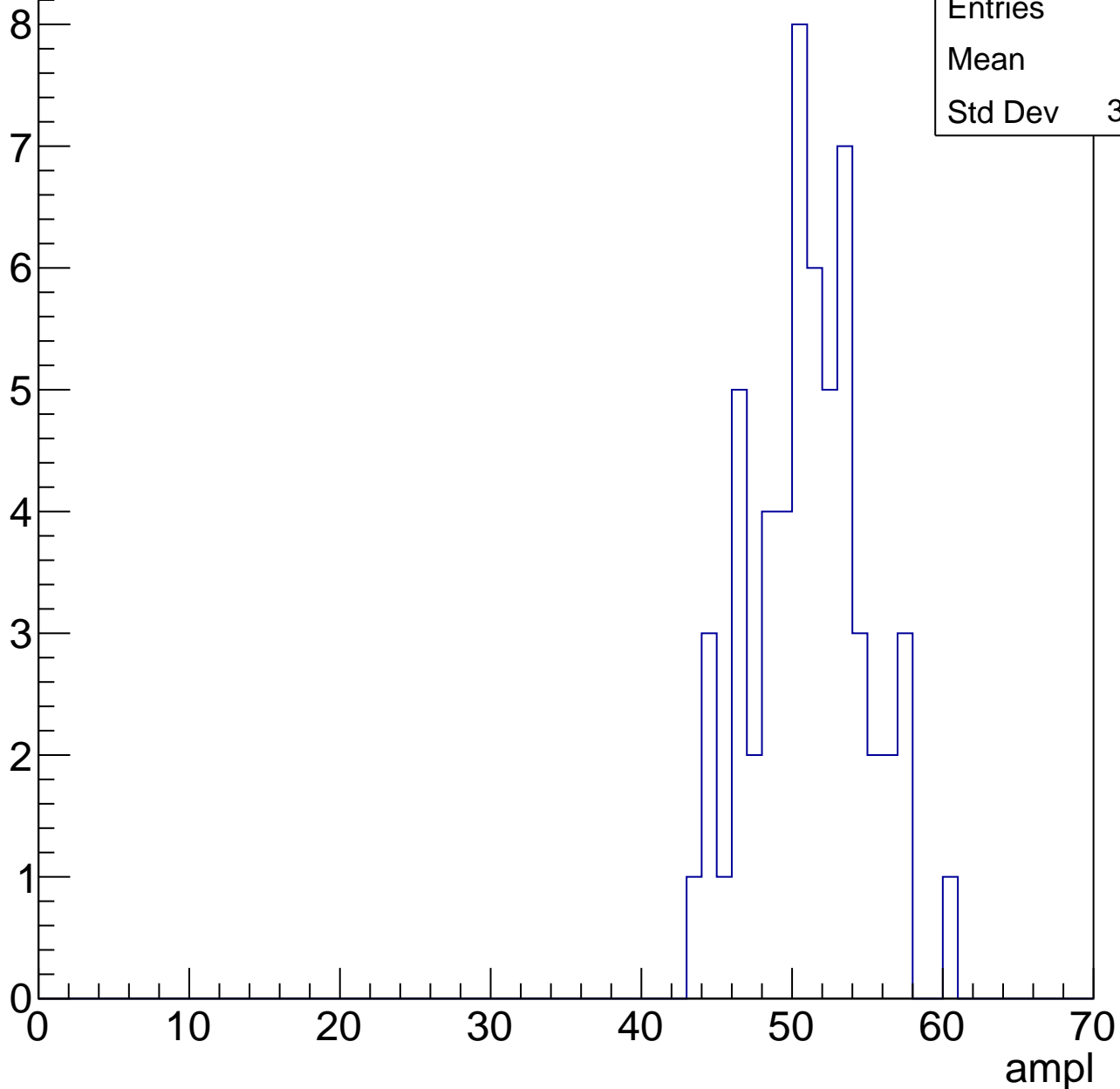


B1L100S, U13-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

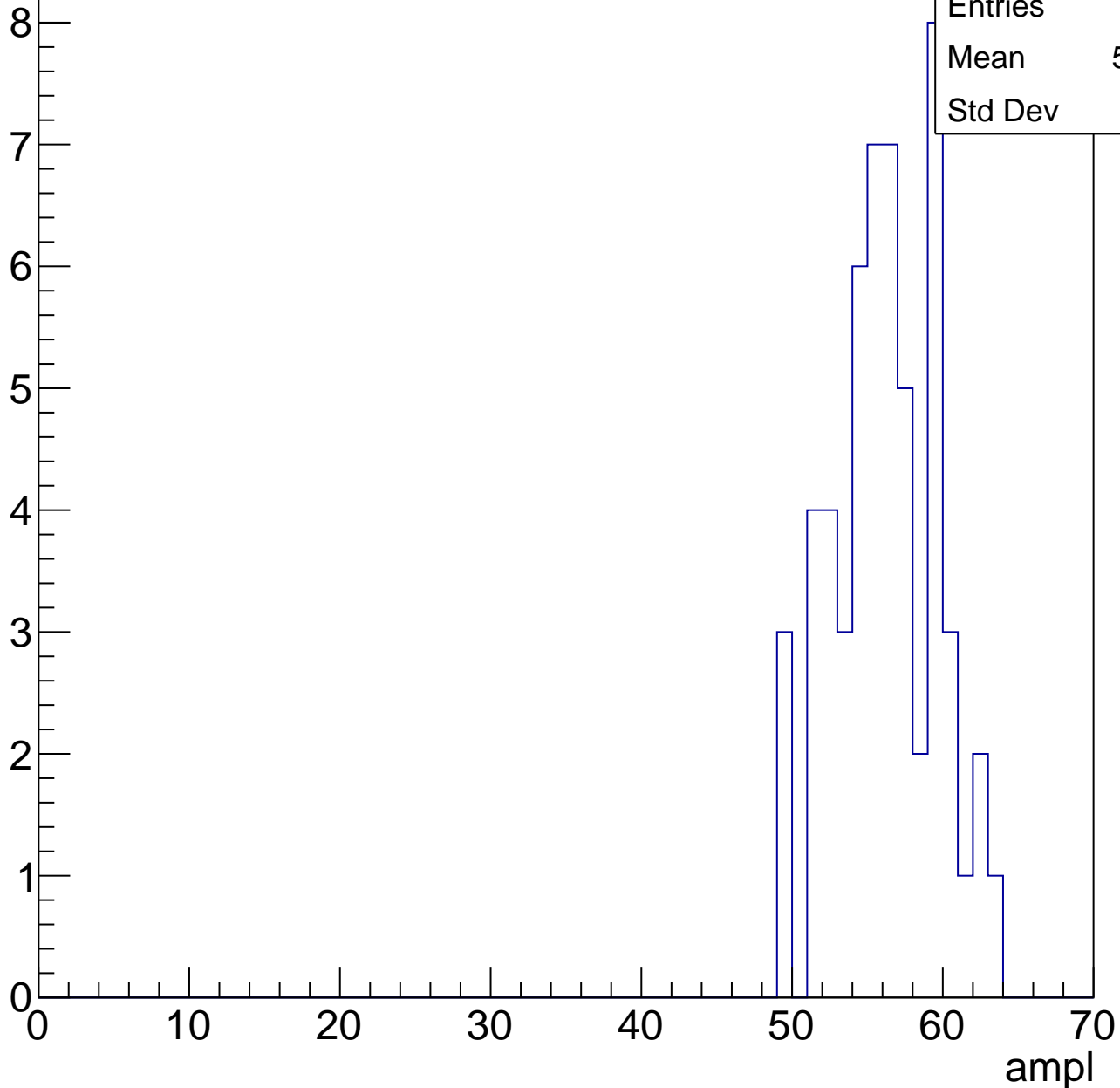
Entries	57
Mean	50.6
Std Dev	3.713



B1L100S, U13-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

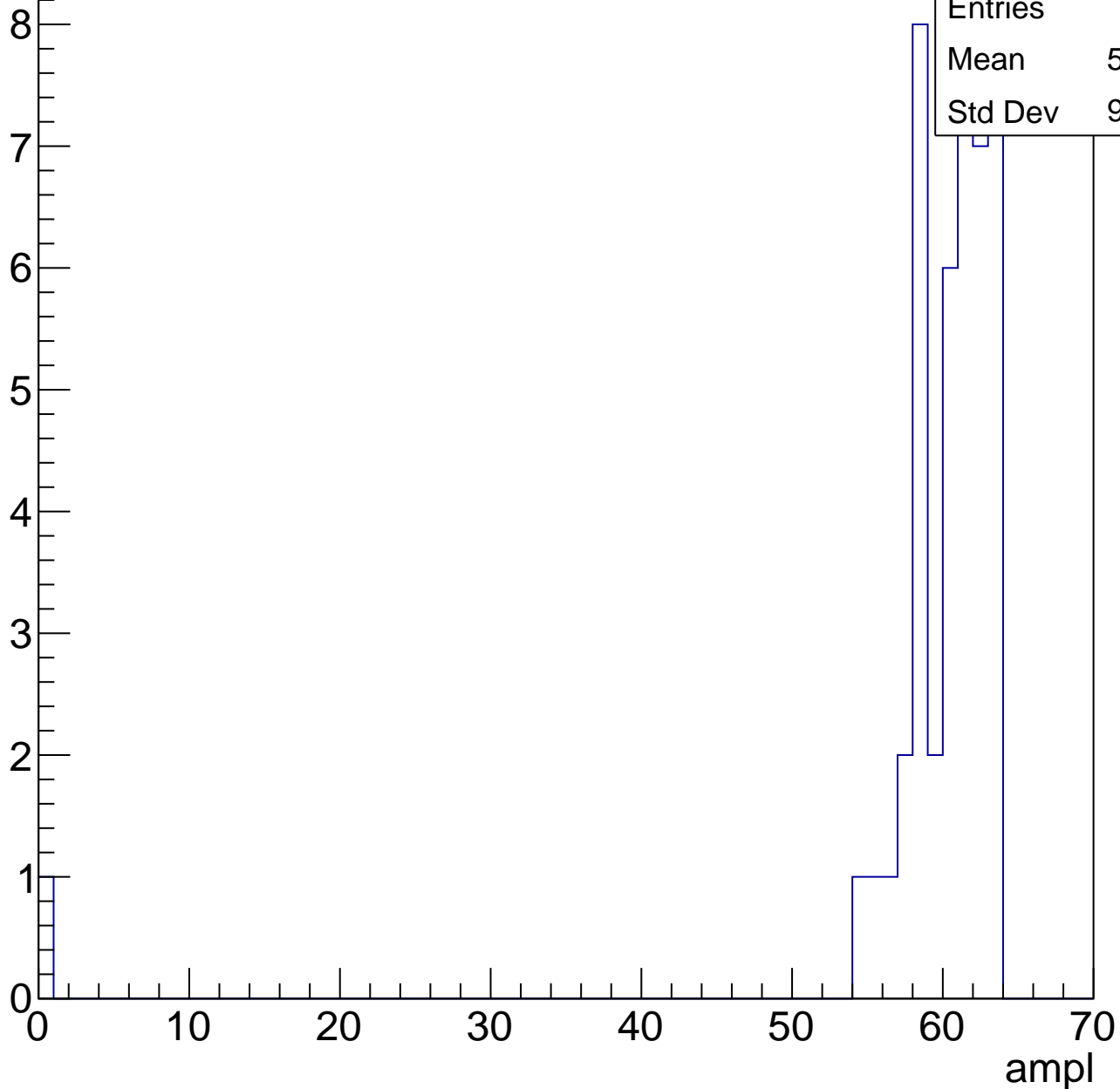
Entry



B1L100S, U13-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U13-ch117, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	31.59
Std Dev	4.992

Gaus mean : 32.0786

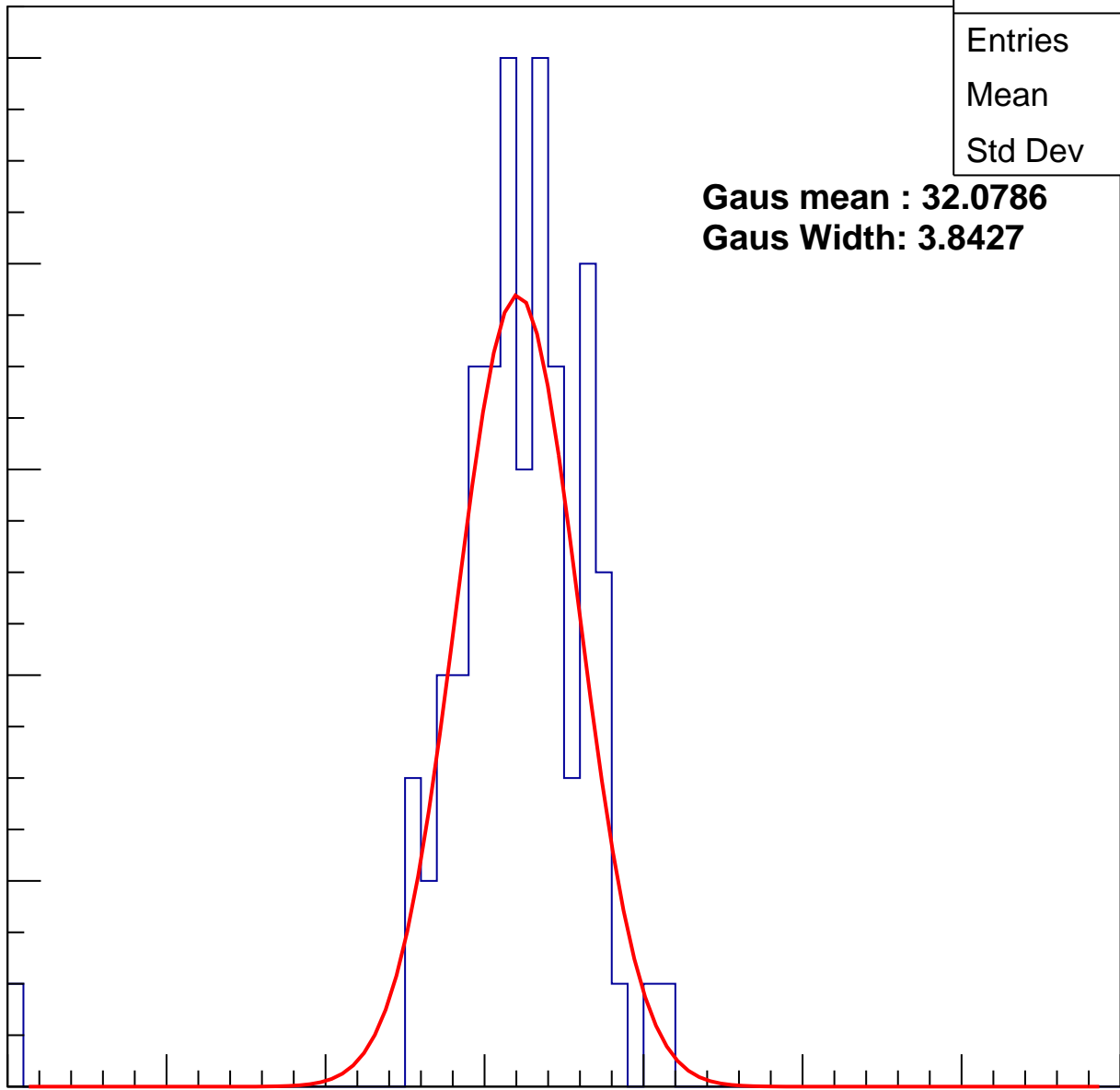
Gaus Width: 3.8427

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch117, adc1

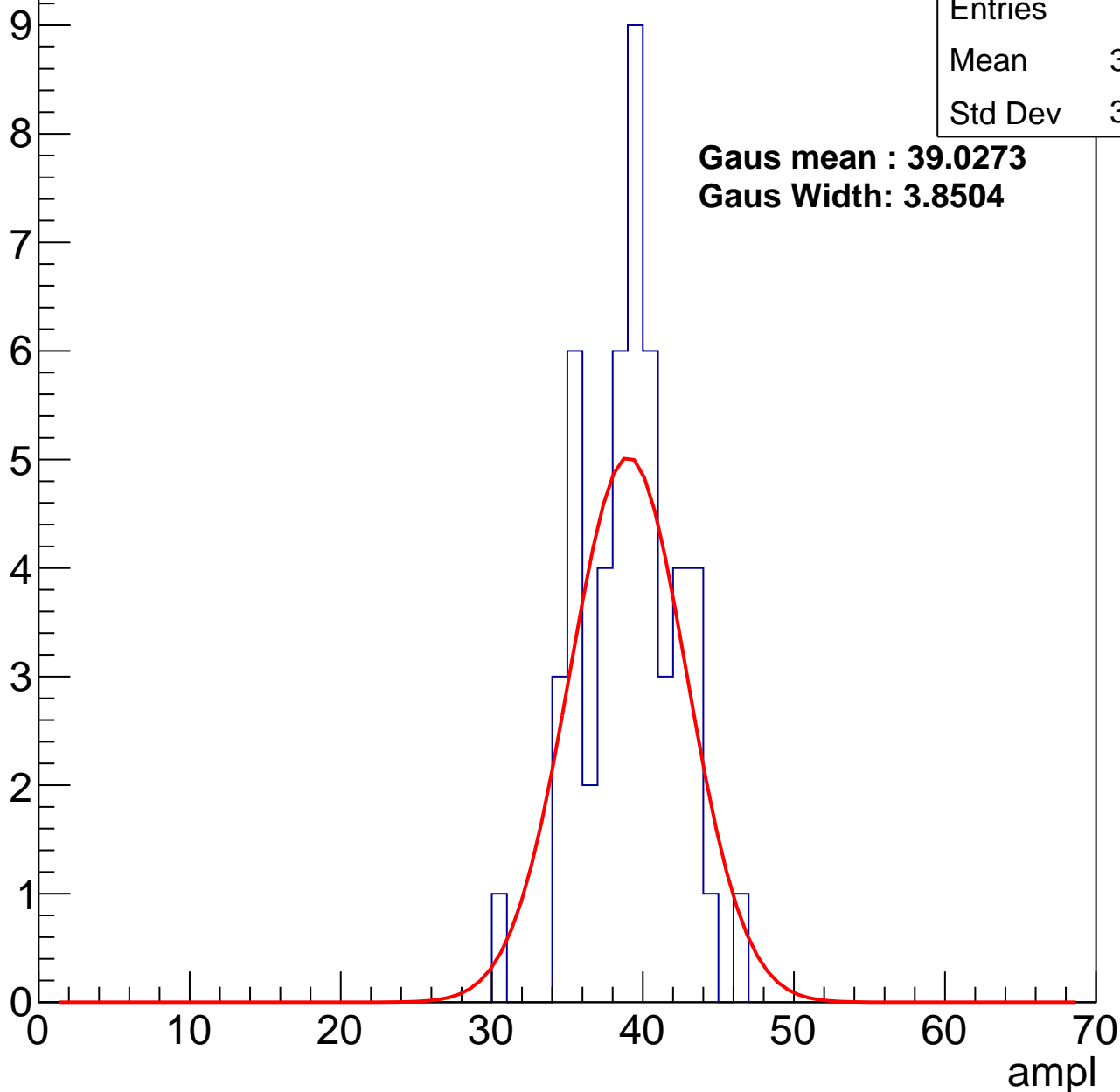
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	38.68
Std Dev	3.095

Gaus mean : 39.0273

Gaus Width: 3.8504



B1L100S, U13-ch117, adc2

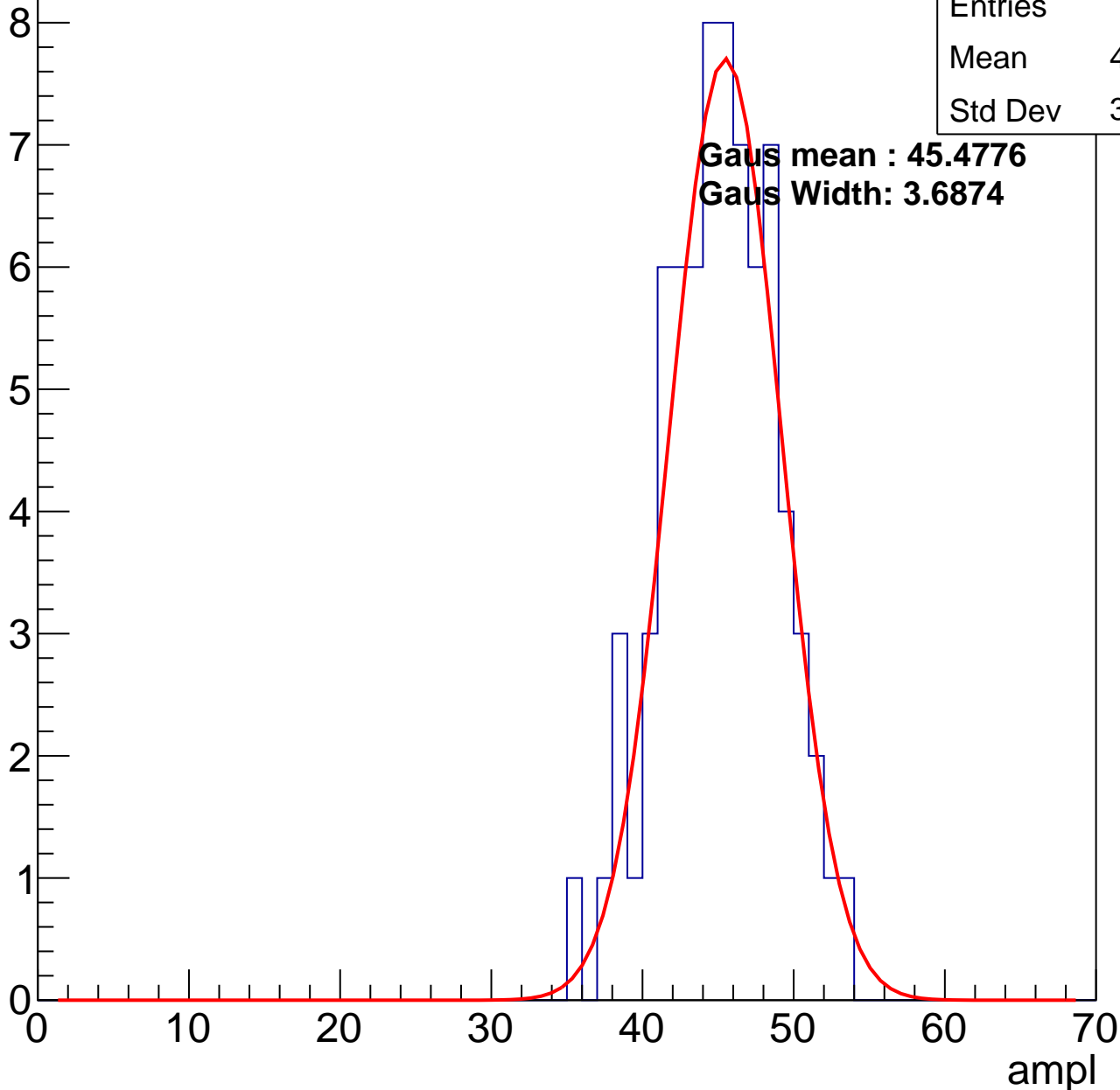
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	44.68
Std Dev	3.702

Gaus mean : 45.4776

Gaus Width: 3.6874

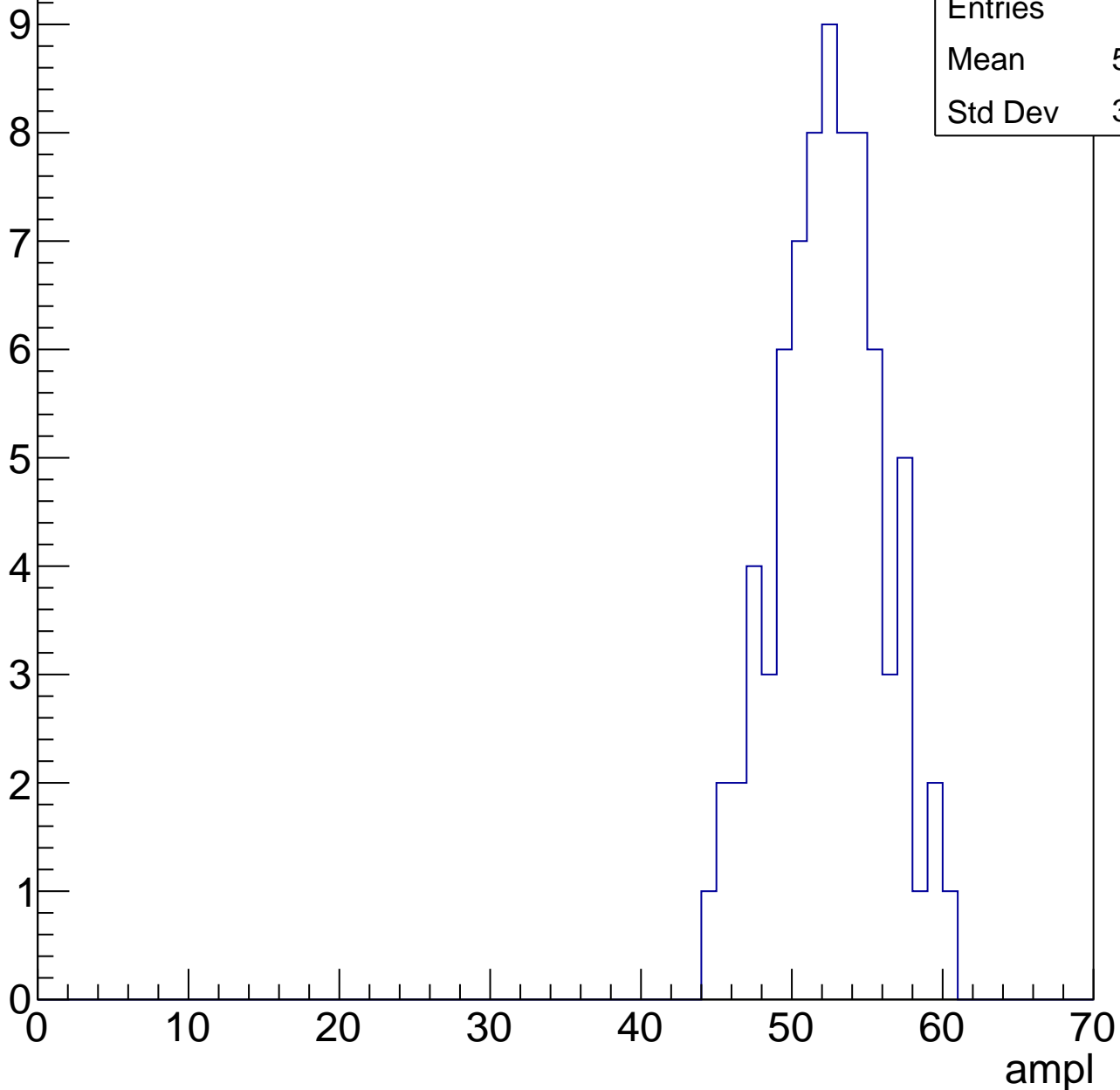


B1L100S, U13-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

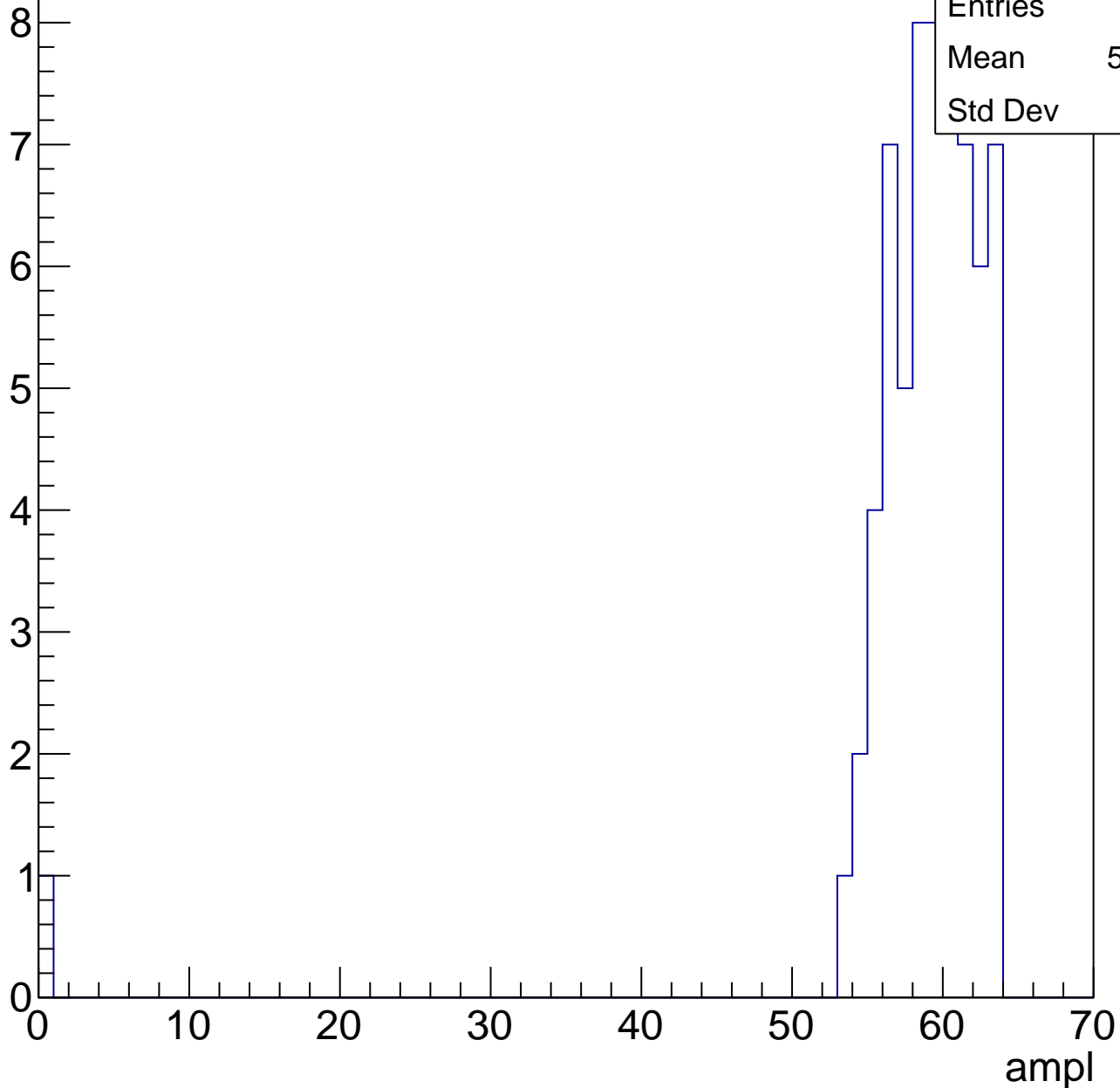
Entries	76
Mean	52.01
Std Dev	3.511



B1L100S, U13-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

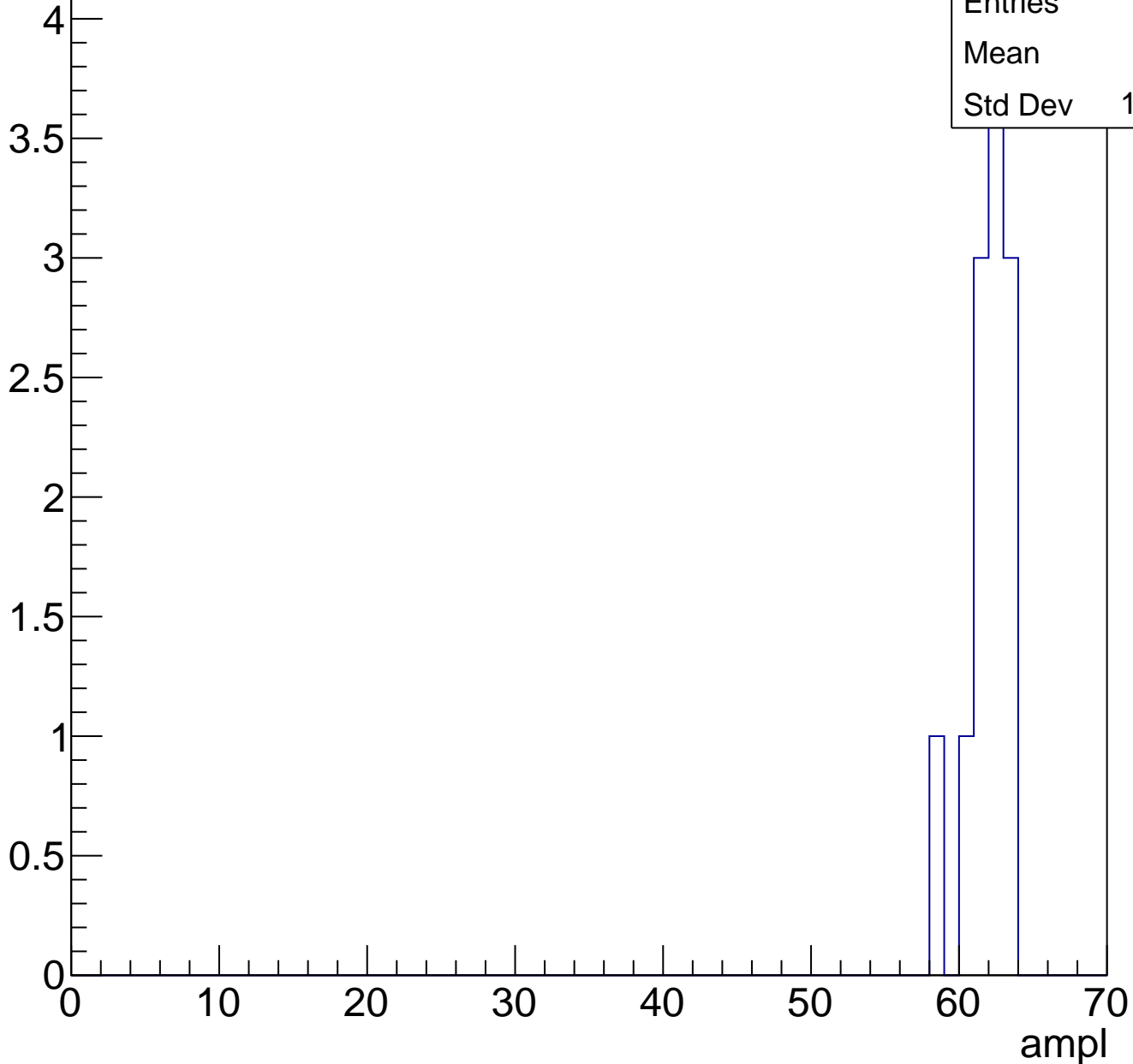
Entry



B1L100S, U13-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

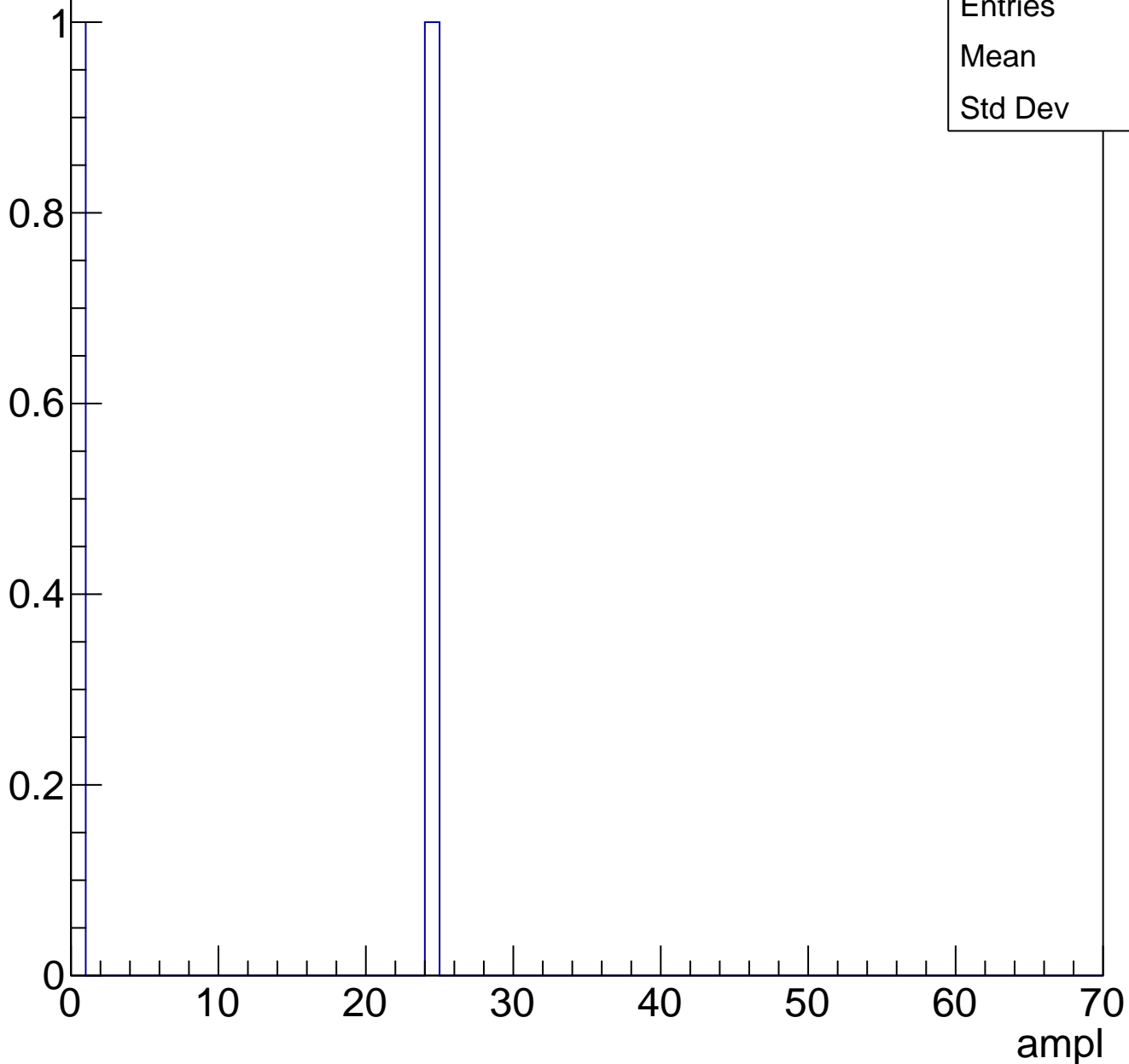
Entry



B1L100S, U13-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch118, adc0

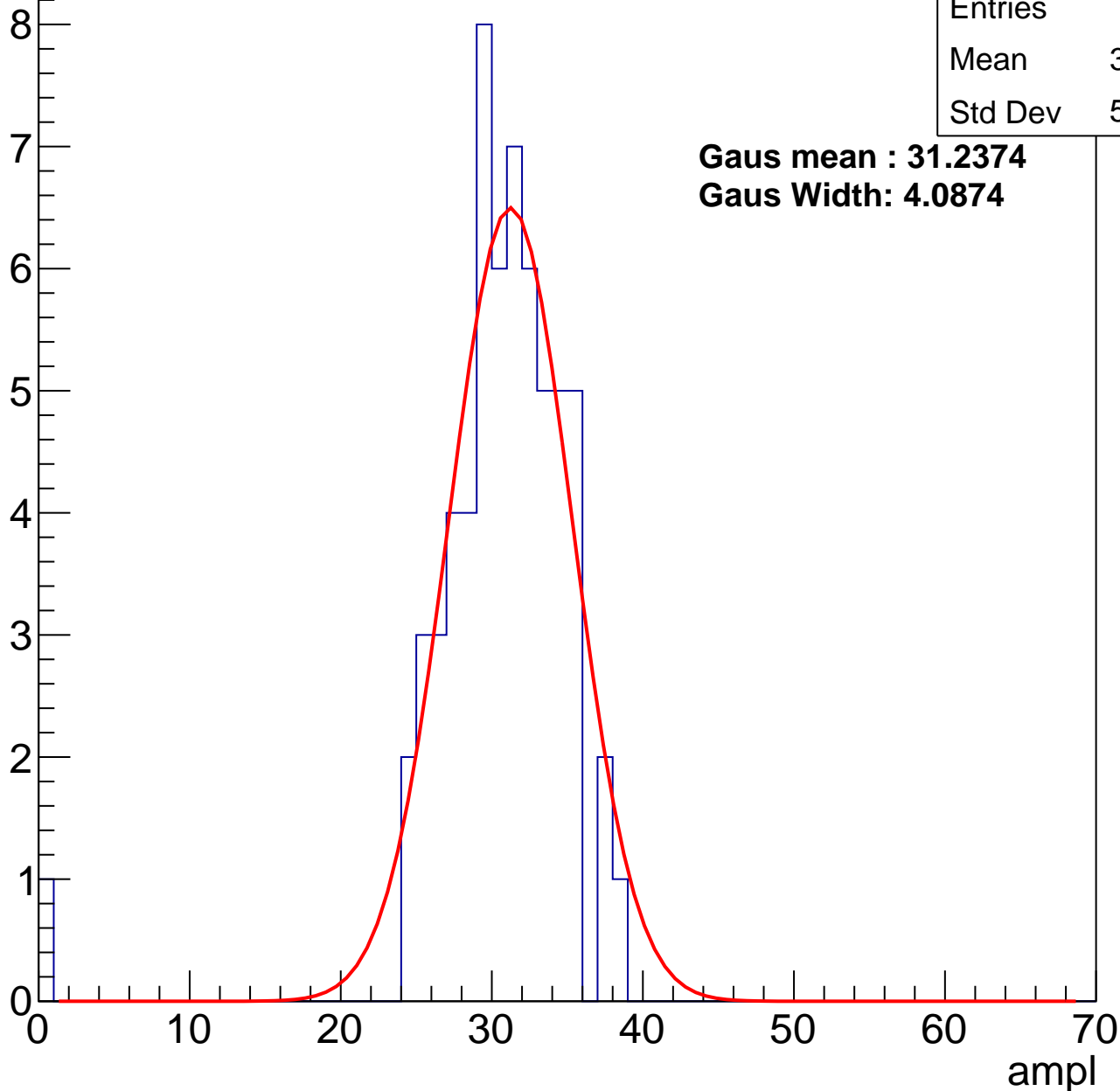
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.06
Std Dev	5.076

Gaus mean : 31.2374

Gaus Width: 4.0874



B1L100S, U13-ch118, adc1

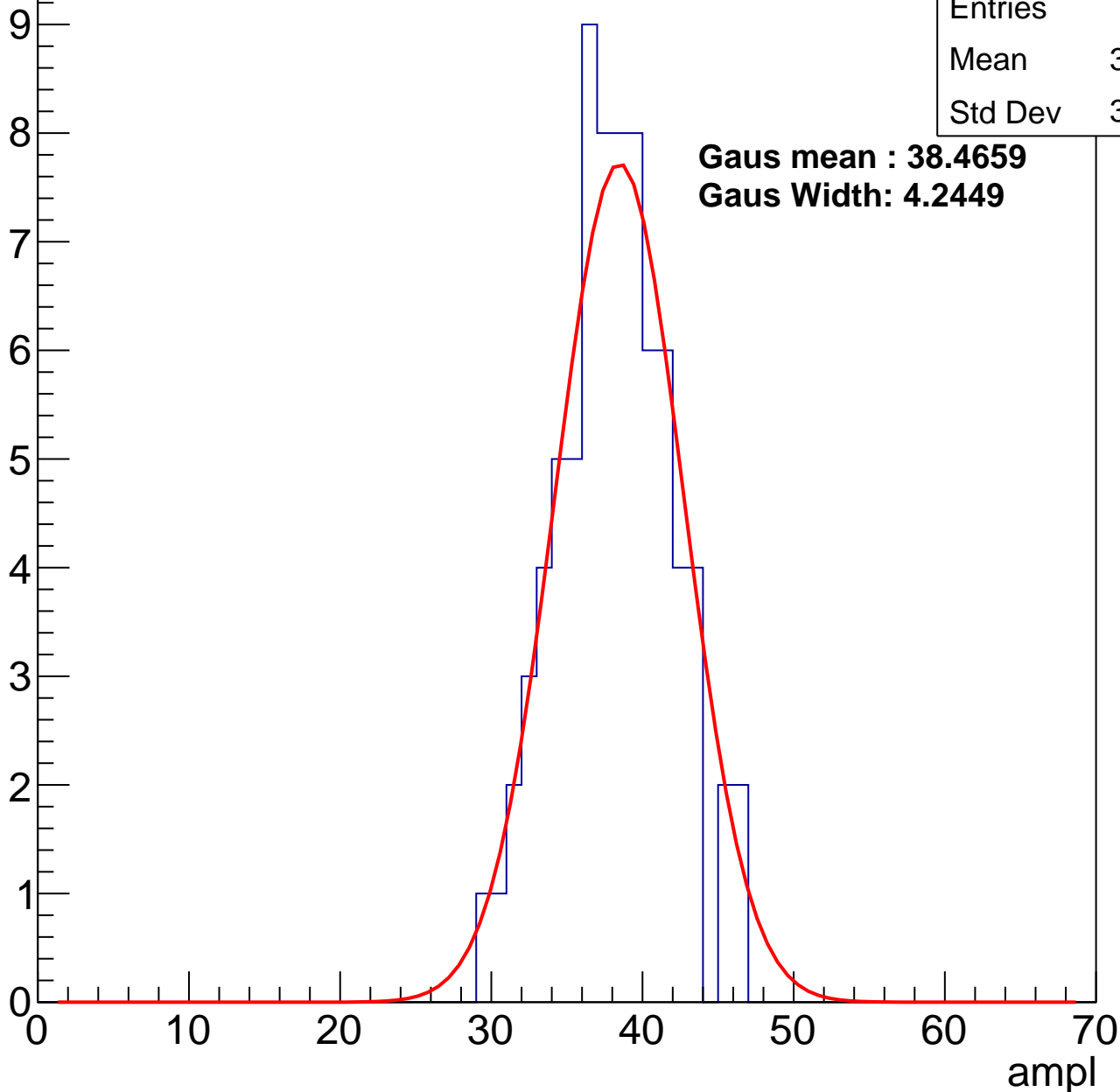
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	37.67
Std Dev	3.734

Gaus mean : 38.4659

Gaus Width: 4.2449



B1L100S, U13-ch118, adc2

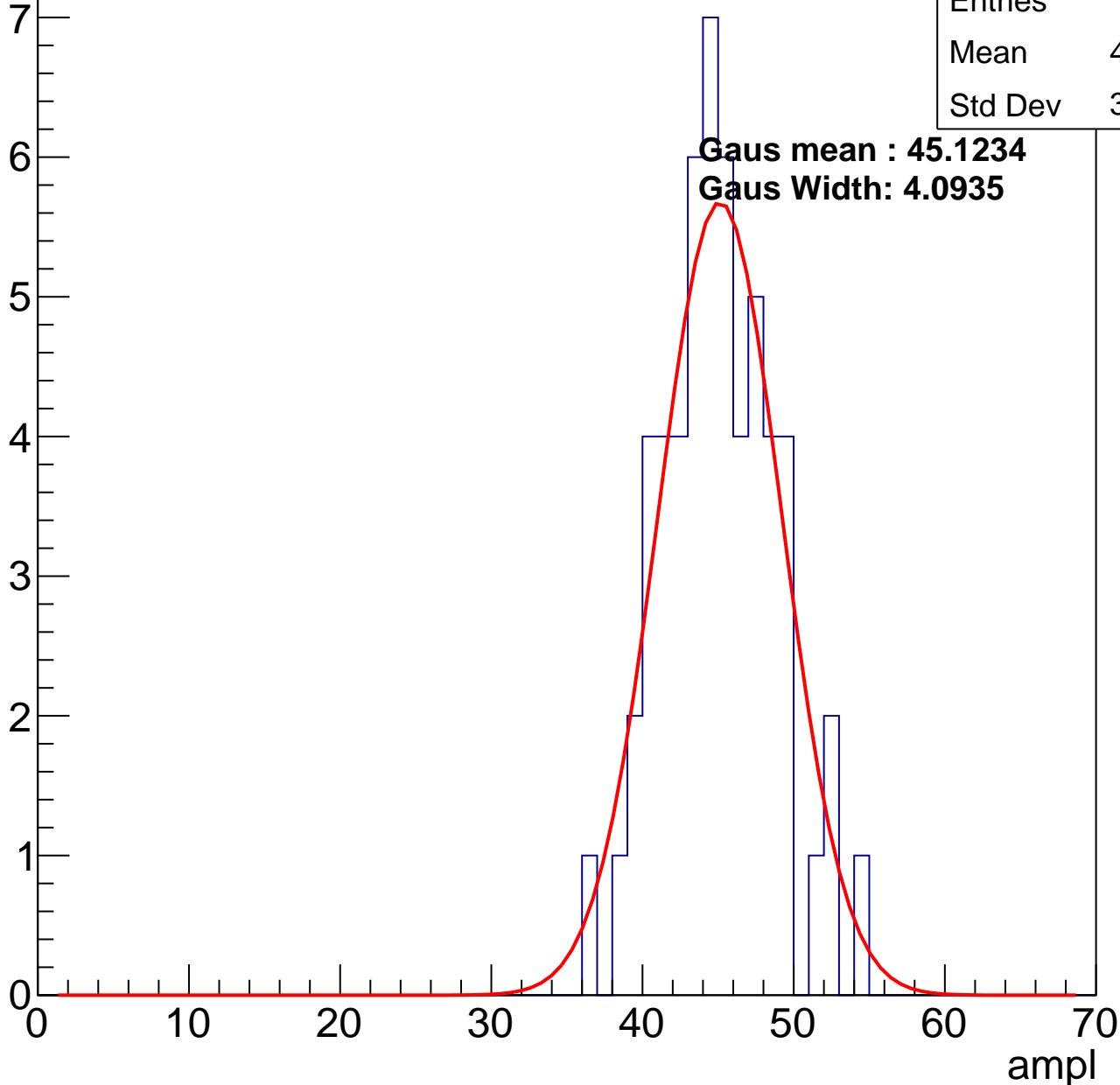
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.57
Std Dev	3.688

Gaus mean : 45.1234

Gaus Width: 4.0935

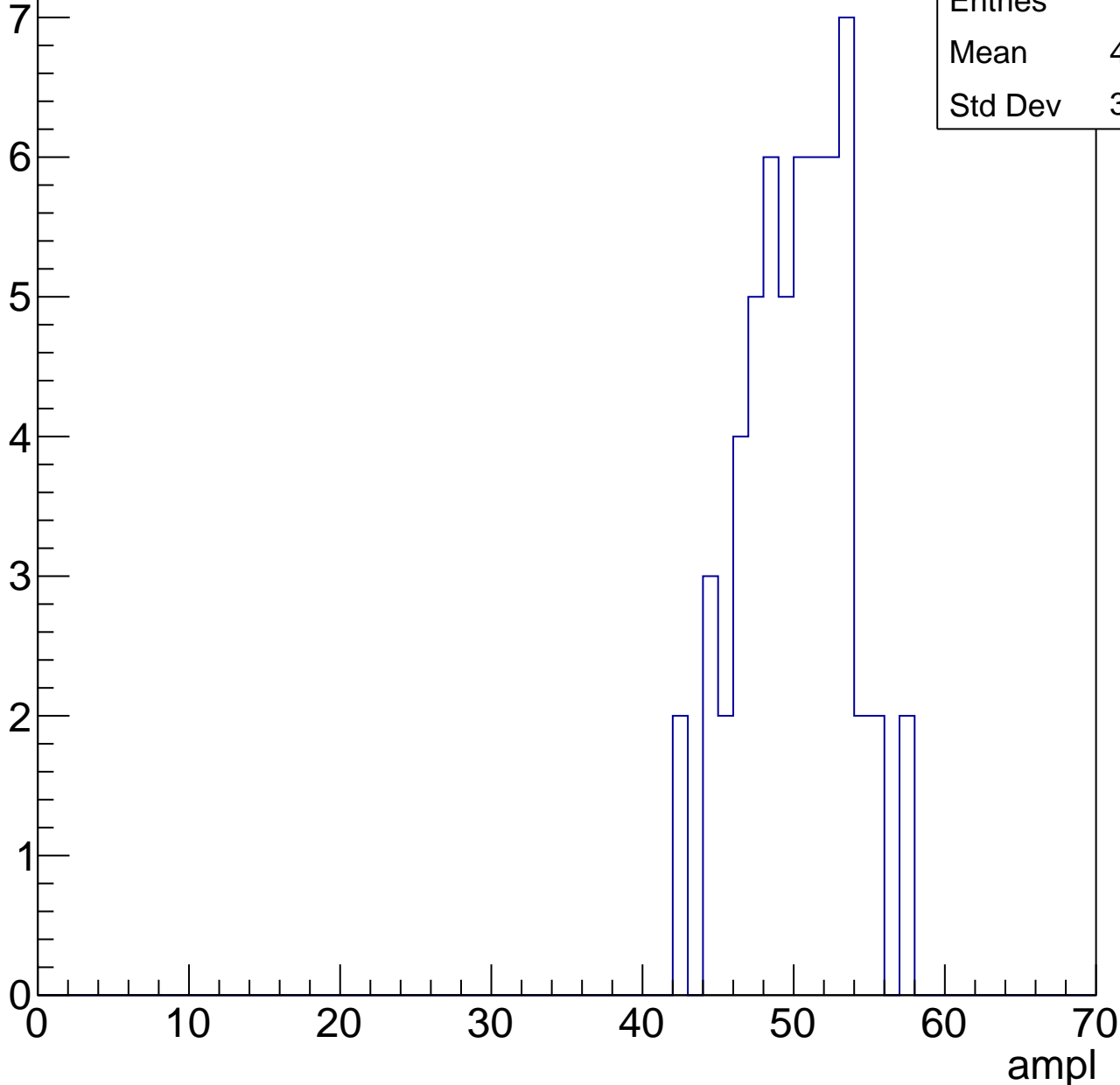


B1L100S, U13-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.64
Std Dev	3.448

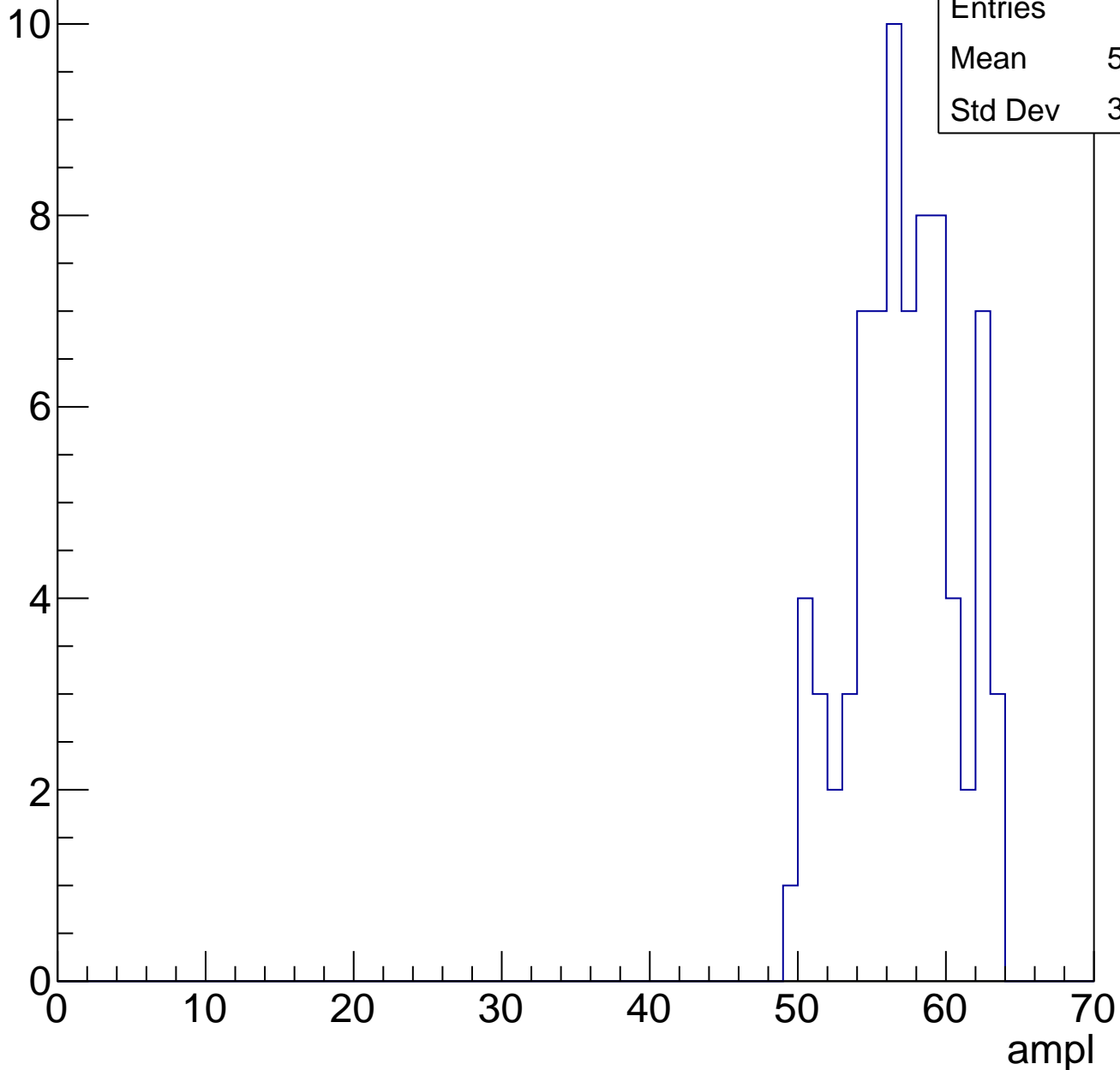


B1L100S, U13-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	56.68
Std Dev	3.536

Entry

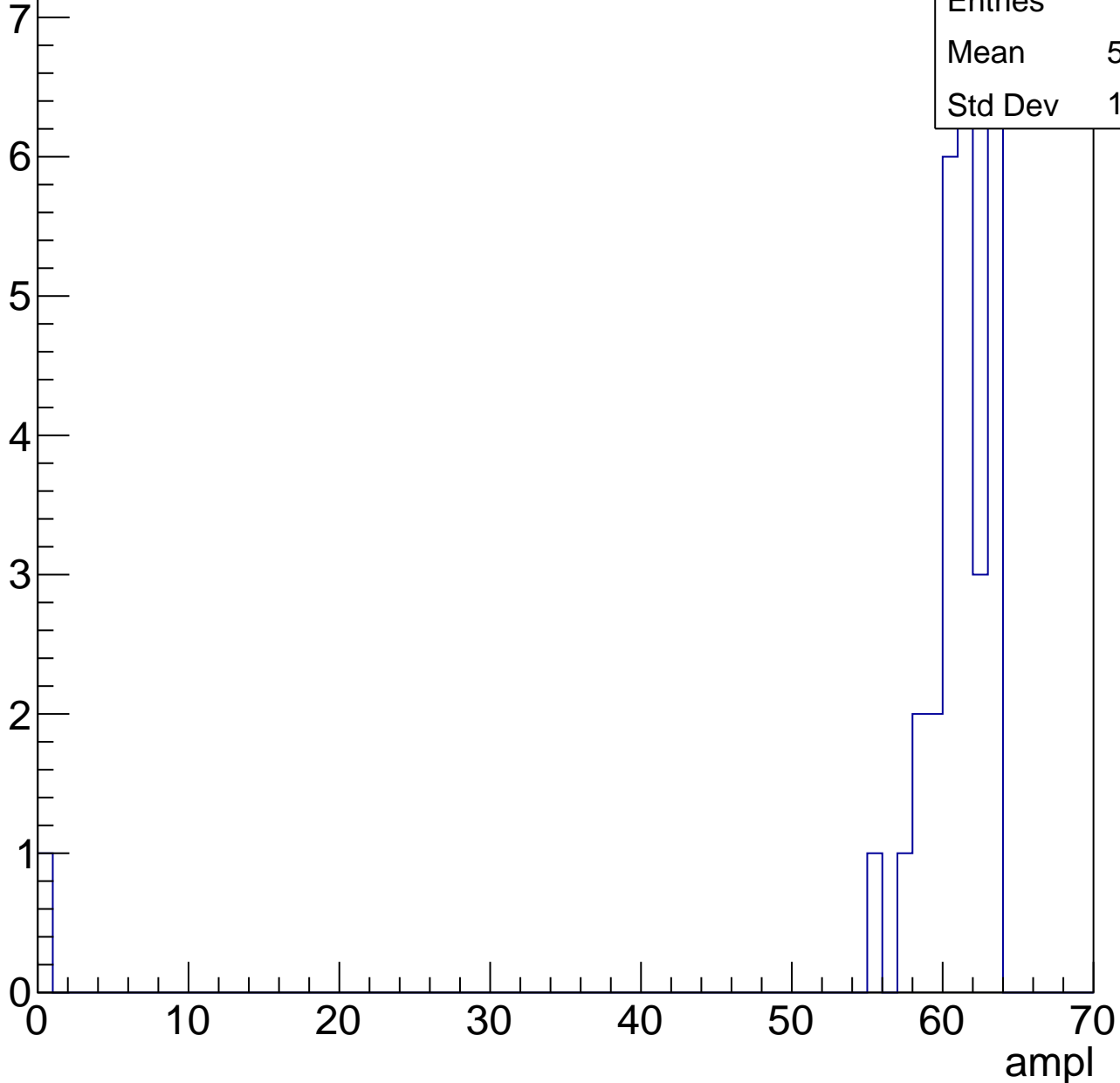


B1L100S, U13-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.67
Std Dev	11.06



B1L100S, U13-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U13-ch119, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	29.69
Std Dev	5.686

Gaus mean : 31.1100

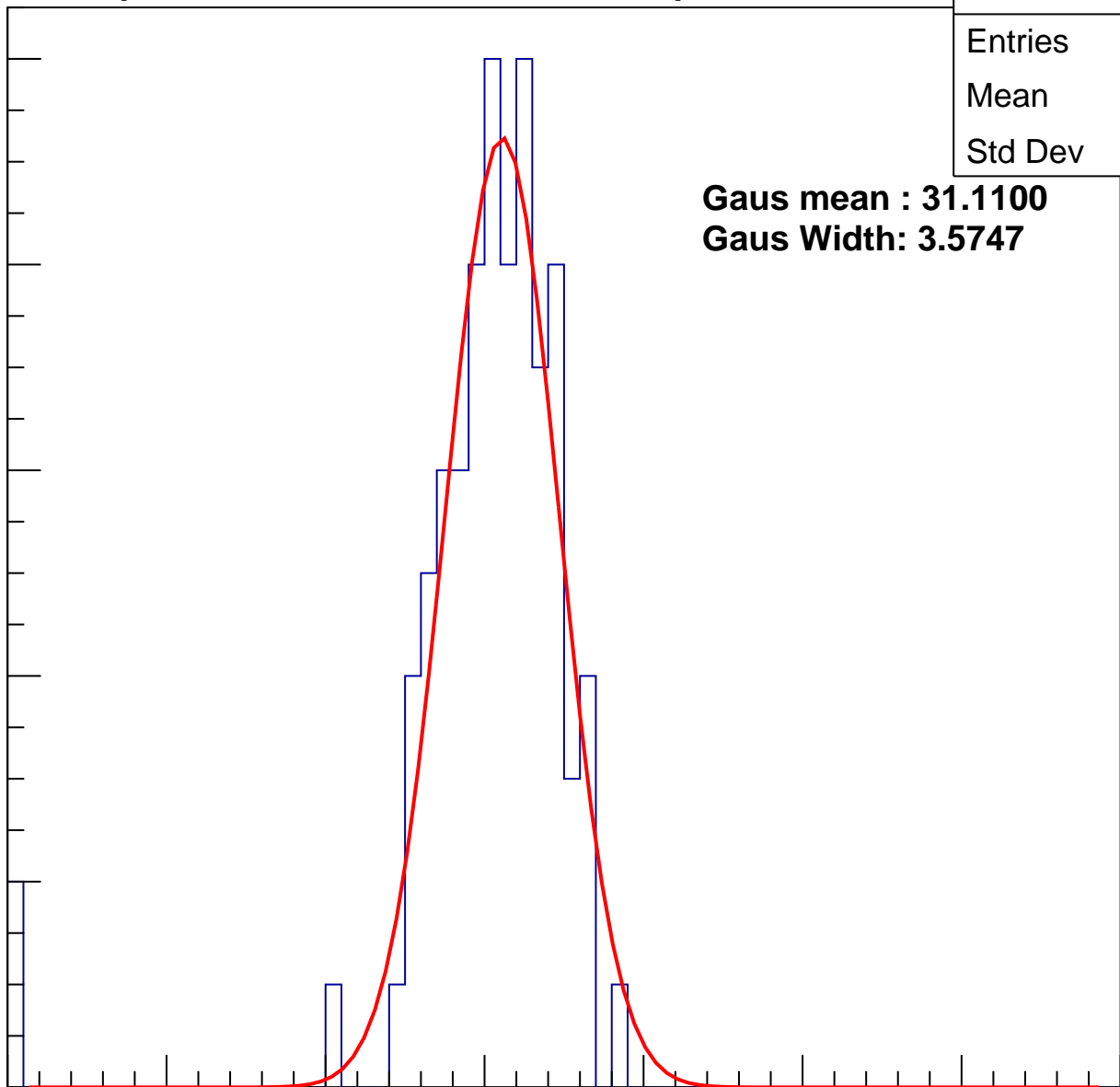
Gaus Width: 3.5747

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch119, adc1

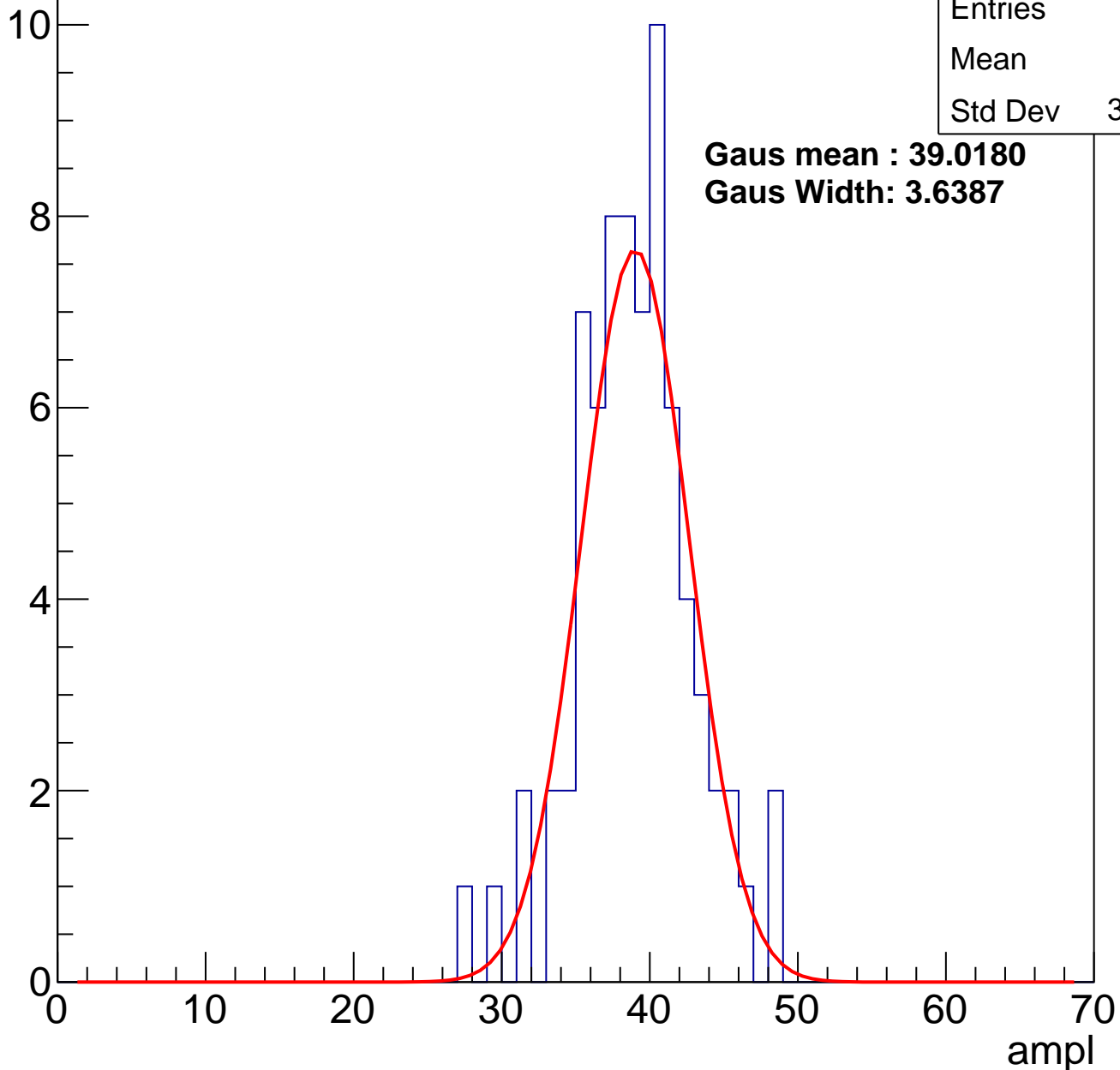
calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	38.5
Std Dev	3.912

Gaus mean : 39.0180

Gaus Width: 3.6387

Entry



B1L100S, U13-ch119, adc2

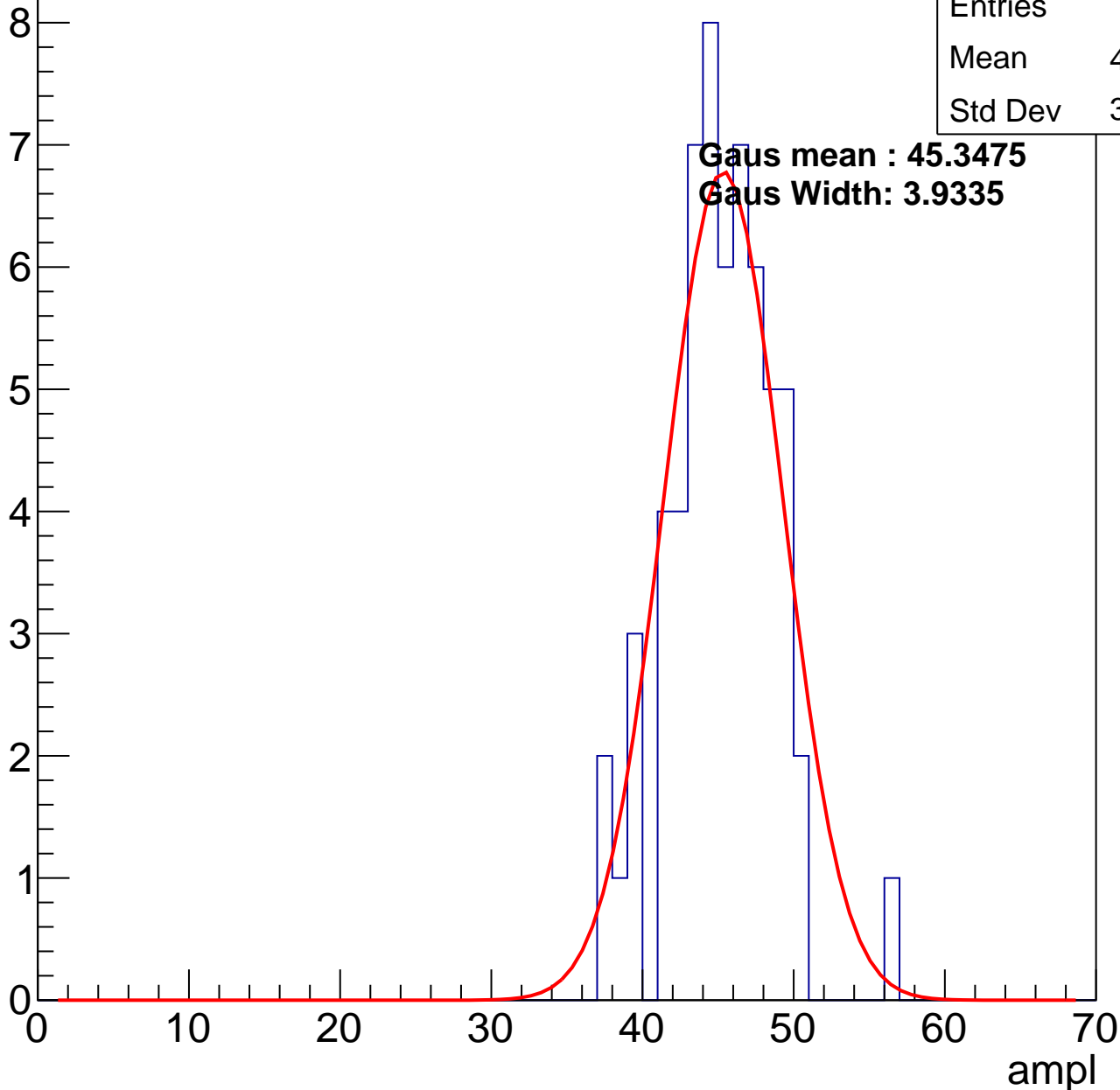
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.74
Std Dev	3.492

Gaus mean : 45.3475

Gaus Width: 3.9335

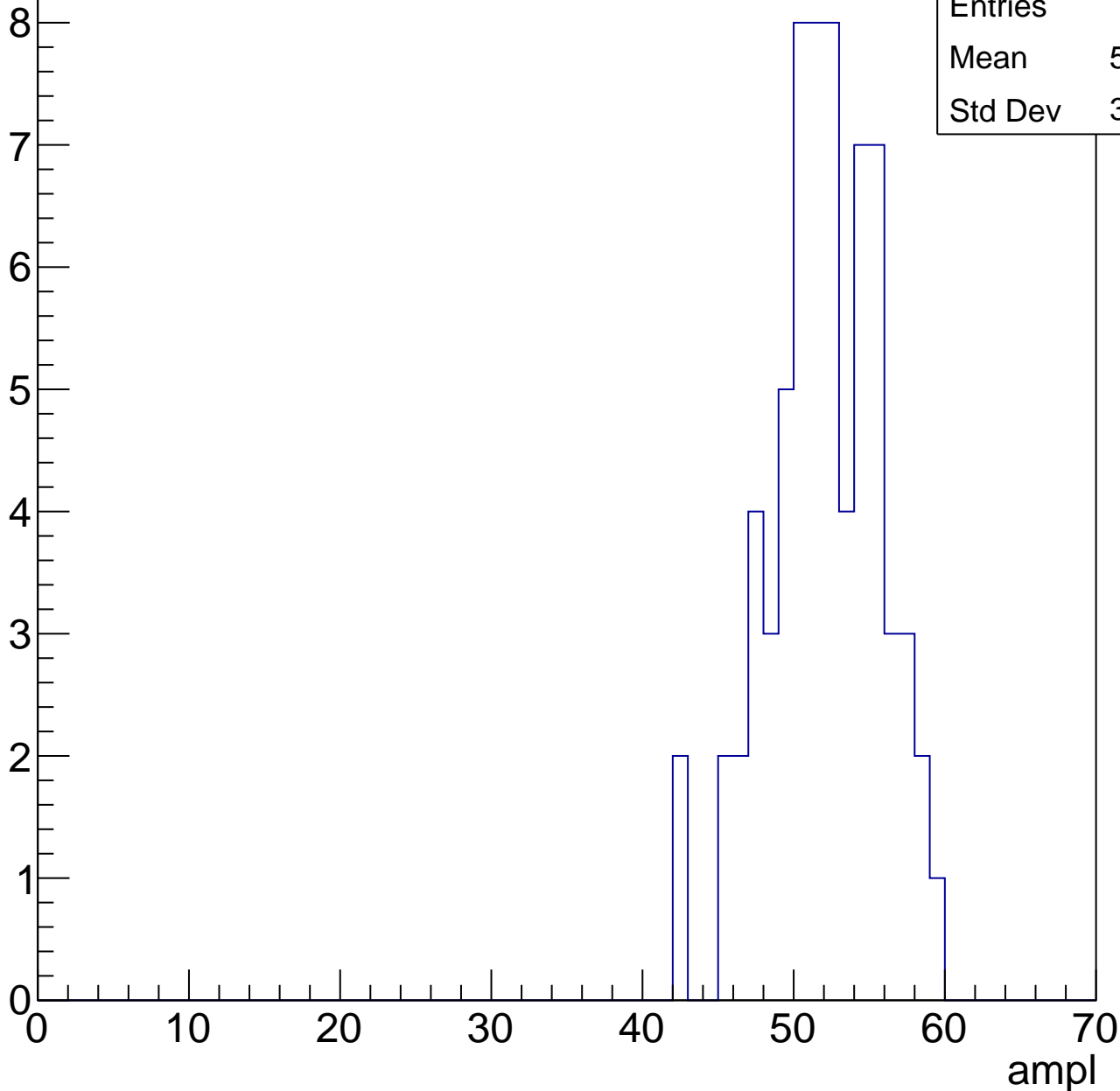


B1L100S, U13-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	51.54
Std Dev	3.666

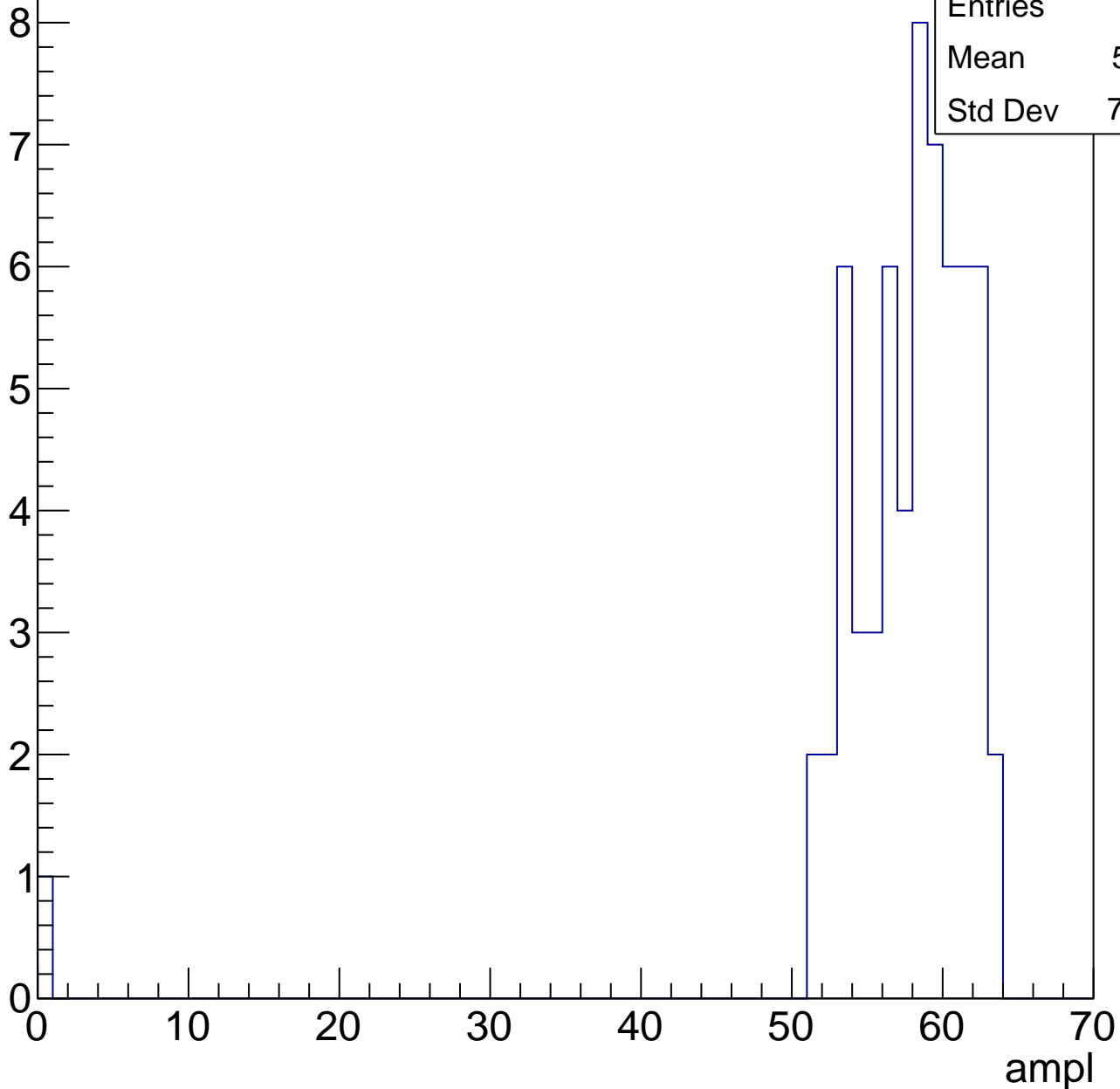


B1L100S, U13-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	56.71
Std Dev	7.946

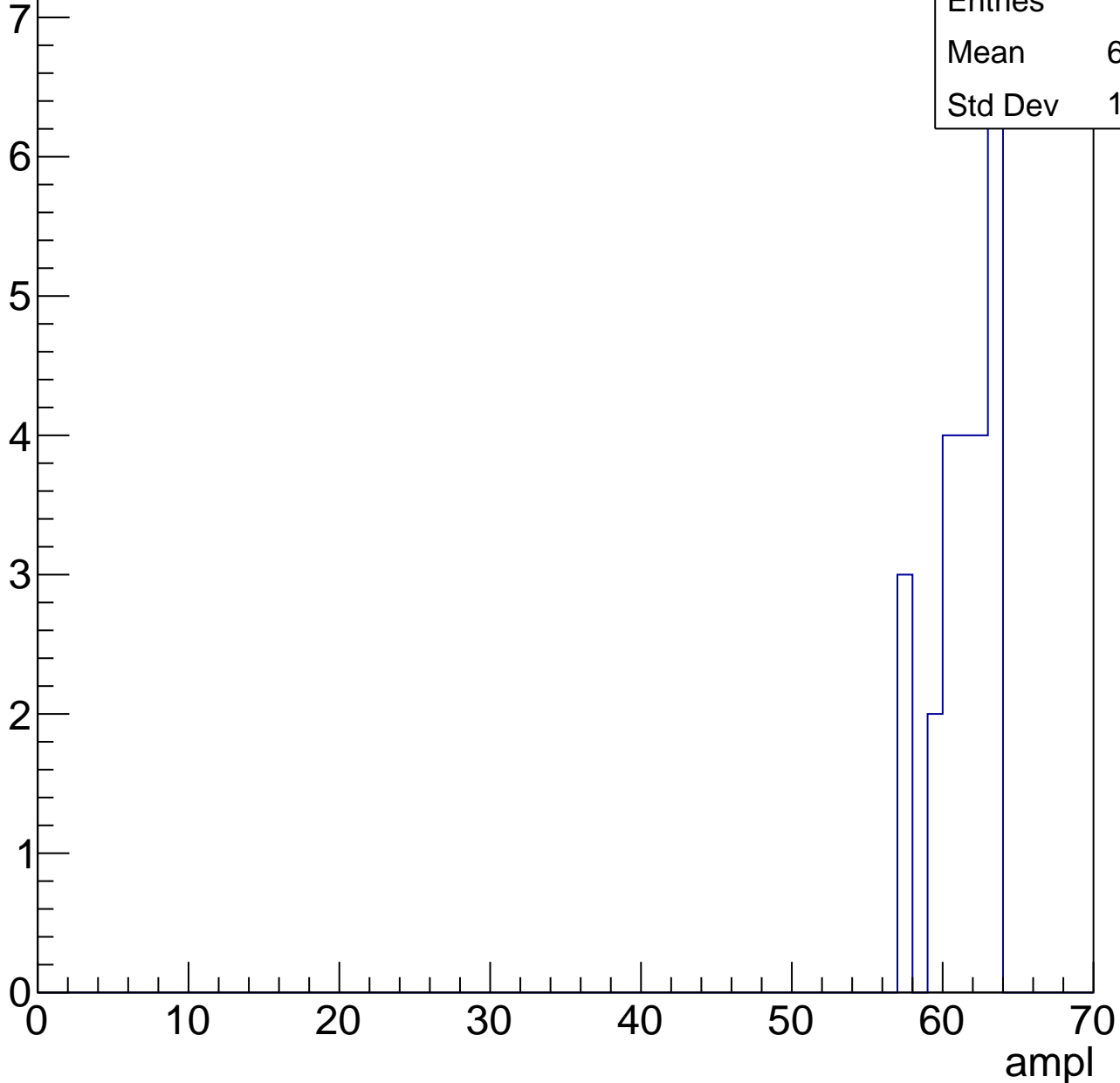


B1L100S, U13-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	60.92
Std Dev	1.956



B1L100S, U13-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

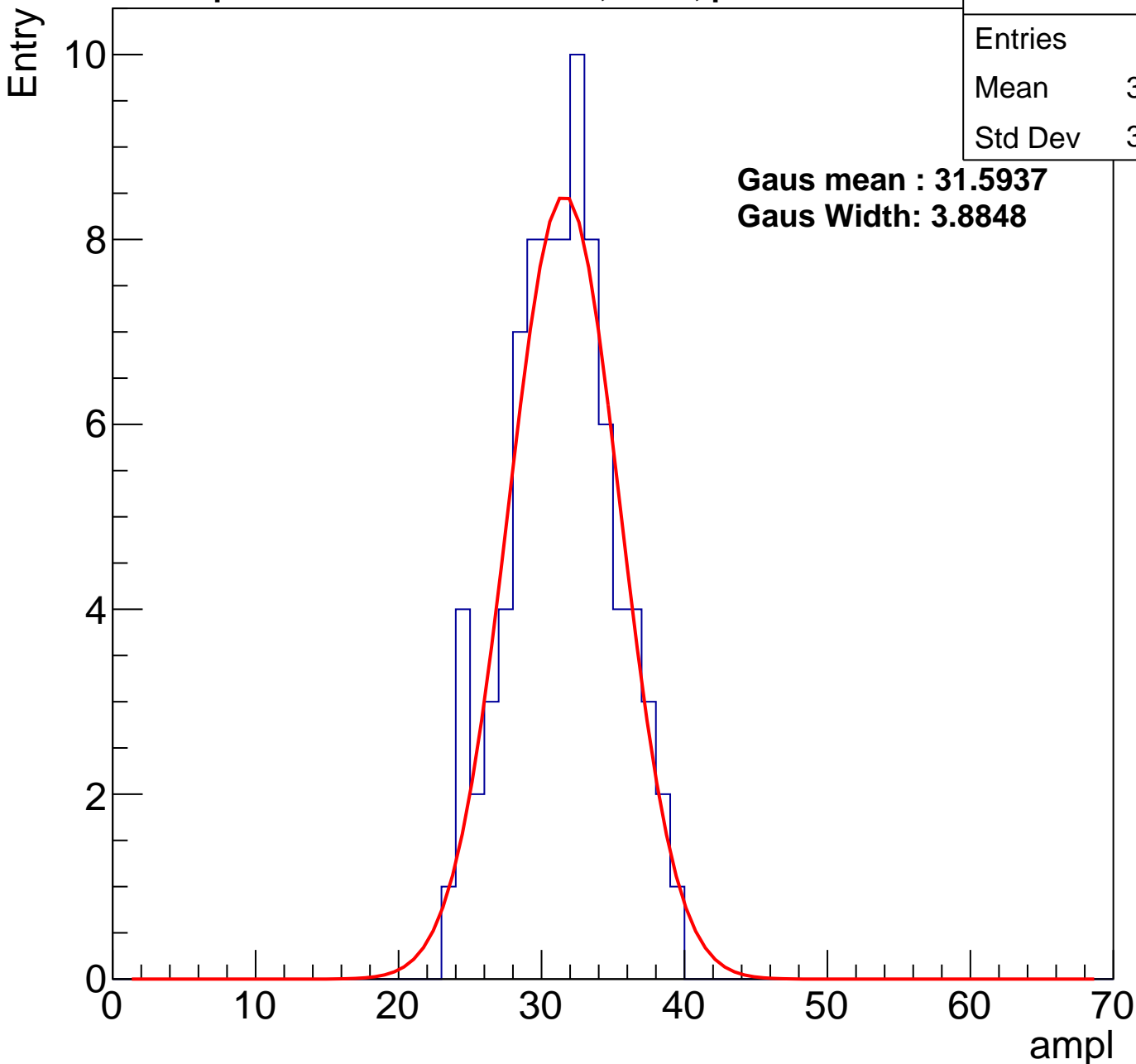


B1L100S, U13-ch120, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	30.95
Std Dev	3.647

Gaus mean : 31.5937
Gaus Width: 3.8848



B1L100S, U13-ch120, adc1

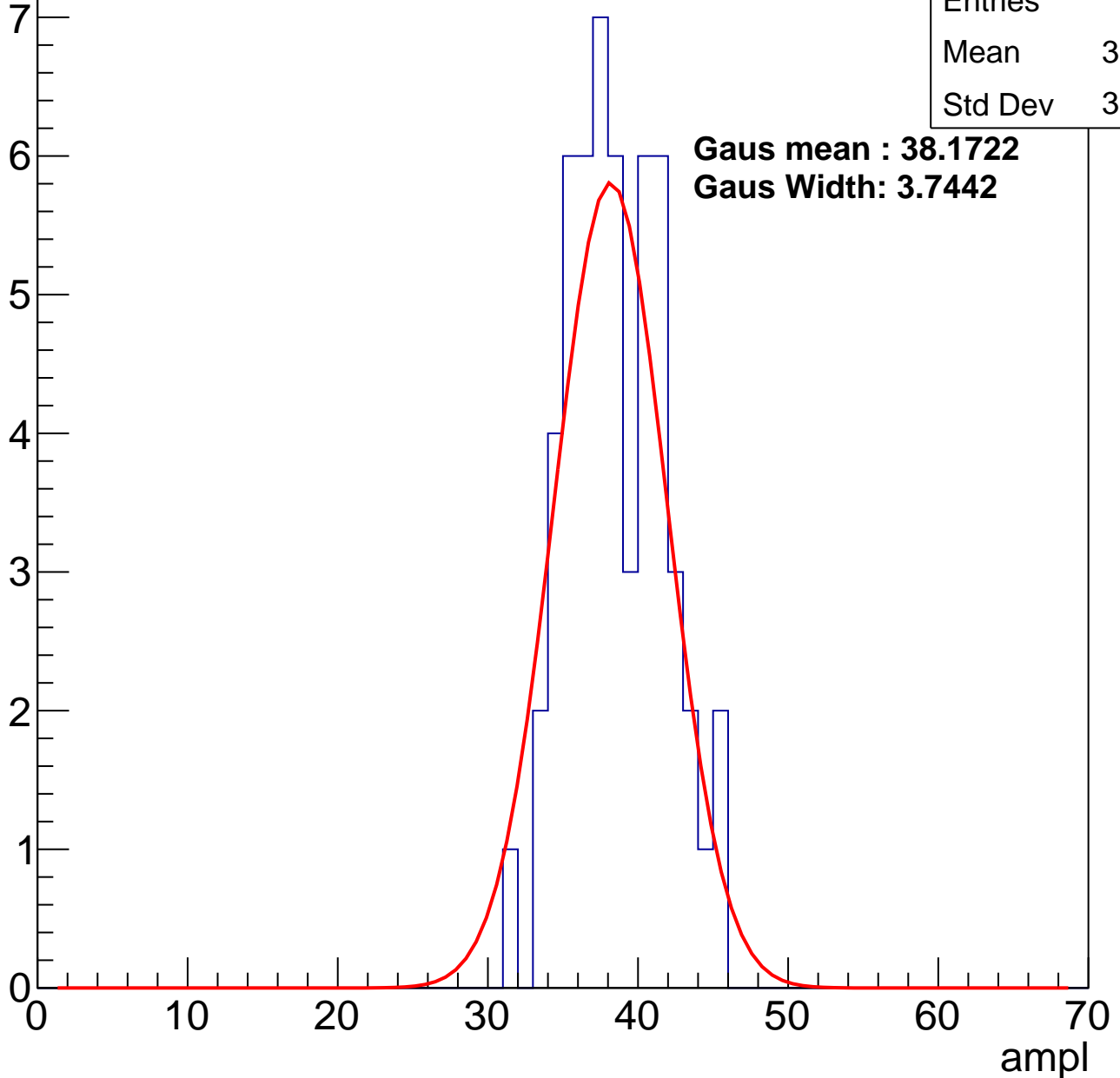
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	38.09
Std Dev	3.192

Gaus mean : 38.1722

Gaus Width: 3.7442



B1L100S, U13-ch120, adc2

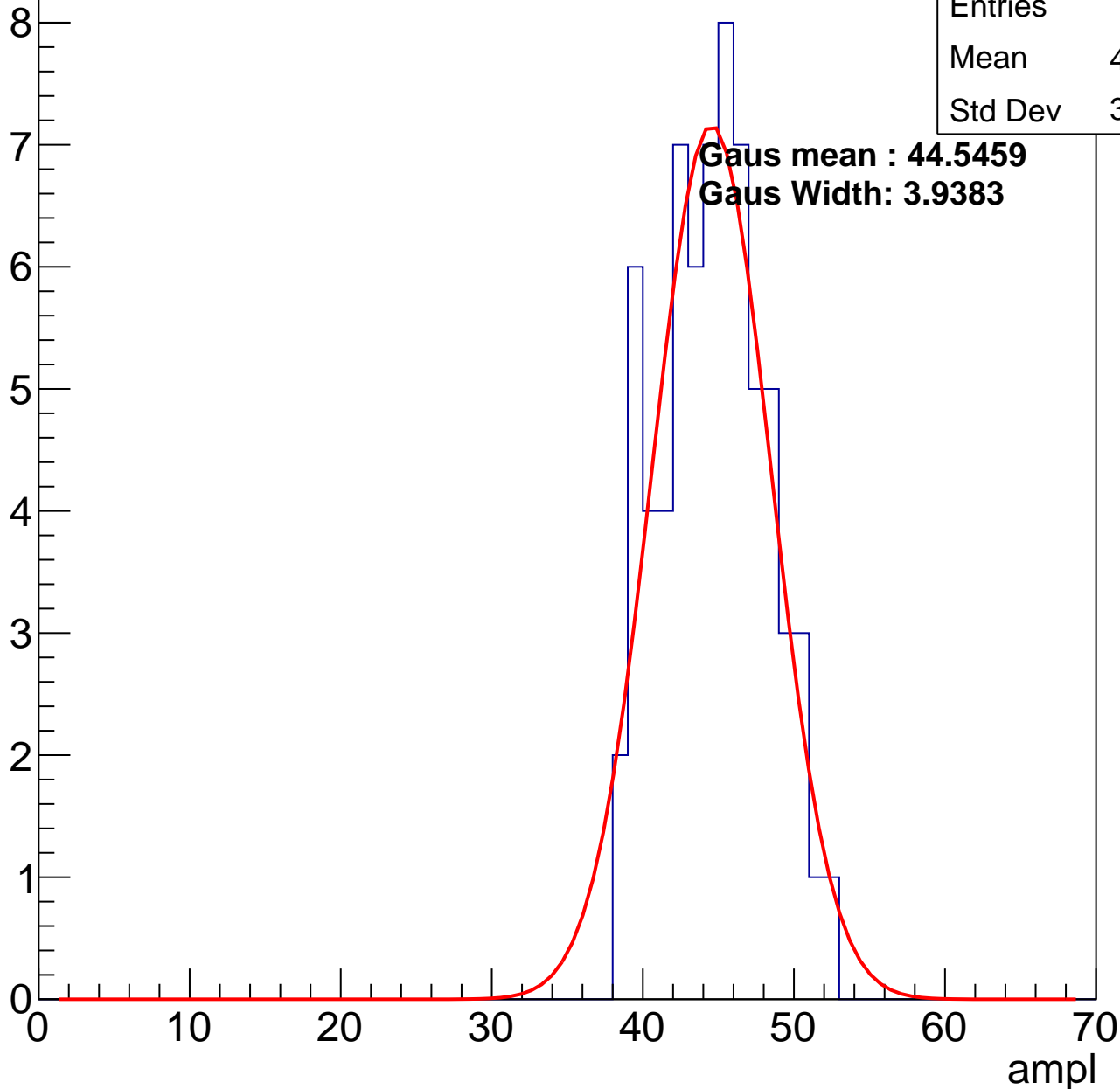
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.22
Std Dev	3.426

Gaus mean : 44.5459

Gaus Width: 3.9383

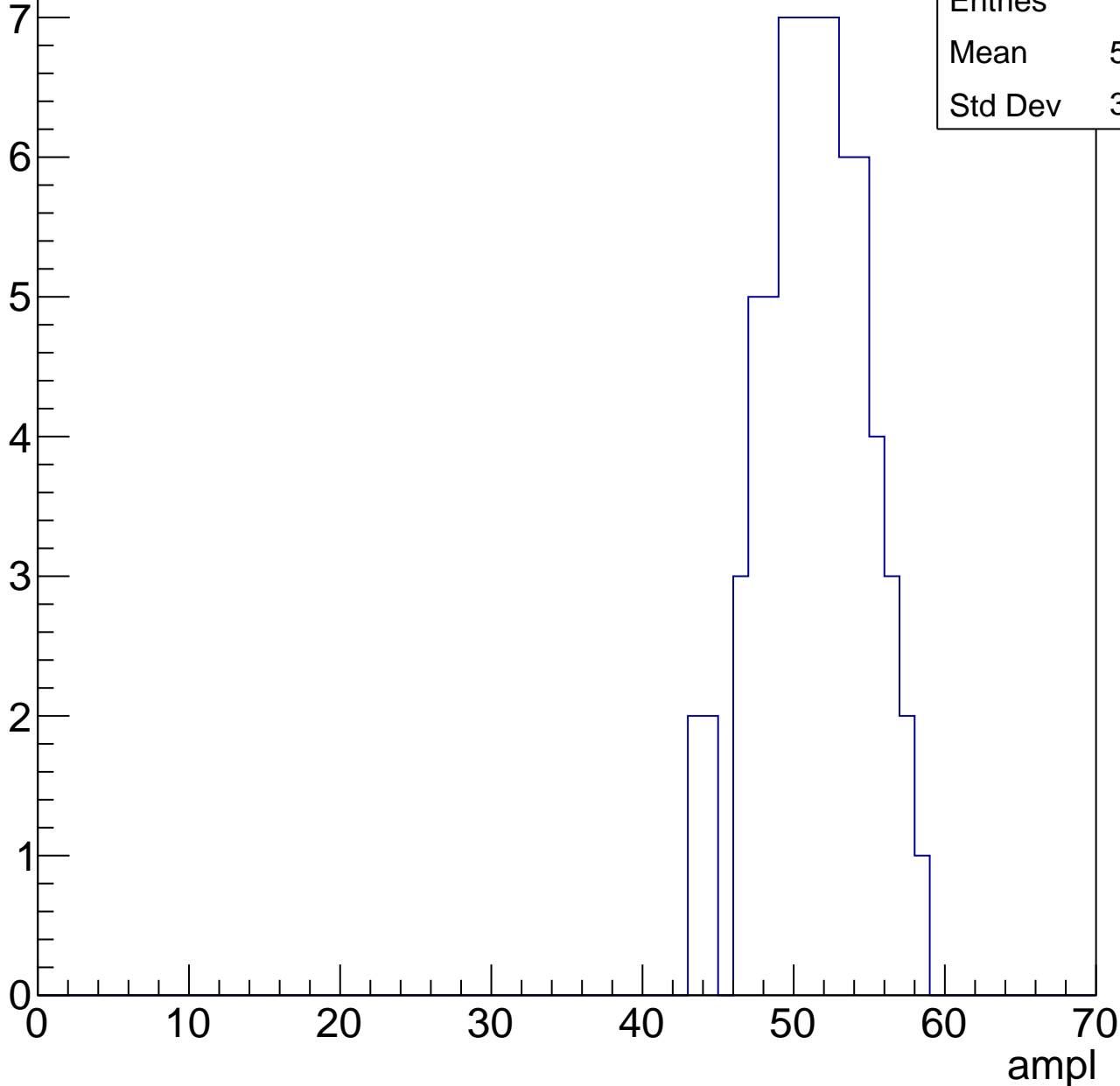


B1L100S, U13-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	50.79
Std Dev	3.462

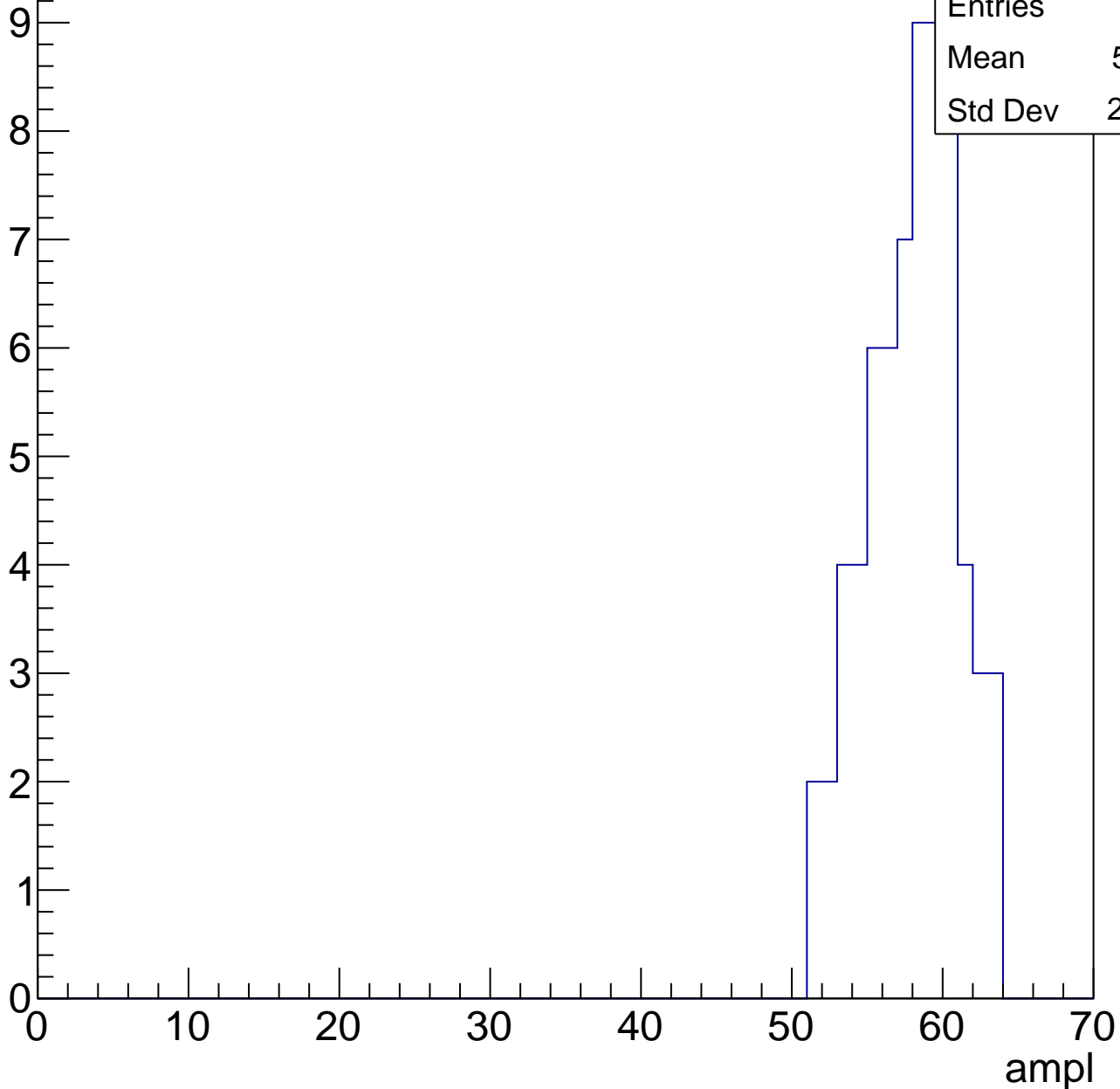


B1L100S, U13-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	57.51
Std Dev	2.988

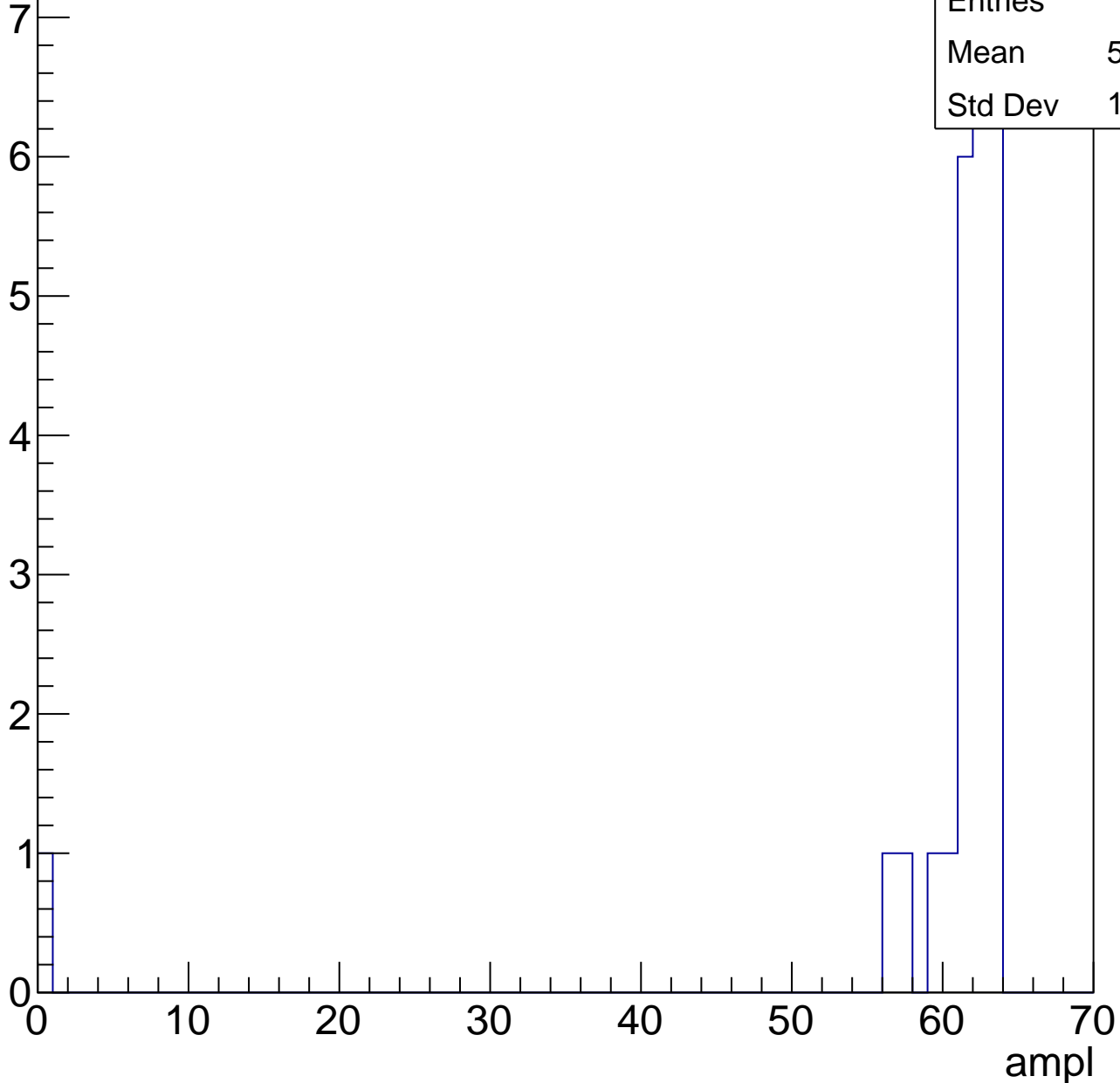


B1L100S, U13-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.92
Std Dev	12.16



B1L100S, U13-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch121, adc0

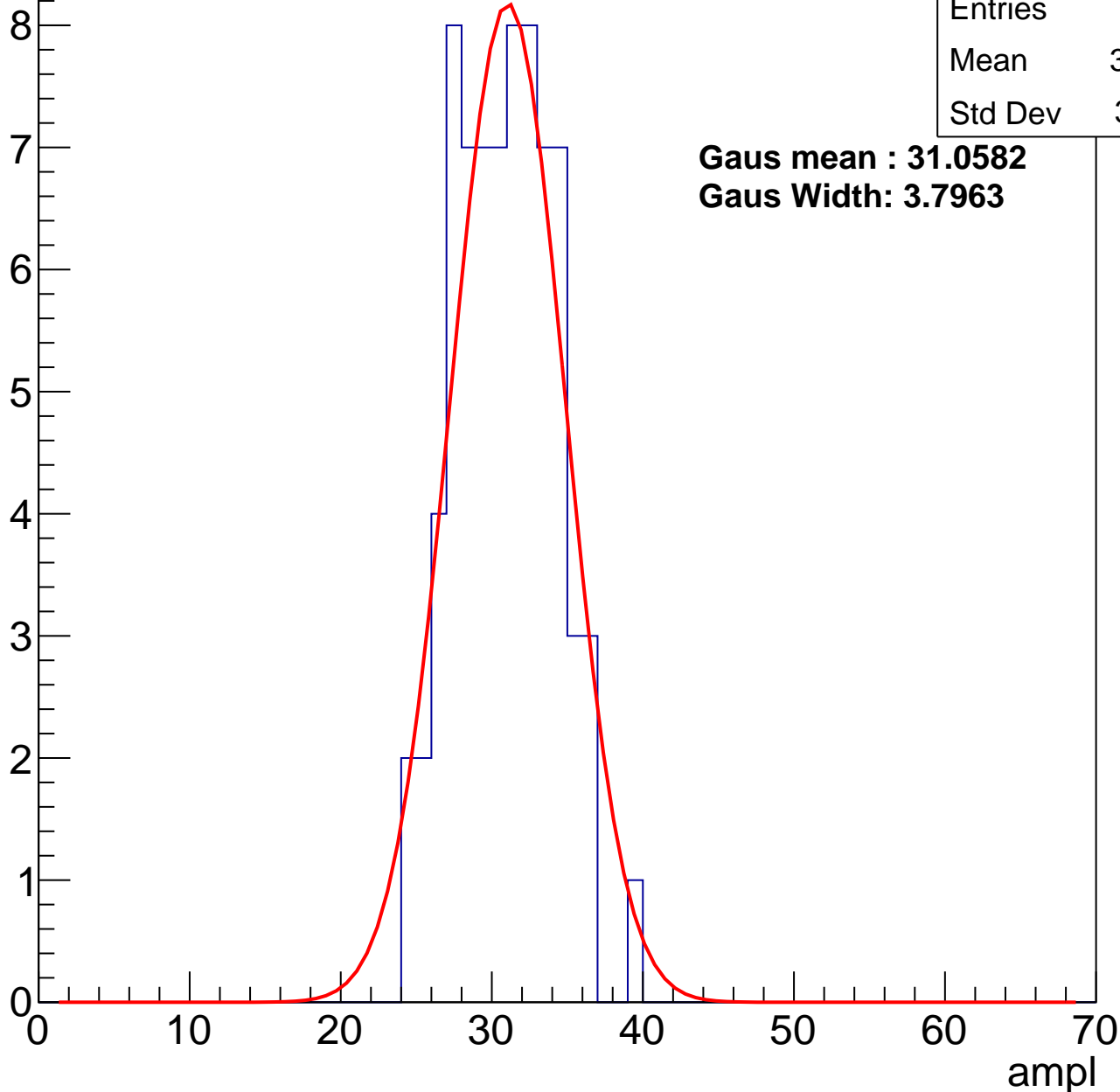
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.43
Std Dev	3.201

Gaus mean : 31.0582

Gaus Width: 3.7963



B1L100S, U13-ch121, adc1

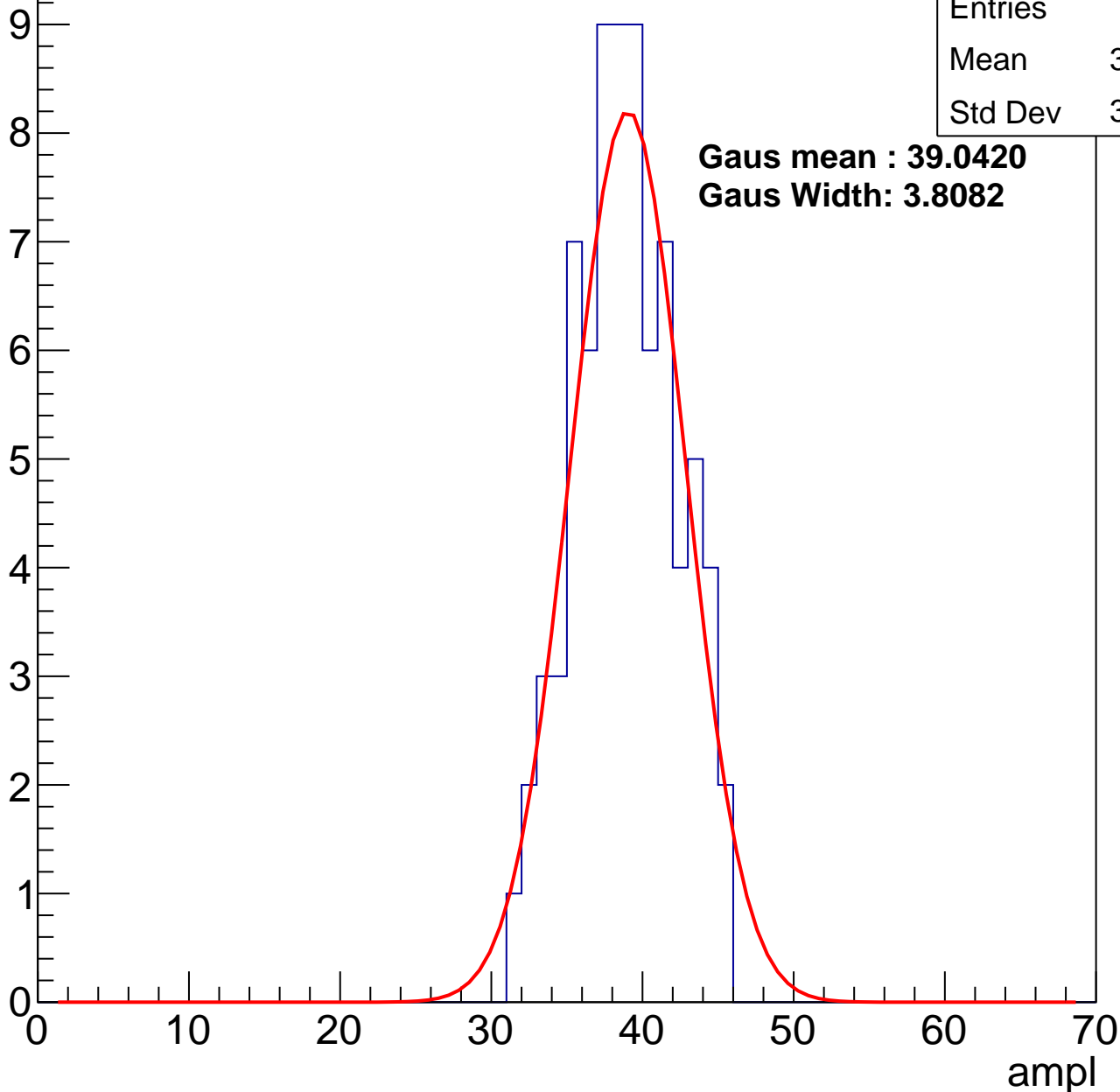
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.43
Std Dev	3.328

Gaus mean : 39.0420

Gaus Width: 3.8082



B1L100S, U13-ch121, adc2

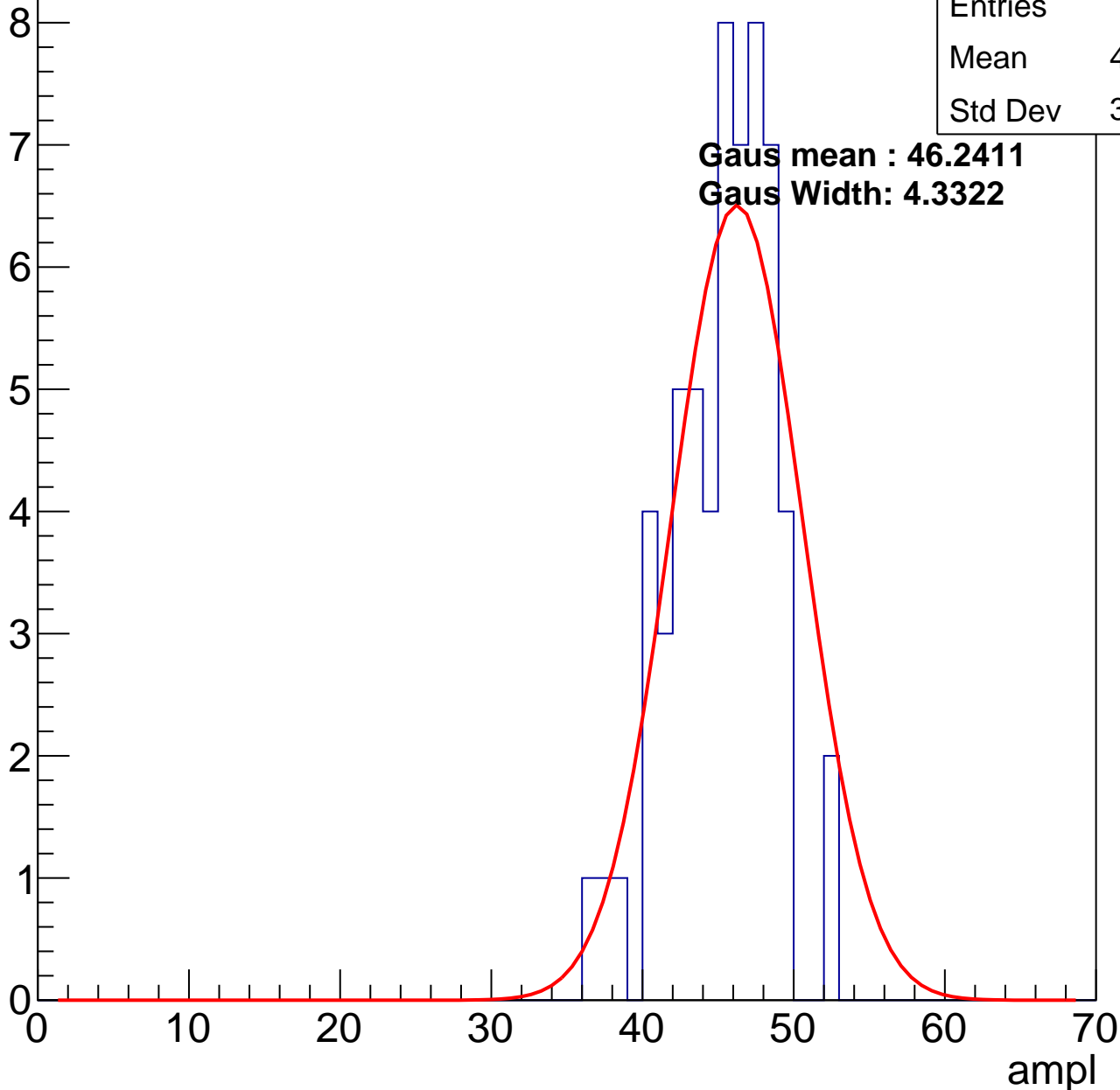
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.82
Std Dev	3.349

Gaus mean : 46.2411

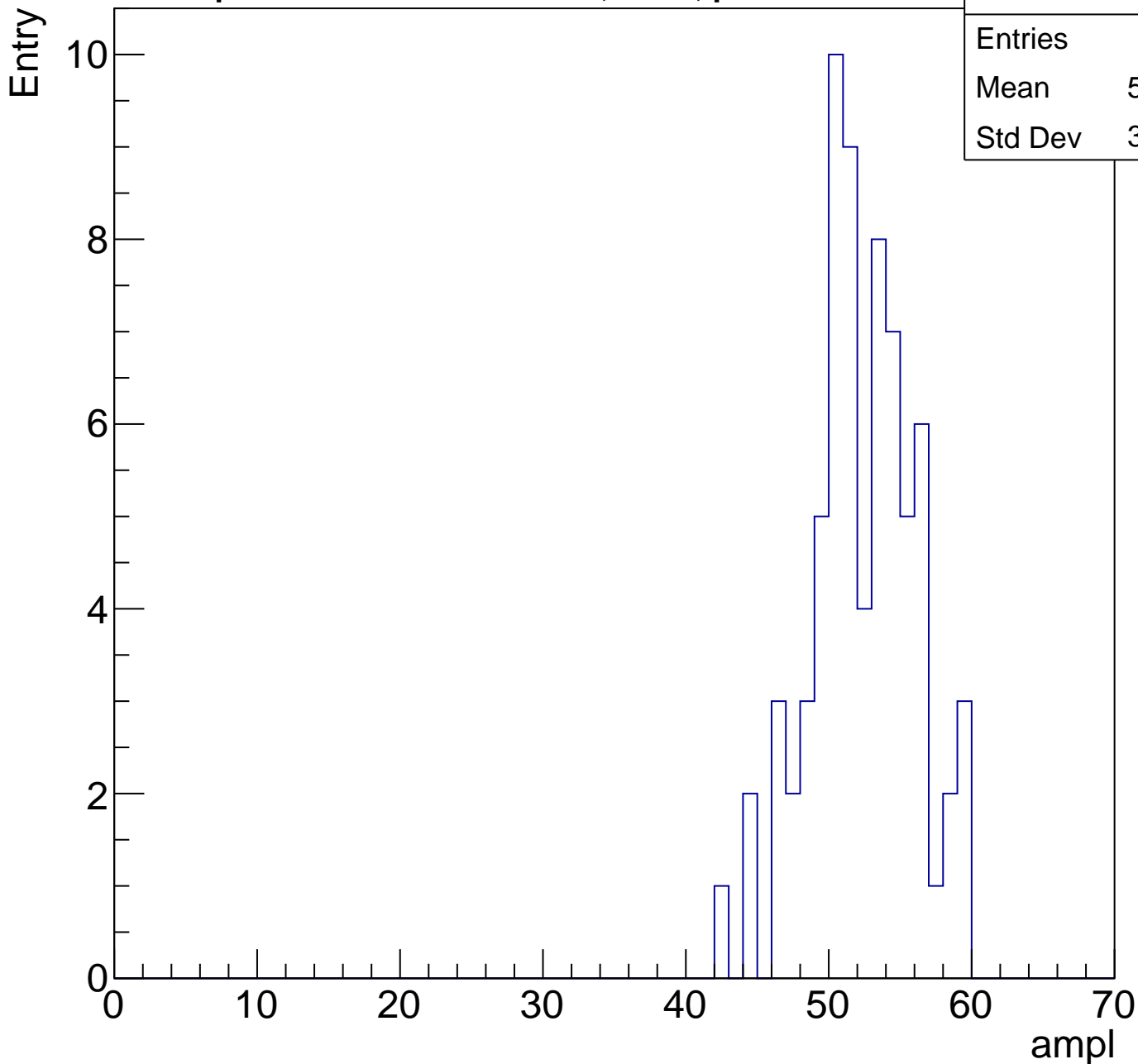
Gaus Width: 4.3322



B1L100S, U13-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	51.85
Std Dev	3.653

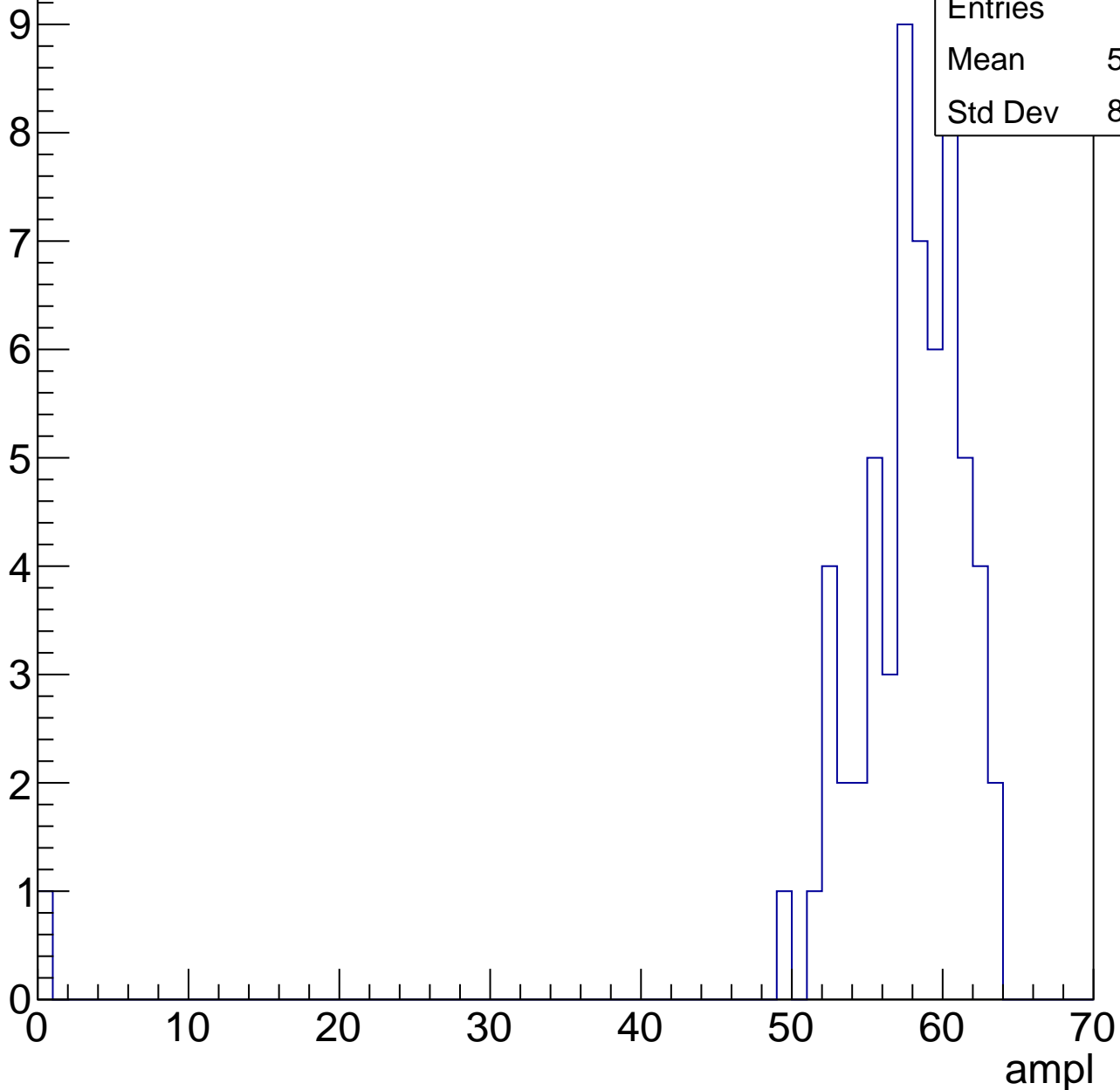


B1L100S, U13-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.62
Std Dev	8.029

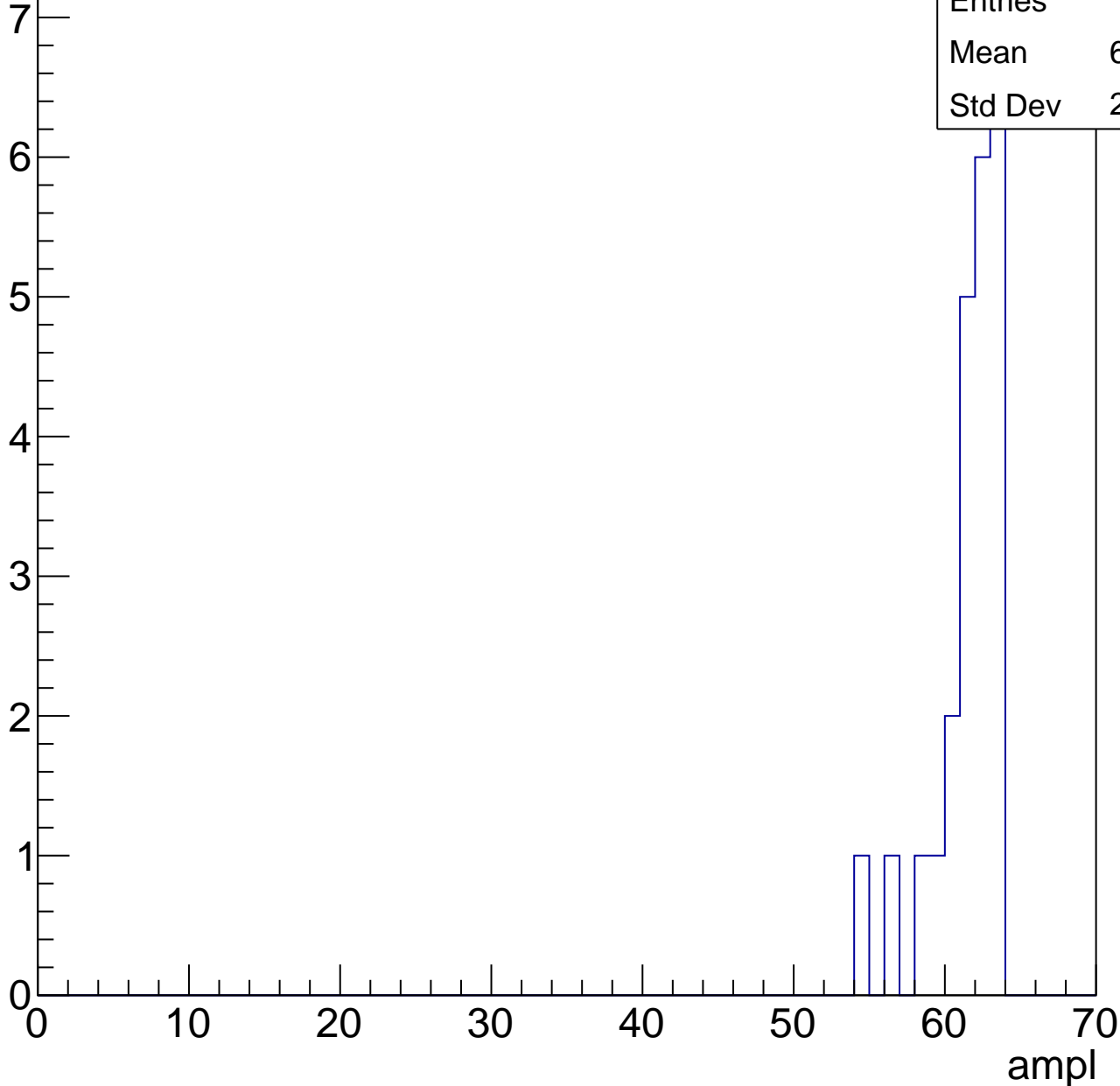


B1L100S, U13-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	61.04
Std Dev	2.263



B1L100S, U13-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch122, adc0

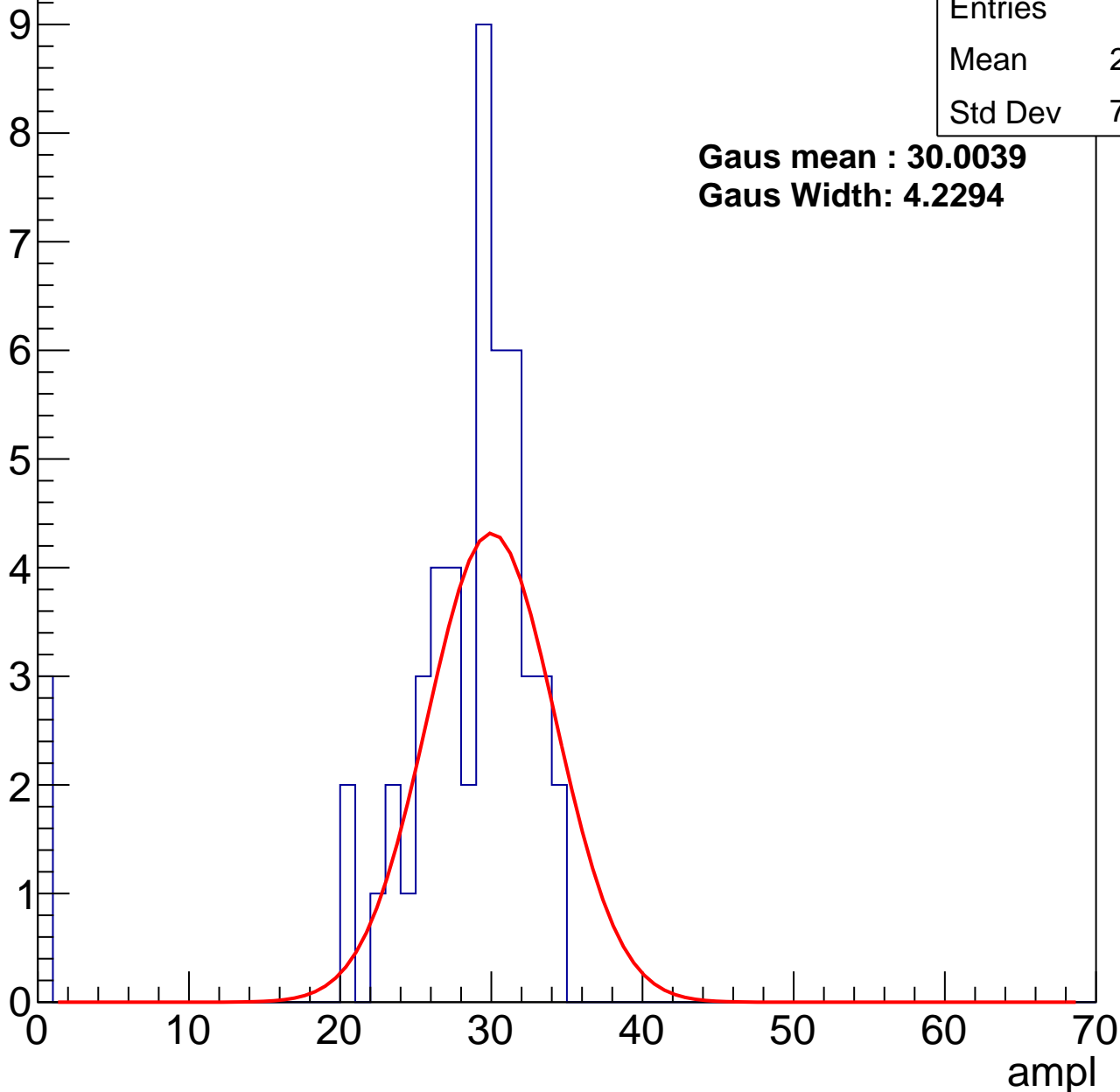
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	26.76
Std Dev	7.453

Gaus mean : 30.0039

Gaus Width: 4.2294



B1L100S, U13-ch122, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

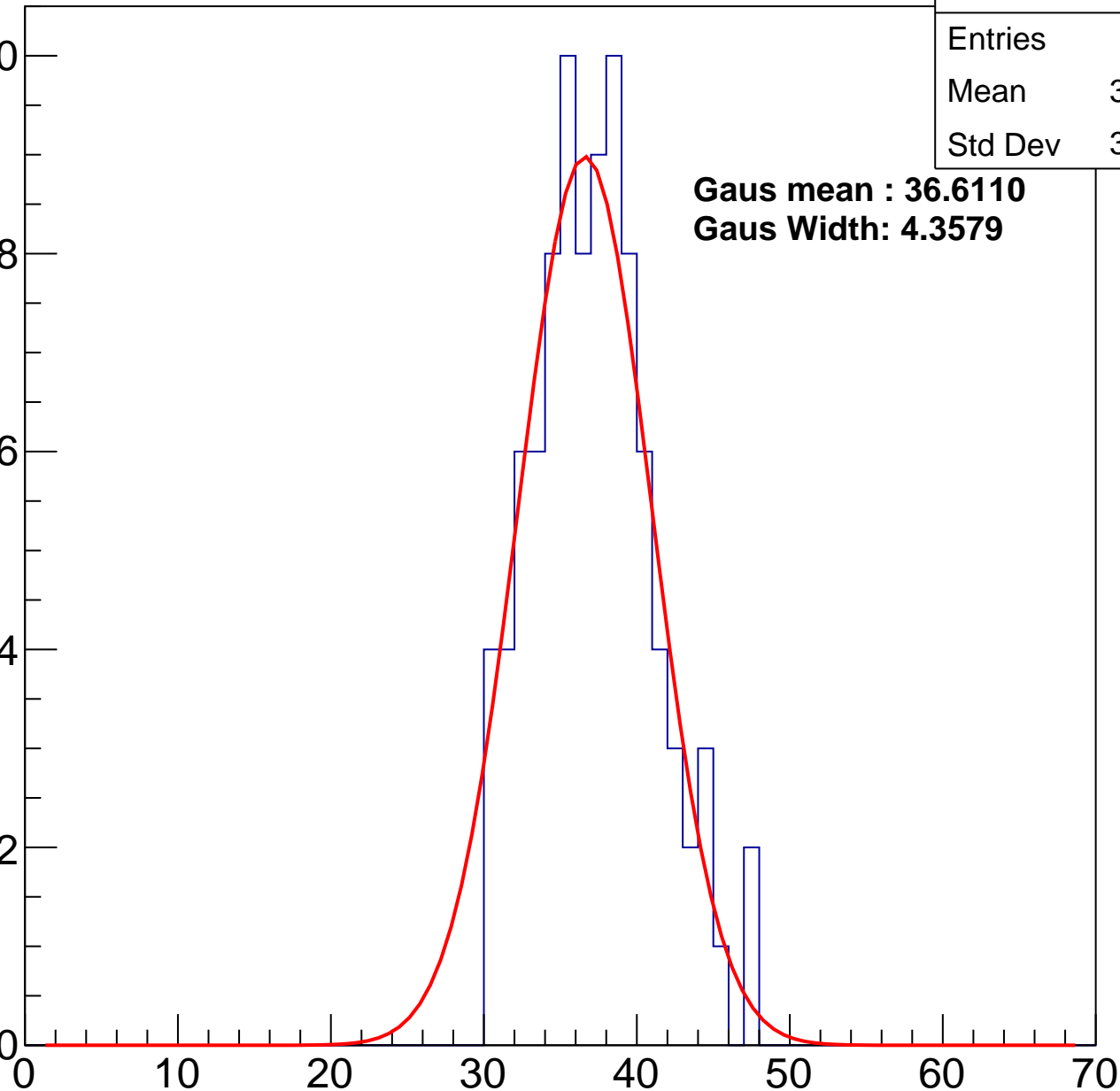
Entries	94
Mean	36.77
Std Dev	3.885

Gaus mean : 36.6110

Gaus Width: 4.3579

10
8
6
4
2
0

ampl



B1L100S, U13-ch122, adc2

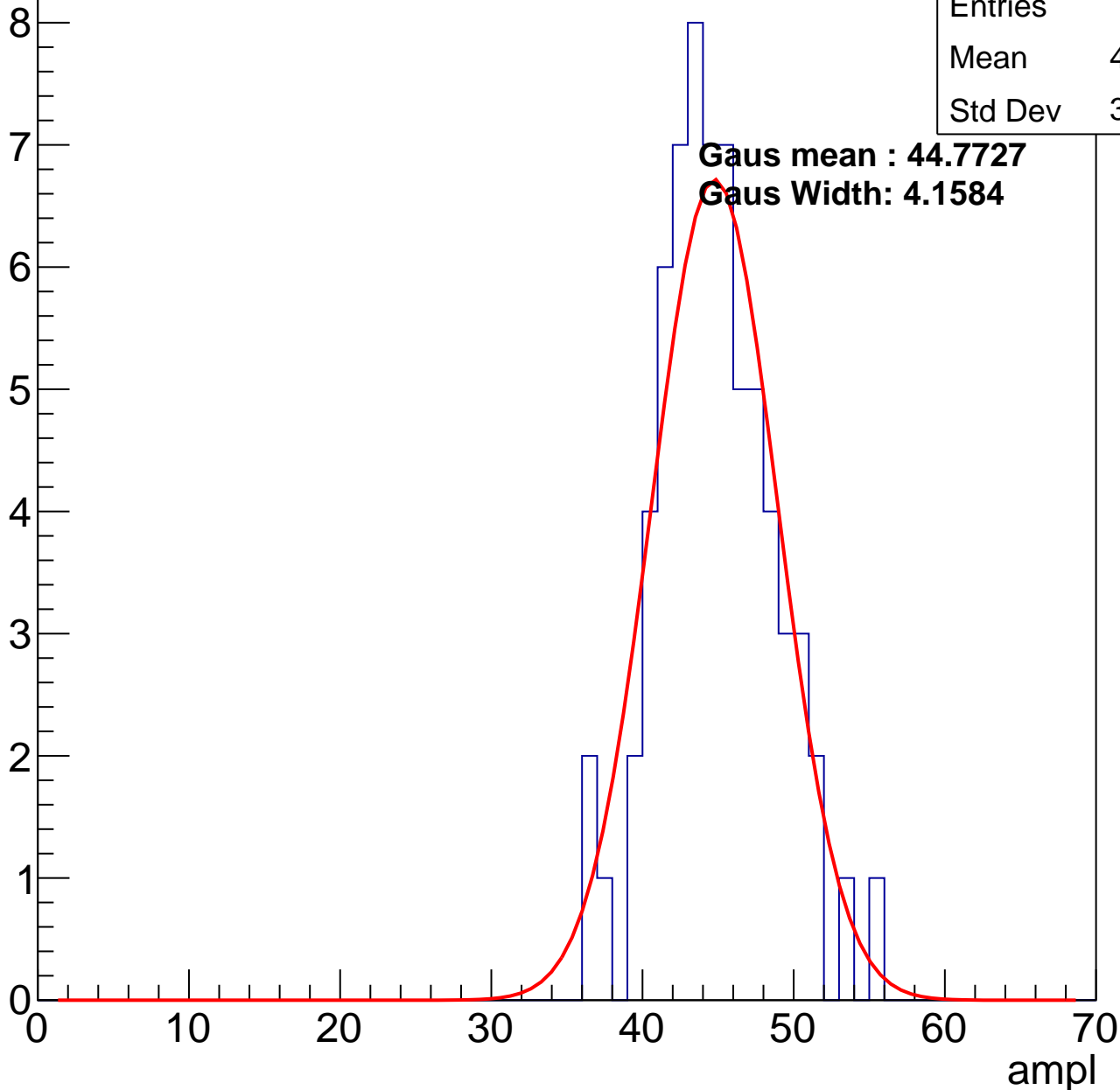
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.38
Std Dev	3.816

Gaus mean : 44.7727

Gaus Width: 4.1584

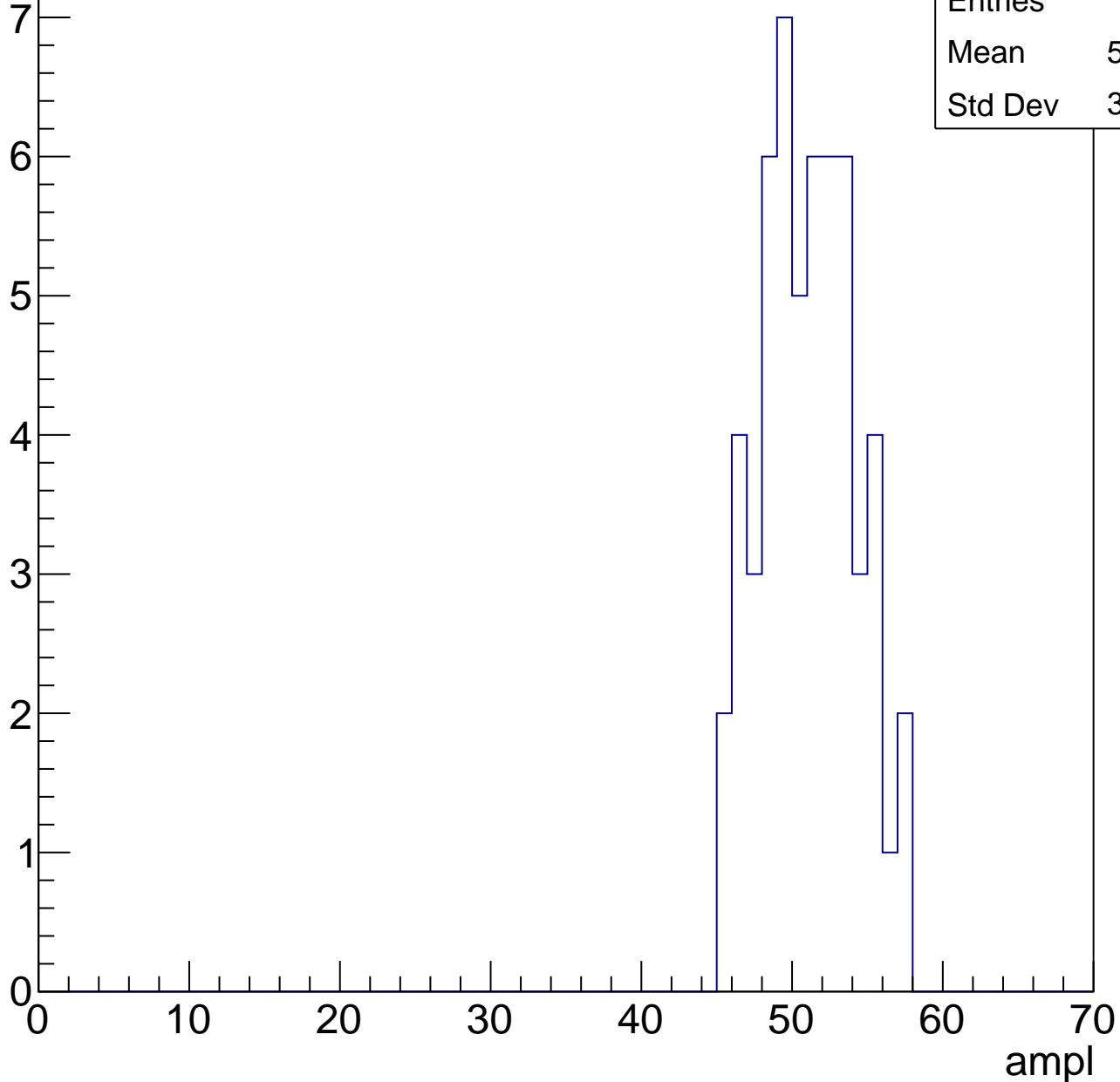


B1L100S, U13-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	50.62
Std Dev	3.066

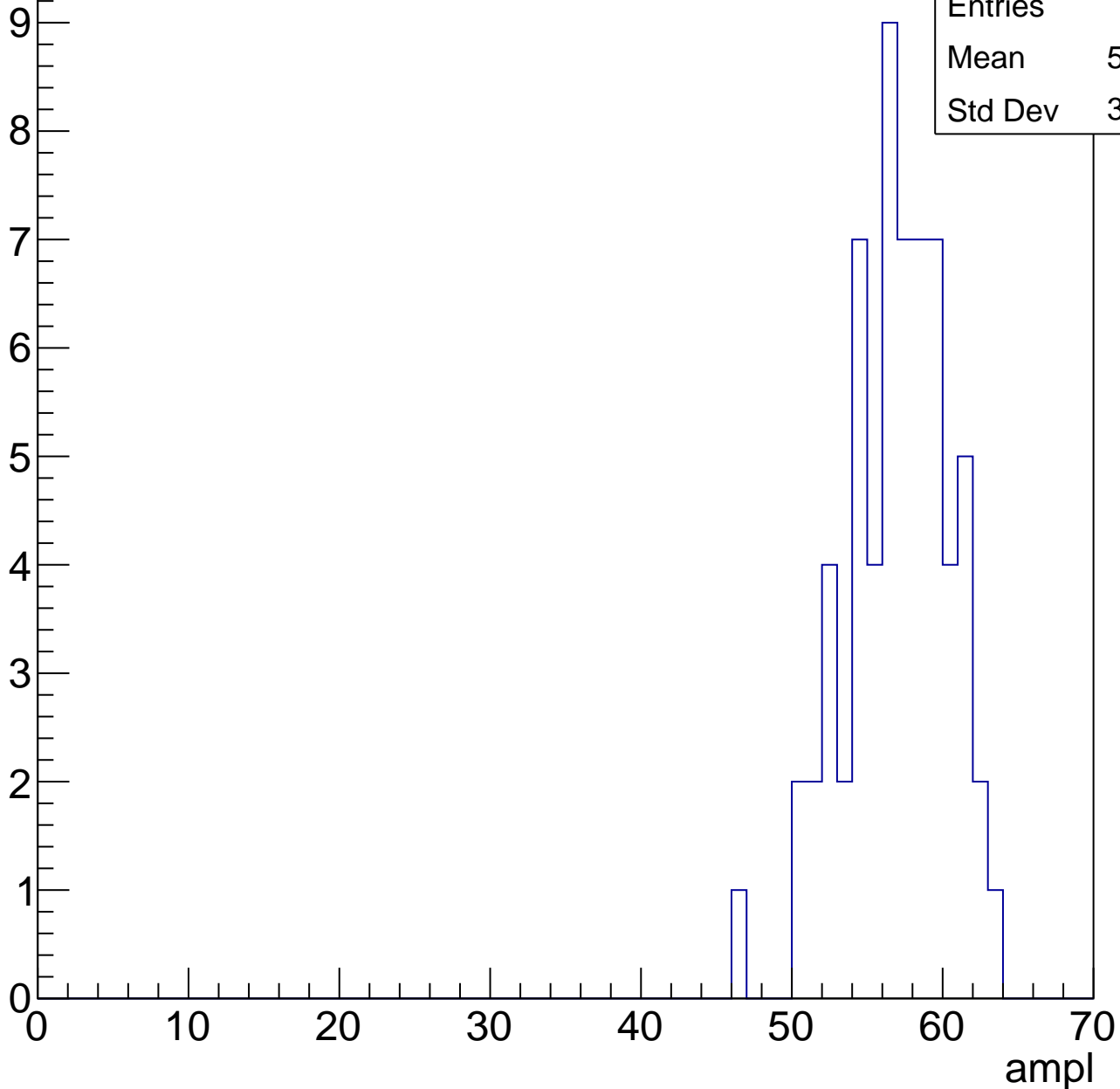


B1L100S, U13-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	56.47
Std Dev	3.377

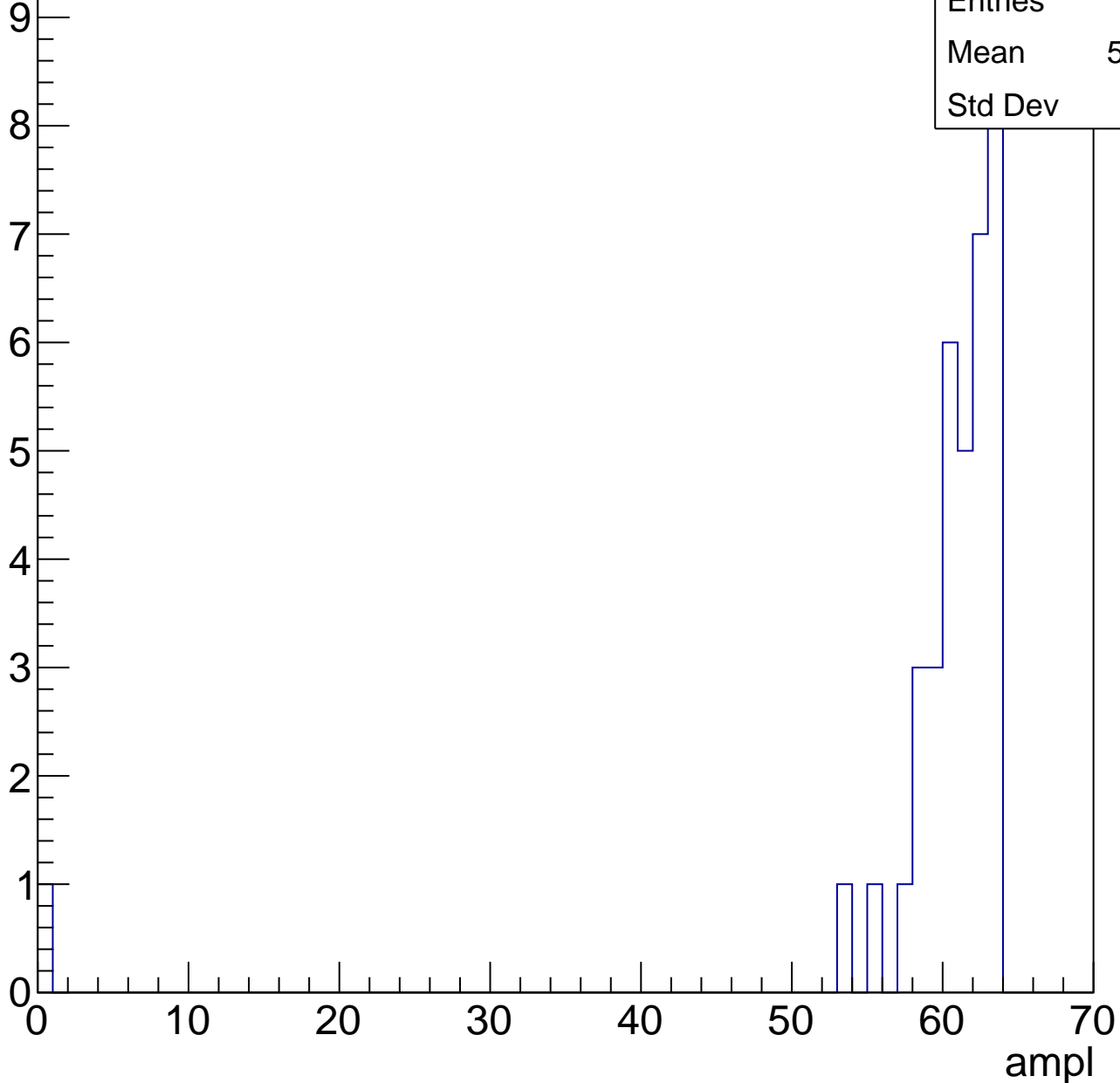


B1L100S, U13-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.97
Std Dev	10.1



B1L100S, U13-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch123, adc0

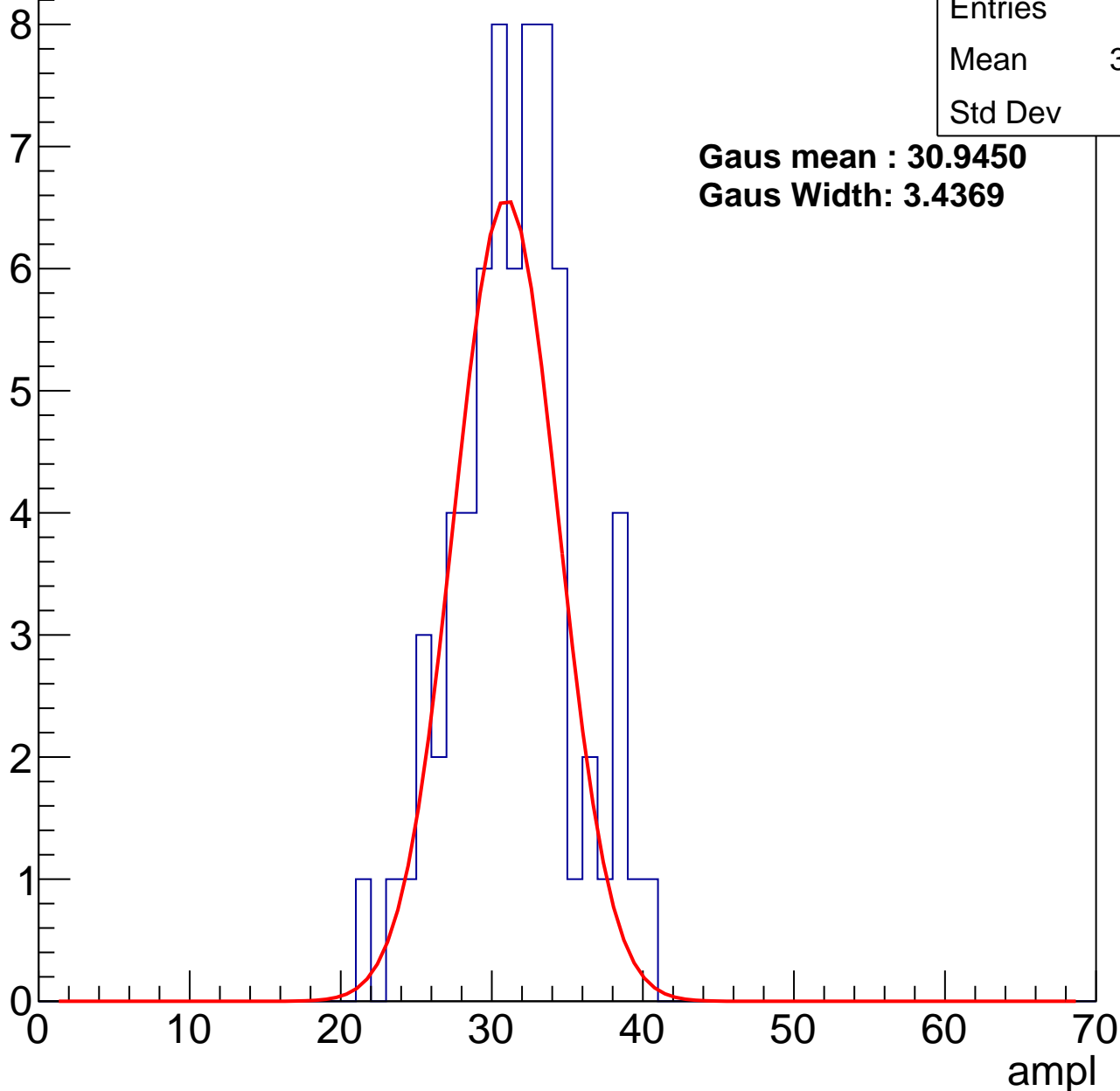
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	31.09
Std Dev	3.91

Gaus mean : 30.9450

Gaus Width: 3.4369



B1L100S, U13-ch123, adc1

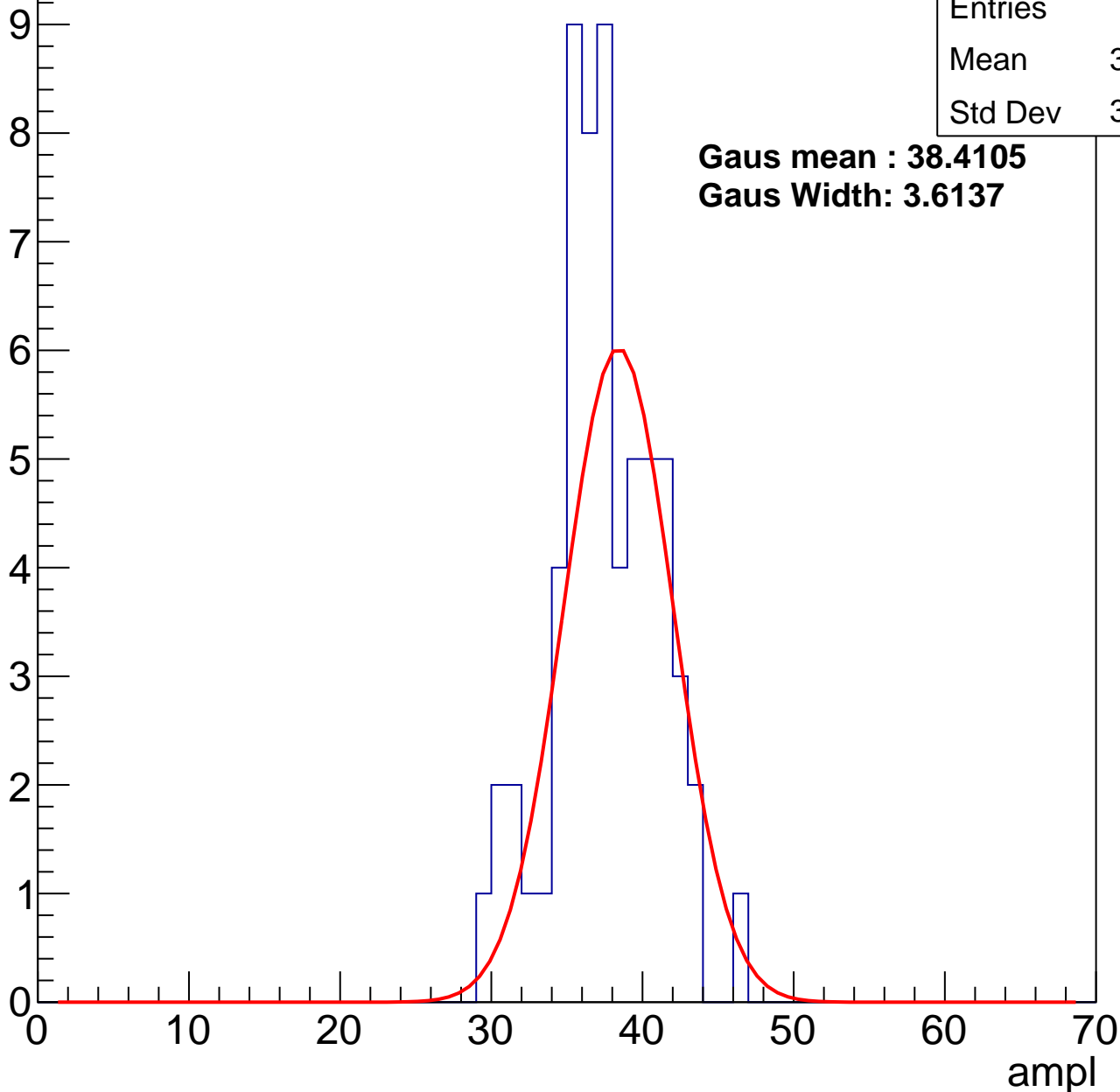
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	37.06
Std Dev	3.435

Gaus mean : 38.4105

Gaus Width: 3.6137



B1L100S, U13-ch123, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	44.94
Std Dev	3.746

Gaus mean : 44.9340

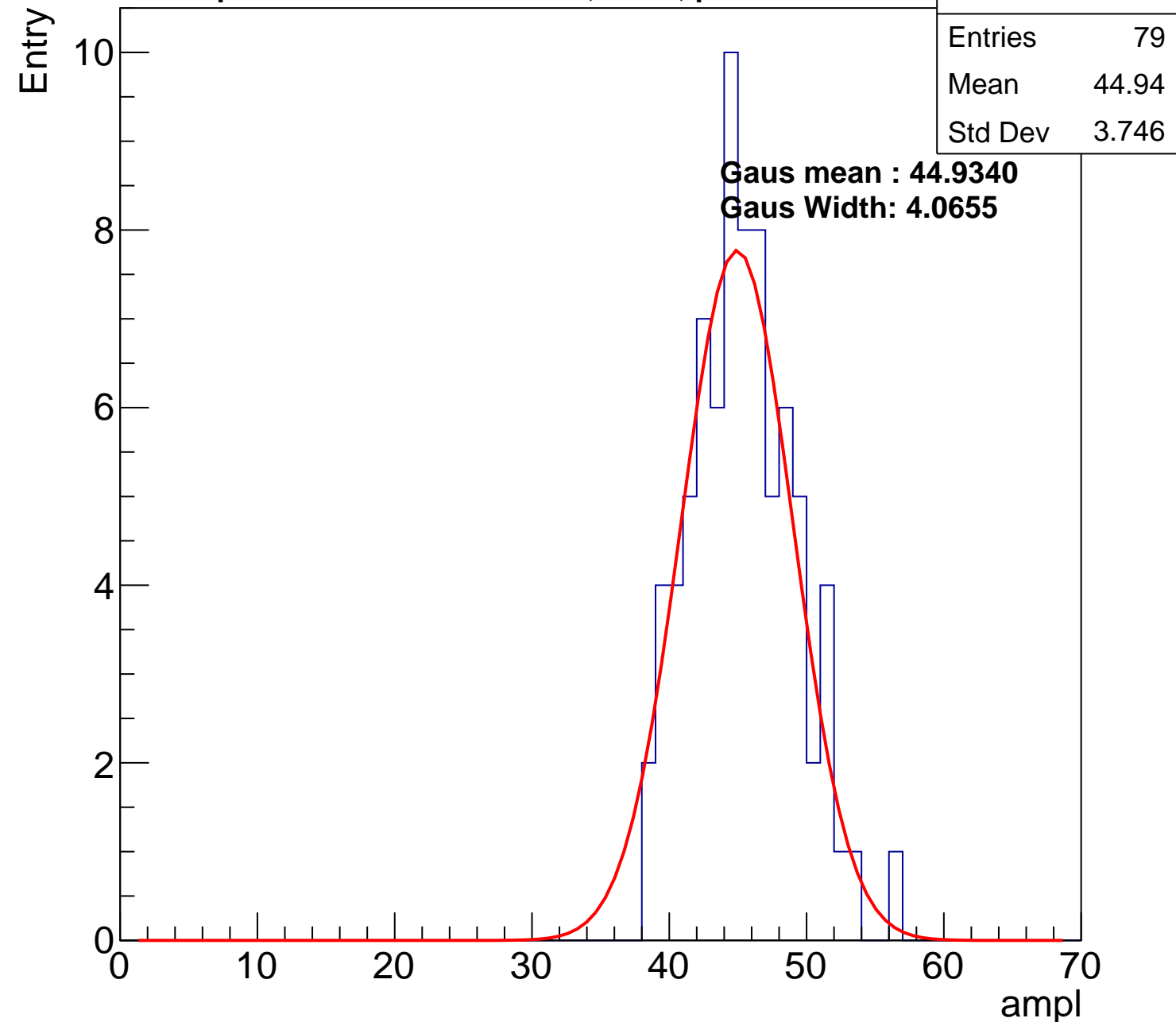
Gaus Width: 4.0655

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

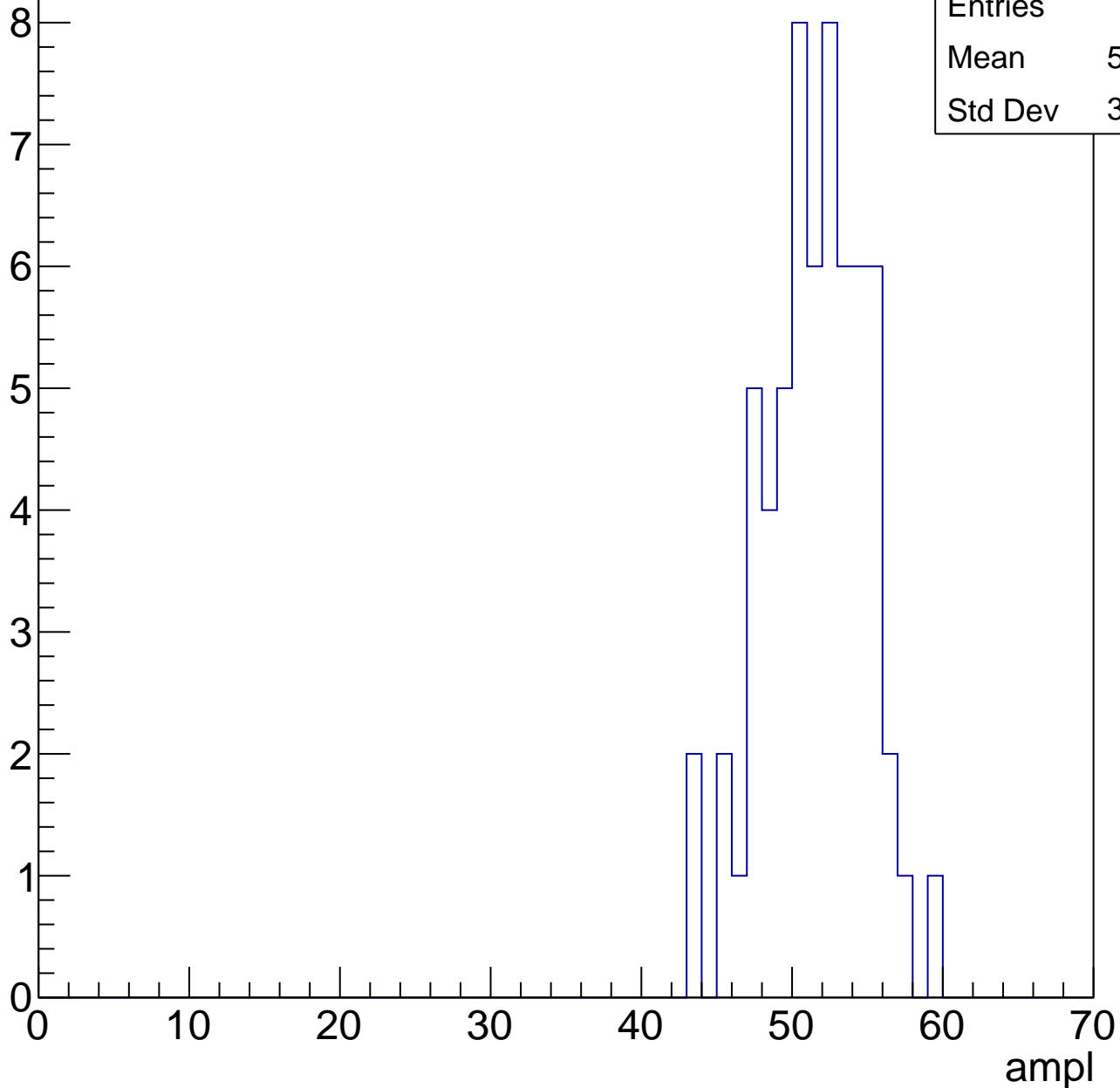


B1L100S, U13-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

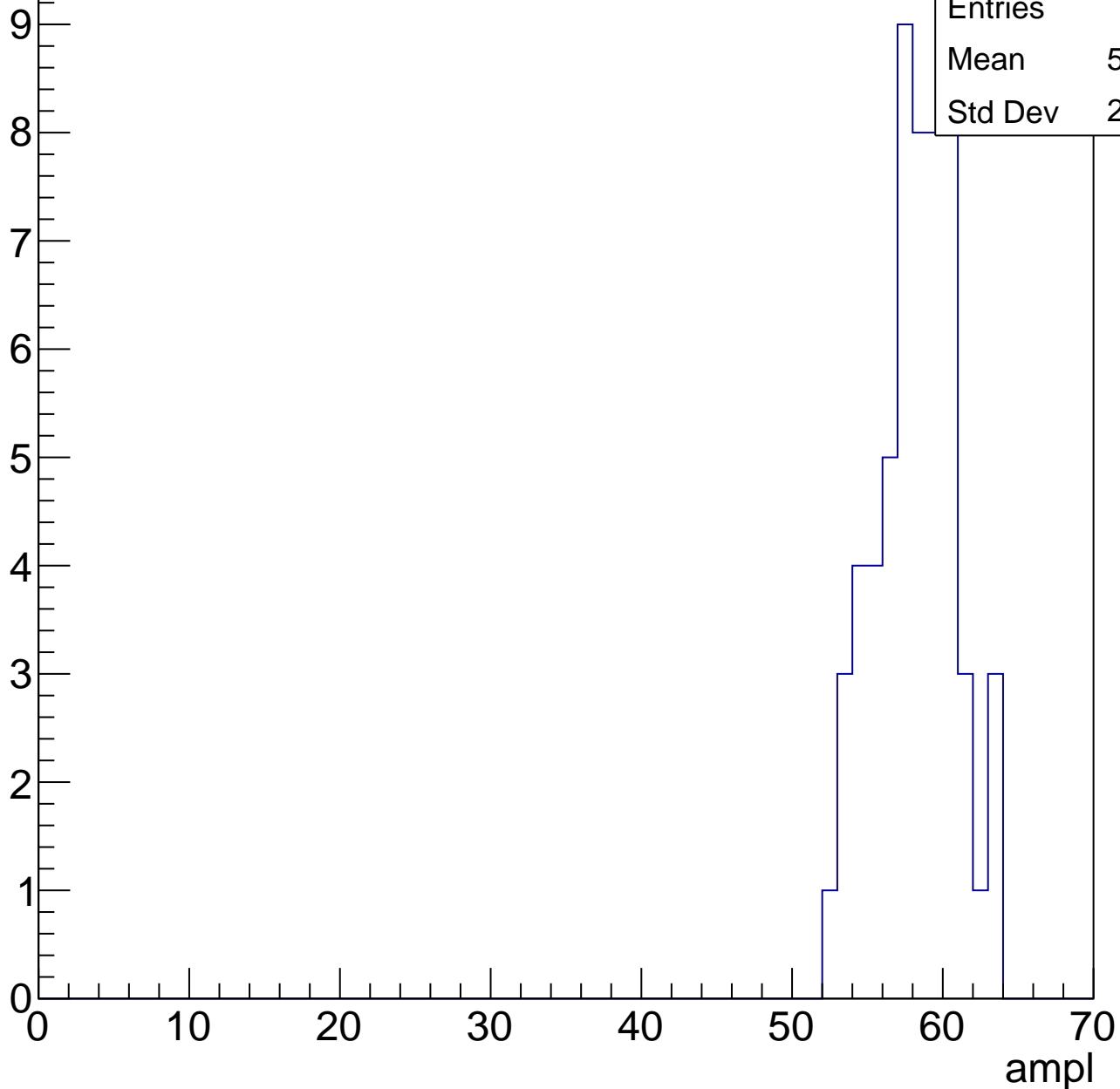
Entries	63
Mean	51.05
Std Dev	3.335



B1L100S, U13-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



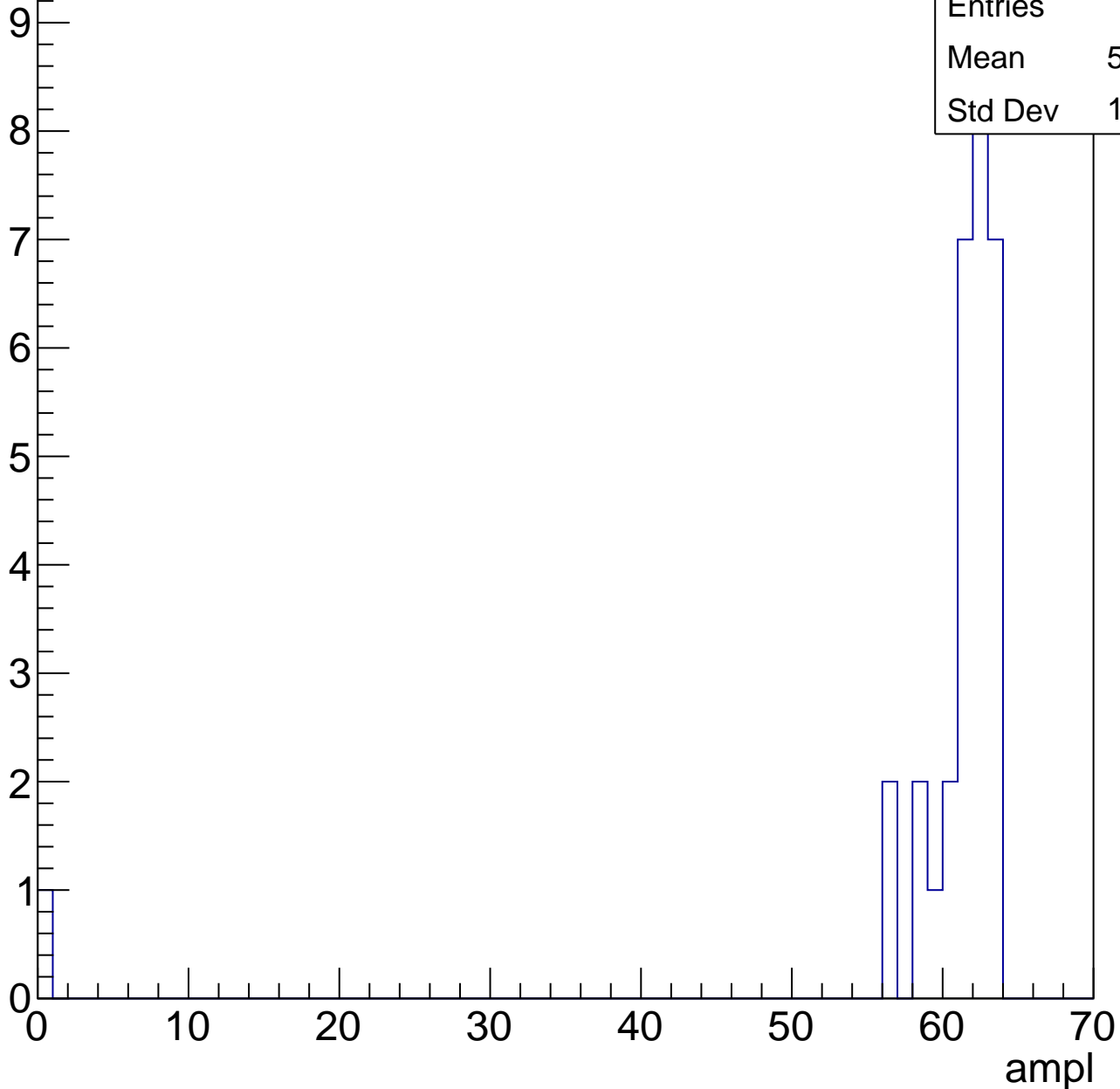
Entries	57
Mean	57.72
Std Dev	2.627

B1L100S, U13-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	59.13
Std Dev	10.96



B1L100S, U13-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U13-ch124, adc0

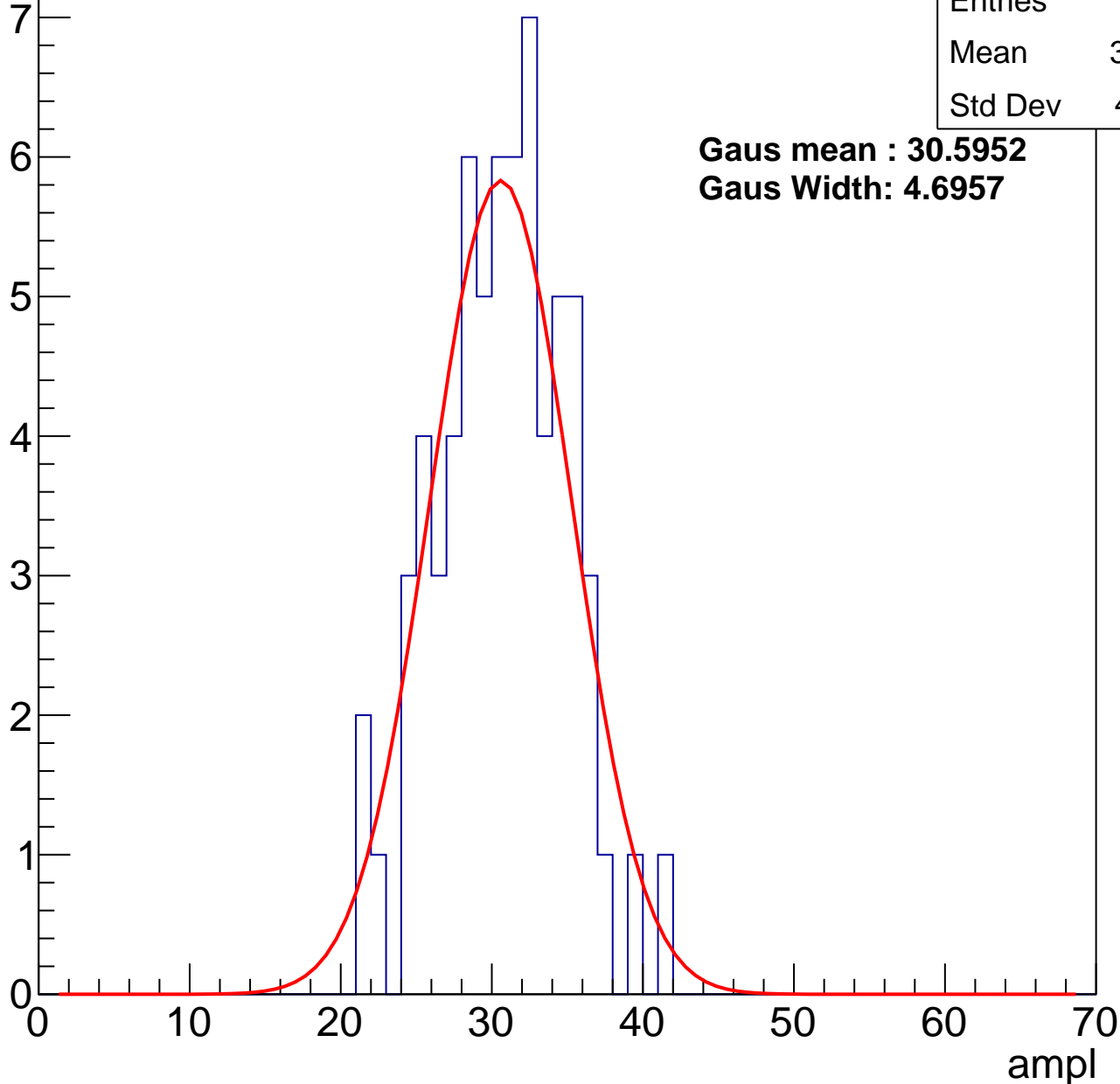
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.25
Std Dev	4.191

Gaus mean : 30.5952

Gaus Width: 4.6957



B1L100S, U13-ch124, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.2
Std Dev	3.677

Gaus mean : 37.4811

Gaus Width: 3.7809

10

8

6

4

2

0

0

10

20

30

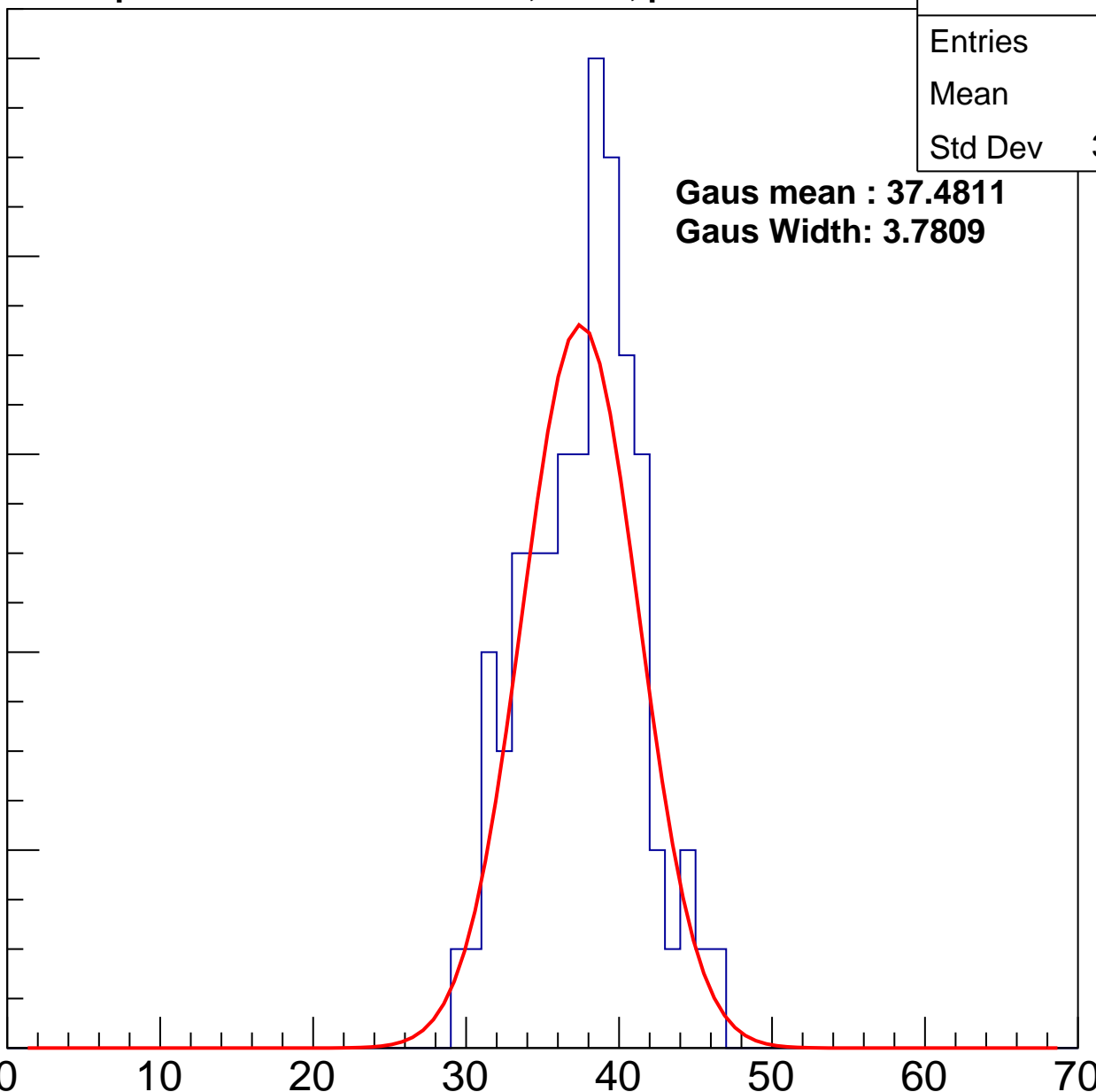
40

50

60

70

ampl



B1L100S, U13-ch124, adc2

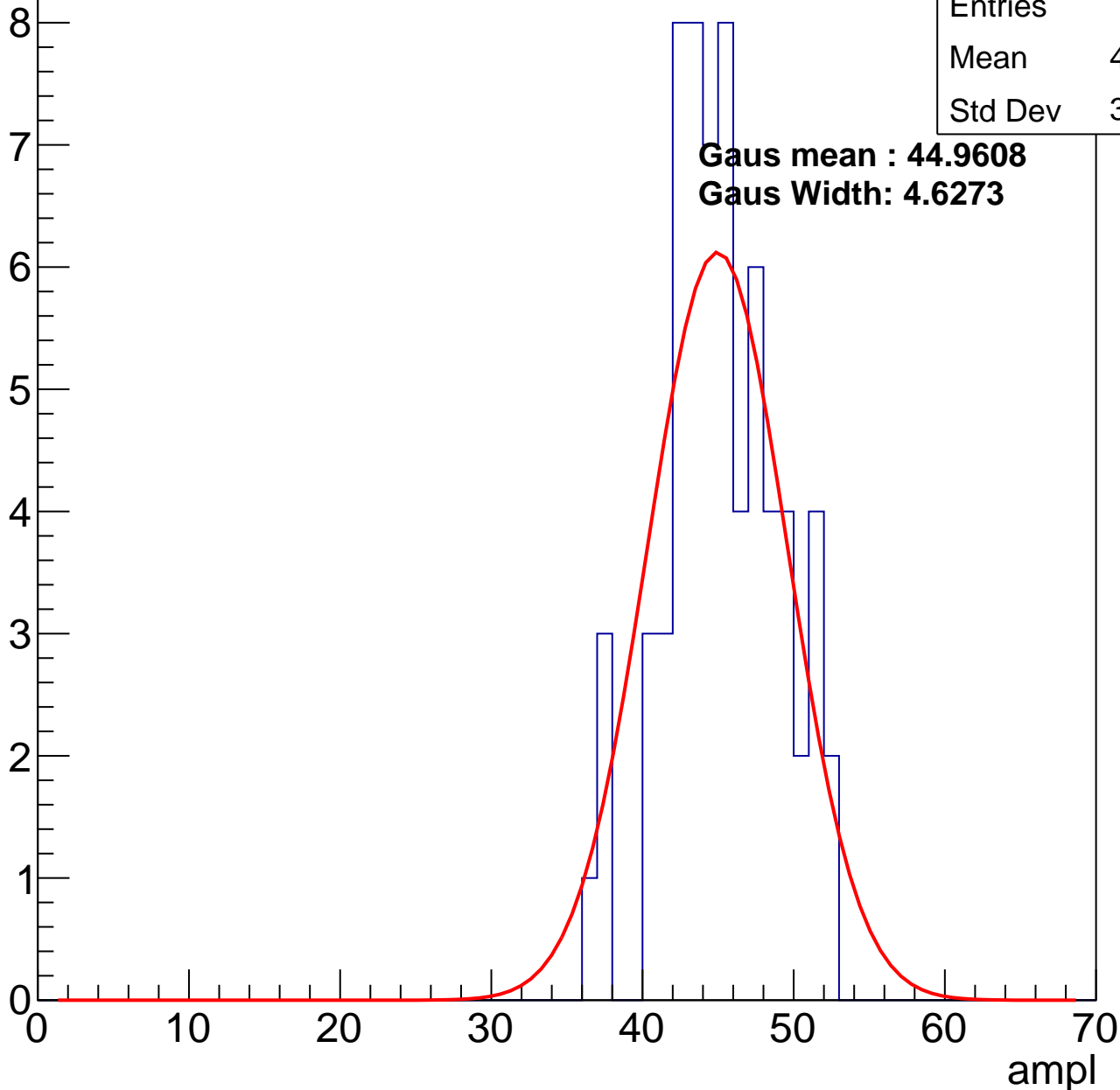
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.78
Std Dev	3.713

Gaus mean : 44.9608

Gaus Width: 4.6273

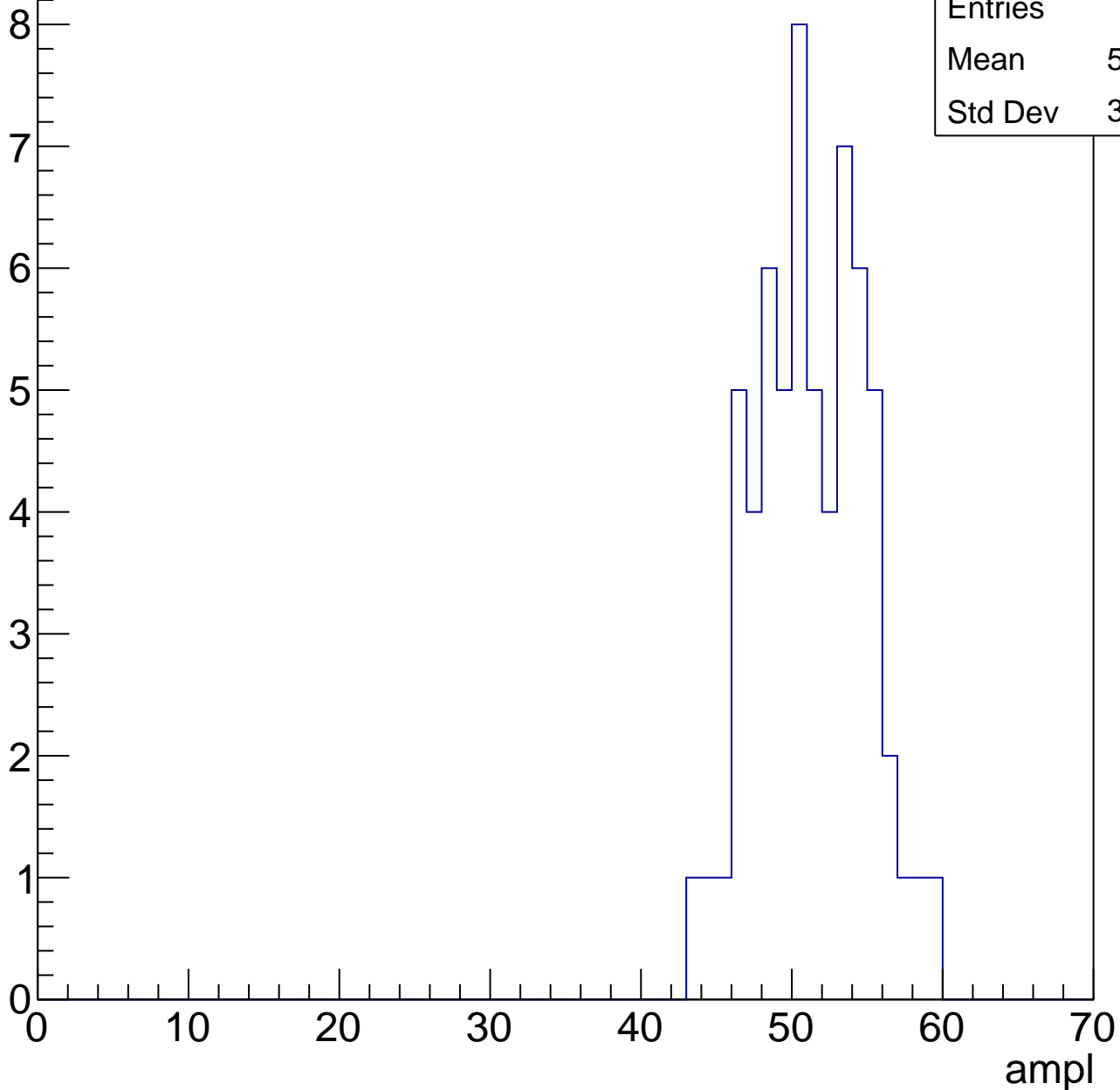


B1L100S, U13-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	50.83
Std Dev	3.526

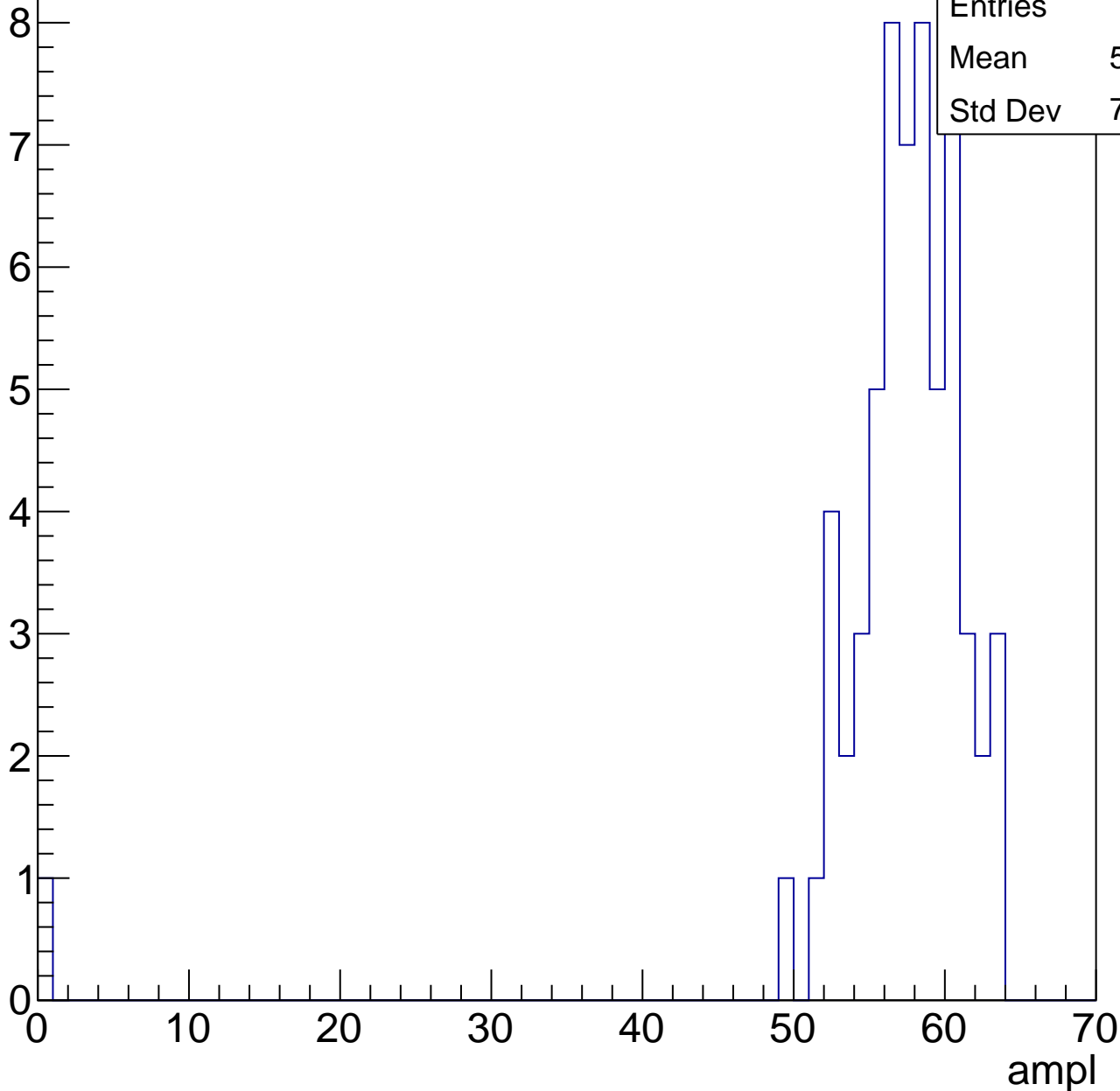


B1L100S, U13-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.28
Std Dev	7.903

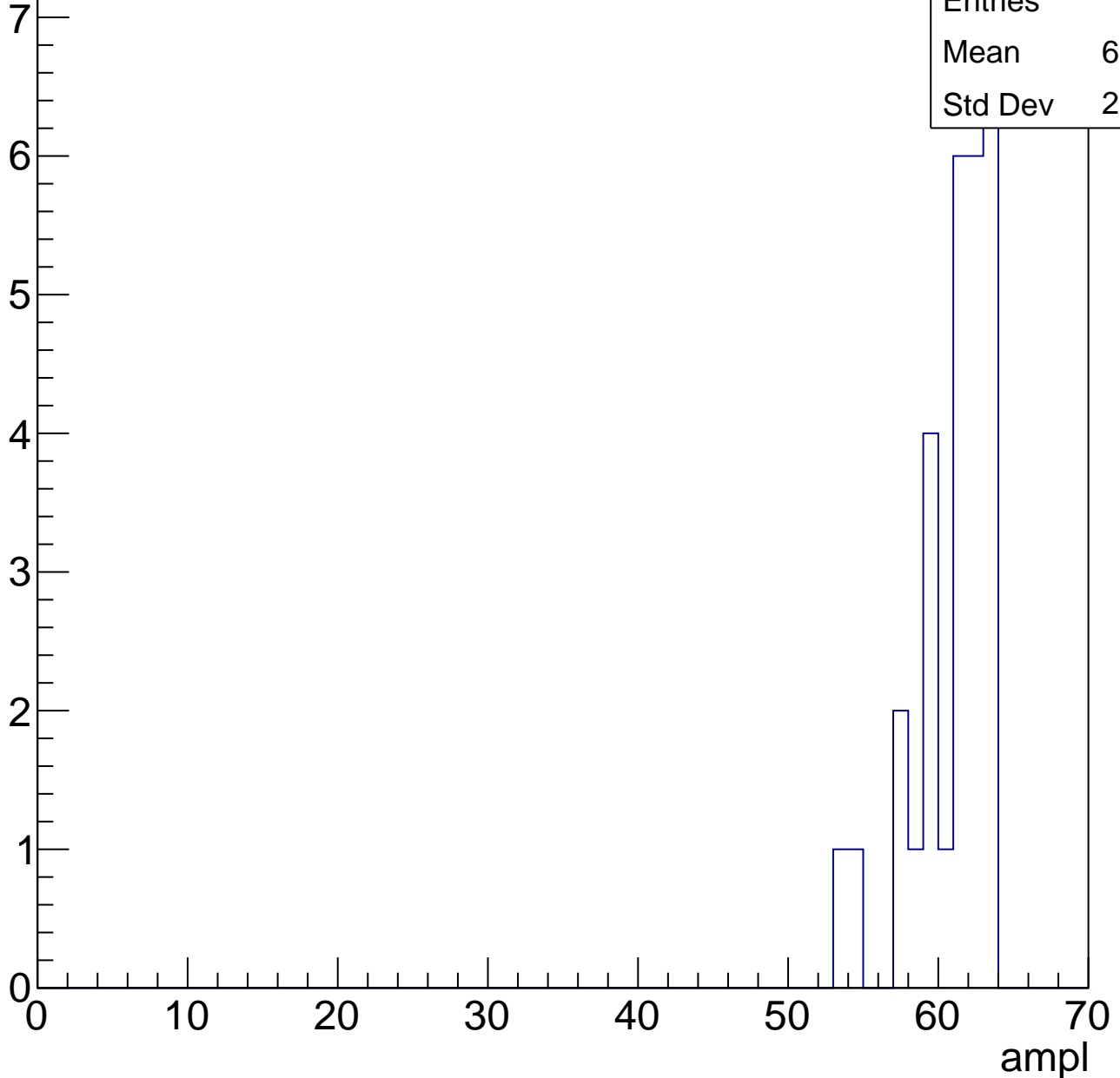


B1L100S, U13-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	60.48
Std Dev	2.608



B1L100S, U13-ch124, adc6

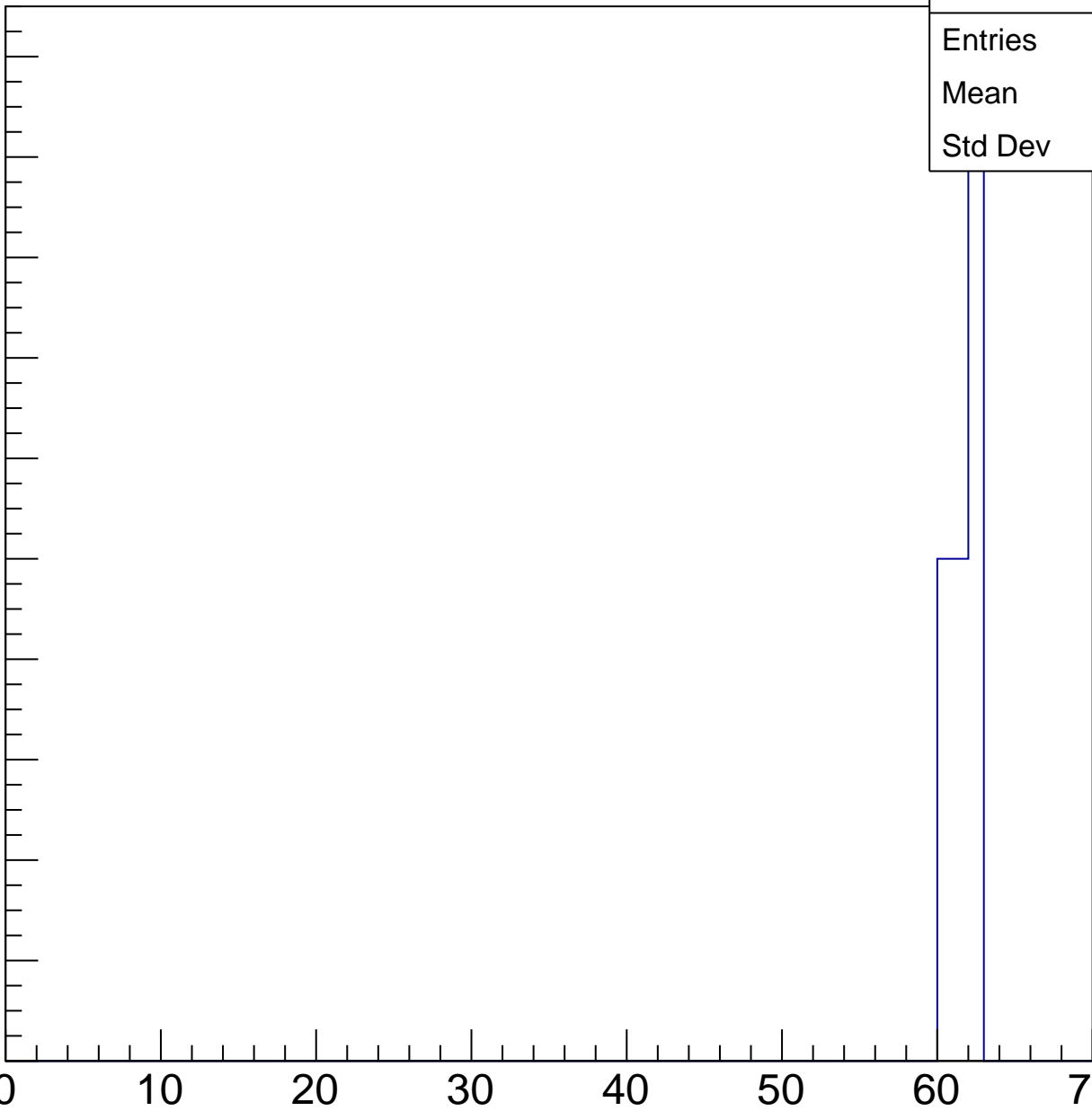
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	0.8292

ampl



B1L100S, U13-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch125, adc0

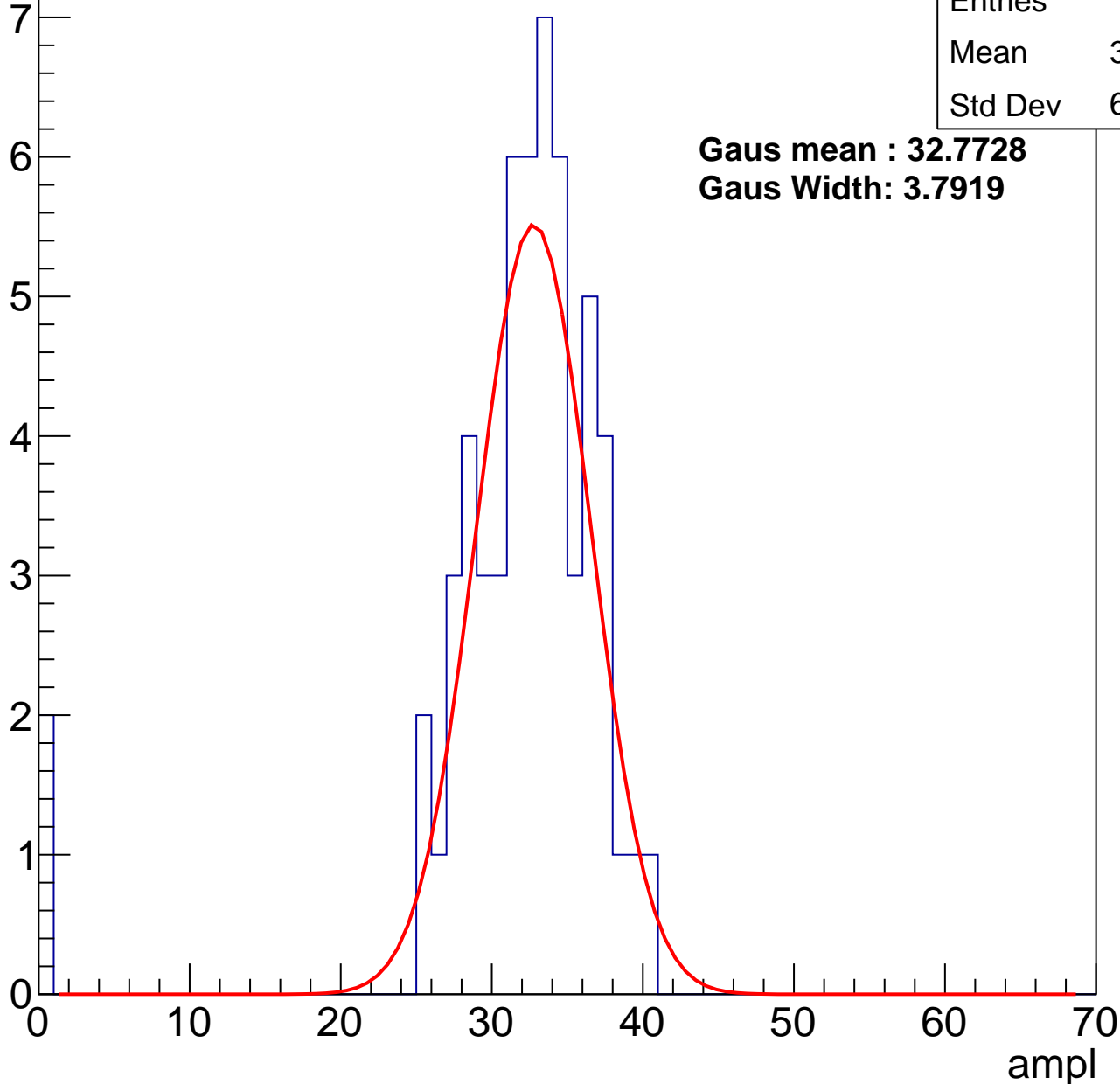
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	31.19
Std Dev	6.842

Gaus mean : 32.7728

Gaus Width: 3.7919



B1L100S, U13-ch125, adc1

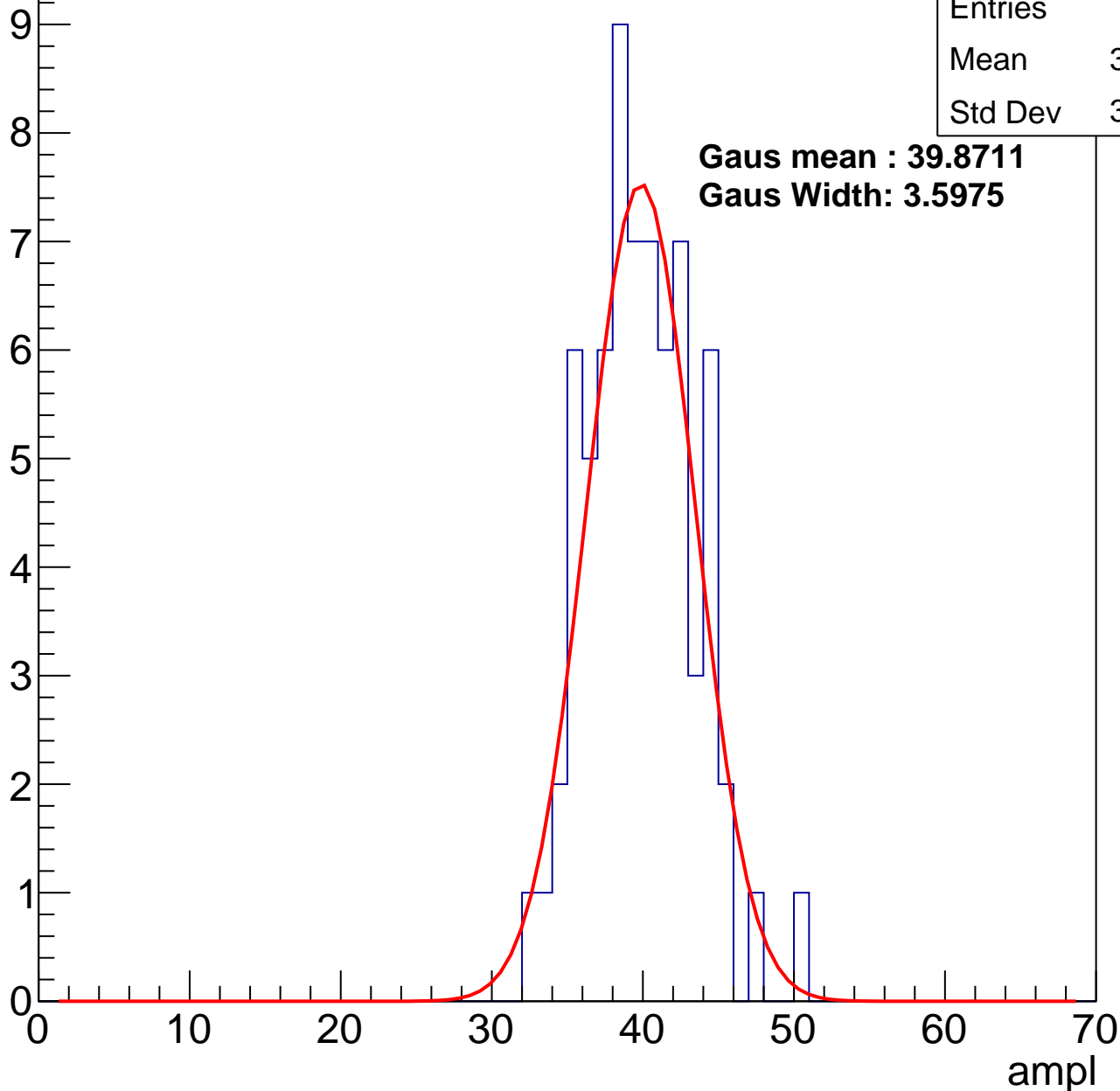
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	39.43
Std Dev	3.475

Gaus mean : 39.8711

Gaus Width: 3.5975



B1L100S, U13-ch125, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	46.07
Std Dev	3.411

Gaus mean : 46.4706

Gaus Width: 3.8096

Entry

10

8

6

4

2

0

0

10

20

30

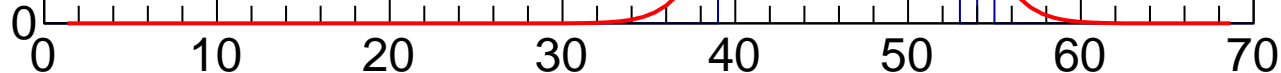
40

50

60

70

ampl

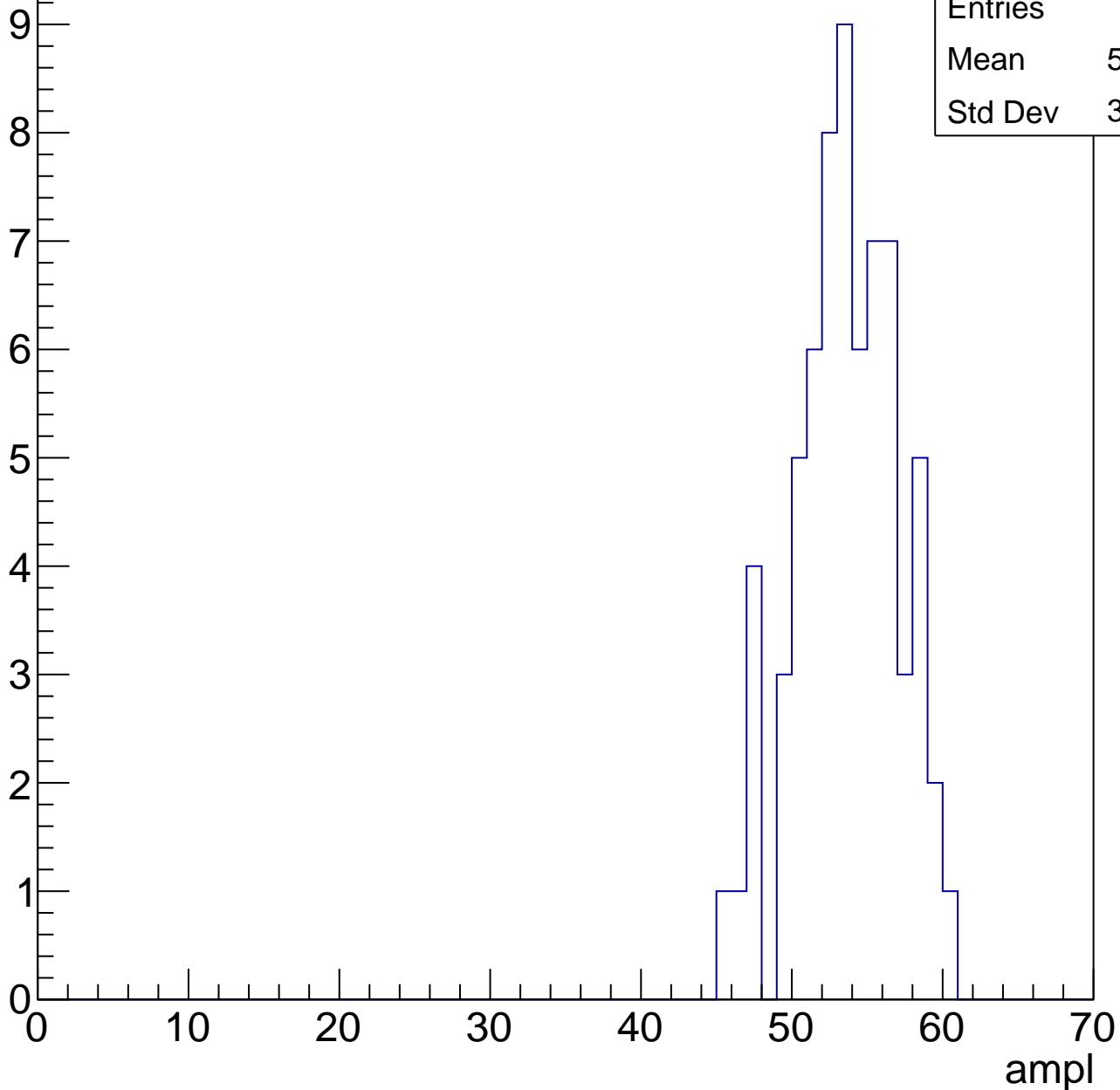


B1L100S, U13-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

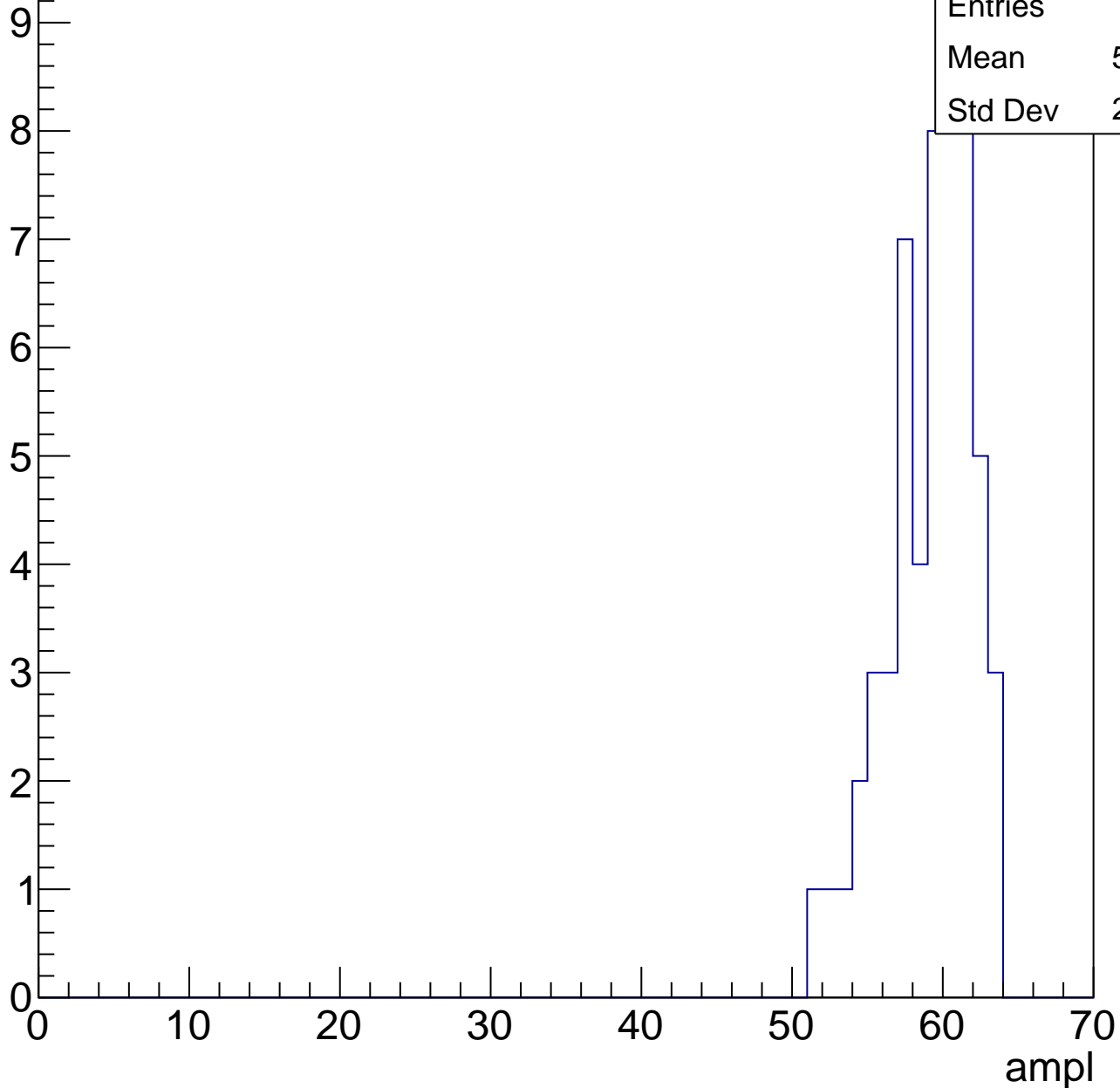
Entries	68
Mean	53.16
Std Dev	3.368



B1L100S, U13-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

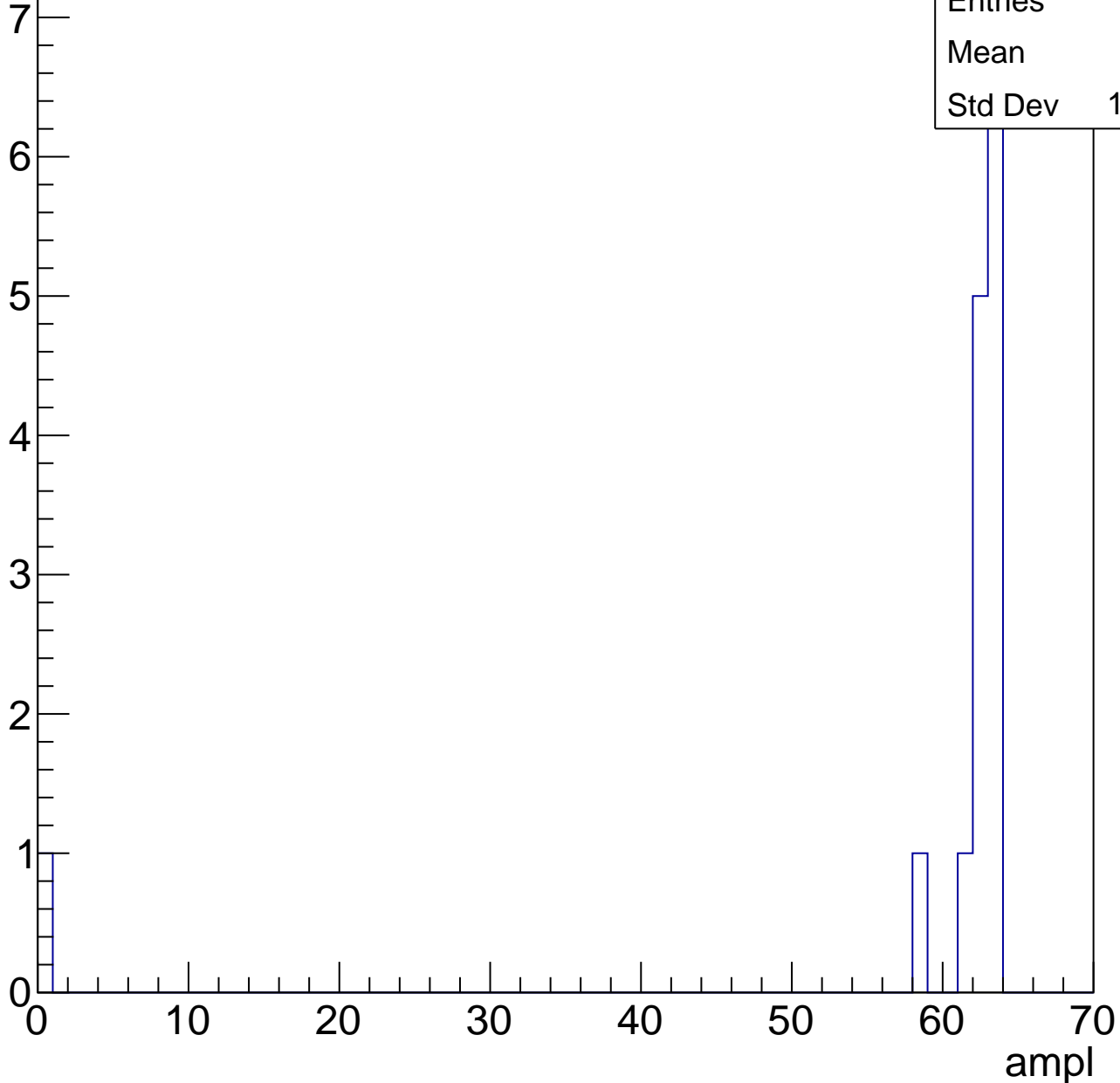


B1L100S, U13-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	58
Std Dev	15.55



B1L100S, U13-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U13-ch126, adc0

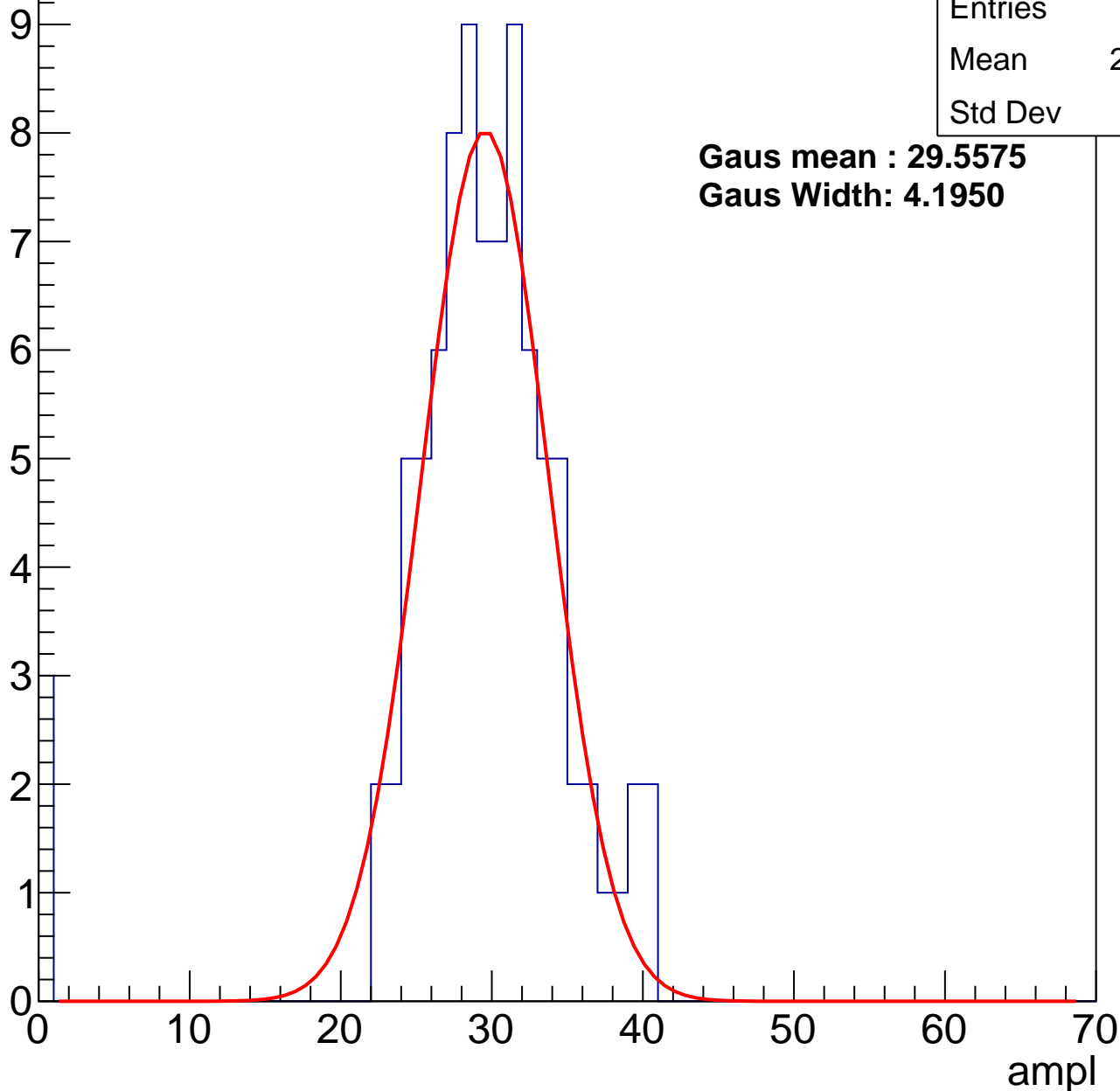
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	28.69
Std Dev	6.74

Gaus mean : 29.5575

Gaus Width: 4.1950



B1L100S, U13-ch126, adc1

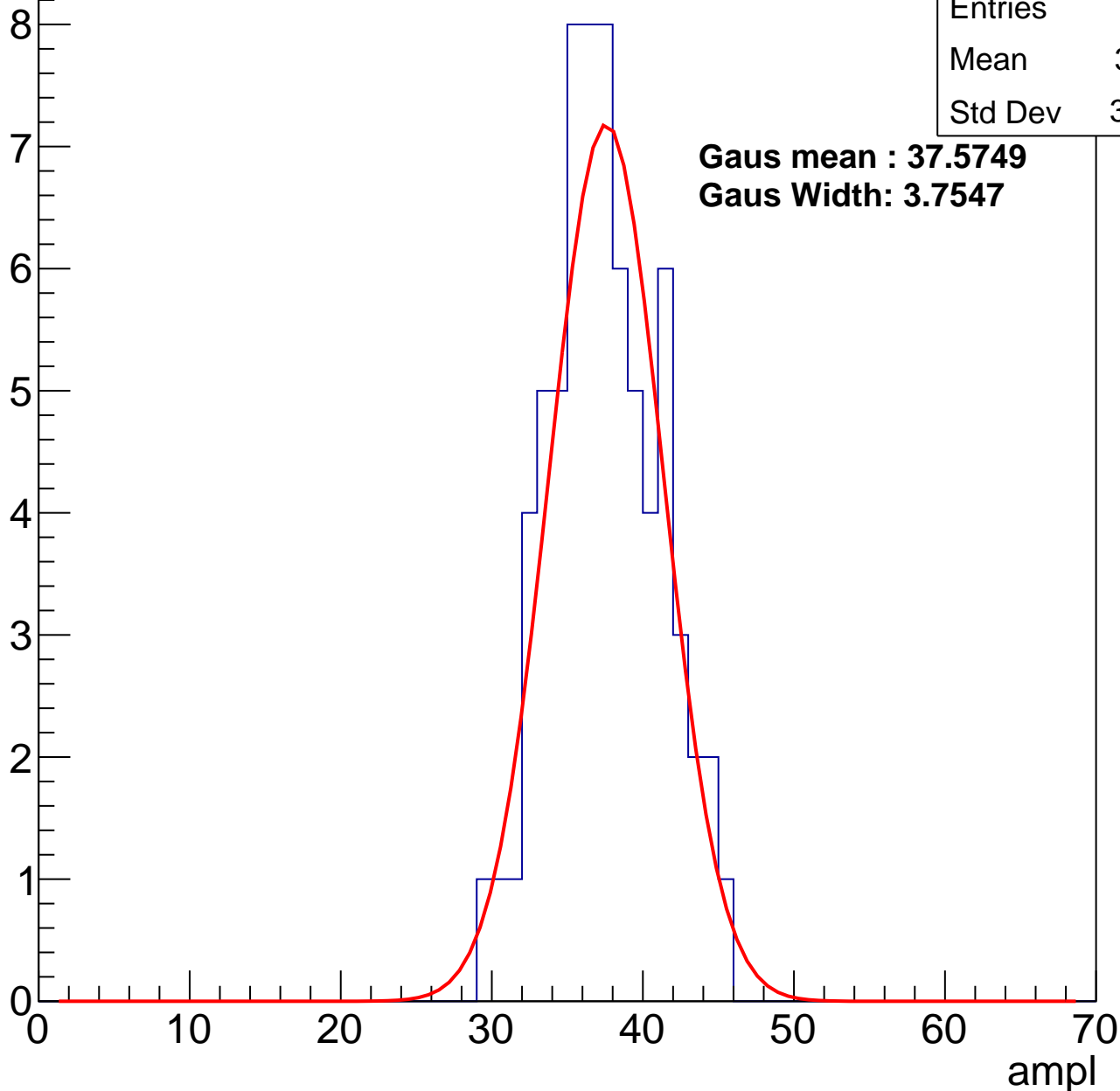
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.01
Std Dev	3.548

Gaus mean : 37.5749

Gaus Width: 3.7547



B1L100S, U13-ch126, adc2

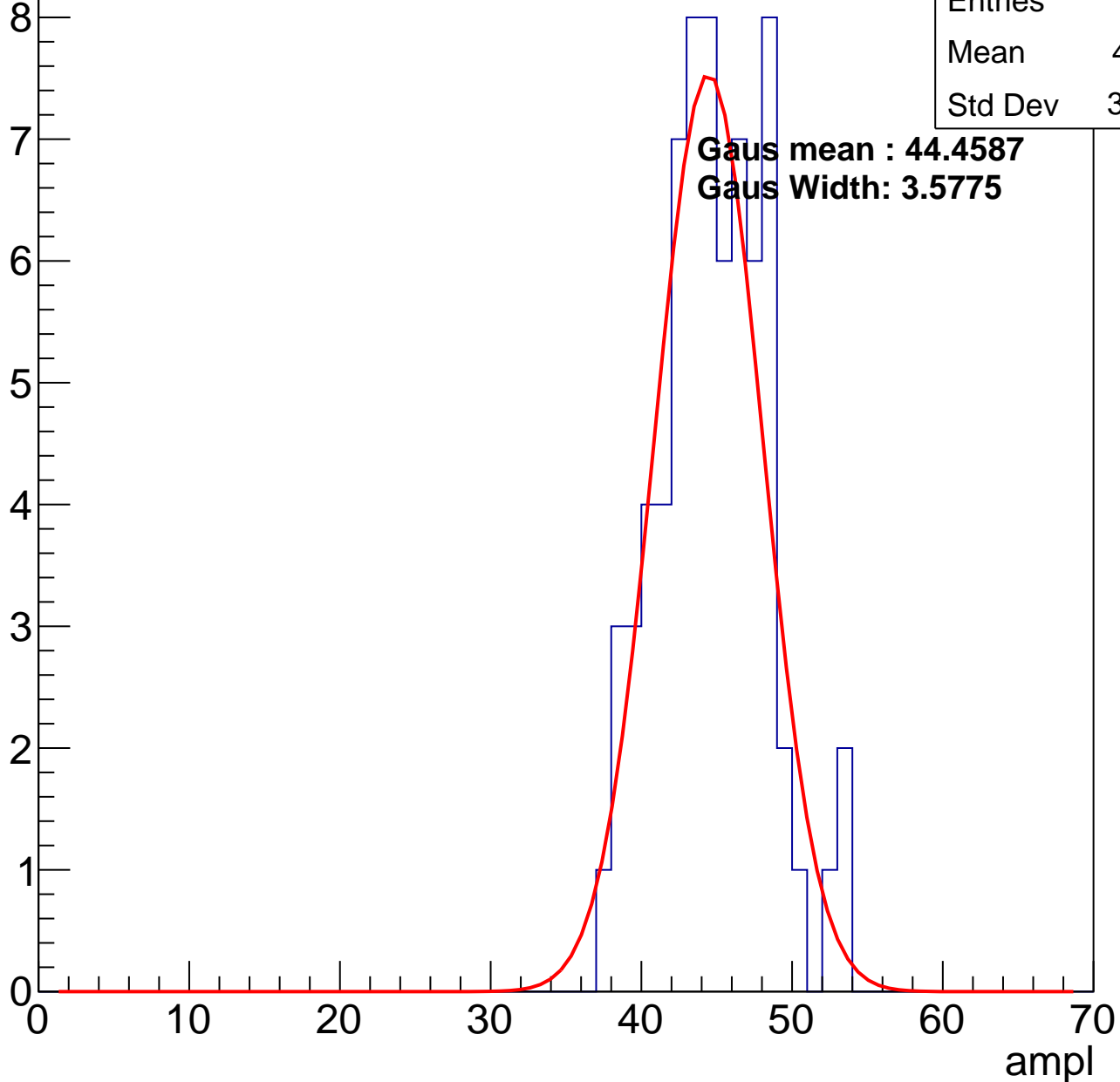
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.31
Std Dev	3.539

Gaus mean : 44.4587

Gaus Width: 3.5775

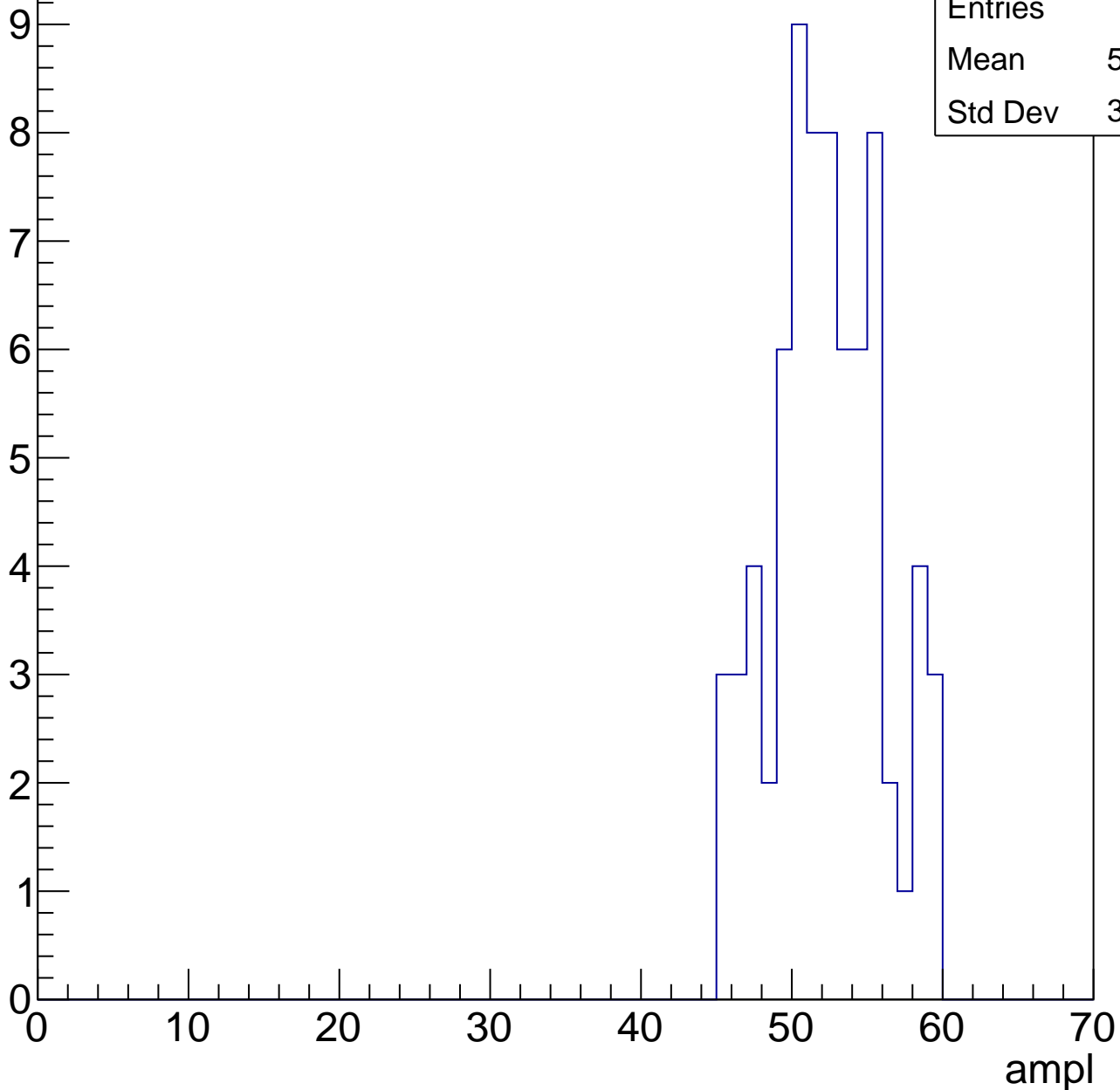


B1L100S, U13-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	51.85
Std Dev	3.576

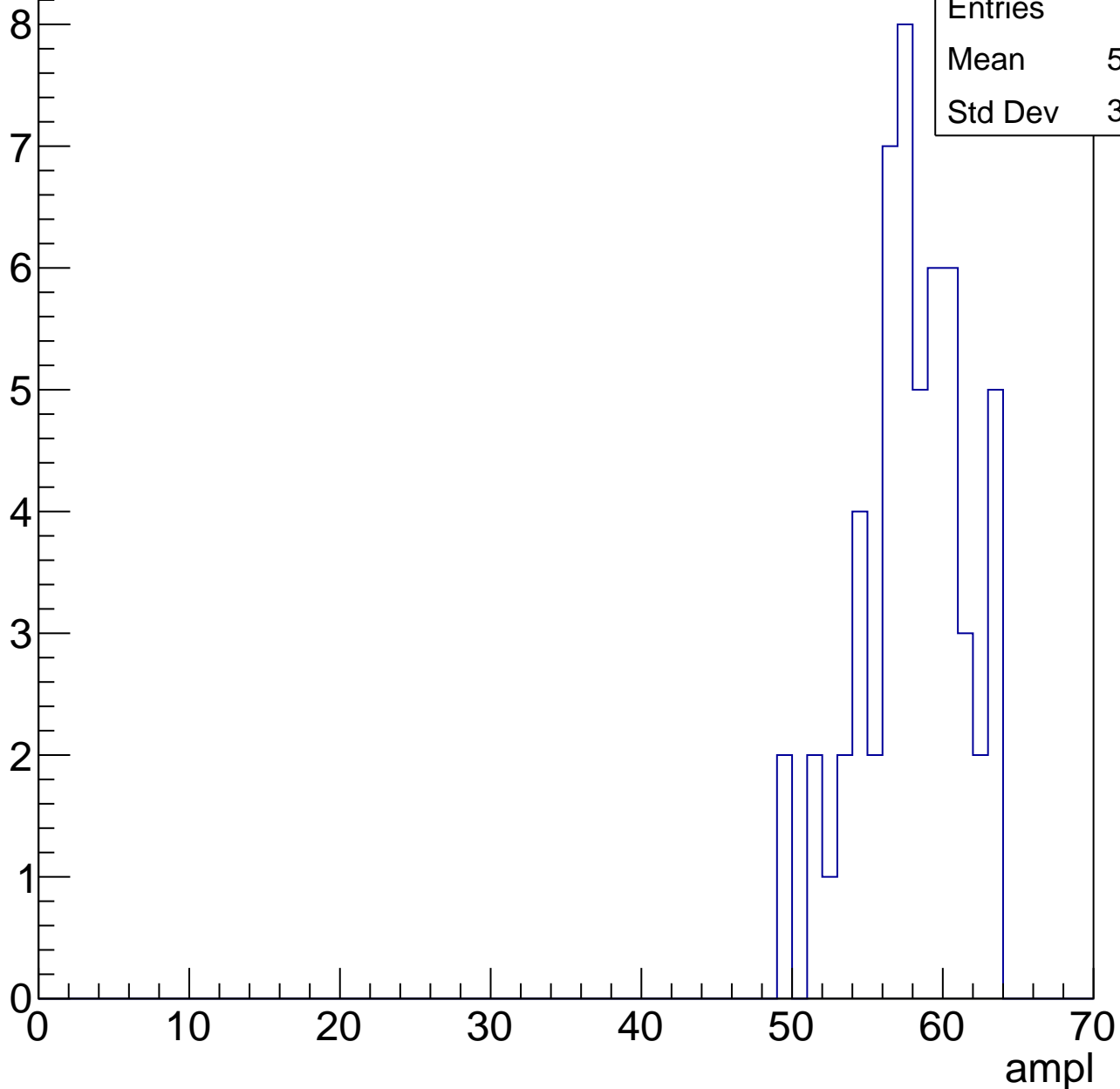


B1L100S, U13-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.42
Std Dev	3.462

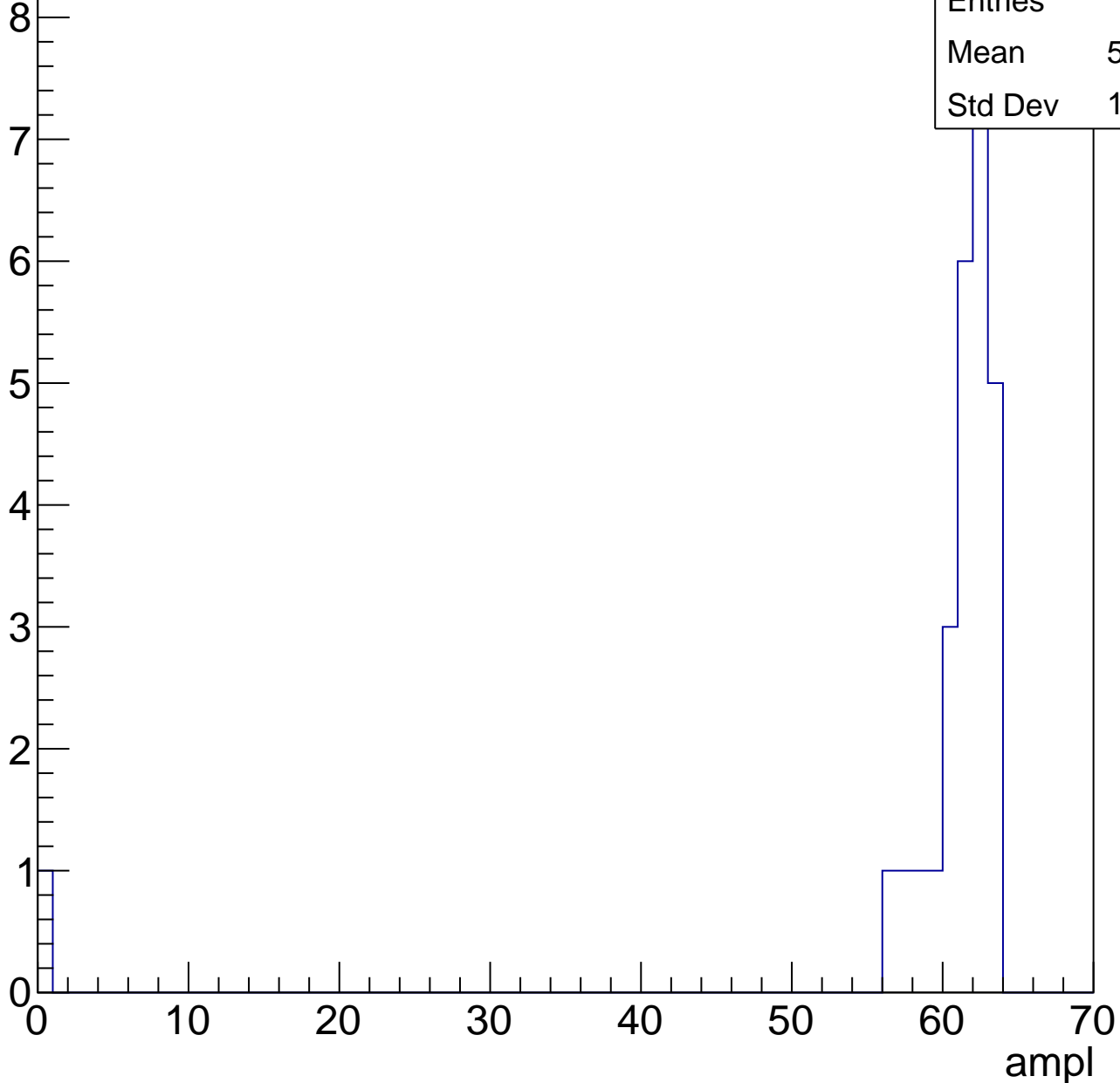


B1L100S, U13-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

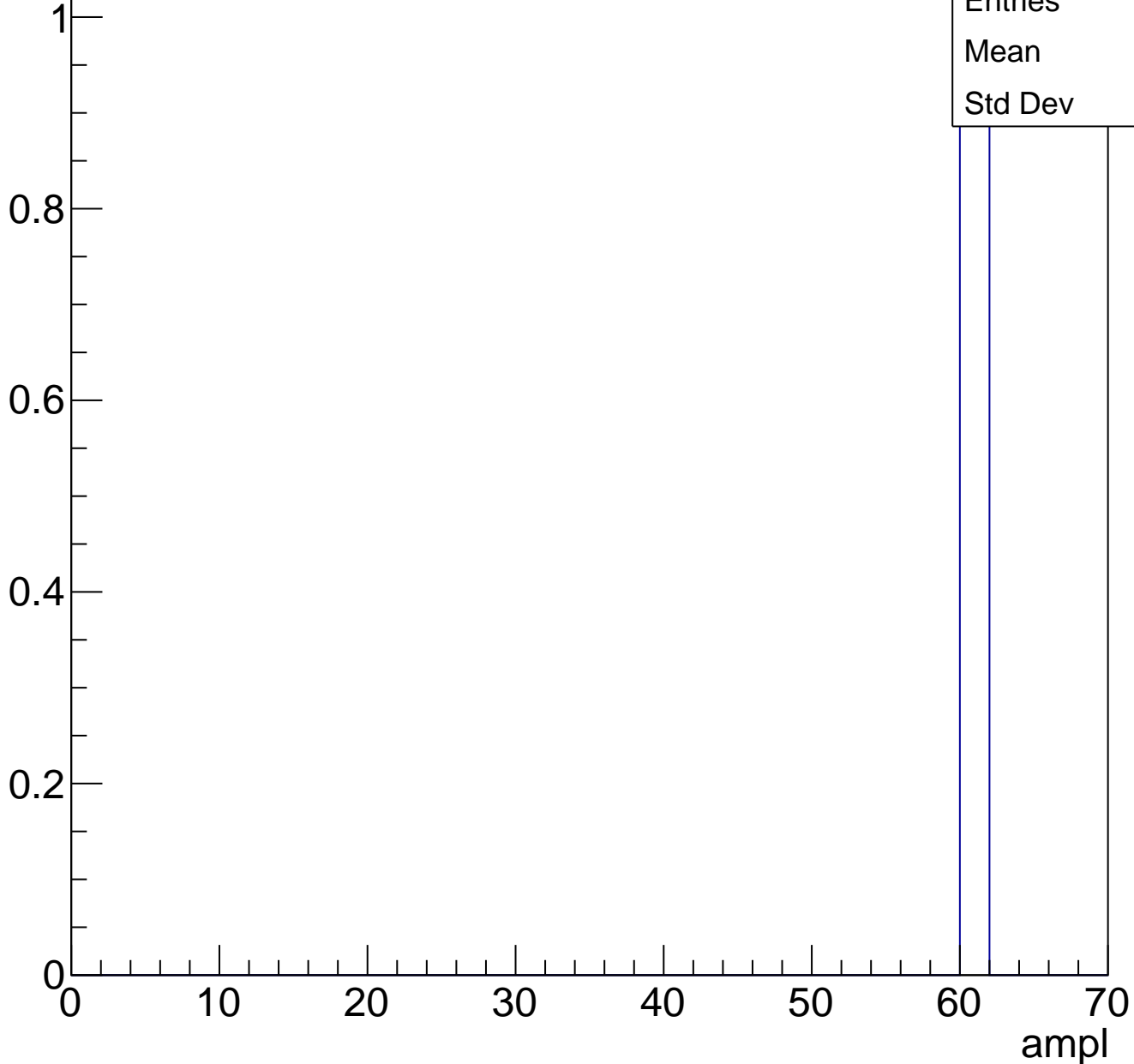
Entries	27
Mean	58.78
Std Dev	11.66



B1L100S, U13-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch127, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	29.89
Std Dev	3.241

Gaus mean : 30.4568

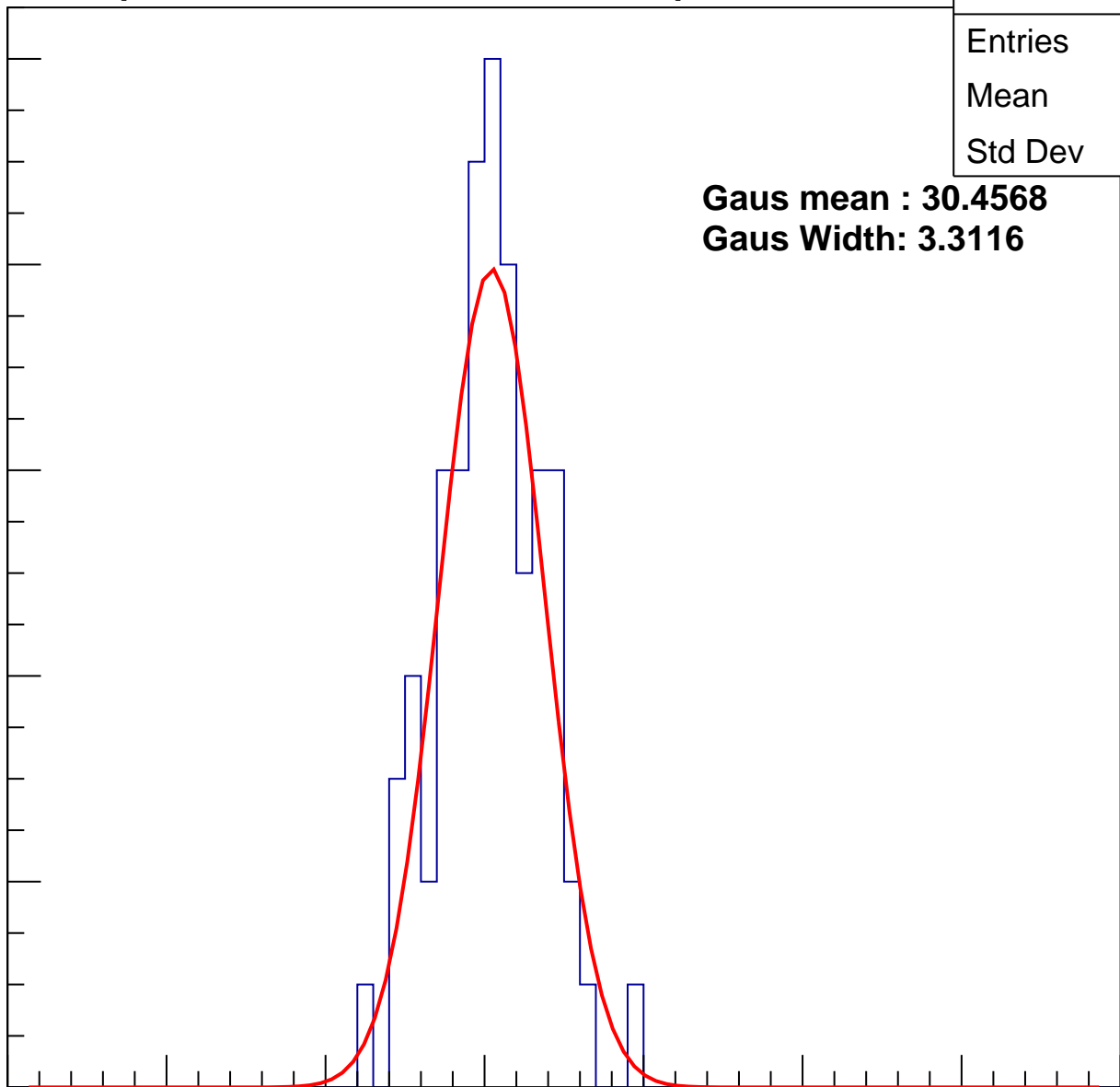
Gaus Width: 3.3116

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U13-ch127, adc1

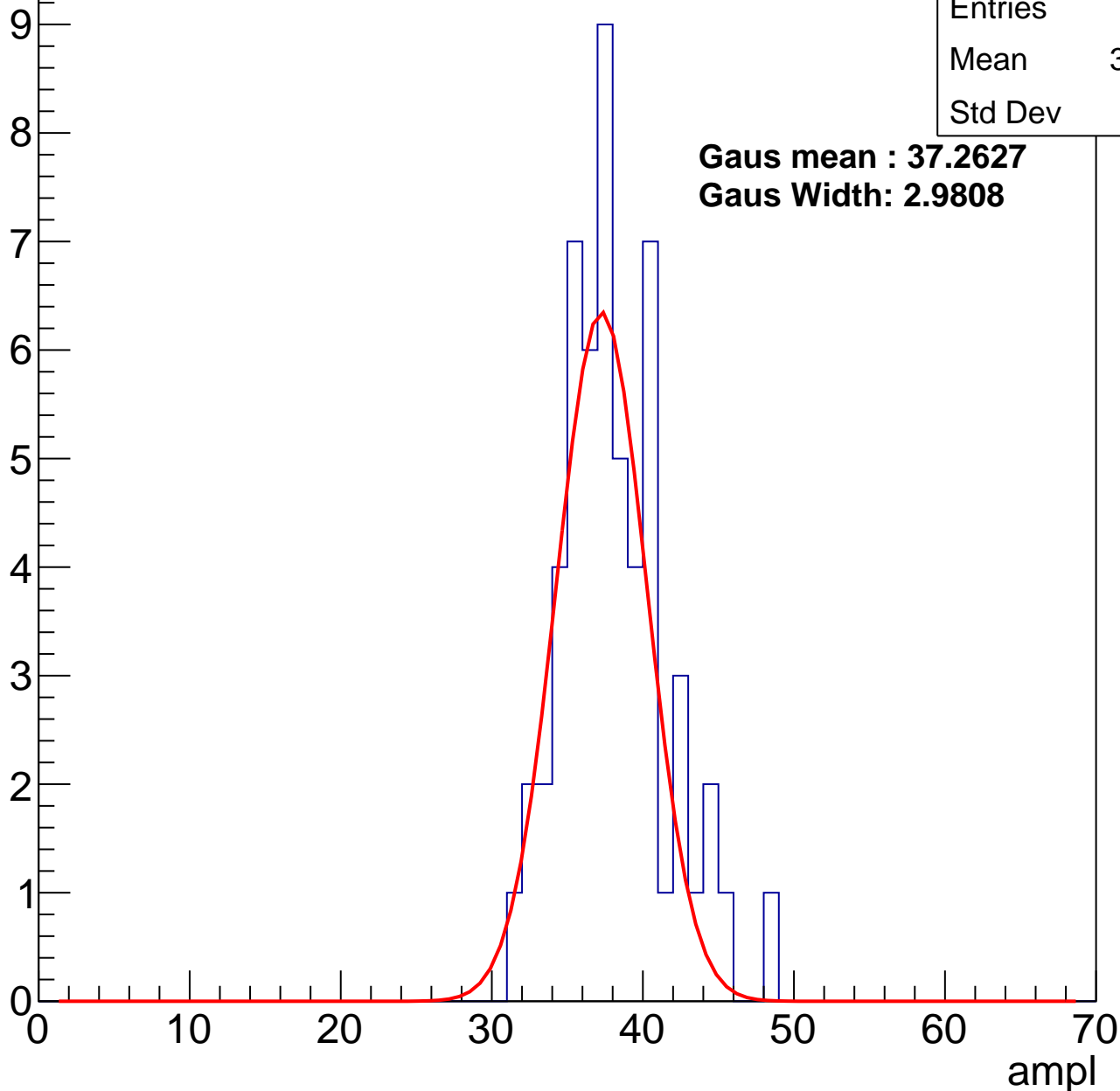
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.64
Std Dev	3.43

Gaus mean : 37.2627

Gaus Width: 2.9808



B1L100S, U13-ch127, adc2

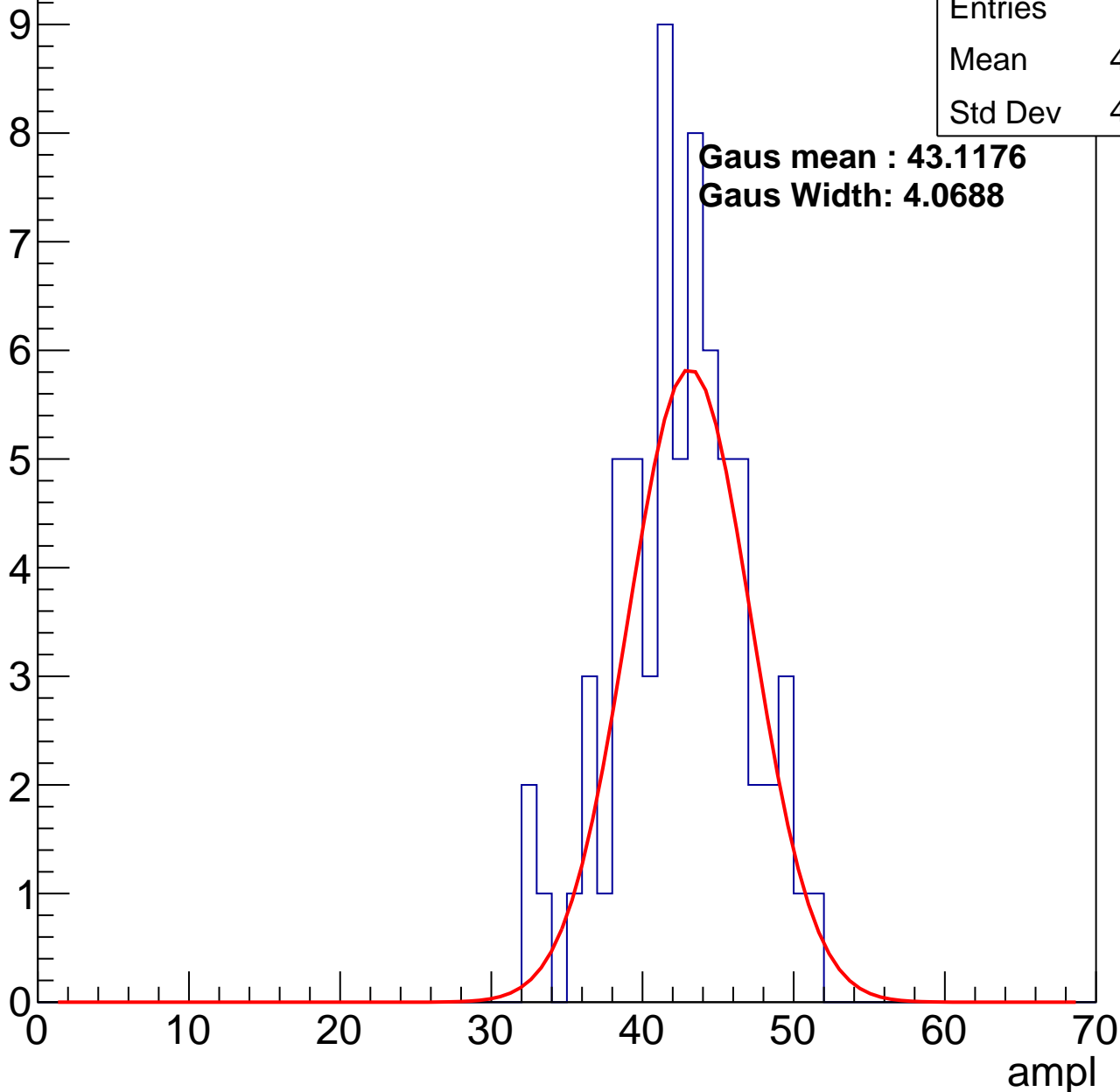
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.09
Std Dev	4.165

Gaus mean : 43.1176

Gaus Width: 4.0688

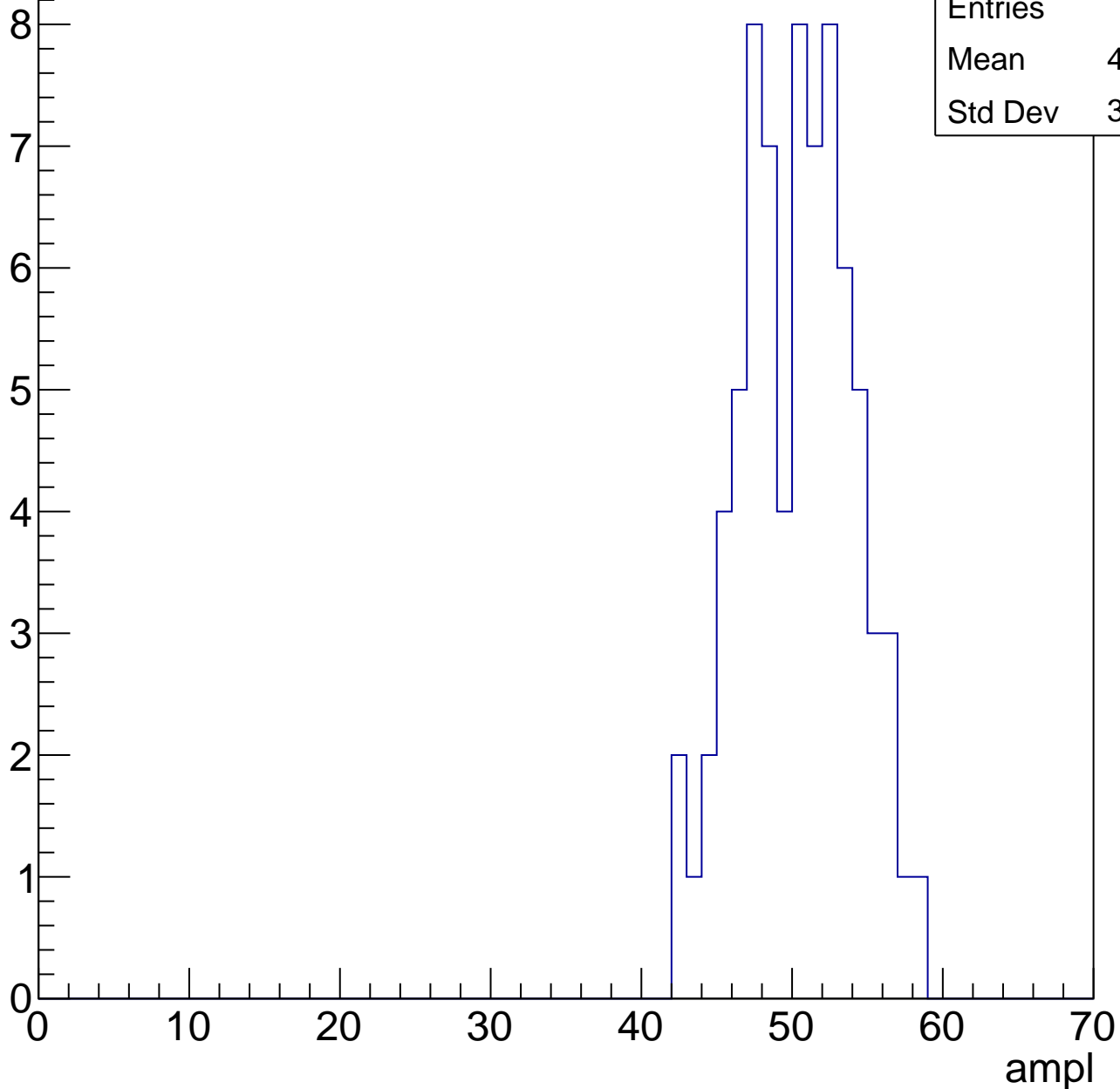


B1L100S, U13-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	49.89
Std Dev	3.654

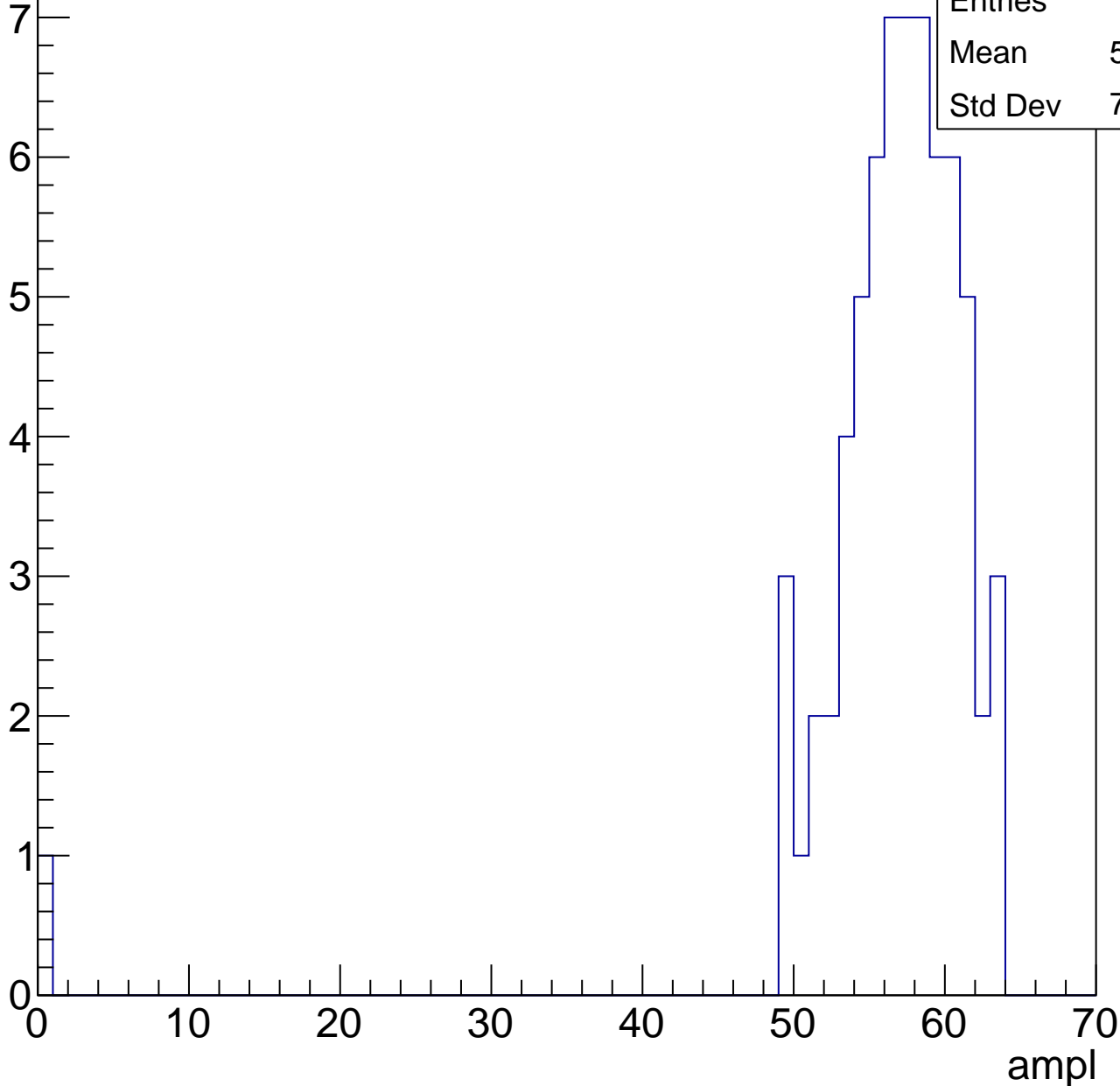


B1L100S, U13-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	55.88
Std Dev	7.718

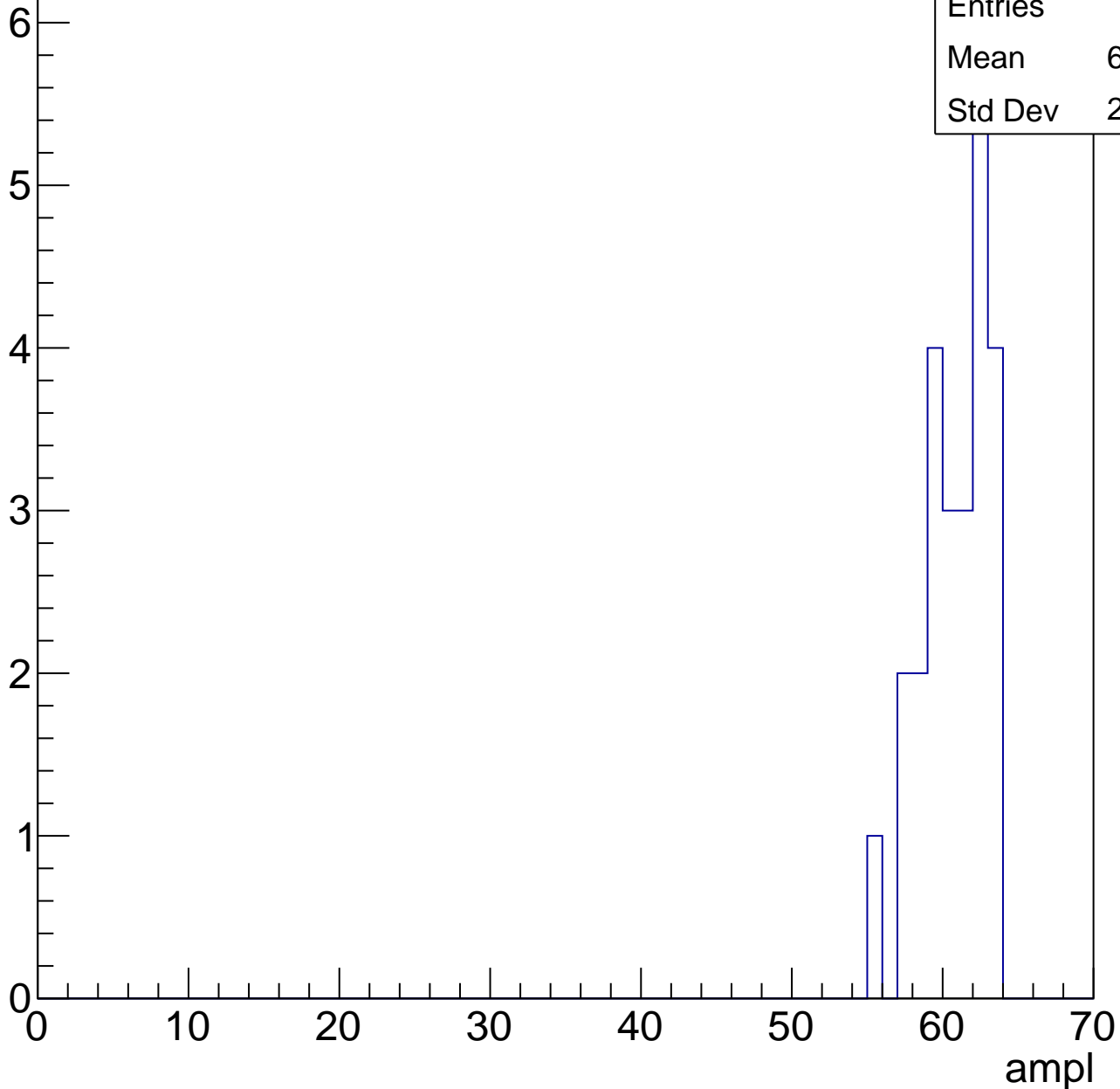


B1L100S, U13-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

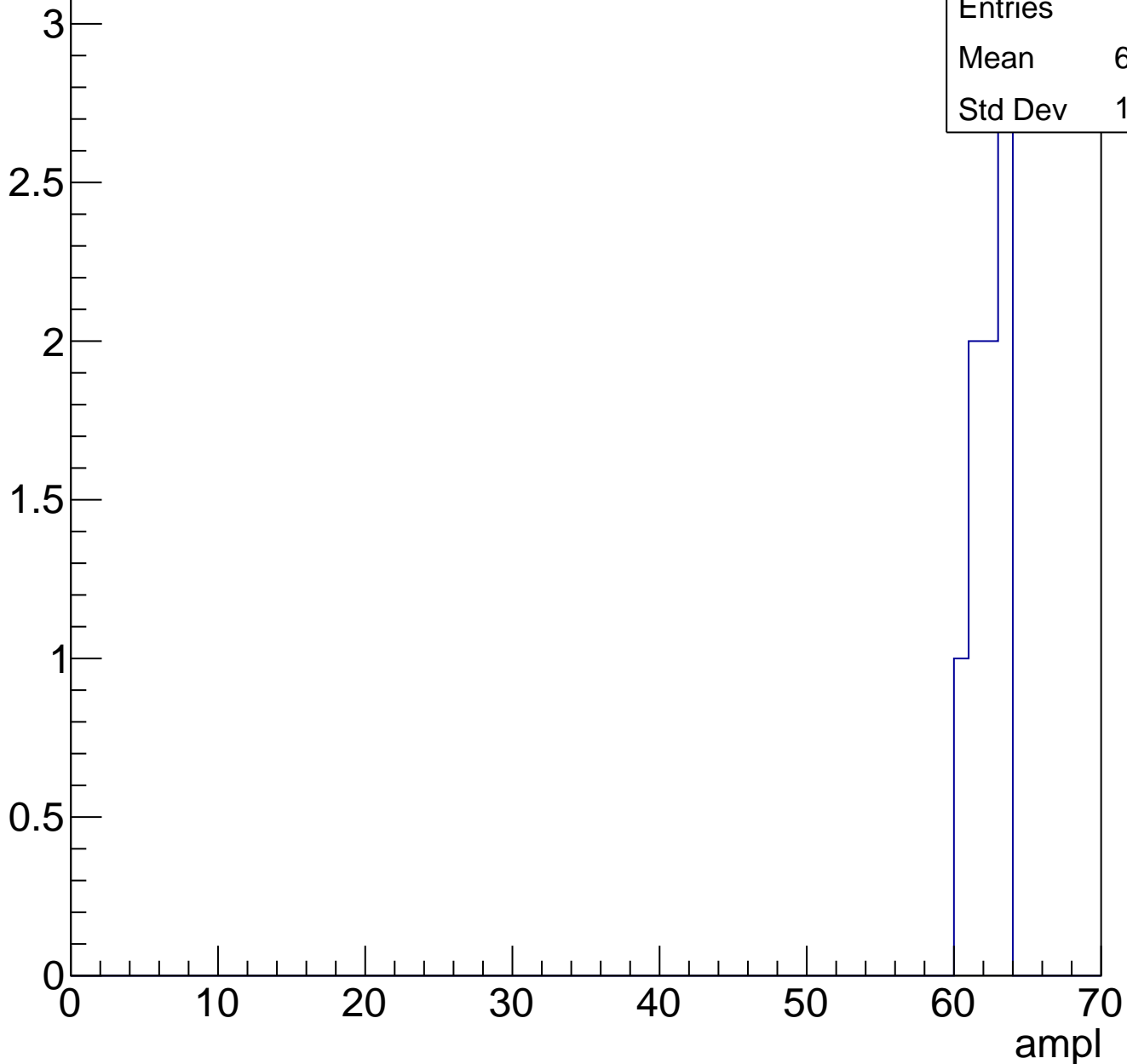
Entries	25
Mean	60.32
Std Dev	2.149



B1L100S, U13-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0