

B1L103S, U6-ch0, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	18.54
Std Dev	11.61

Gaus mean : 25.6531

Gaus Width: 4.1670

Entry

25

20

15

10

5

0

0

10

20

30

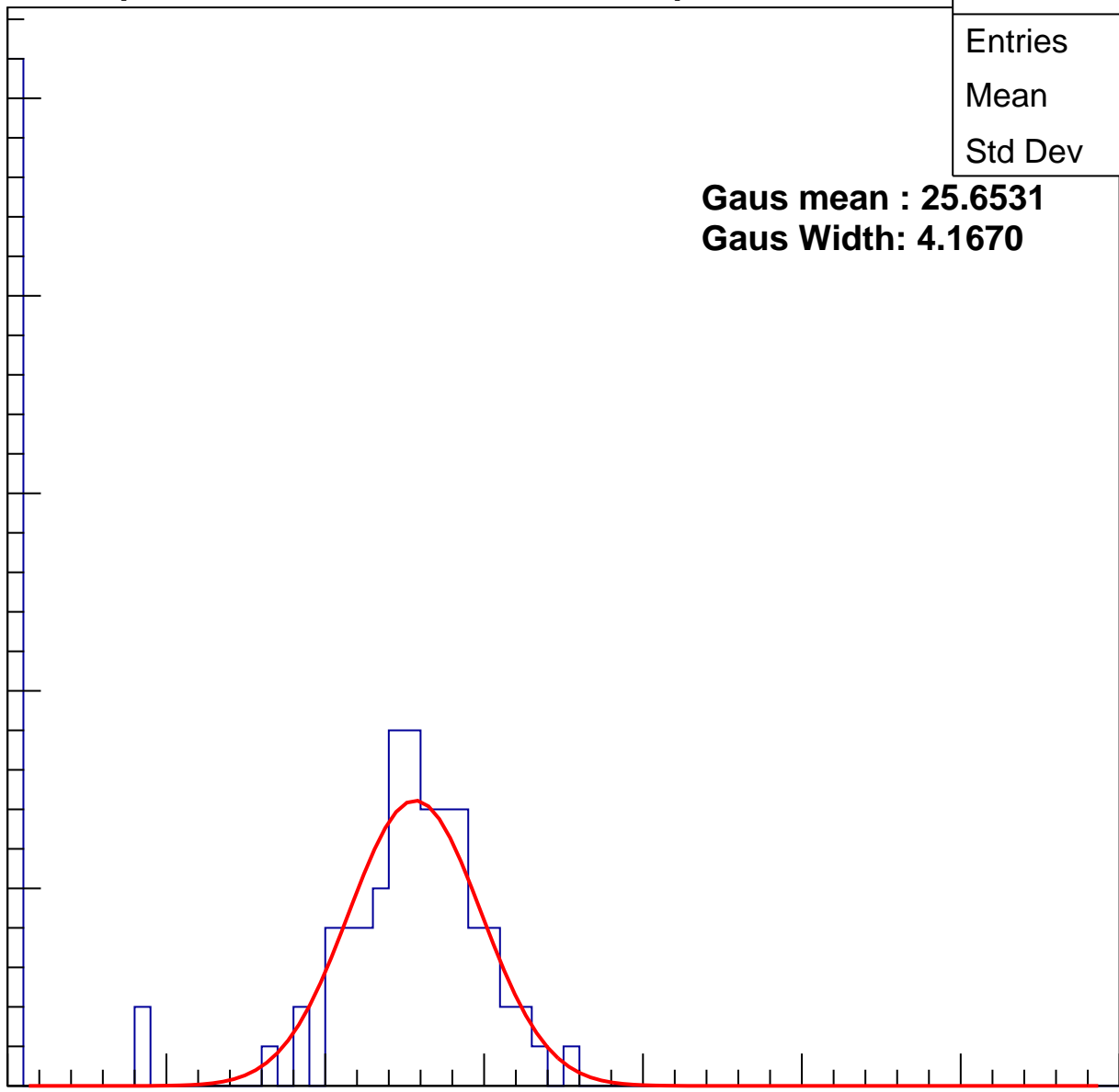
40

50

60

70

ampl



B1L103S, U6-ch0, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.4
Std Dev	12.36

Gaus mean : 32.7971

Gaus Width: 3.6444

Entry

12

10

8

6

4

2

0

0

10

20

30

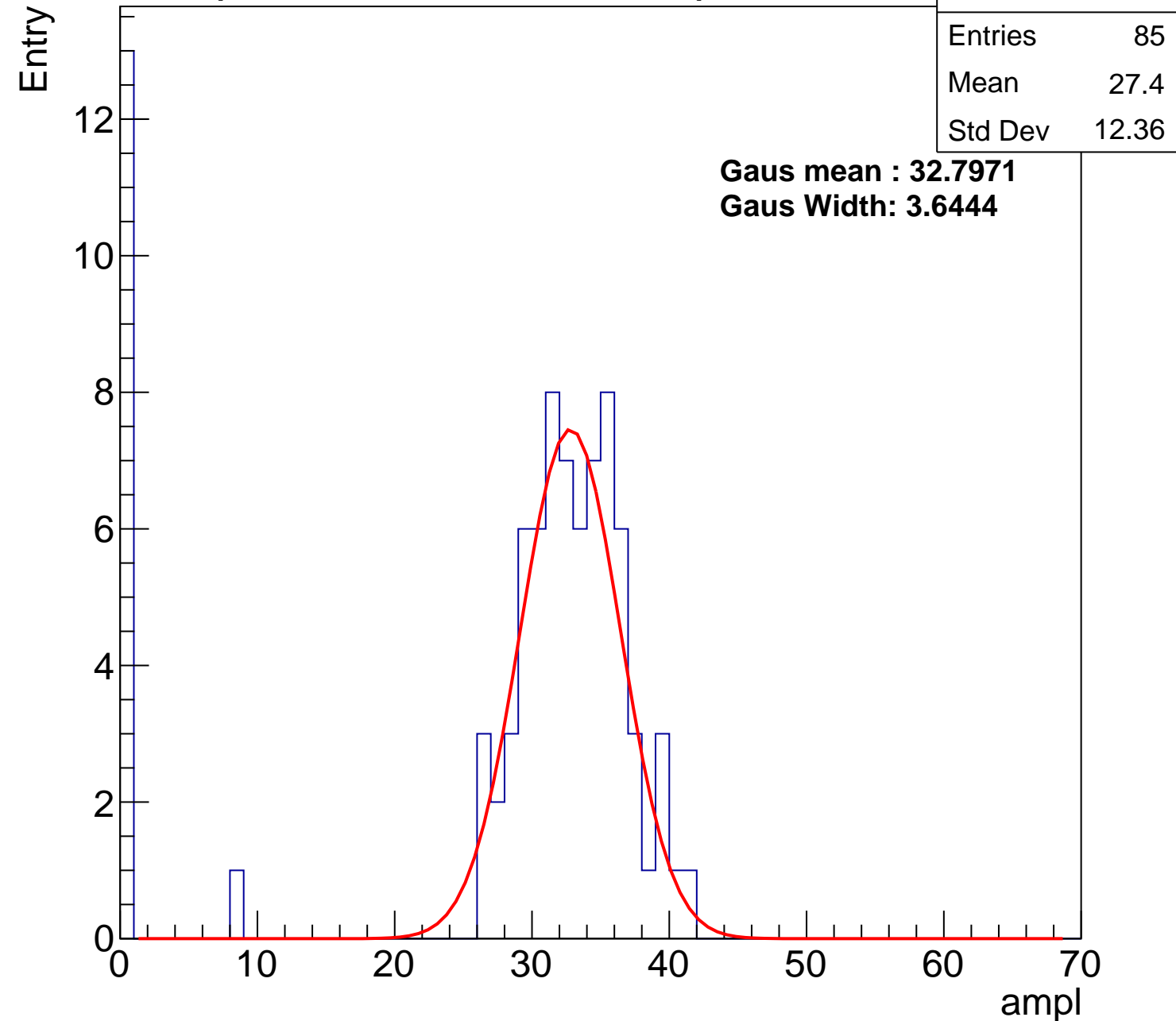
40

50

60

70

ampl



B1L103S, U6-ch0, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 72

Mean 36.35

Std Dev 11.52

Gaus mean : 40.0594

Gaus Width: 3.3632

0

0

10

20

30

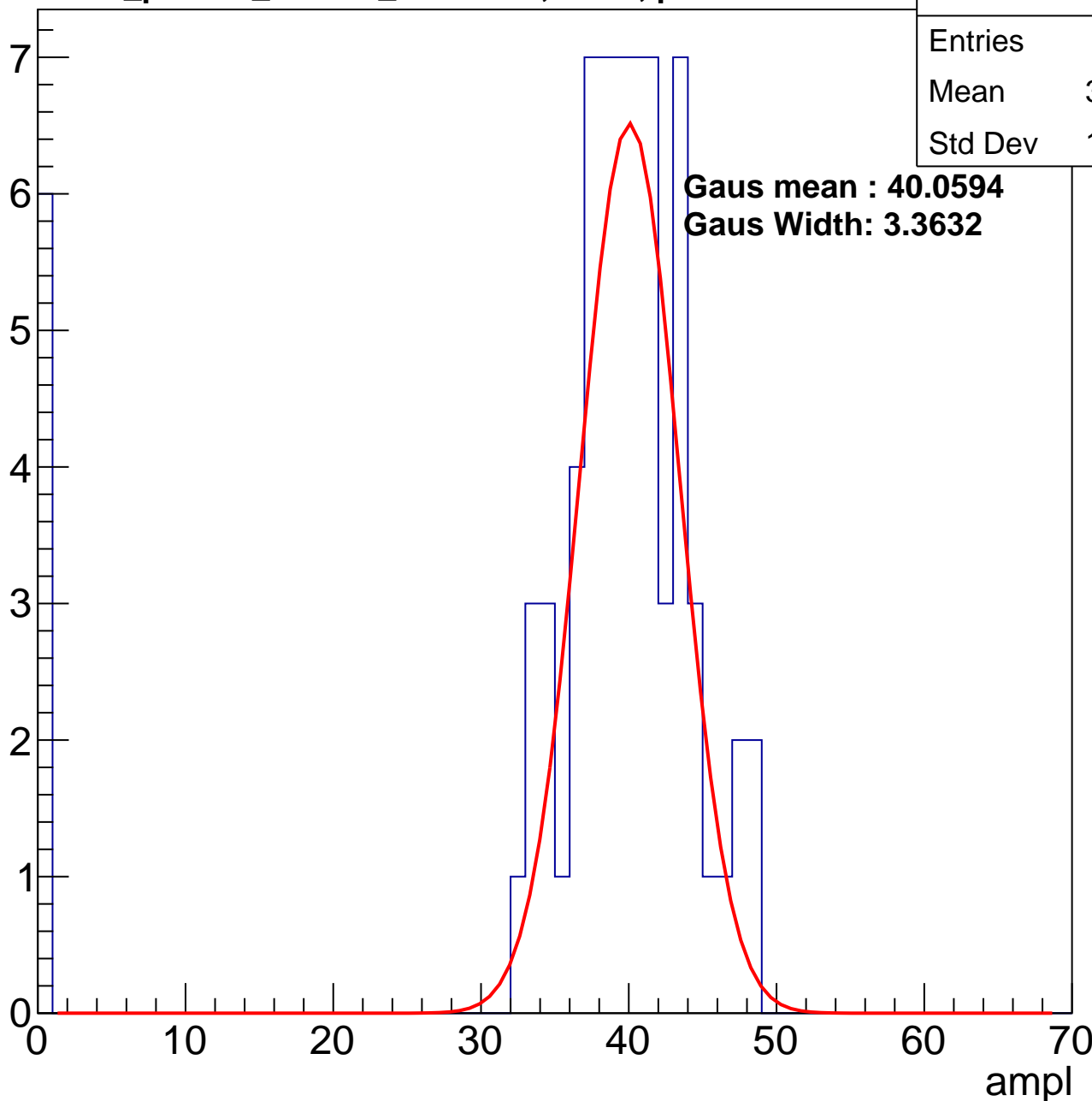
40

50

60

70

ampl

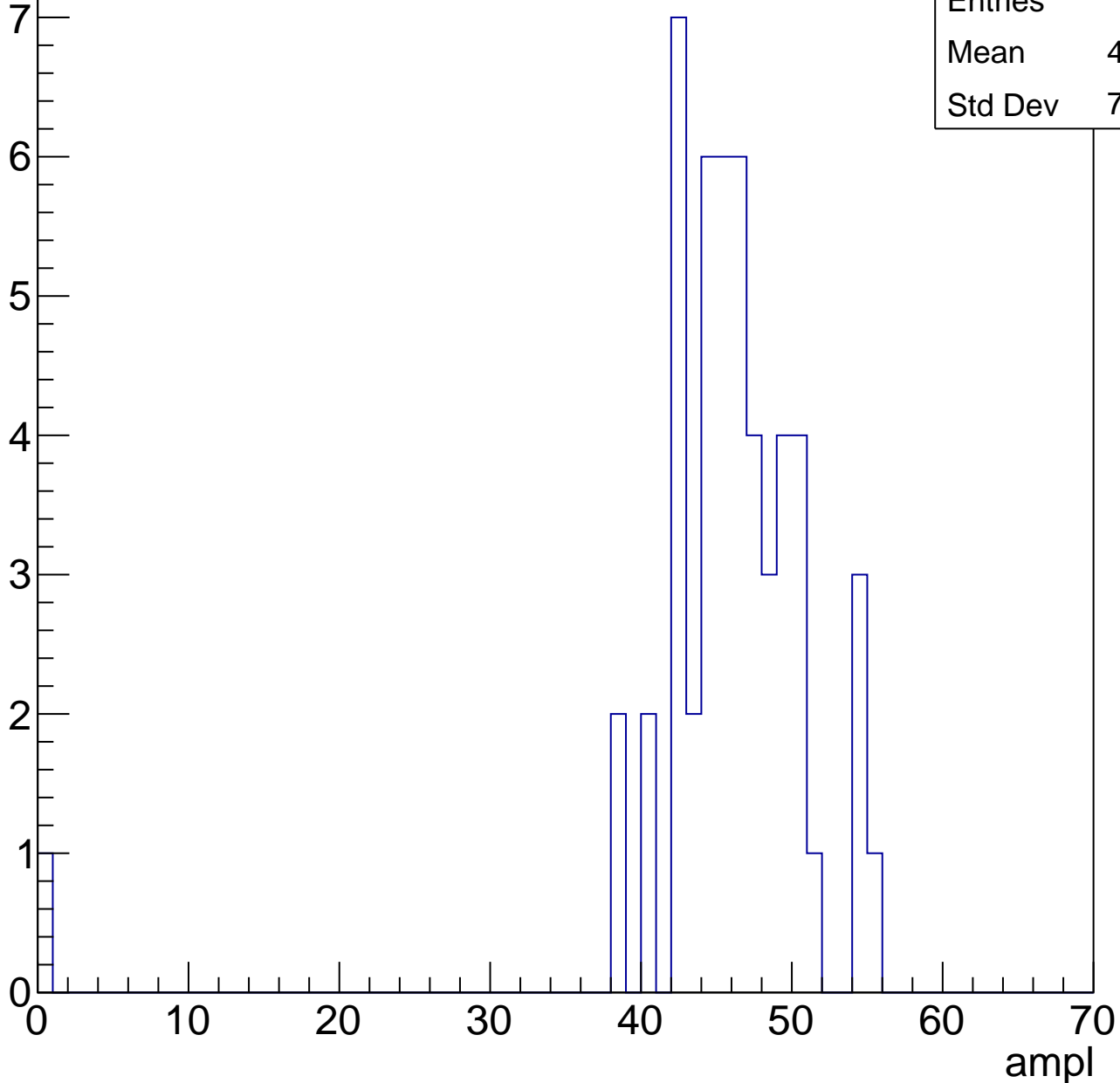


B1L103S, U6-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	45.04
Std Dev	7.393

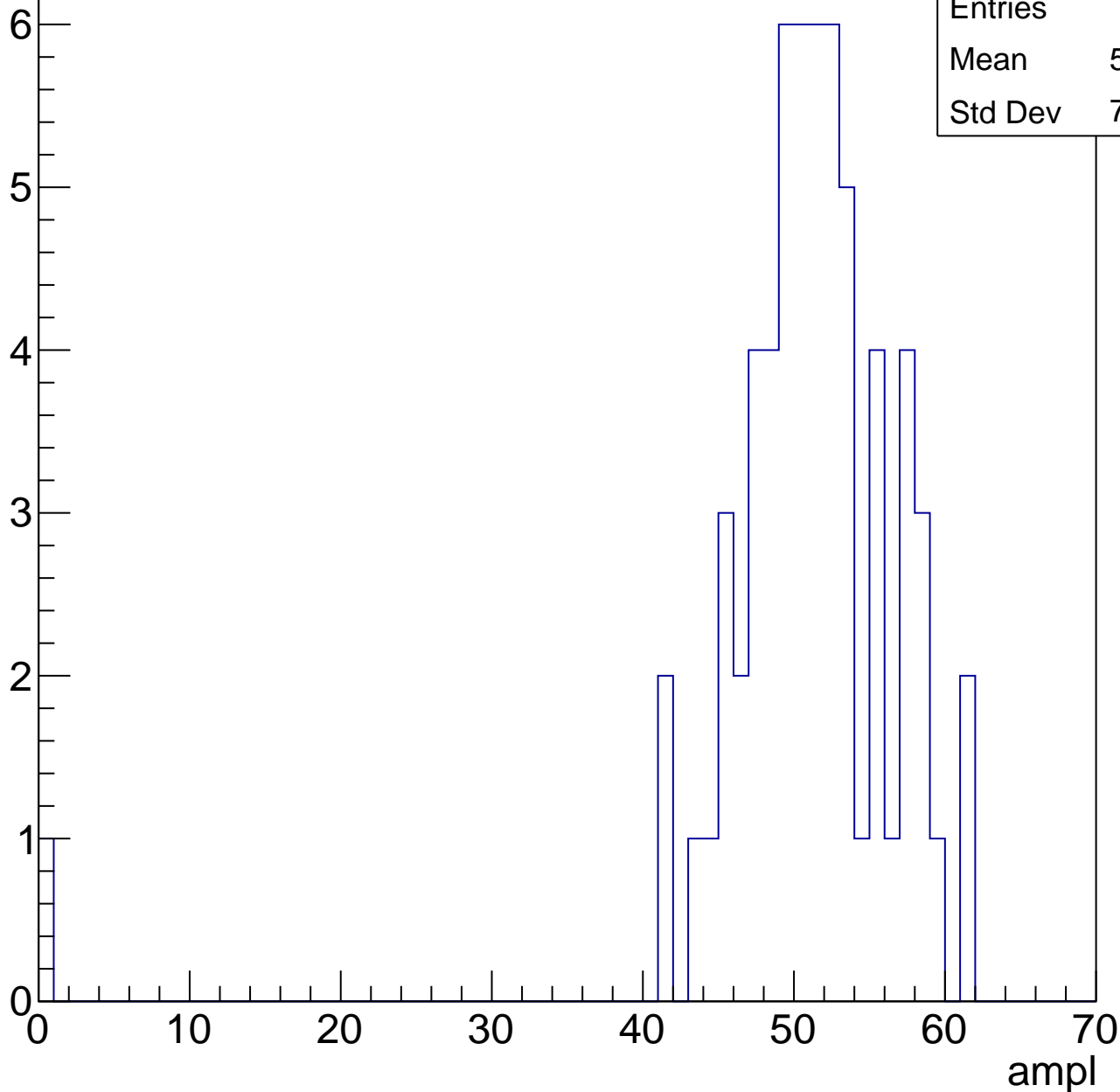


B1L103S, U6-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	50.25
Std Dev	7.813

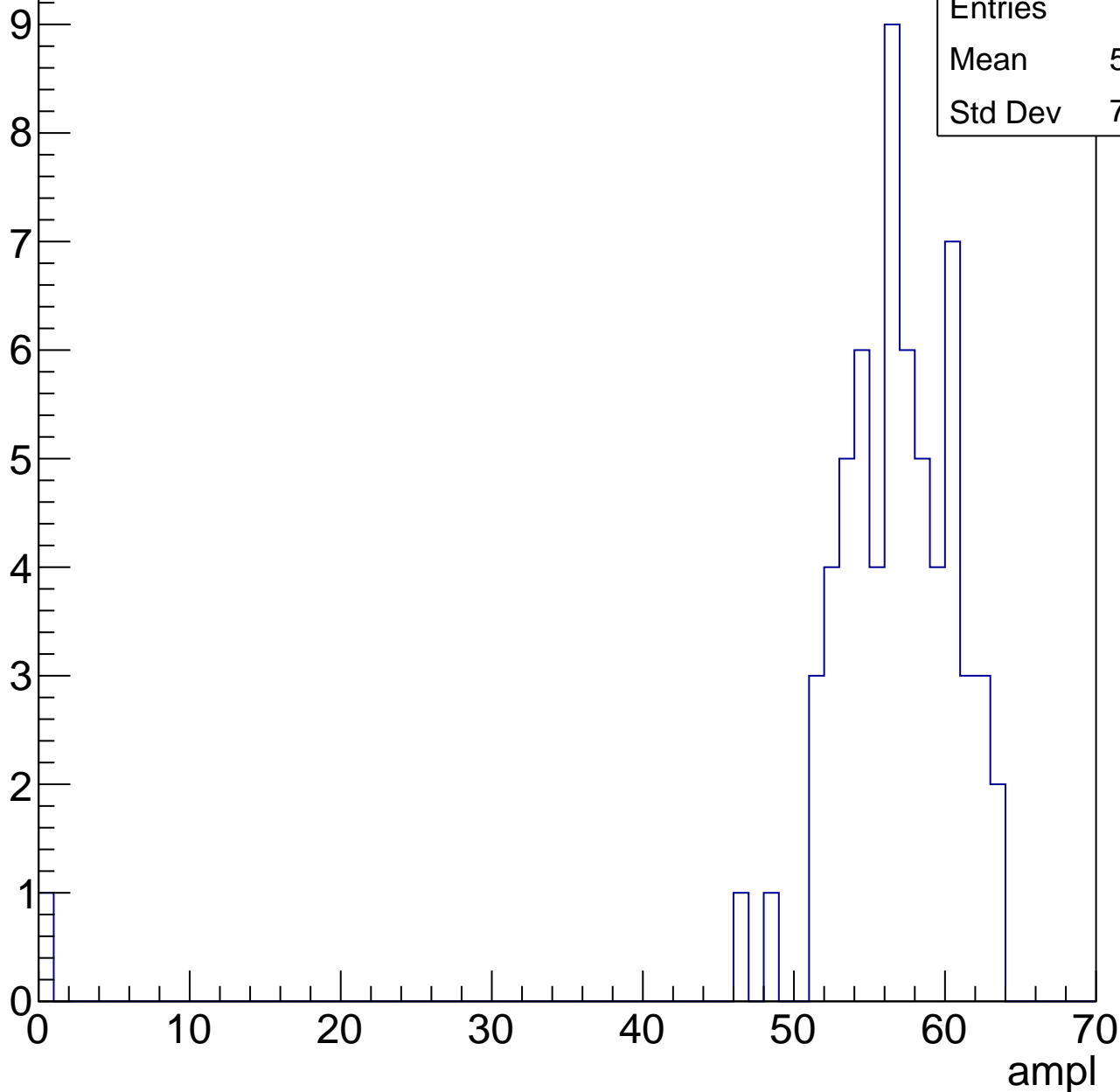


B1L103S, U6-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	55.48
Std Dev	7.856

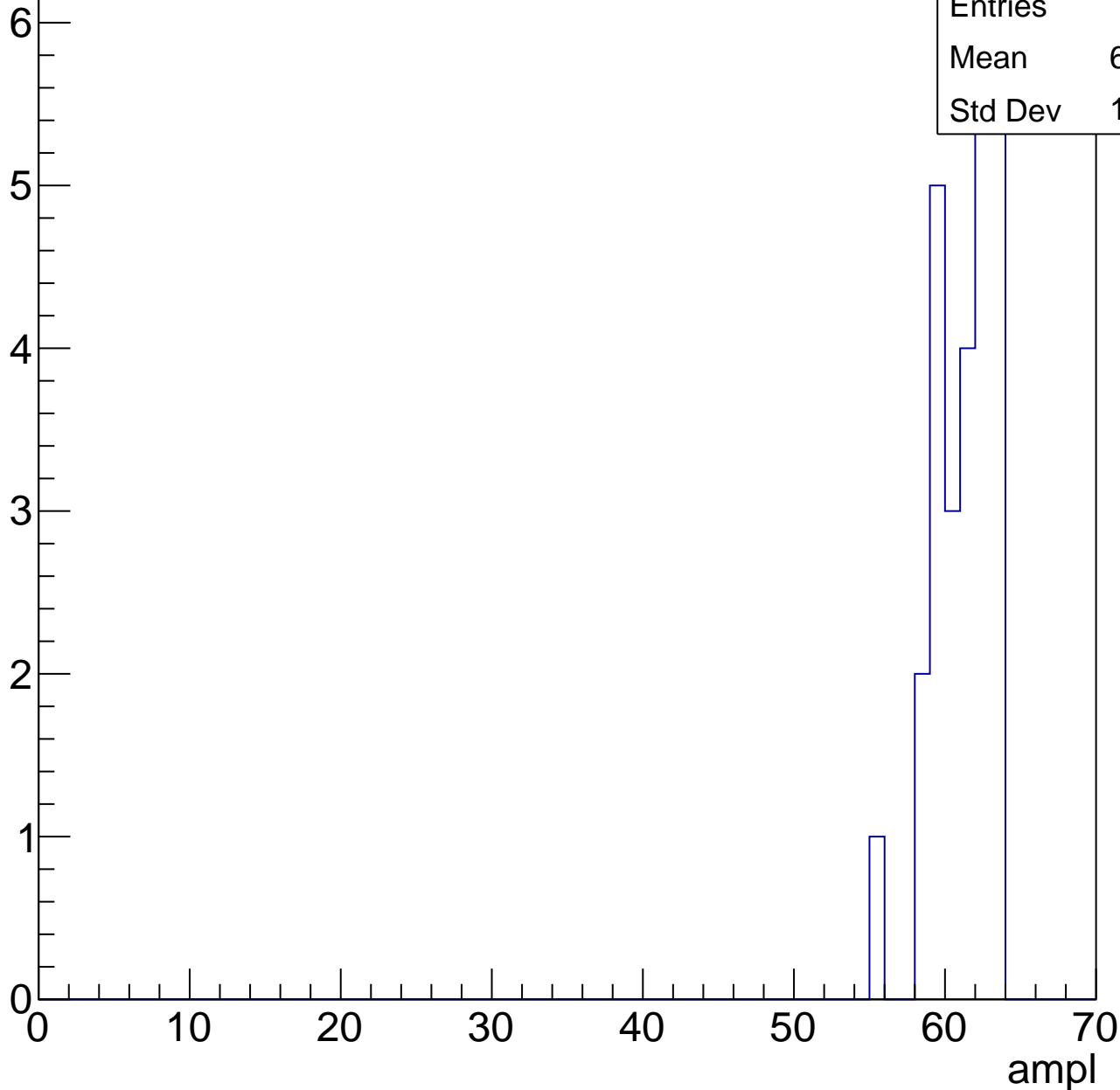


B1L103S, U6-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	60.74
Std Dev	1.974

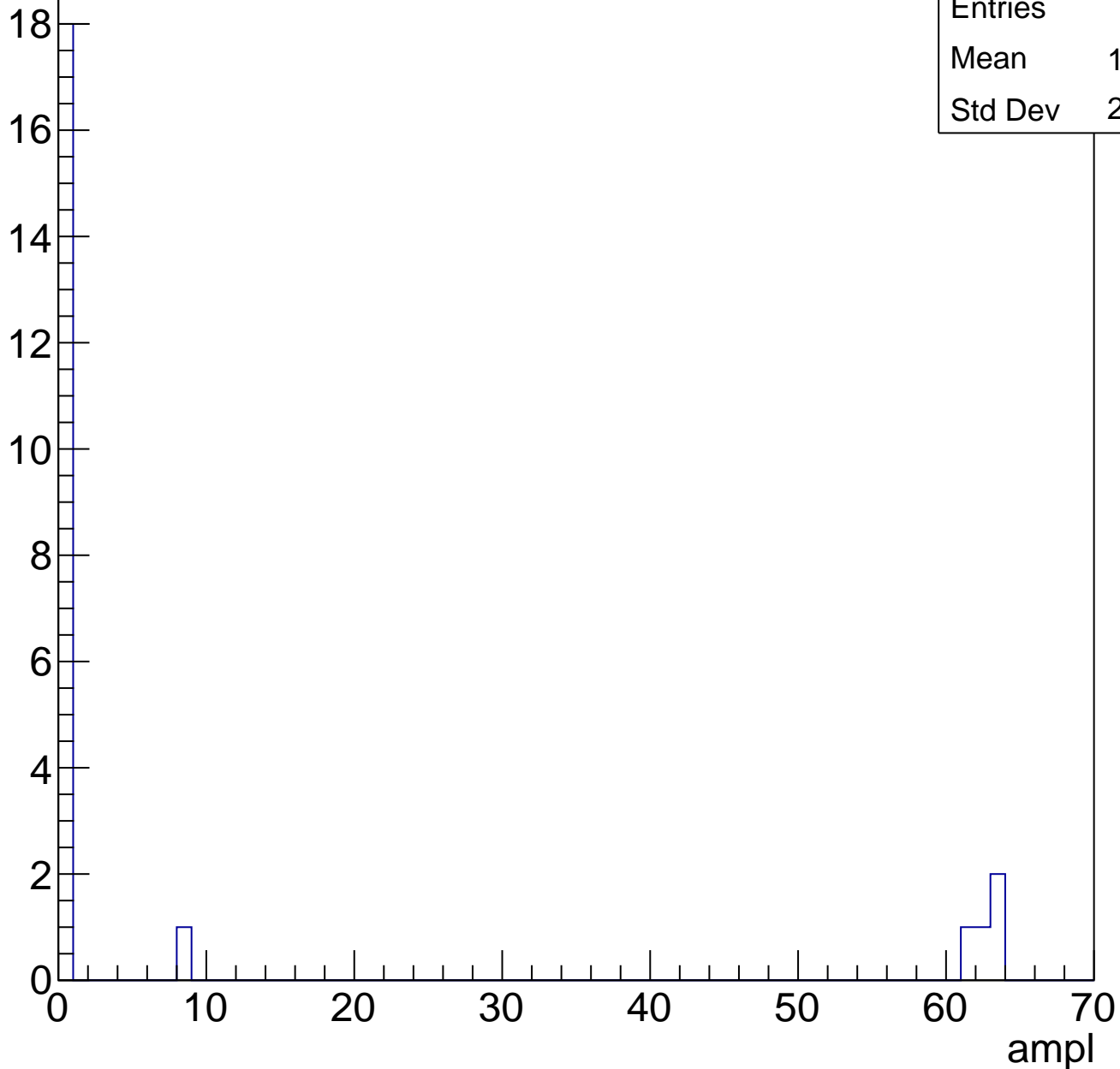


B1L103S, U6-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	11.17
Std Dev	23.49

Entry



B1L103S, U6-ch1, adc0

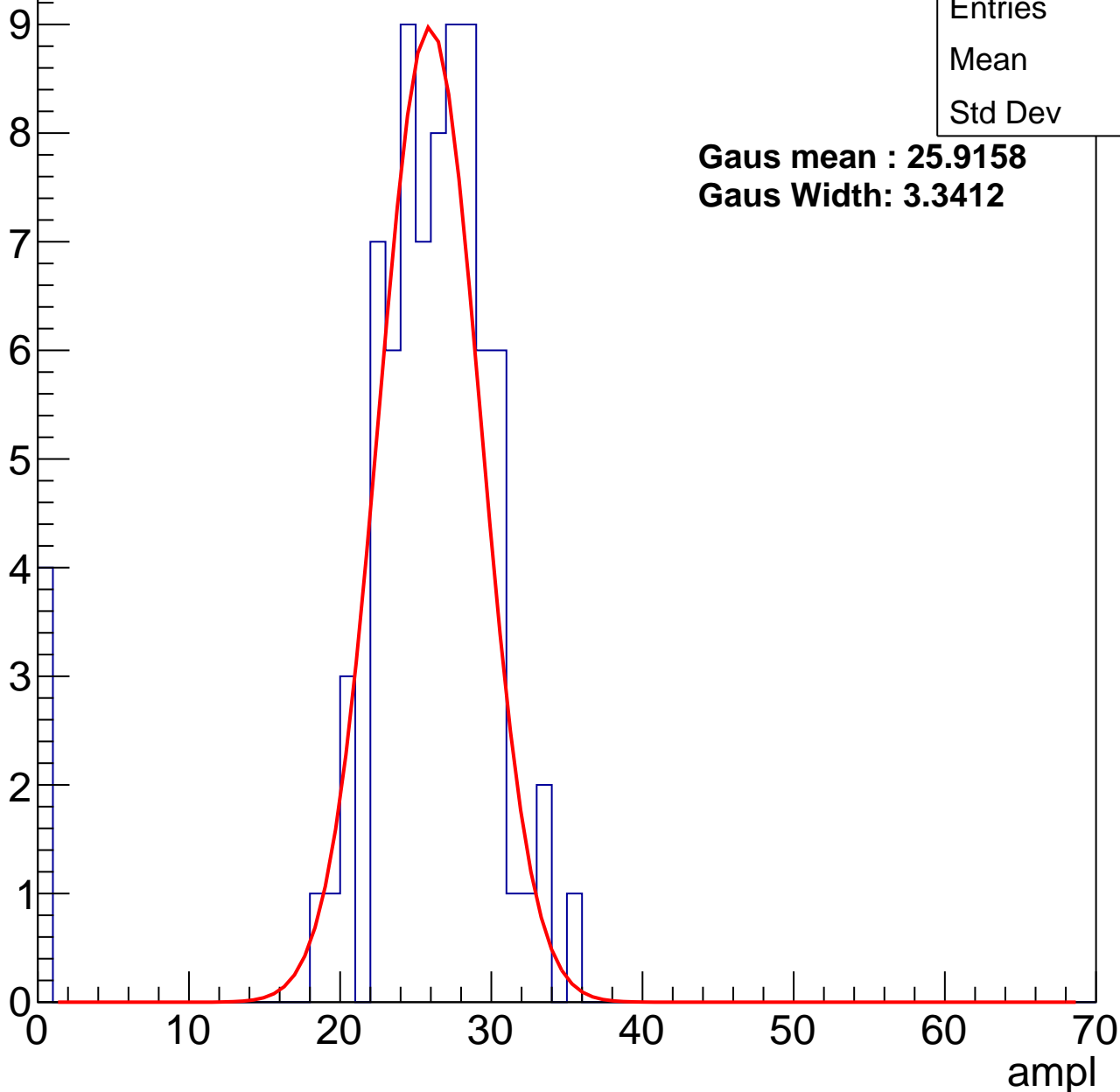
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	24.7
Std Dev	6.51

Gaus mean : 25.9158

Gaus Width: 3.3412



B1L103S, U6-ch1, adc1

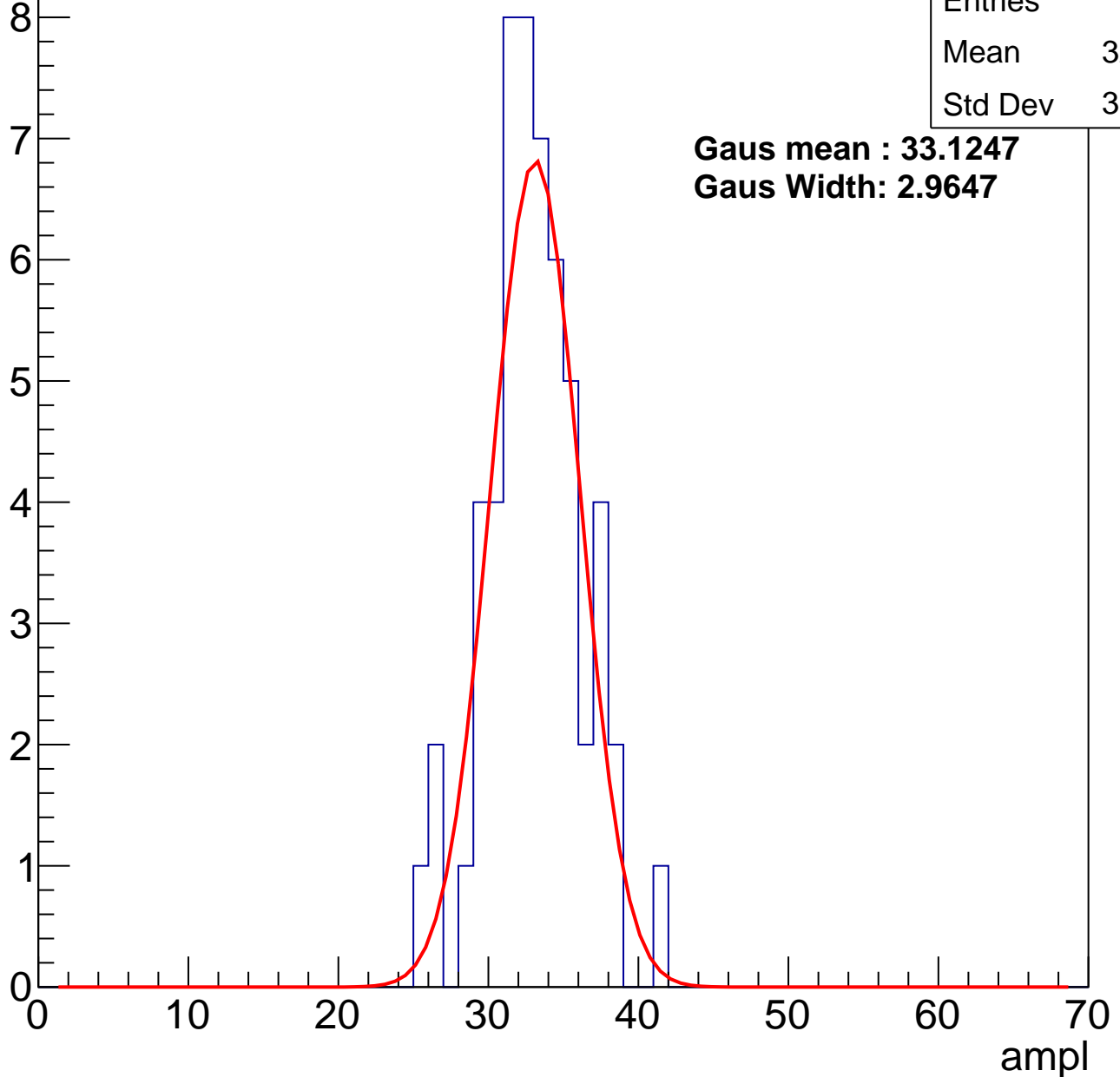
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	32.58
Std Dev	3.132

Gaus mean : 33.1247

Gaus Width: 2.9647



B1L103S, U6-ch1, adc2

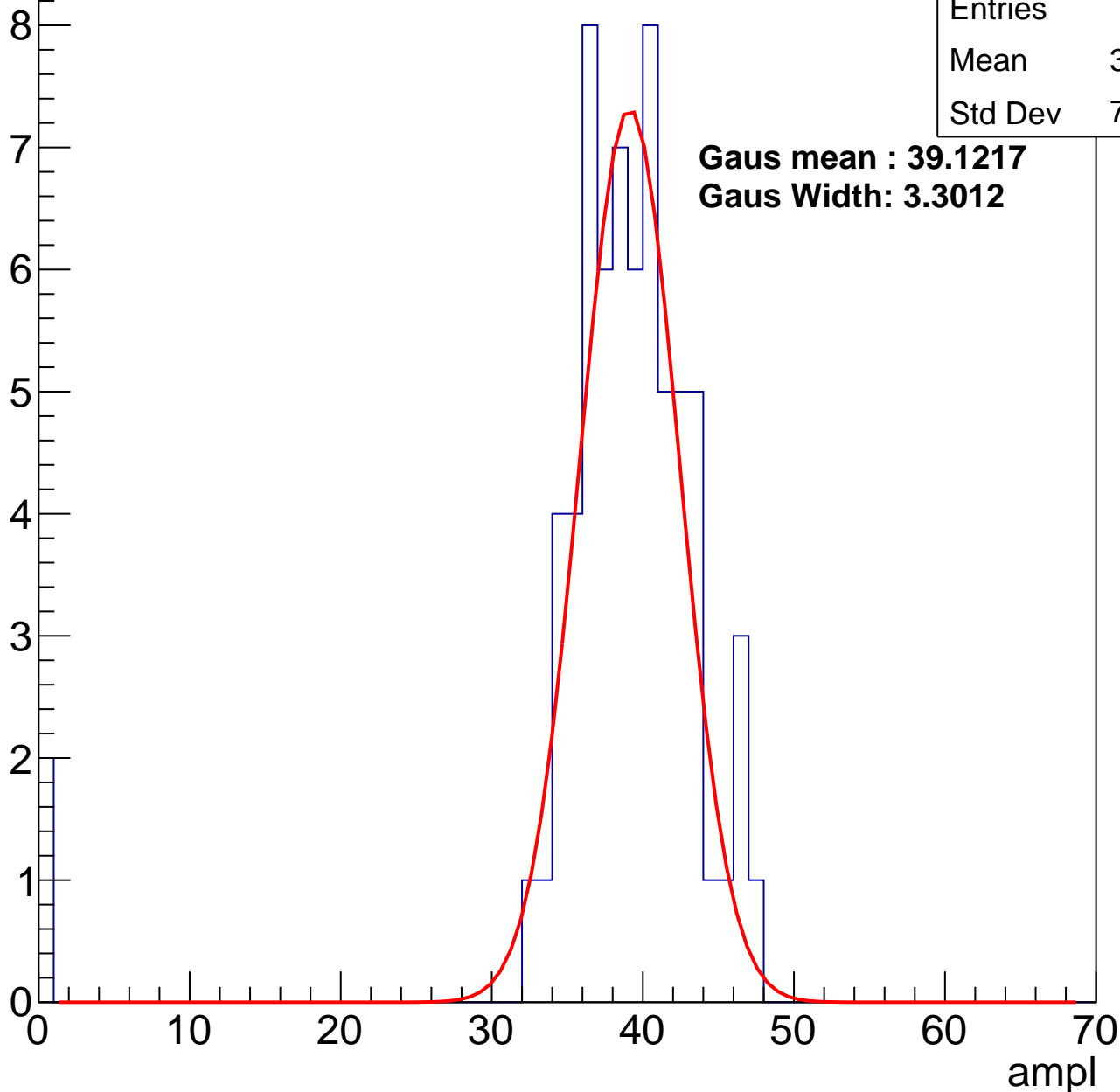
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.87
Std Dev	7.408

Gaus mean : 39.1217

Gaus Width: 3.3012

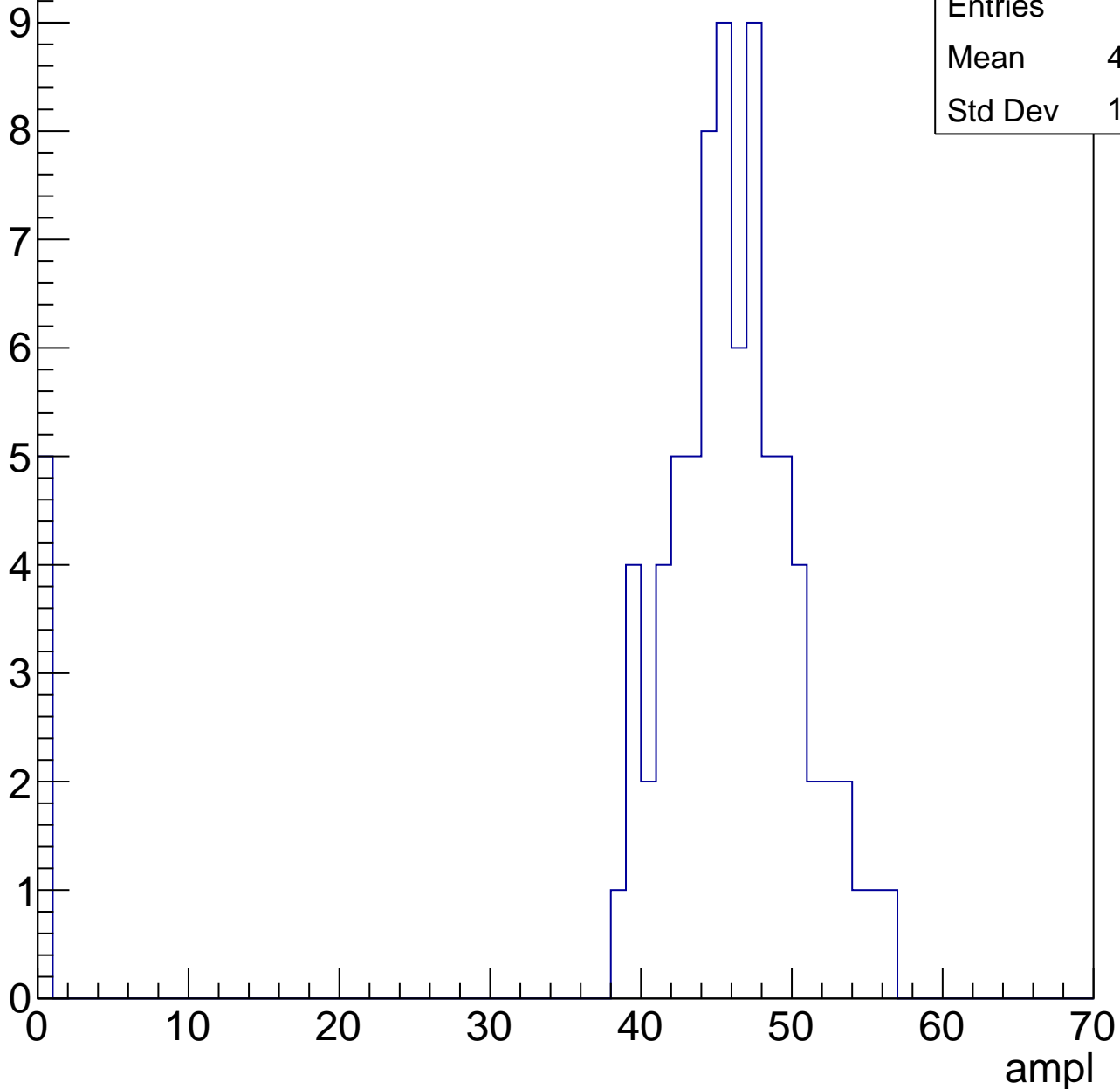


B1L103S, U6-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	42.98
Std Dev	11.67

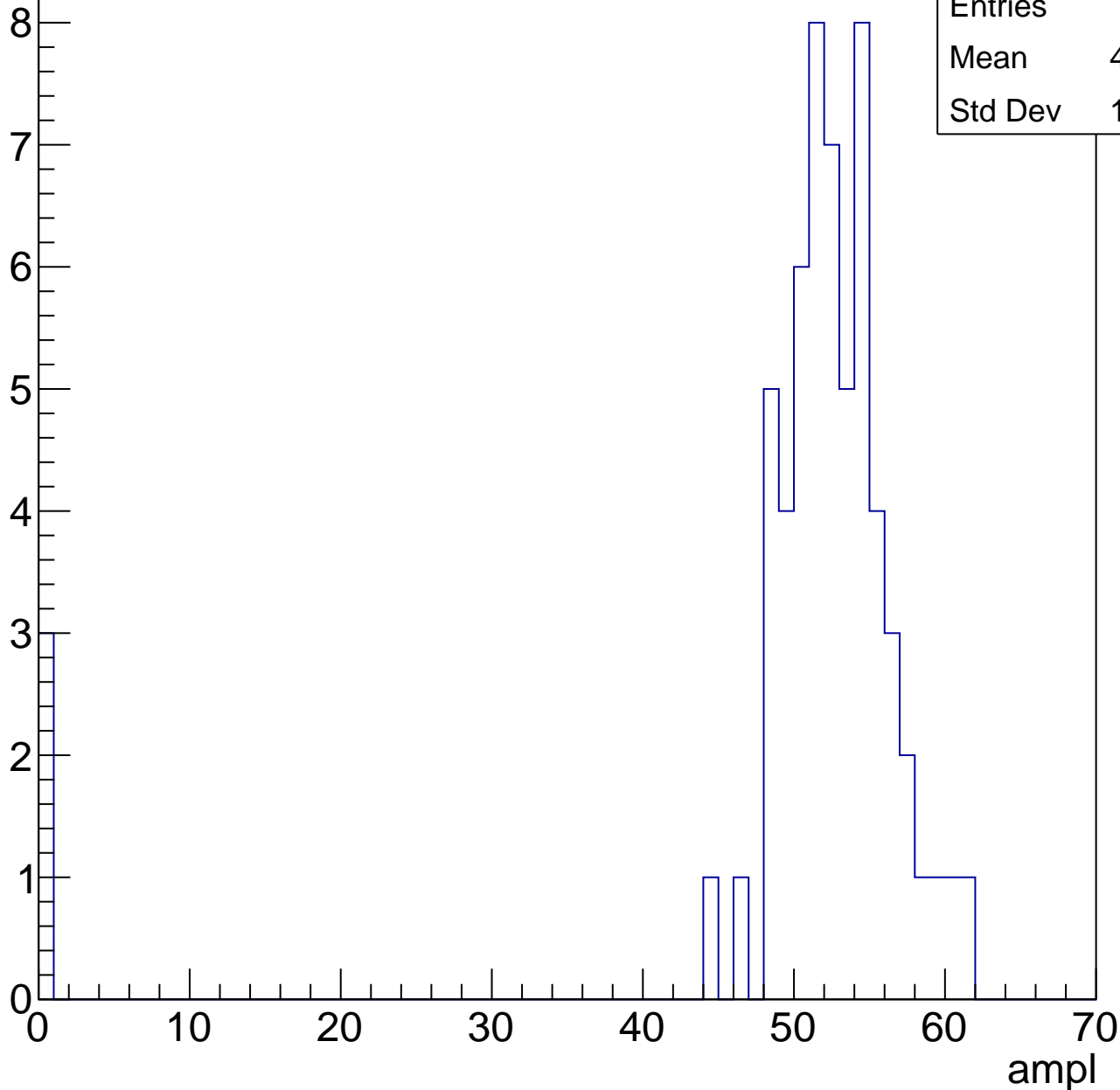


B1L103S, U6-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.75
Std Dev	11.77

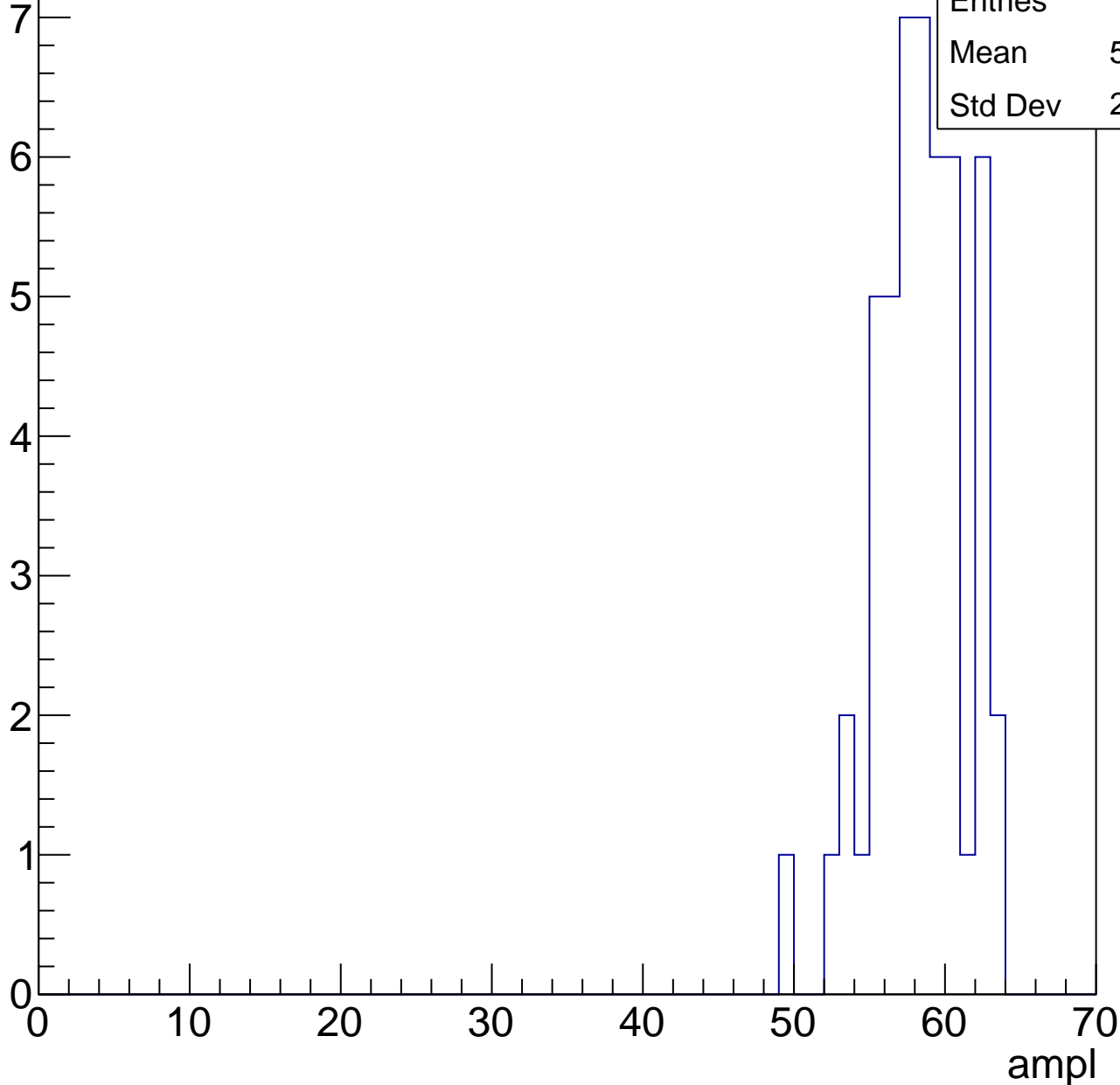


B1L103S, U6-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.88
Std Dev	2.964

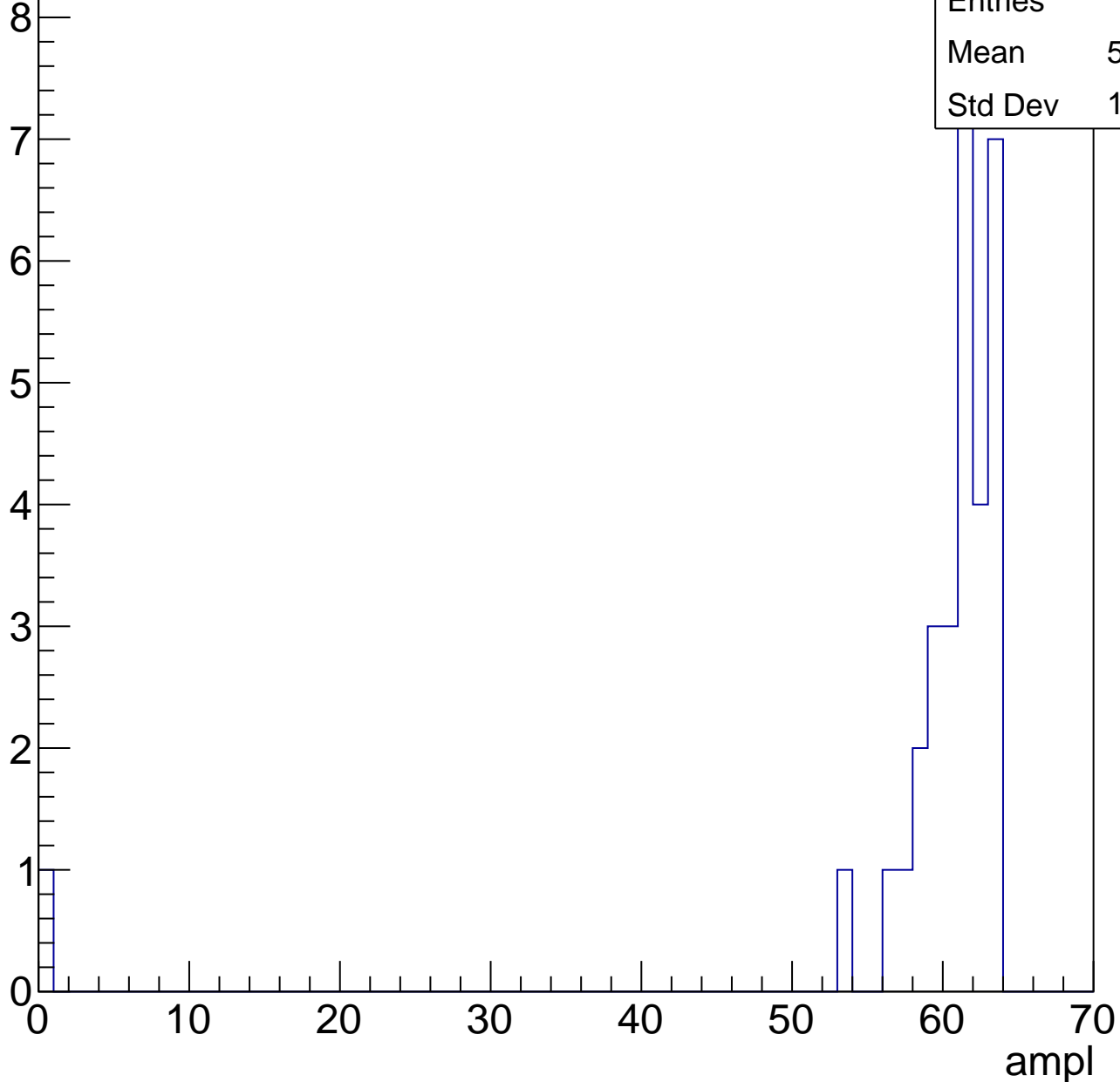


B1L103S, U6-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	58.58
Std Dev	10.94



B1L103S, U6-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U6-ch2, adc0

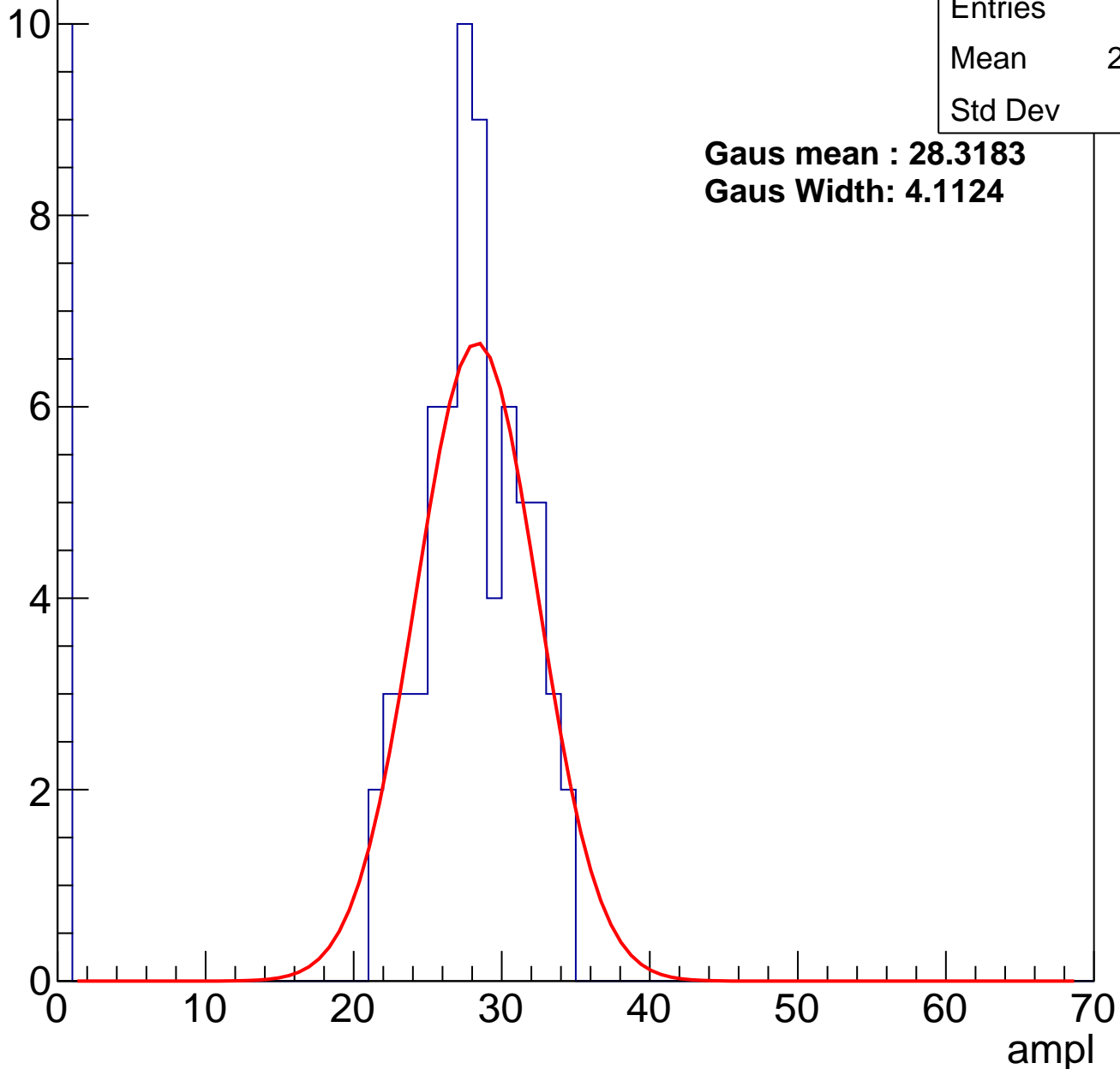
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	24.09
Std Dev	9.79

Gaus mean : 28.3183

Gaus Width: 4.1124

Entry



B1L103S, U6-ch2, adc1

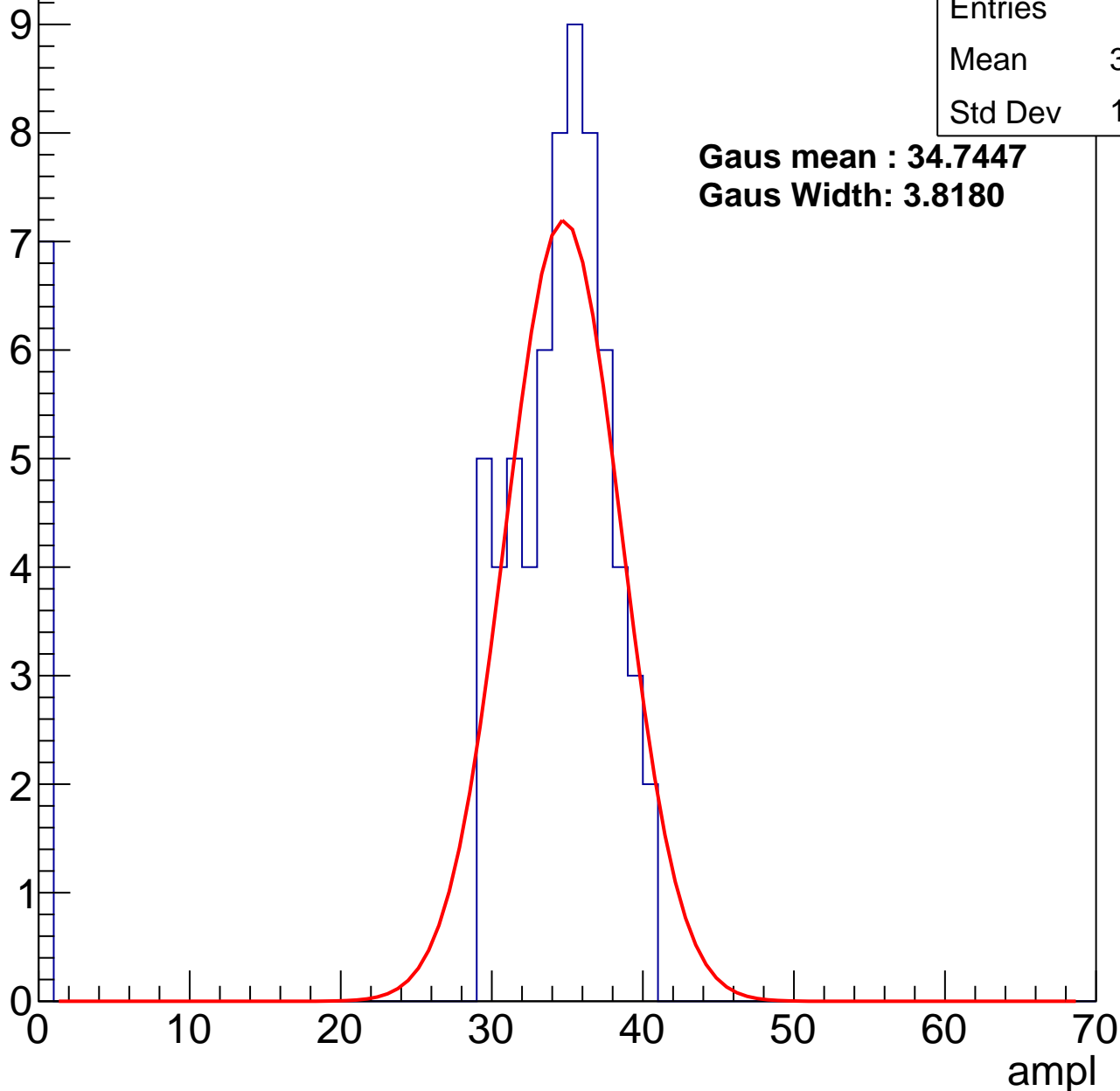
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.87
Std Dev	10.59

Gaus mean : 34.7447

Gaus Width: 3.8180



B1L103S, U6-ch2, adc2

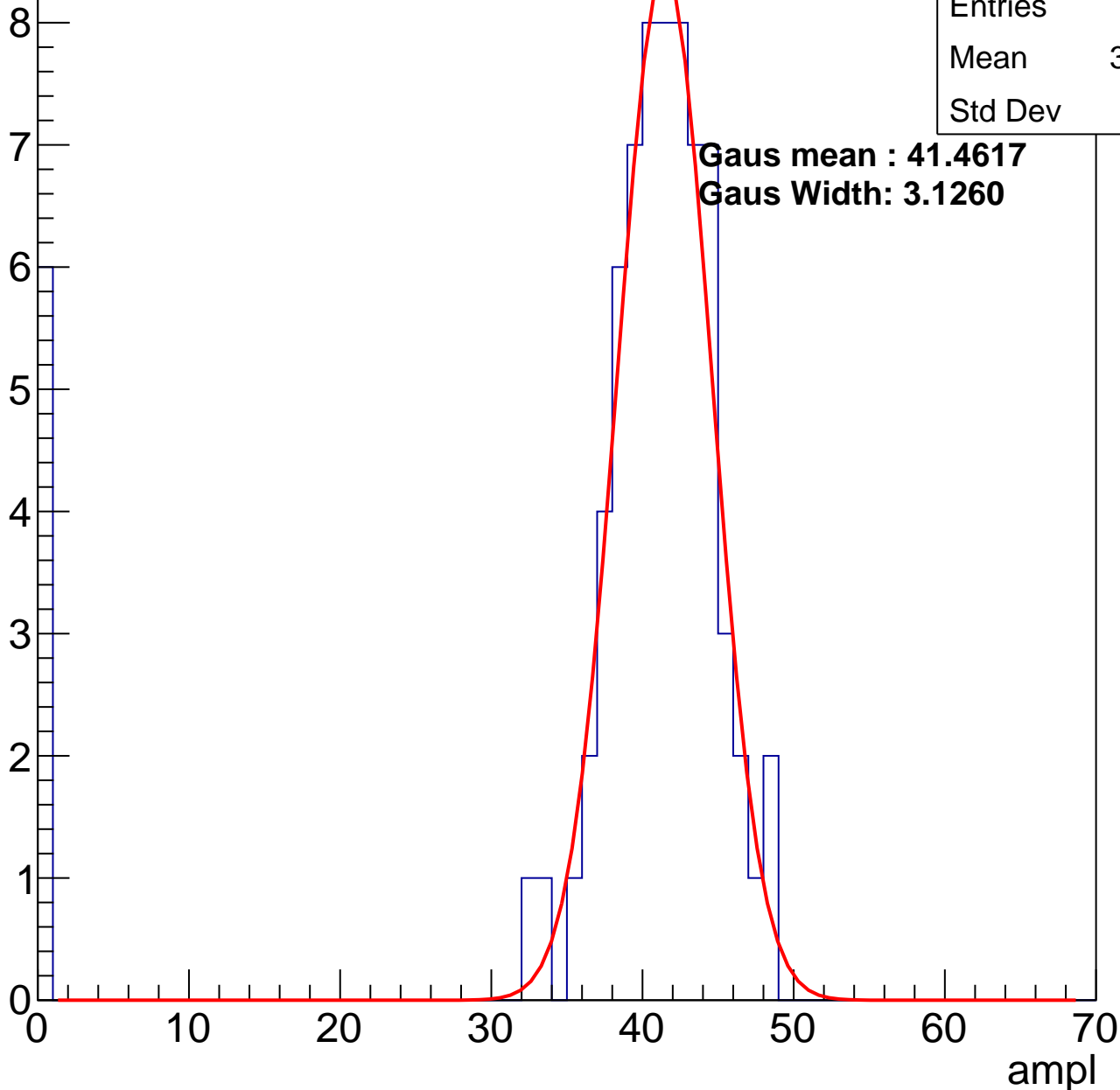
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	37.62
Std Dev	11.6

Gaus mean : 41.4617

Gaus Width: 3.1260

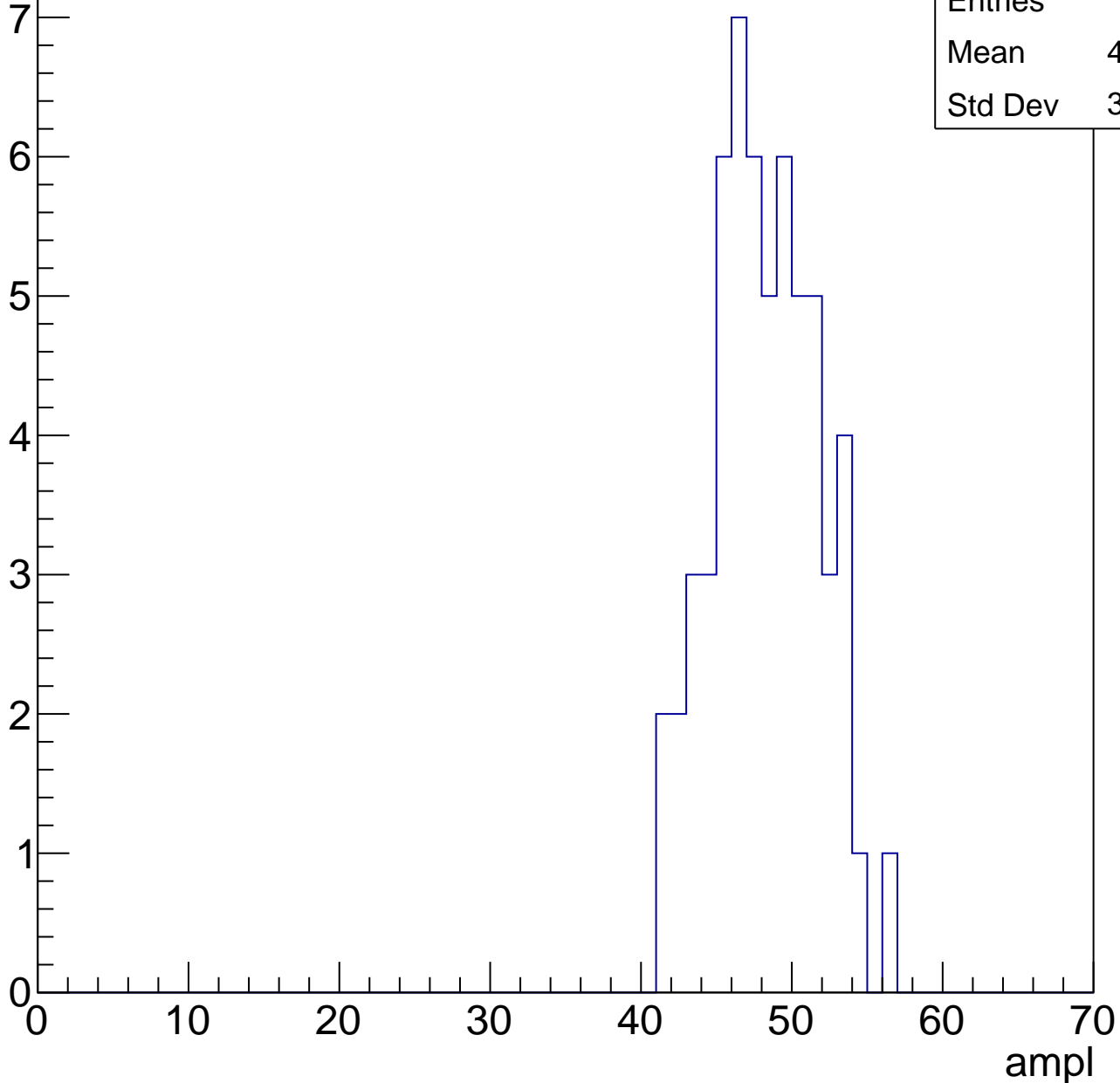


B1L103S, U6-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.76
Std Dev	3.436

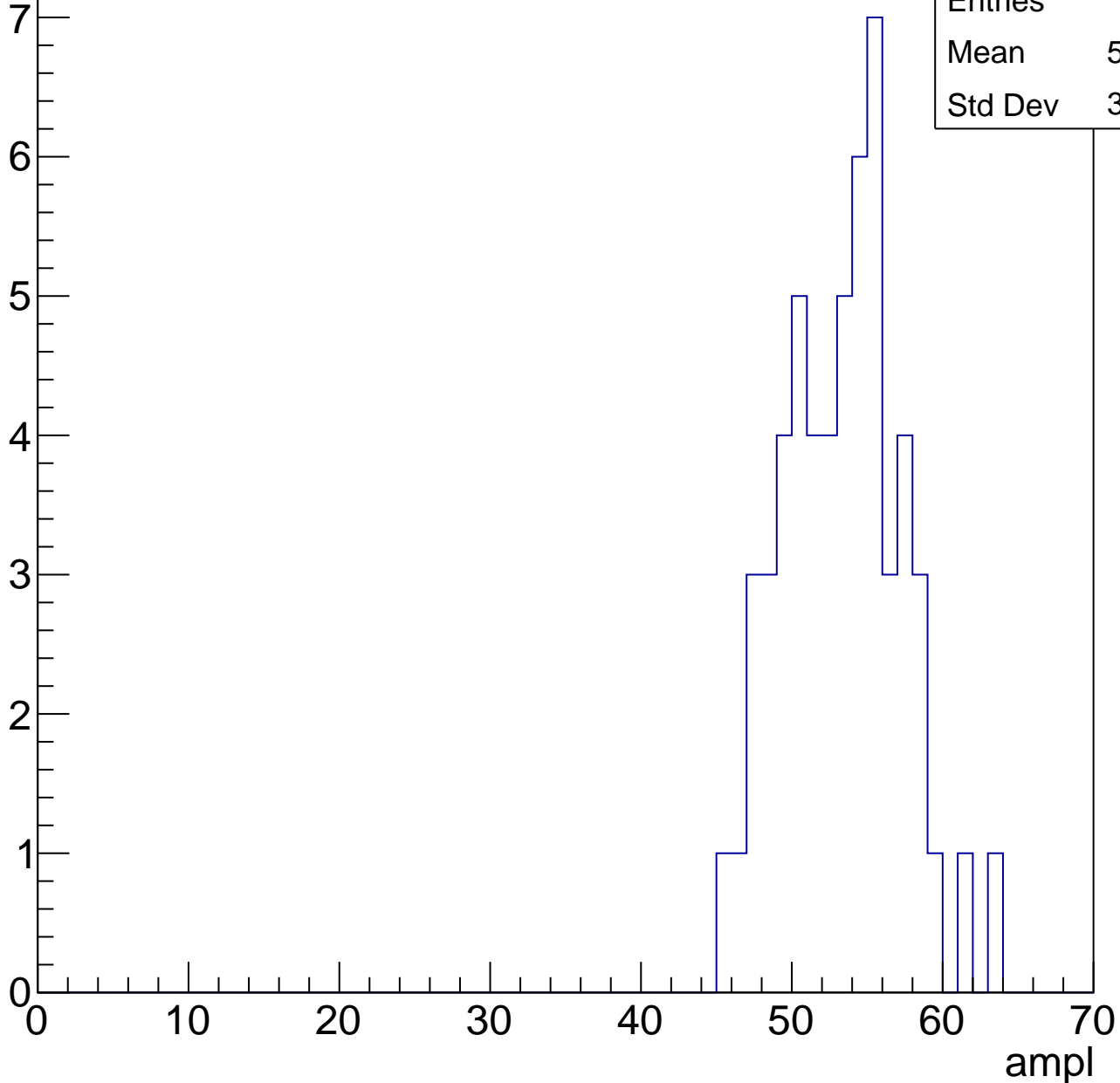


B1L103S, U6-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.88
Std Dev	3.836



B1L103S, U6-ch2, adc5

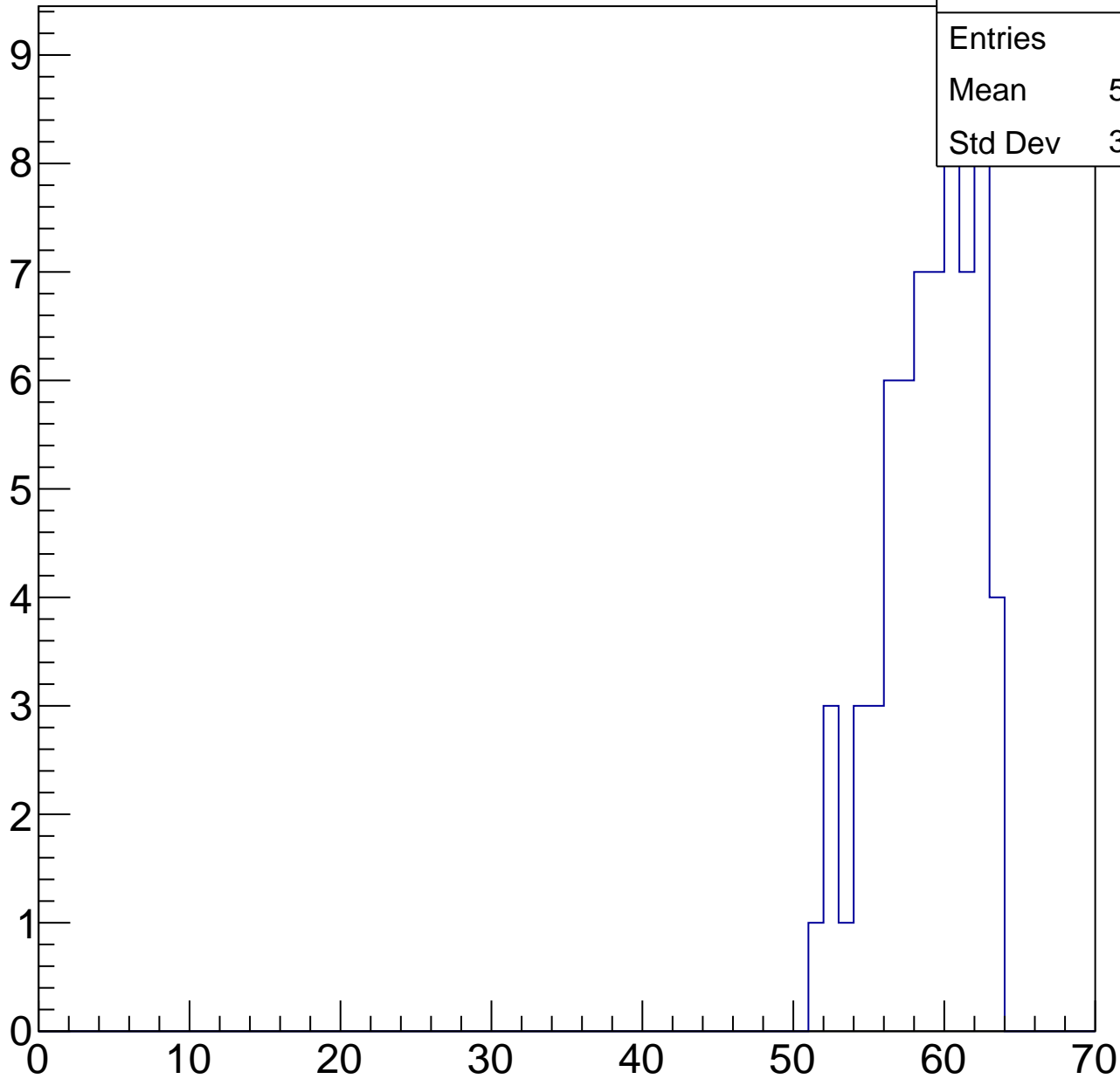
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	58.48
Std Dev	3.084

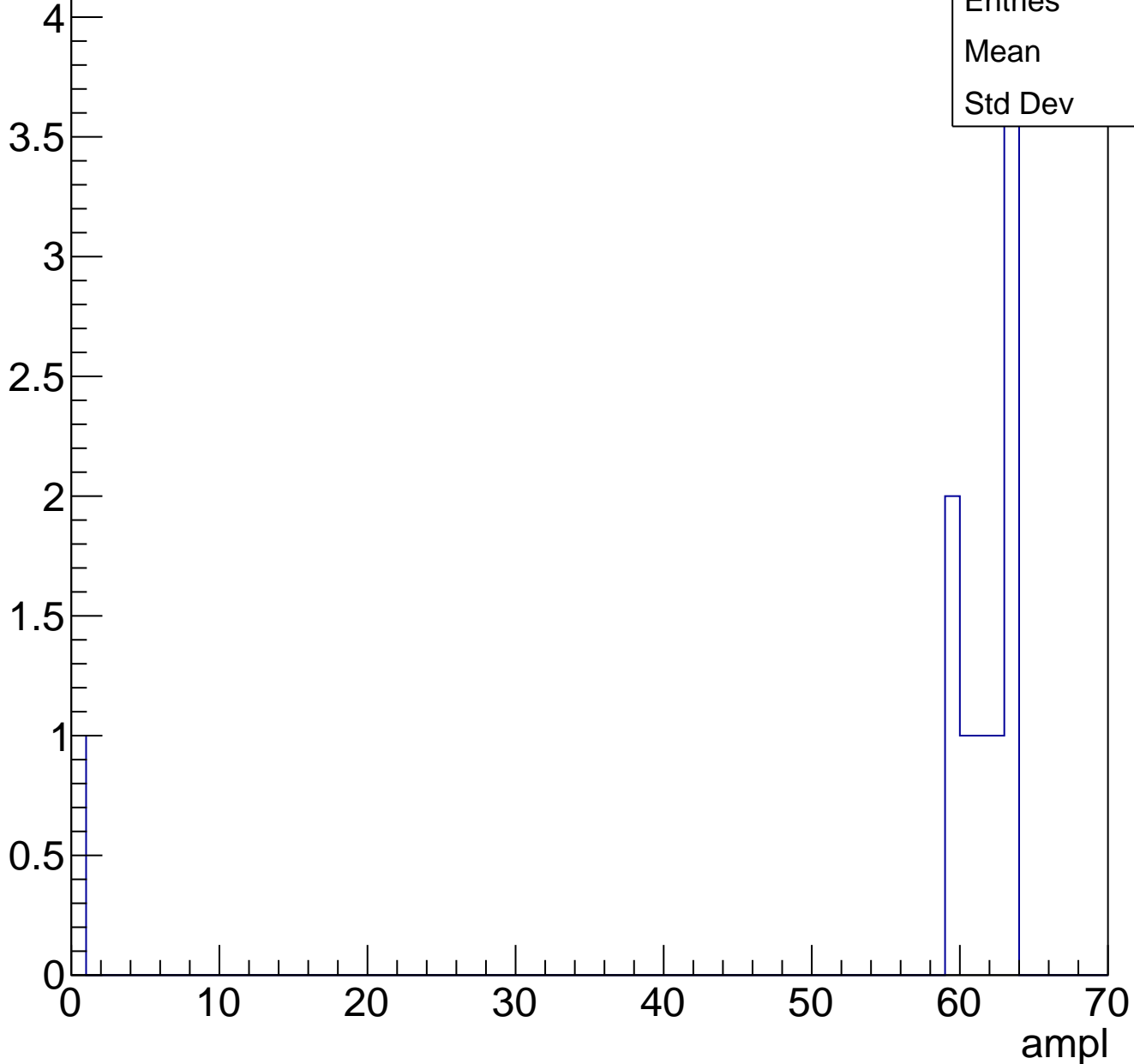
ampl



B1L103S, U6-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

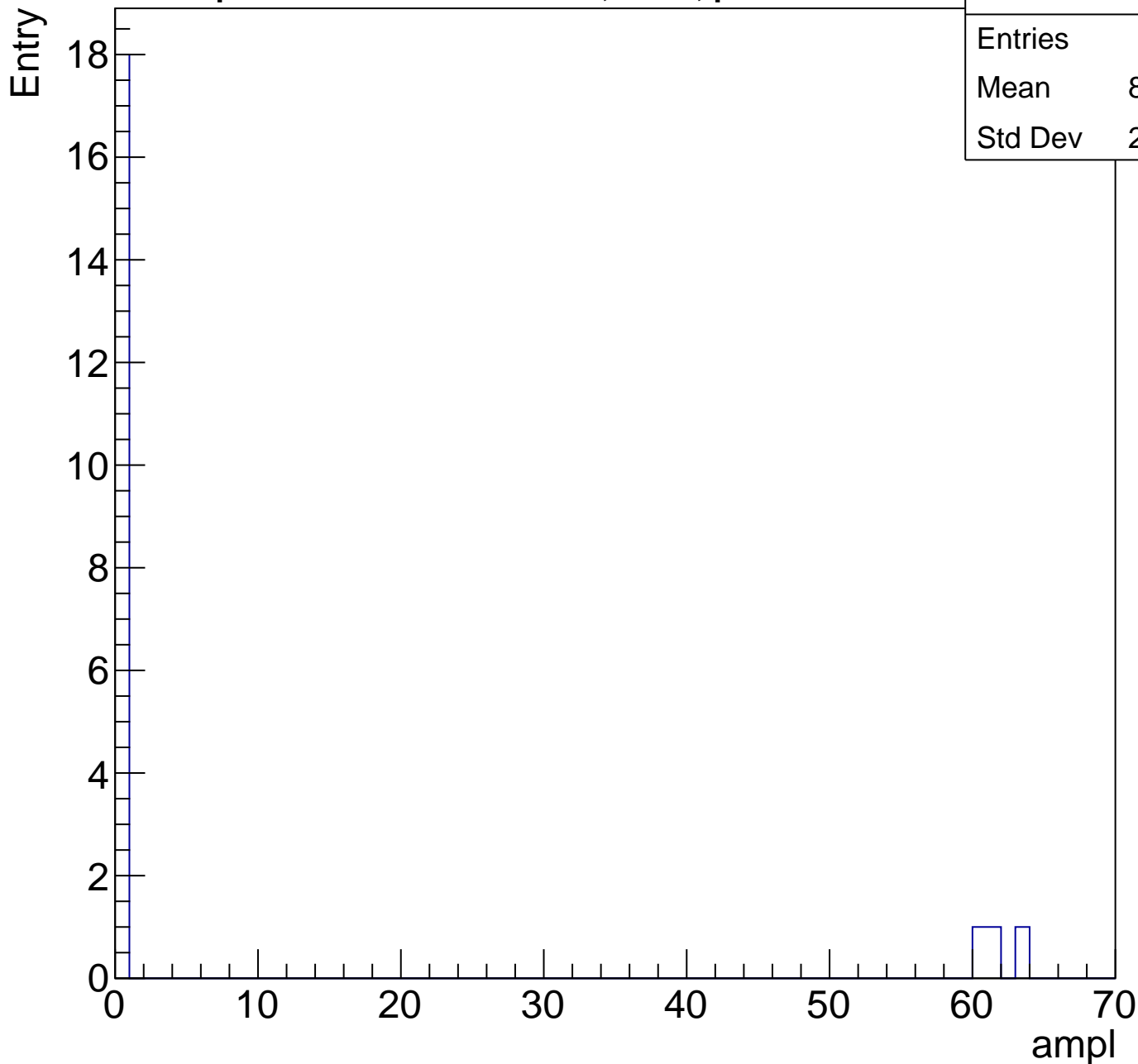
Entry



B1L103S, U6-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.762
Std Dev	21.47



B1L103S, U6-ch3, adc0

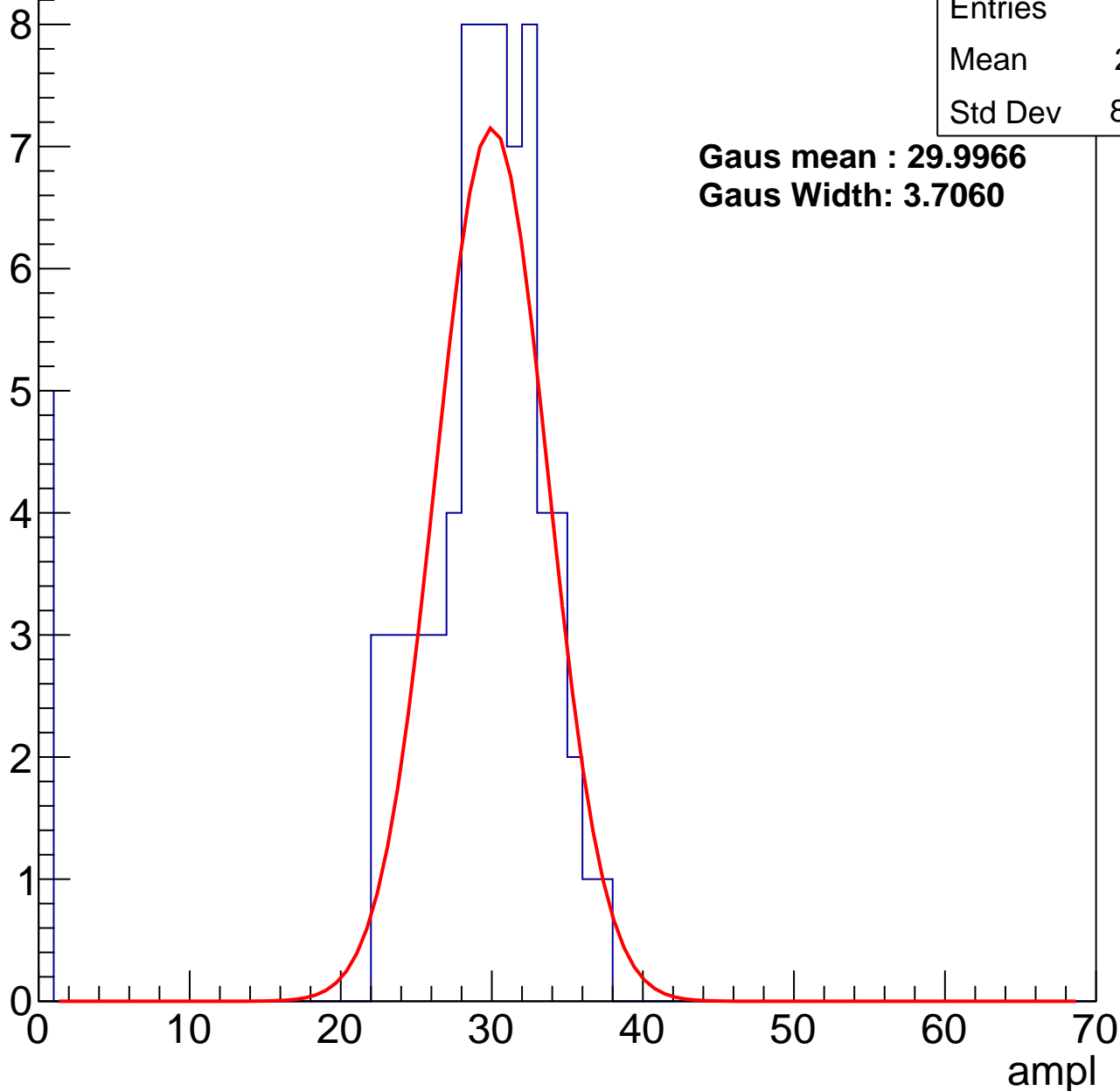
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	27.31
Std Dev	8.063

Gaus mean : 29.9966

Gaus Width: 3.7060



B1L103S, U6-ch3, adc1

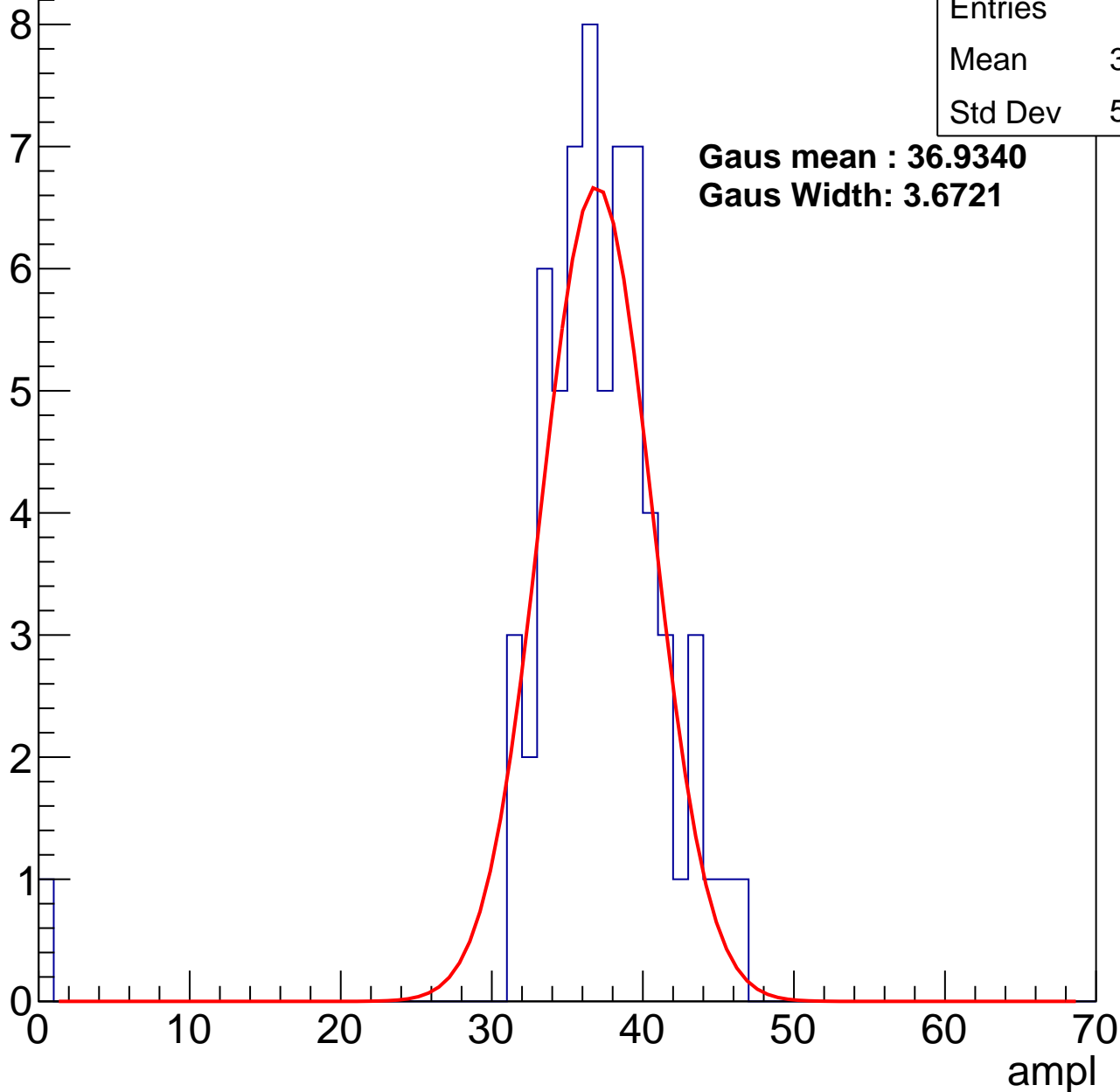
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.48
Std Dev	5.727

Gaus mean : 36.9340

Gaus Width: 3.6721



B1L103S, U6-ch3, adc2

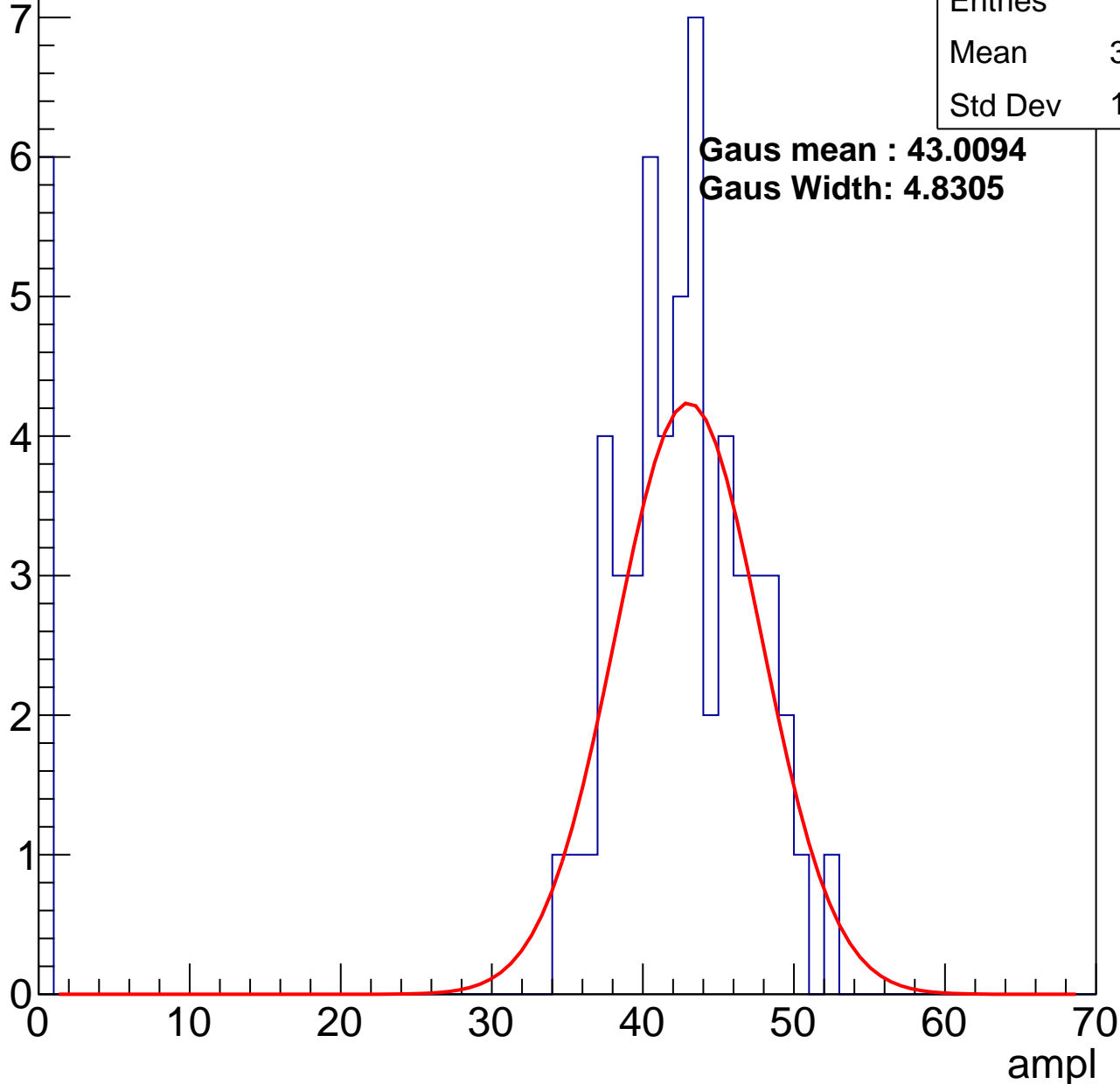
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.17
Std Dev	13.29

Gaus mean : 43.0094

Gaus Width: 4.8305

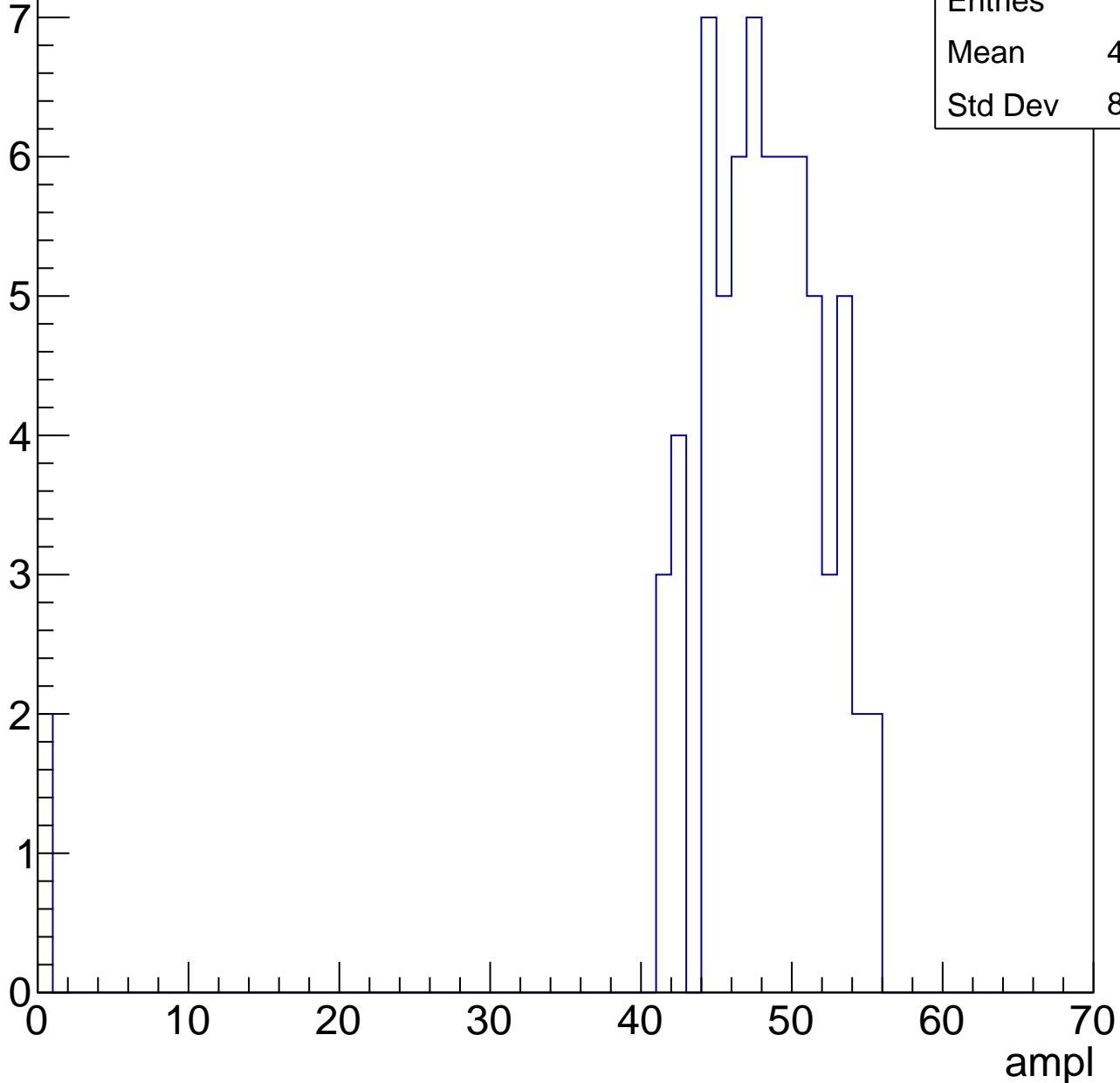


B1L103S, U6-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

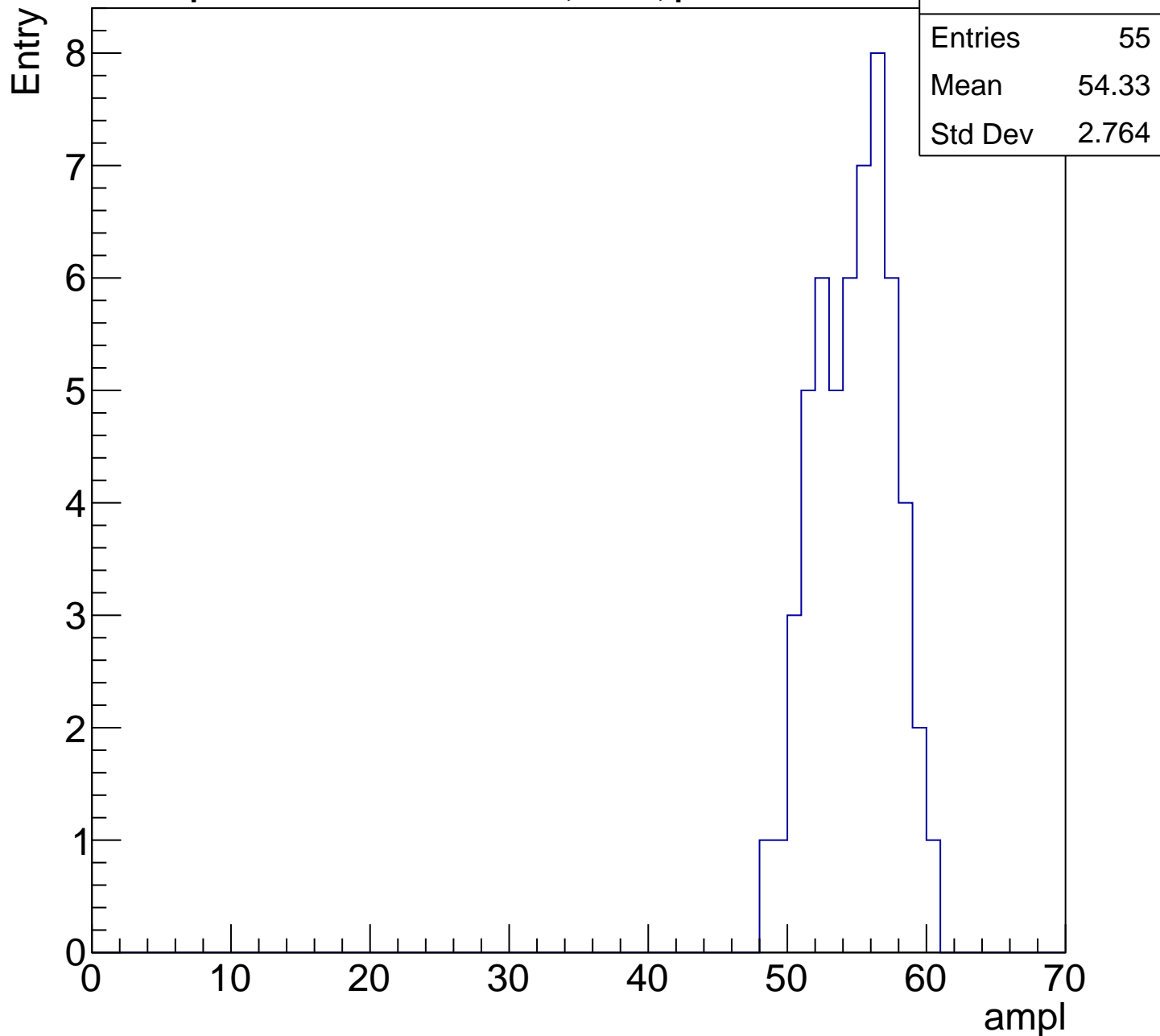
Entry

Entries	69
Mean	46.45
Std Dev	8.796



B1L103S, U6-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	53
Mean	59.49
Std Dev	2.668

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

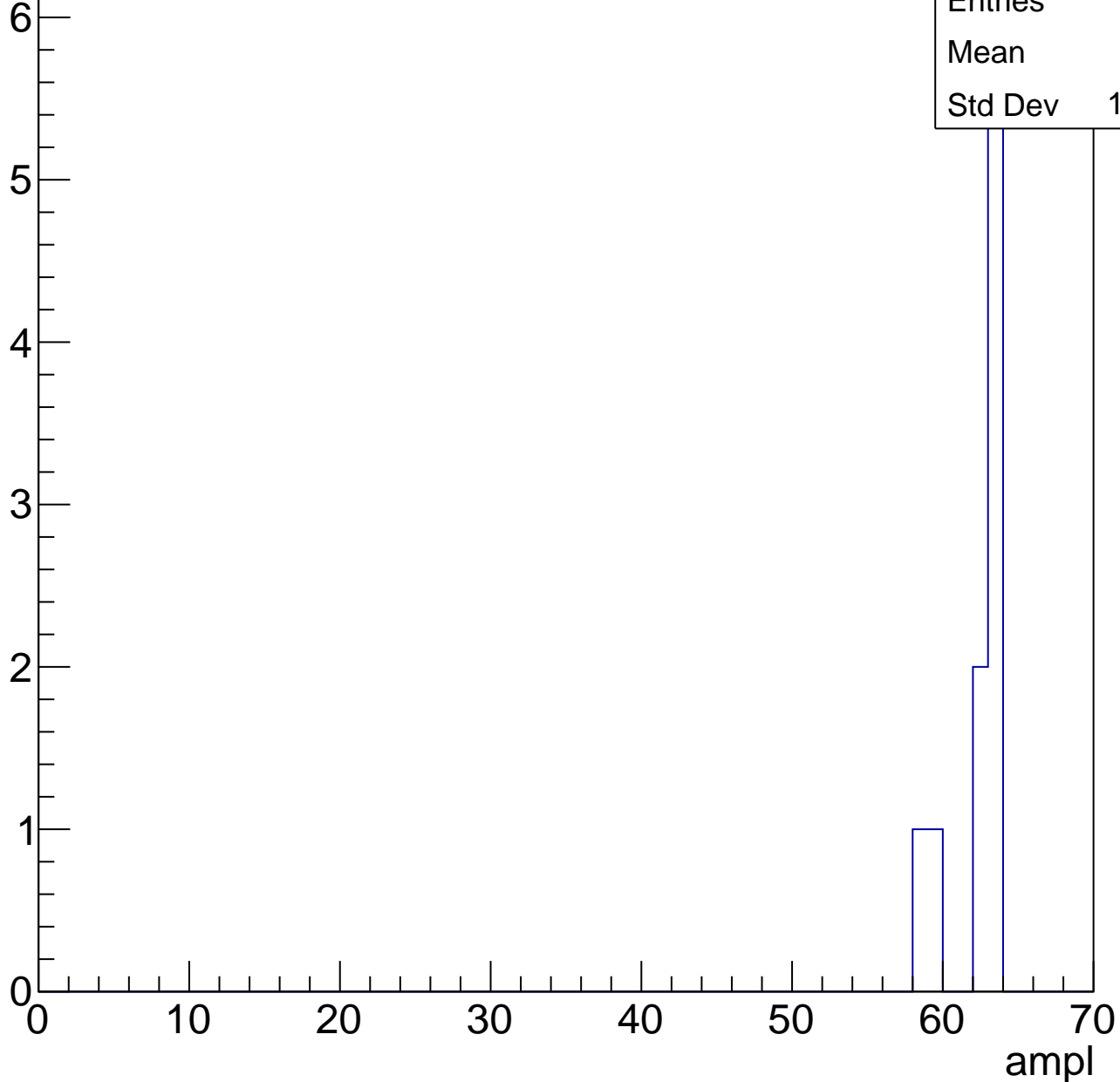
70

B1L103S, U6-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	61.9
Std Dev	1.758



B1L103S, U6-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U6-ch4, adc0

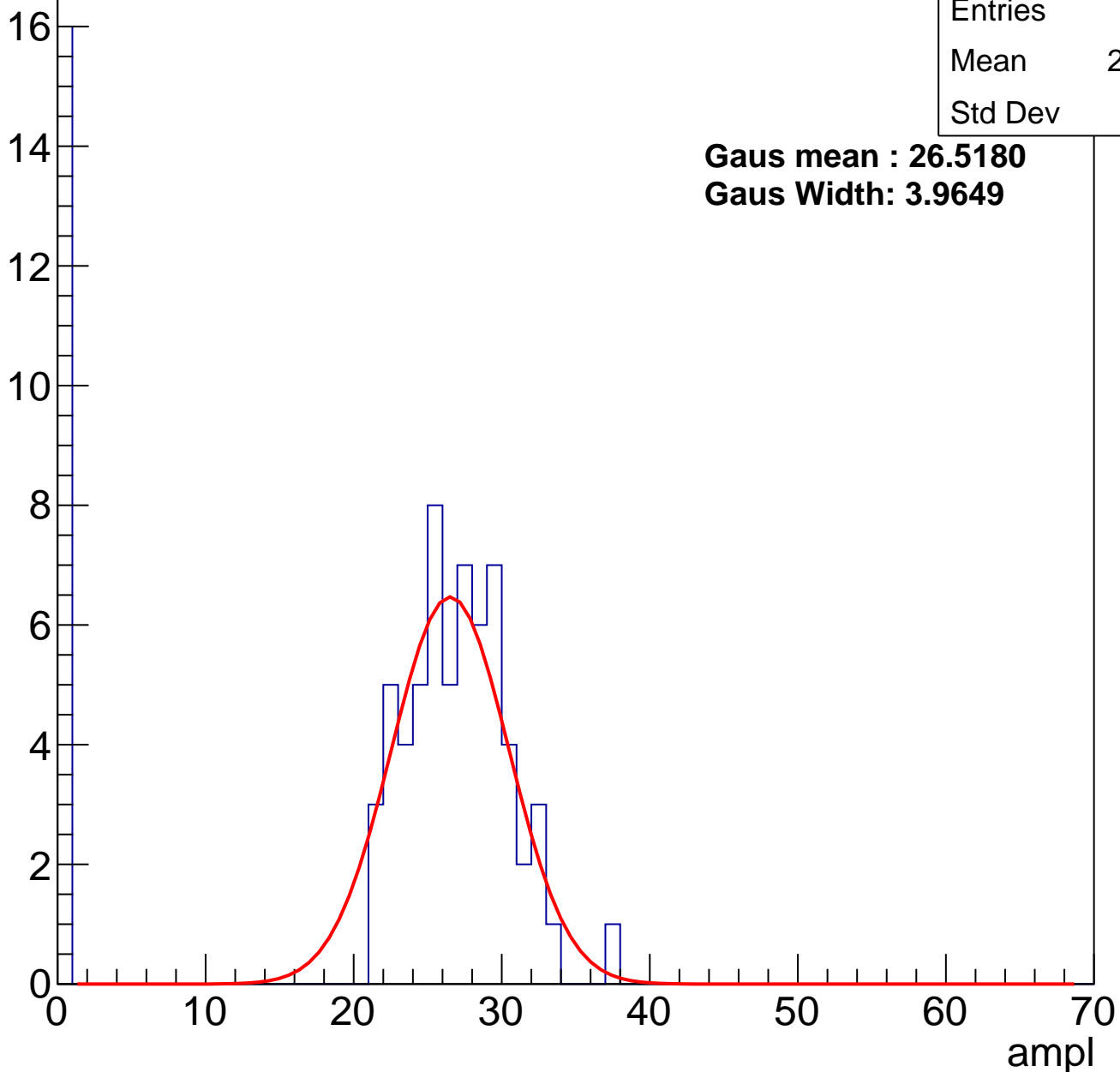
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	21.08
Std Dev	11.2

Gaus mean : 26.5180

Gaus Width: 3.9649

Entry



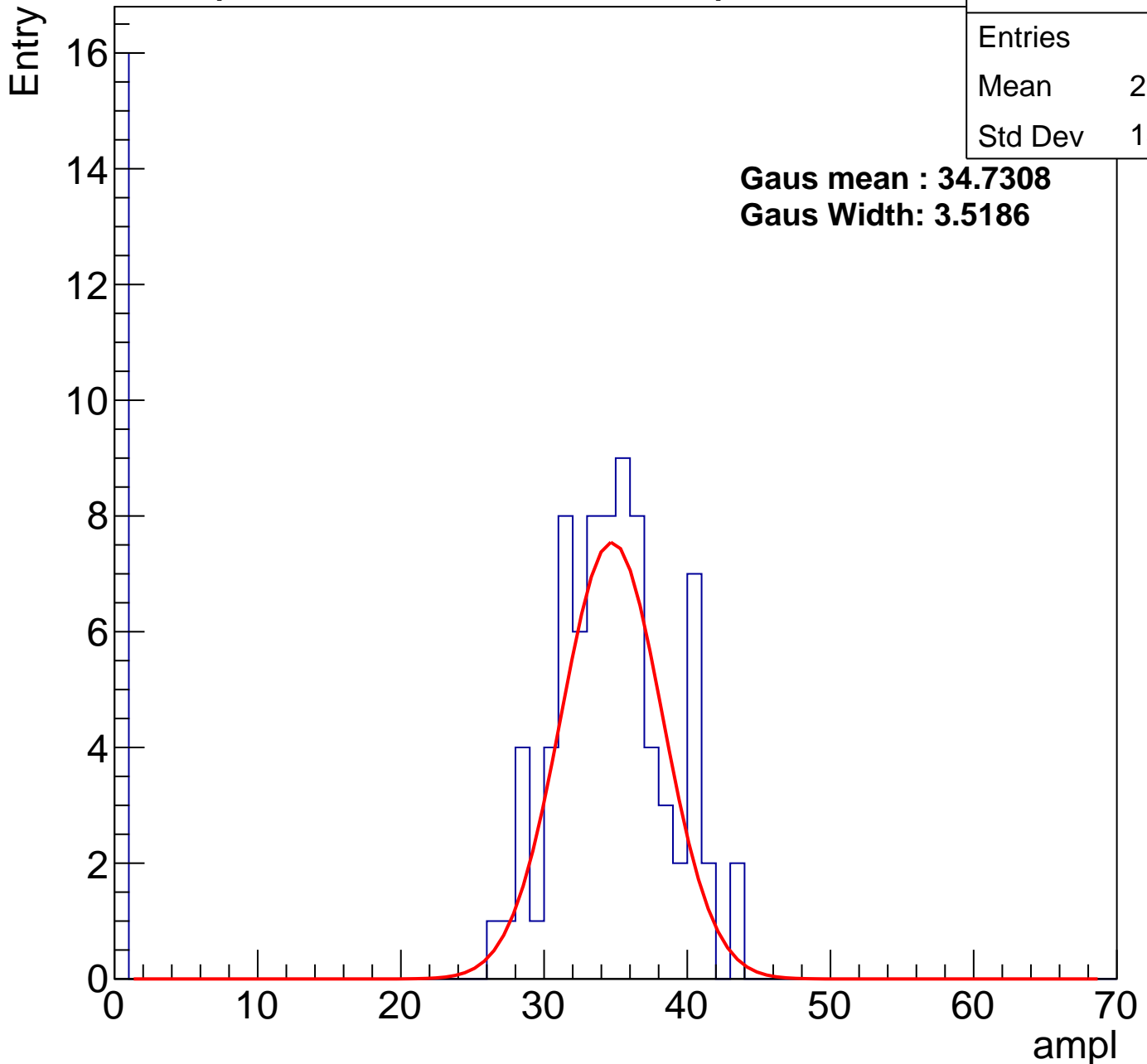
B1L103S, U6-ch4, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	28.52
Std Dev	13.37

Gaus mean : 34.7308

Gaus Width: 3.5186



B1L103S, U6-ch4, adc2

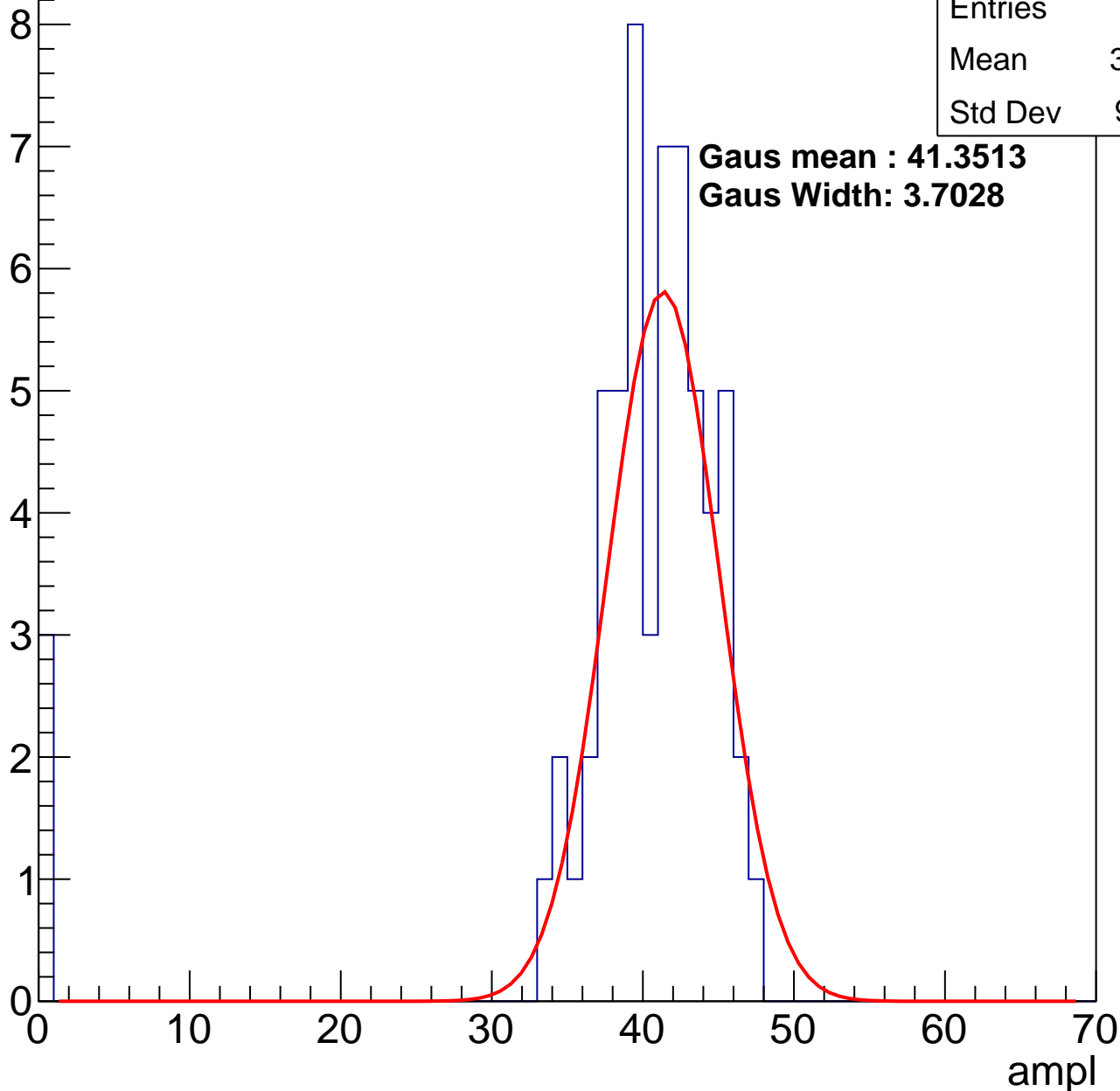
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.54
Std Dev	9.331

Gaus mean : 41.3513

Gaus Width: 3.7028

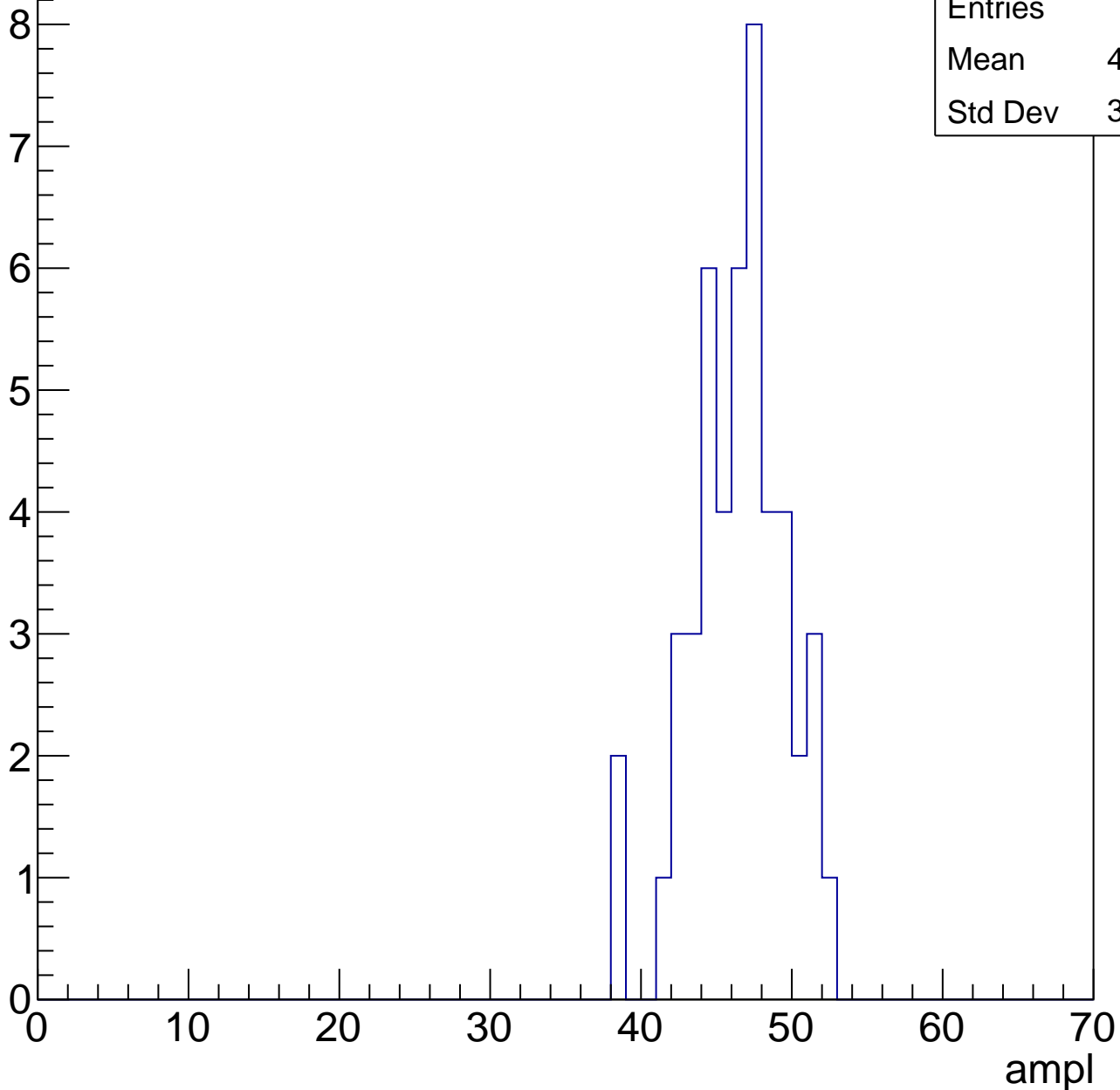


B1L103S, U6-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	45.98
Std Dev	3.125

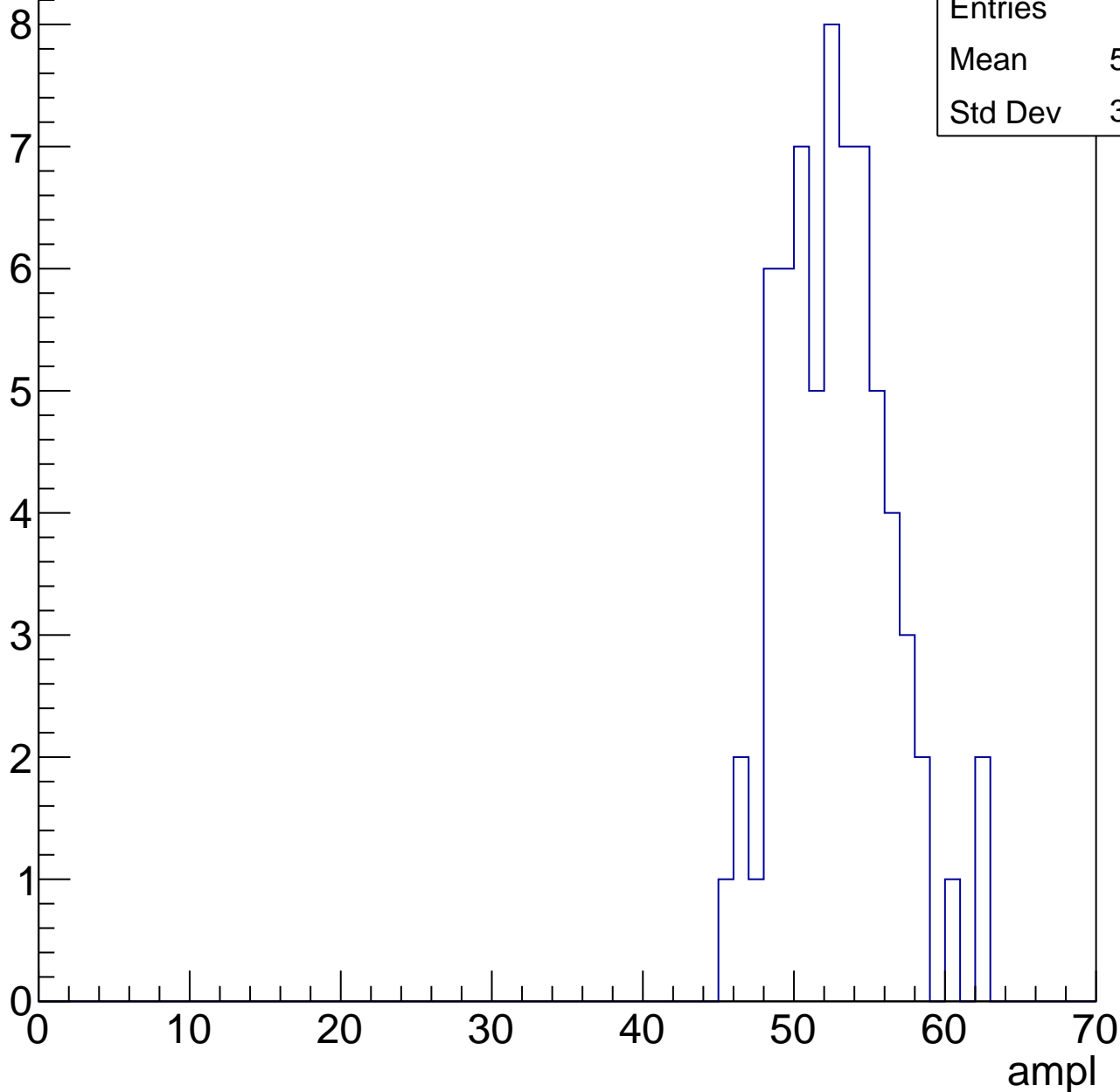


B1L103S, U6-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	52.33
Std Dev	3.617

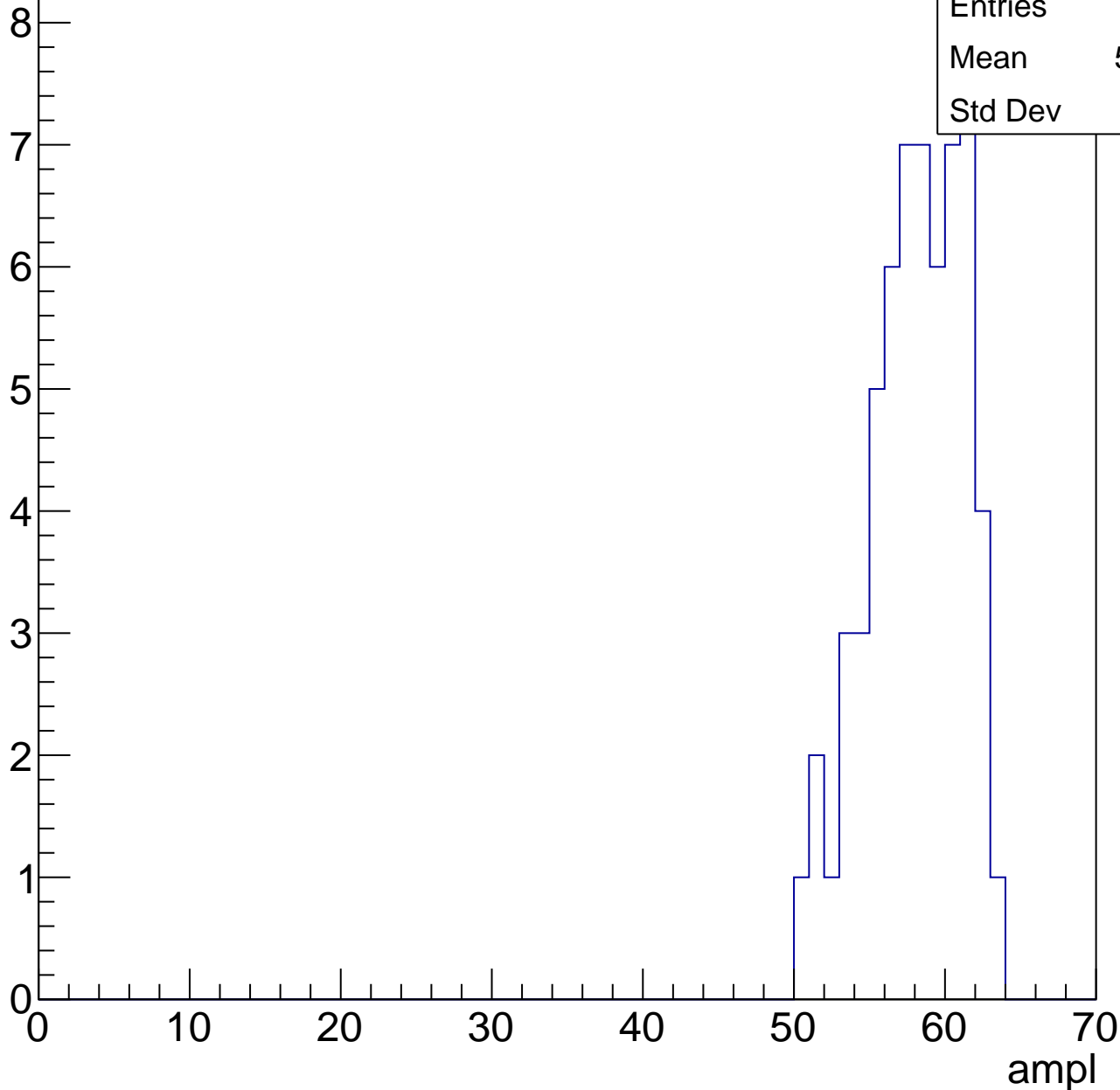


B1L103S, U6-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.61
Std Dev	3.09

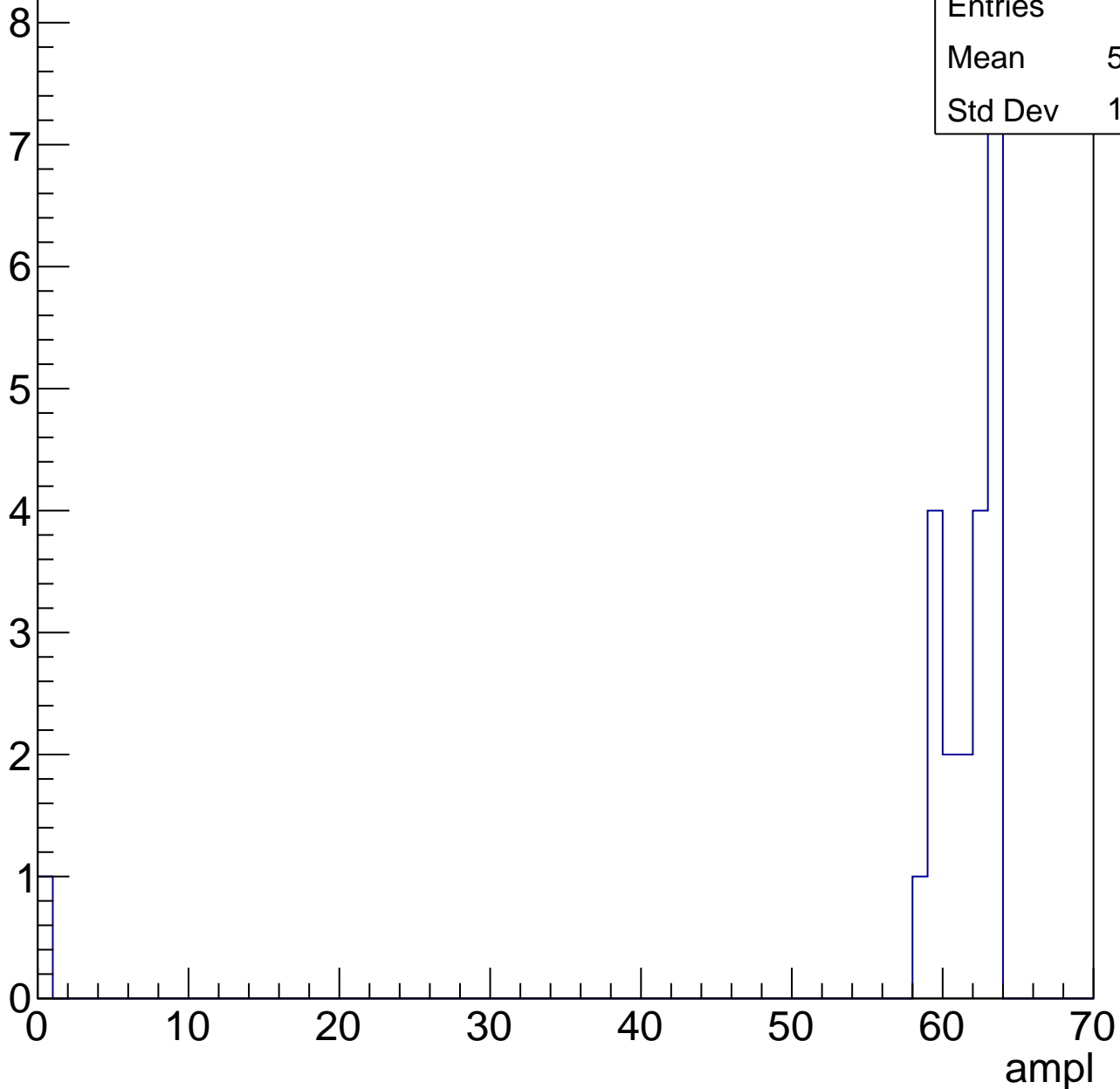


B1L103S, U6-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

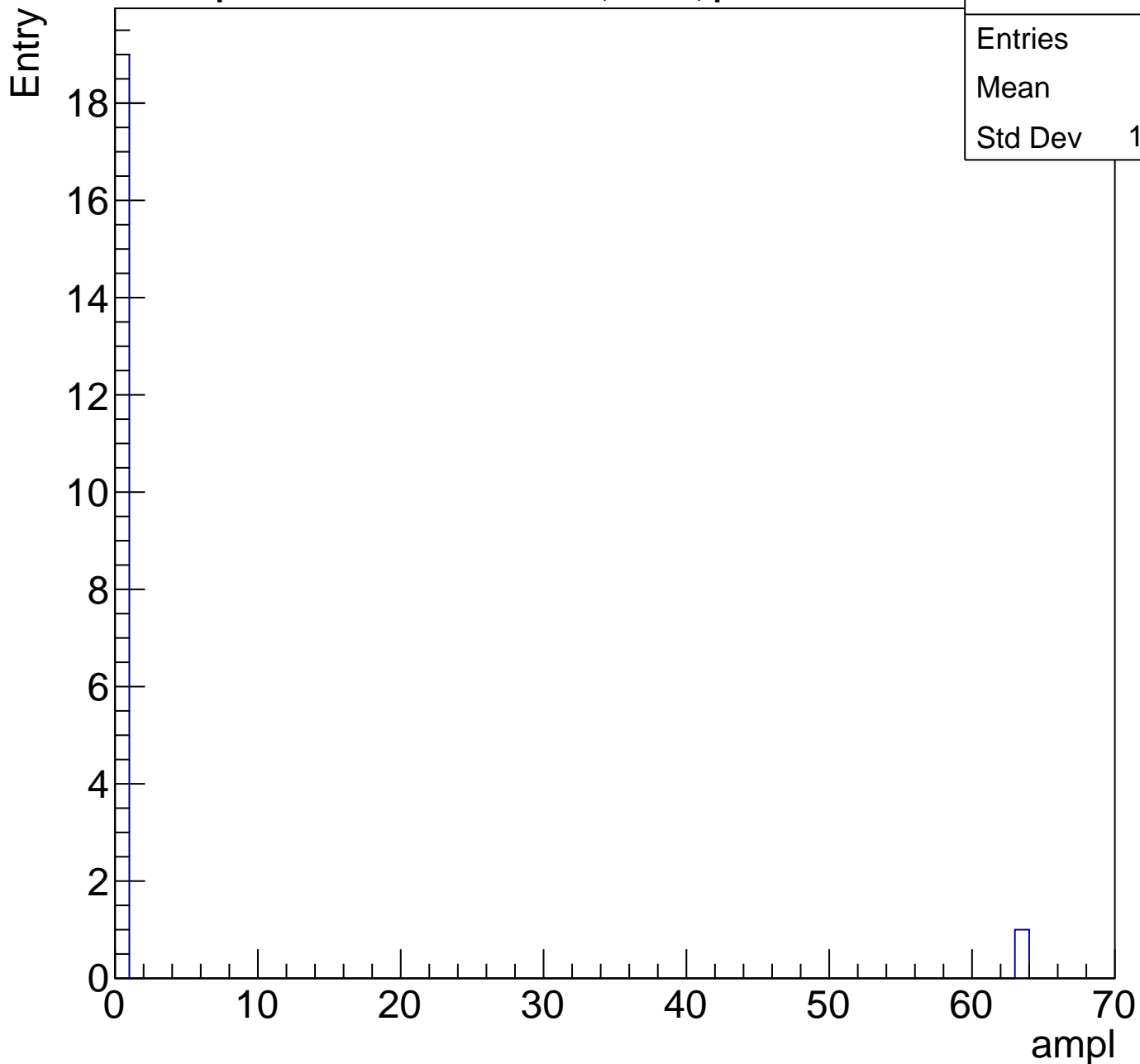
Entries	22
Mean	58.55
Std Dev	12.88



B1L103S, U6-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U6-ch5, adc0

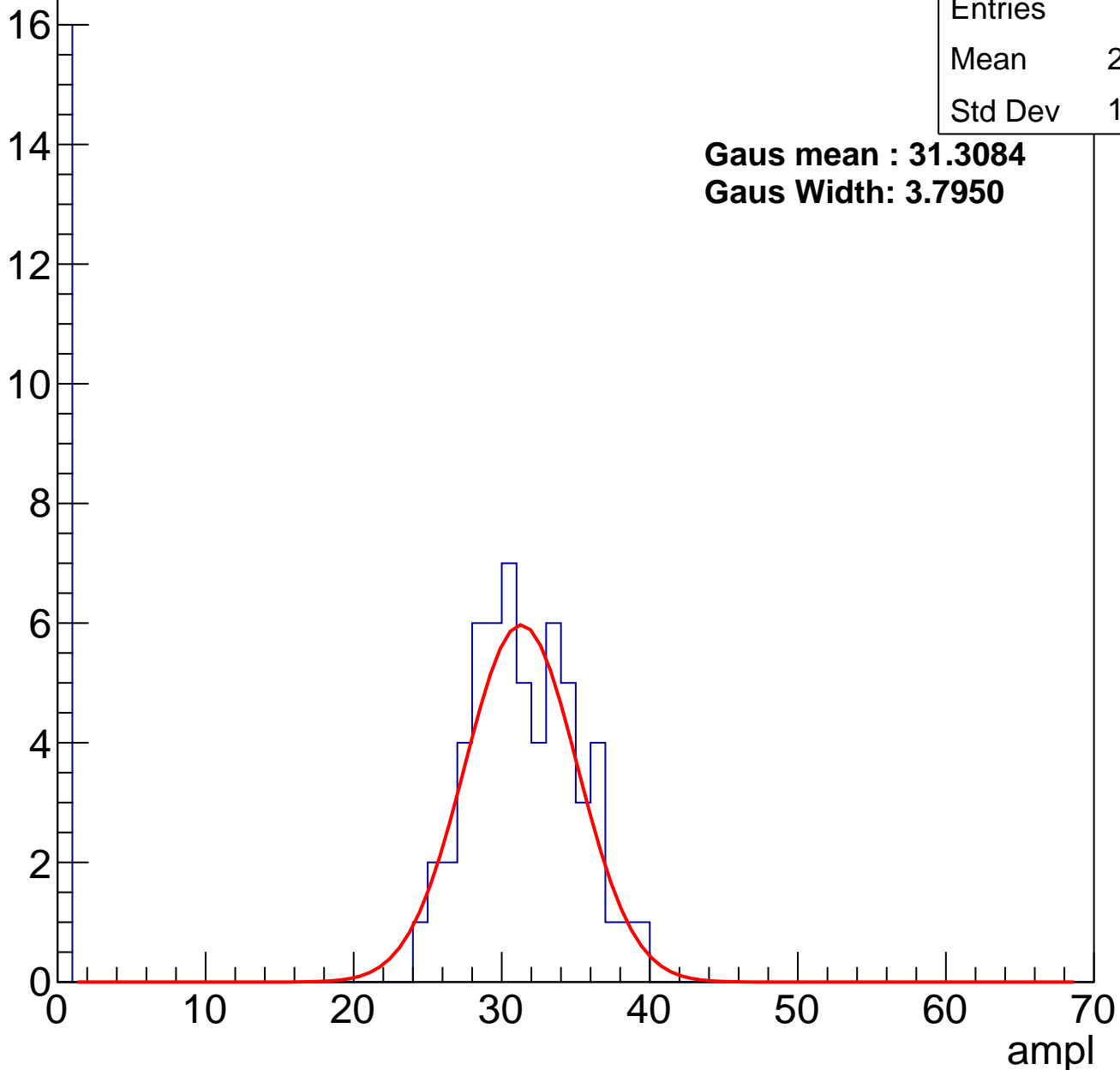
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	24.32
Std Dev	13.14

Gaus mean : 31.3084

Gaus Width: 3.7950

Entry



B1L103S, U6-ch5, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	32.38
Std Dev	13.25

Gaus mean : 37.4833

Gaus Width: 3.2381

Entry

10

8

6

4

2

0

0

10

20

30

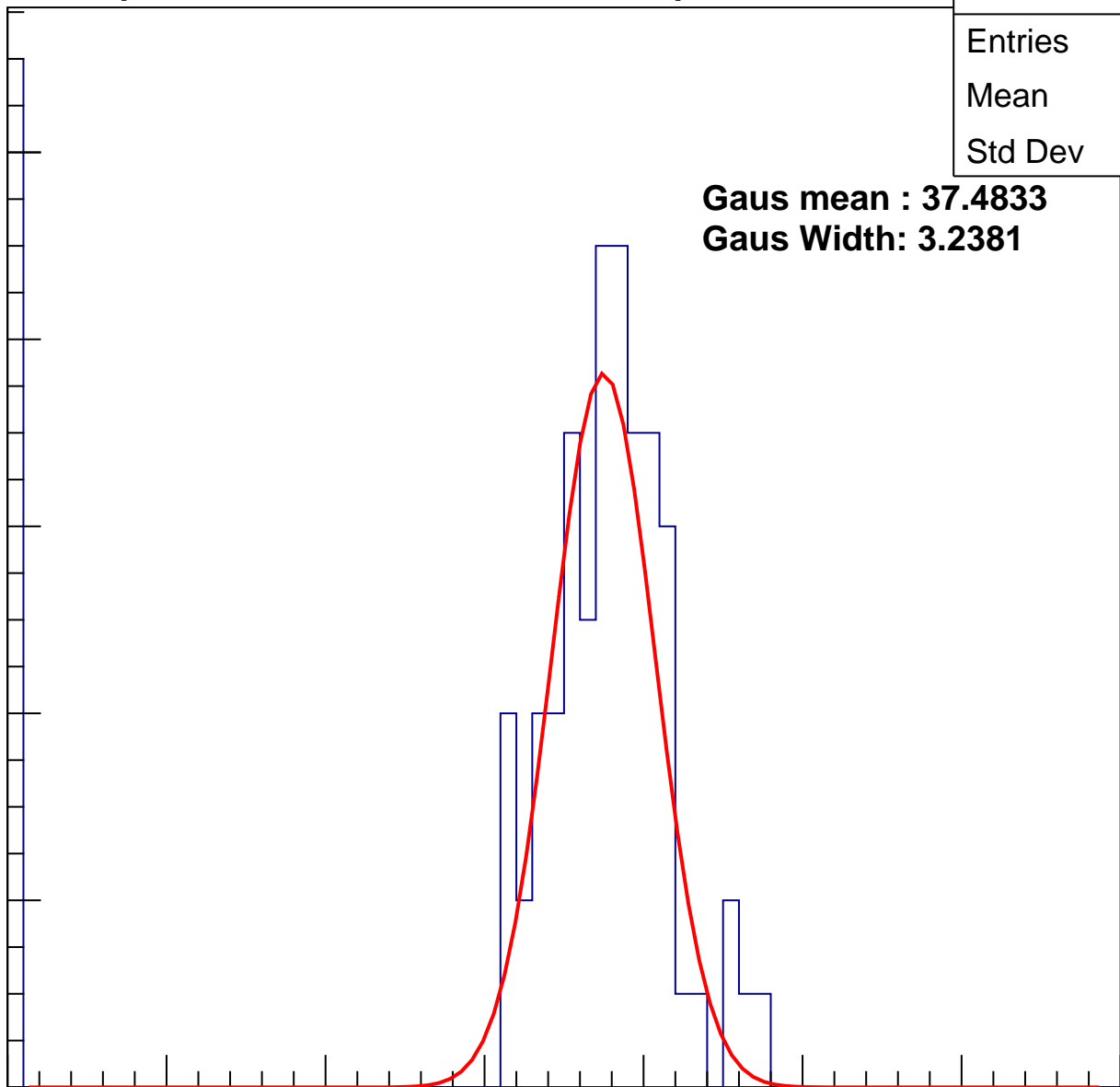
40

50

60

70

ampl



B1L103S, U6-ch5, adc2

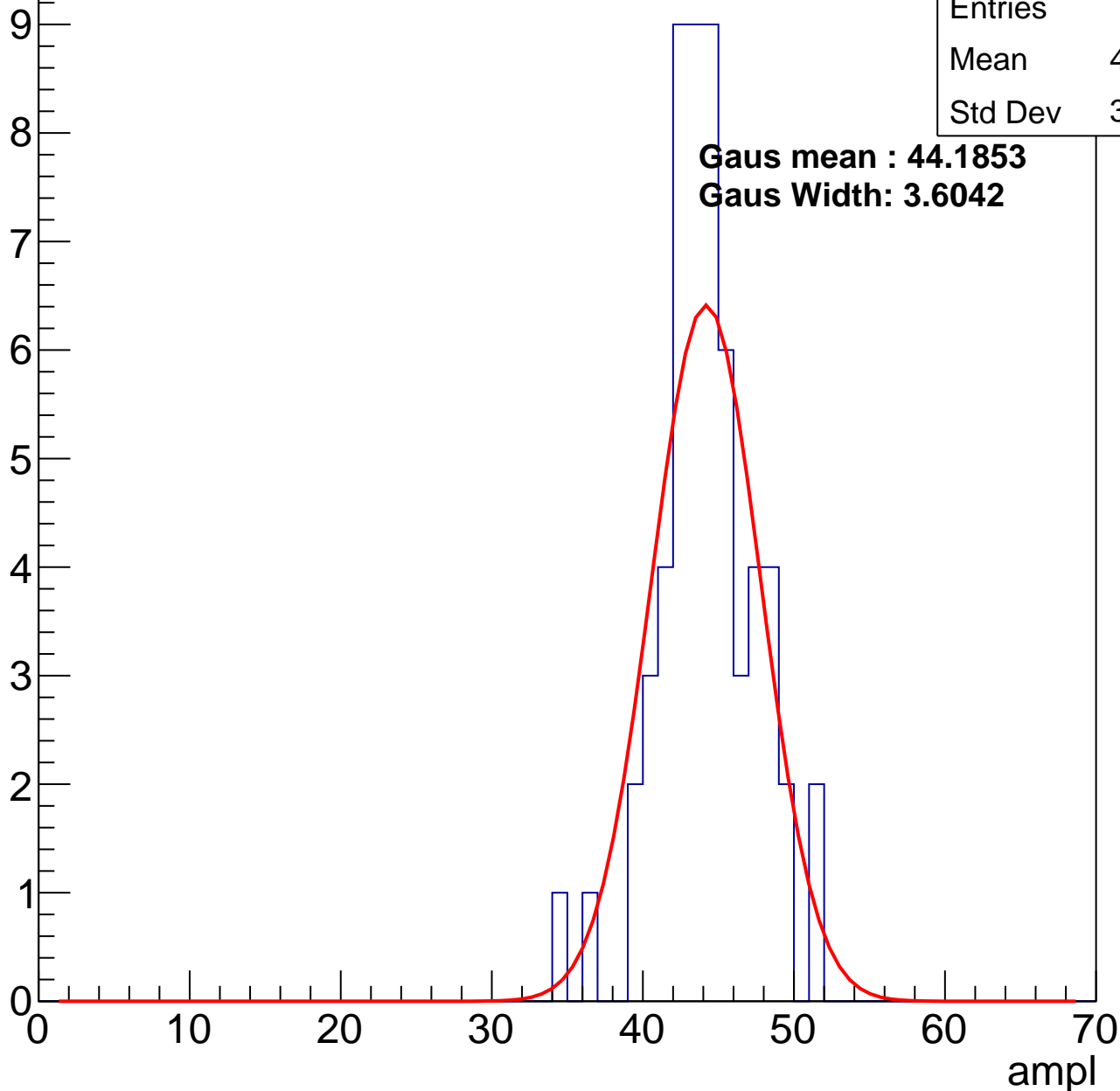
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	43.75
Std Dev	3.203

Gaus mean : 44.1853

Gaus Width: 3.6042

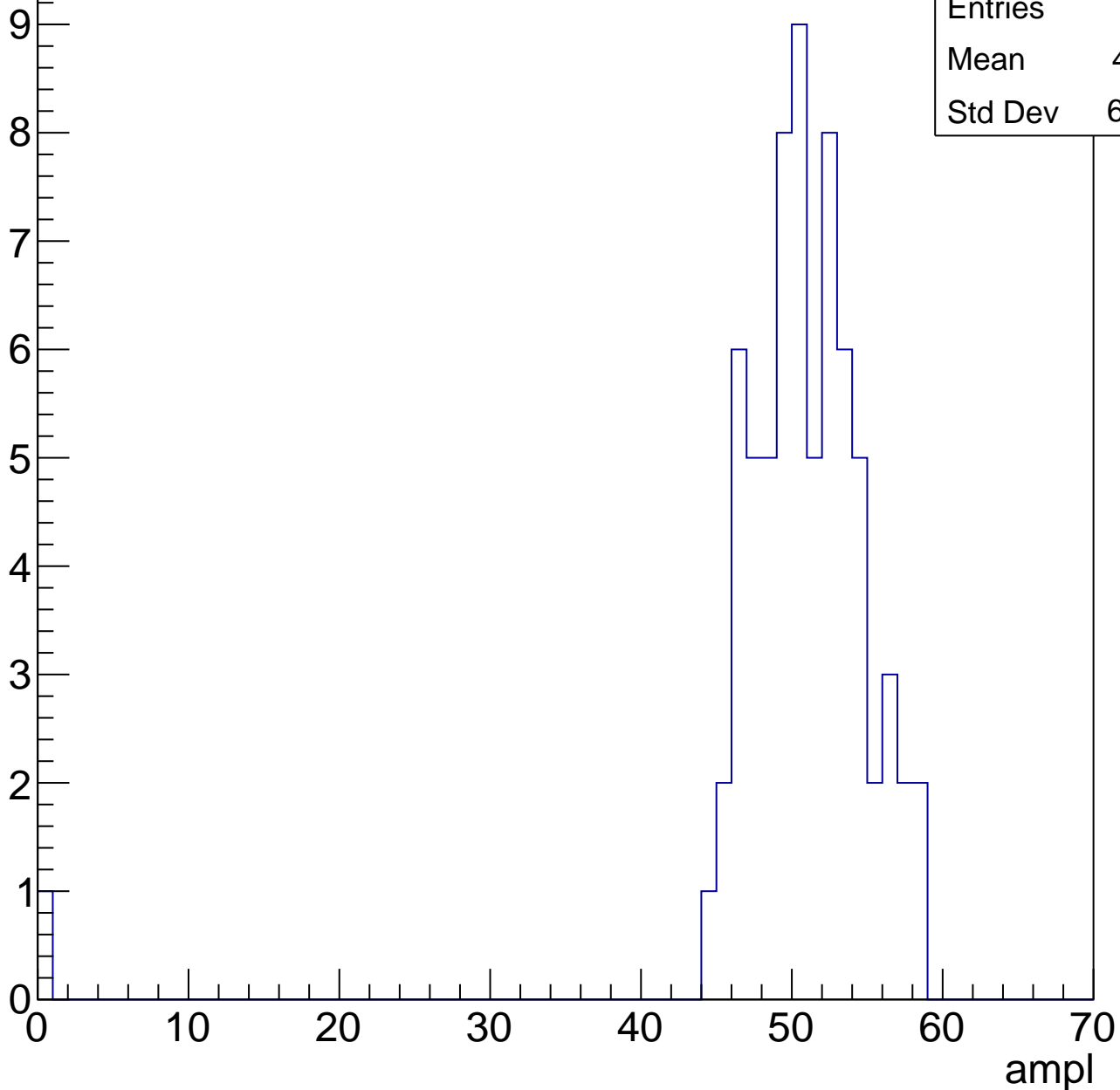


B1L103S, U6-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	49.91
Std Dev	6.876

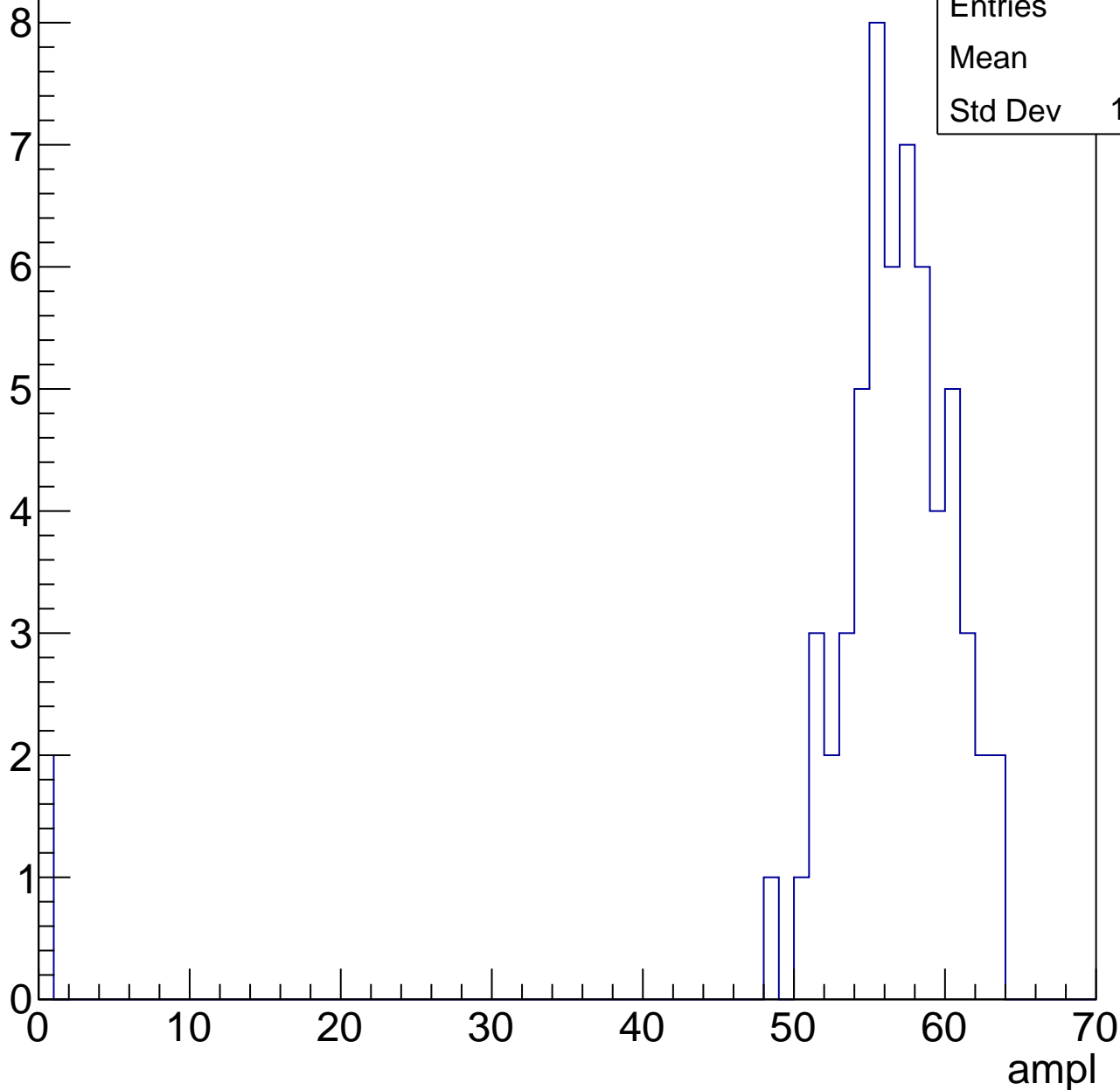


B1L103S, U6-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.6
Std Dev	10.65



B1L103S, U6-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries	35
Mean	60.51
Std Dev	2.247

0

10

20

30

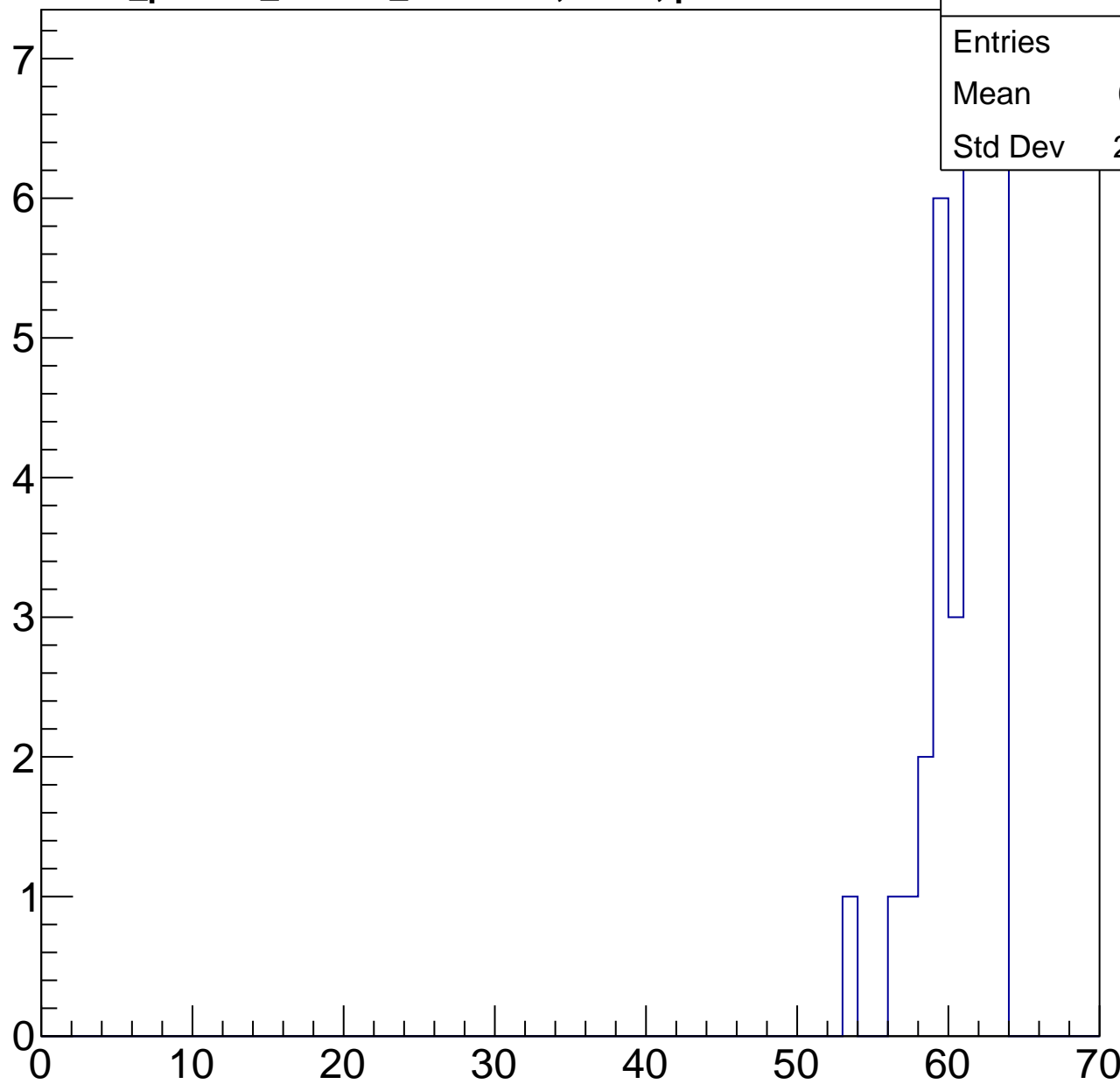
40

50

60

70

ampl



B1L103S, U6-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

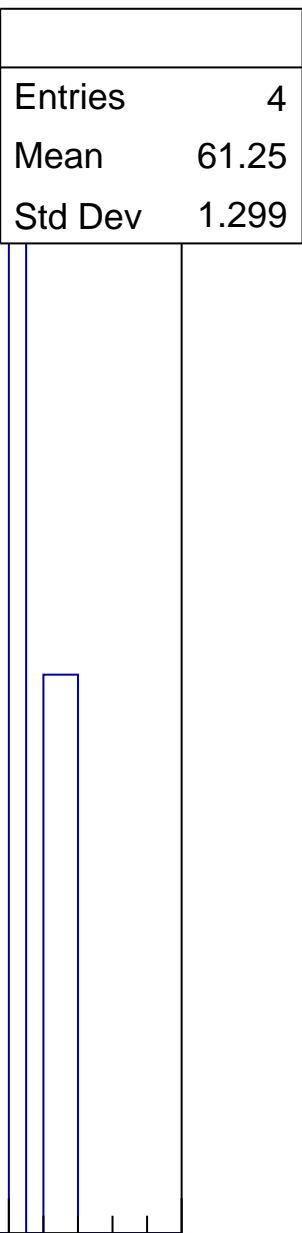
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.299

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U6-ch6, adc0

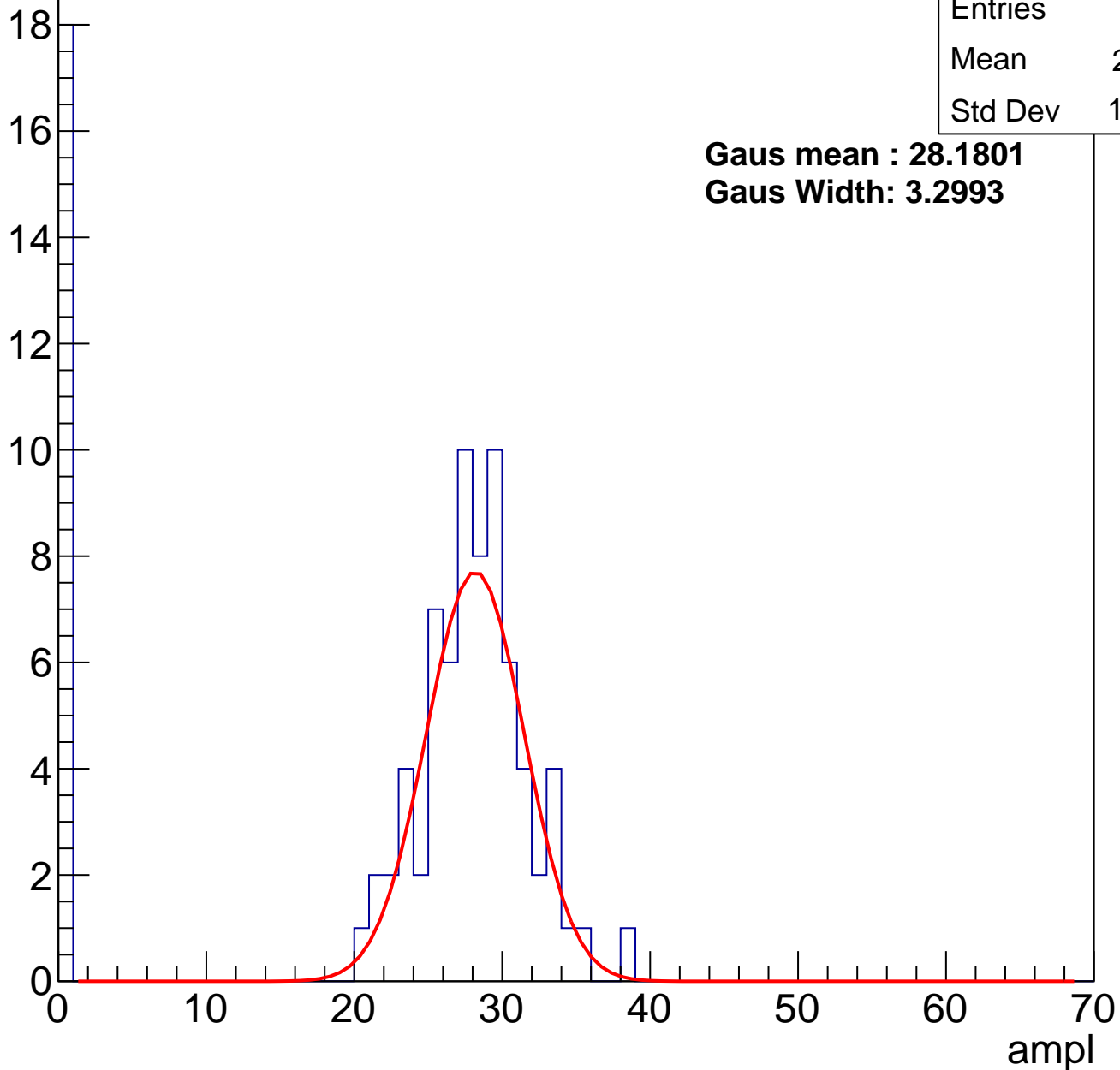
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.11
Std Dev	11.55

Gaus mean : 28.1801

Gaus Width: 3.2993

Entry



B1L103S, U6-ch6, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	29.43
Std Dev	12.25

Gaus mean : 34.5766

Gaus Width: 3.9488

Entry

10

8

6

4

2

0

0

10

20

30

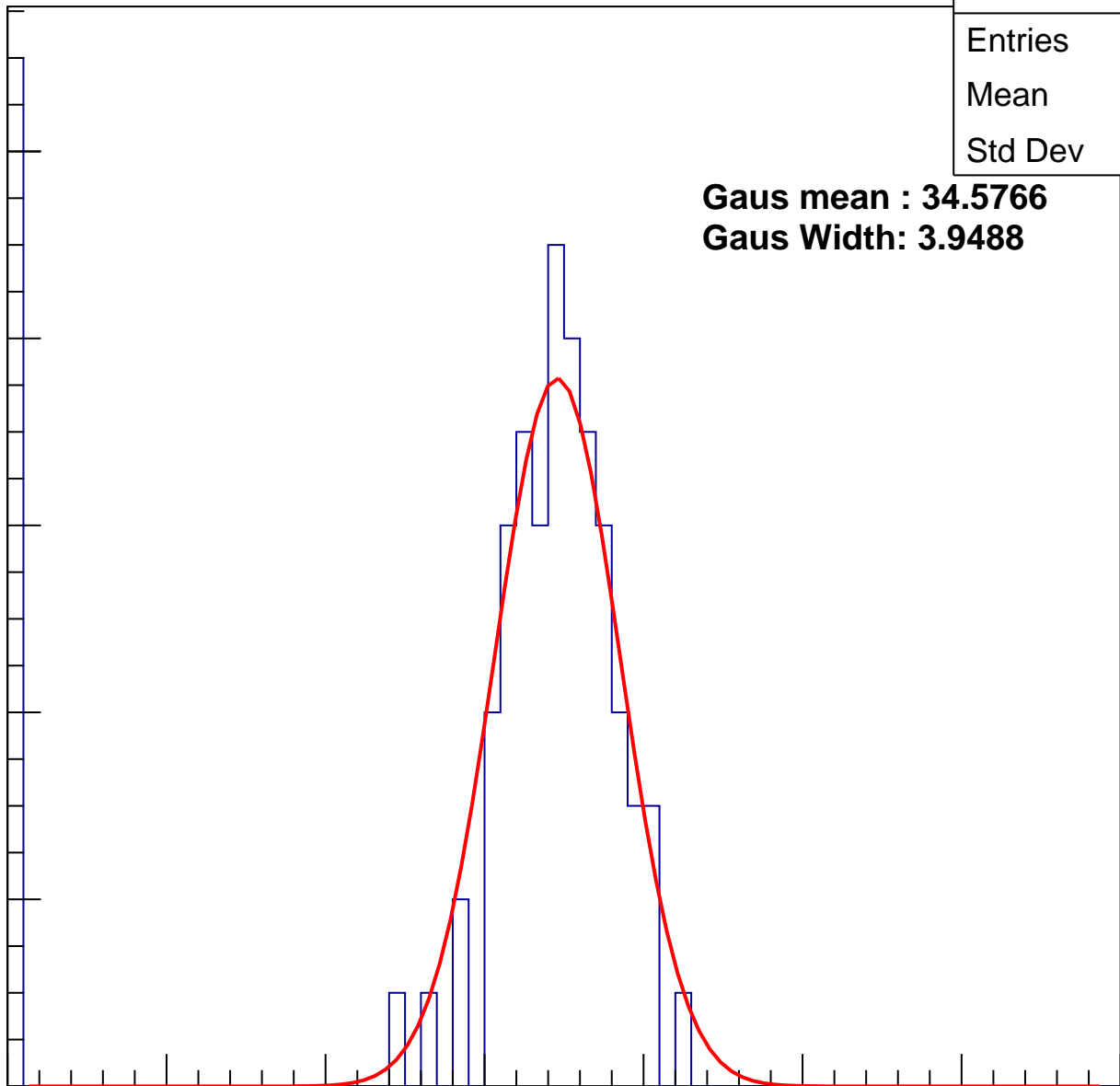
40

50

60

70

ampl



B1L103S, U6-ch6, adc2

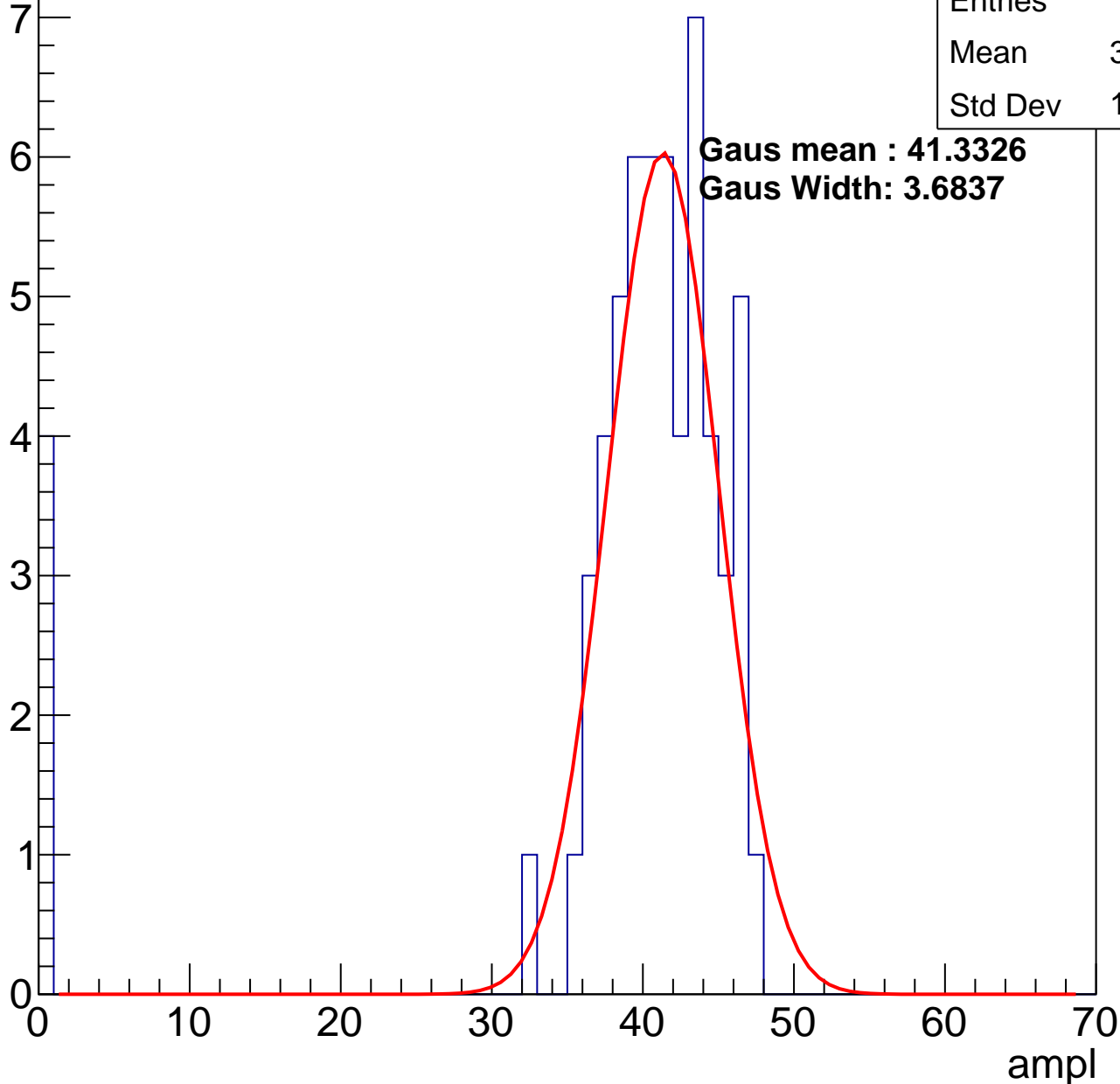
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.17
Std Dev	10.69

Gaus mean : 41.3326

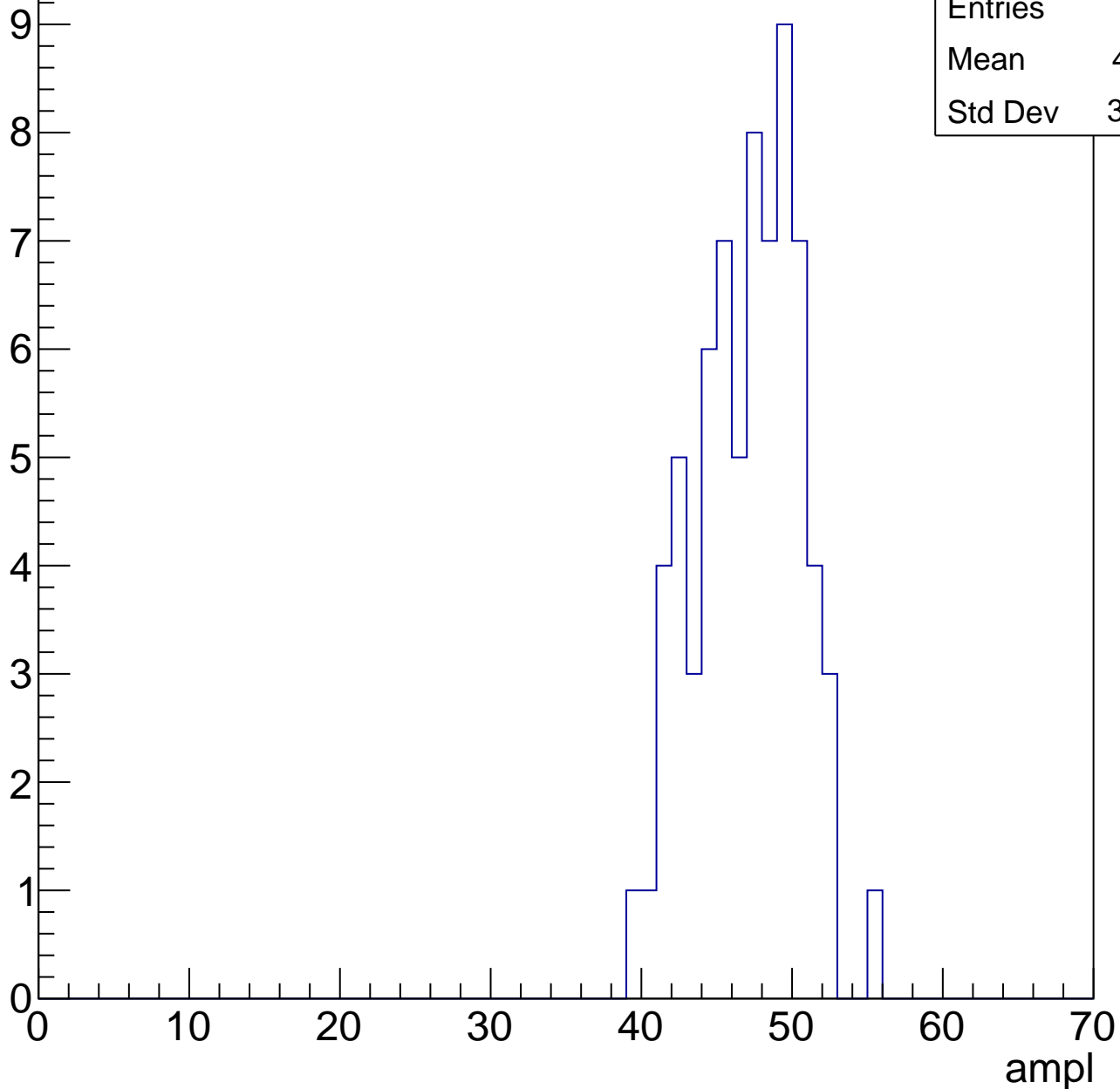
Gaus Width: 3.6837



B1L103S, U6-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch6, adc4

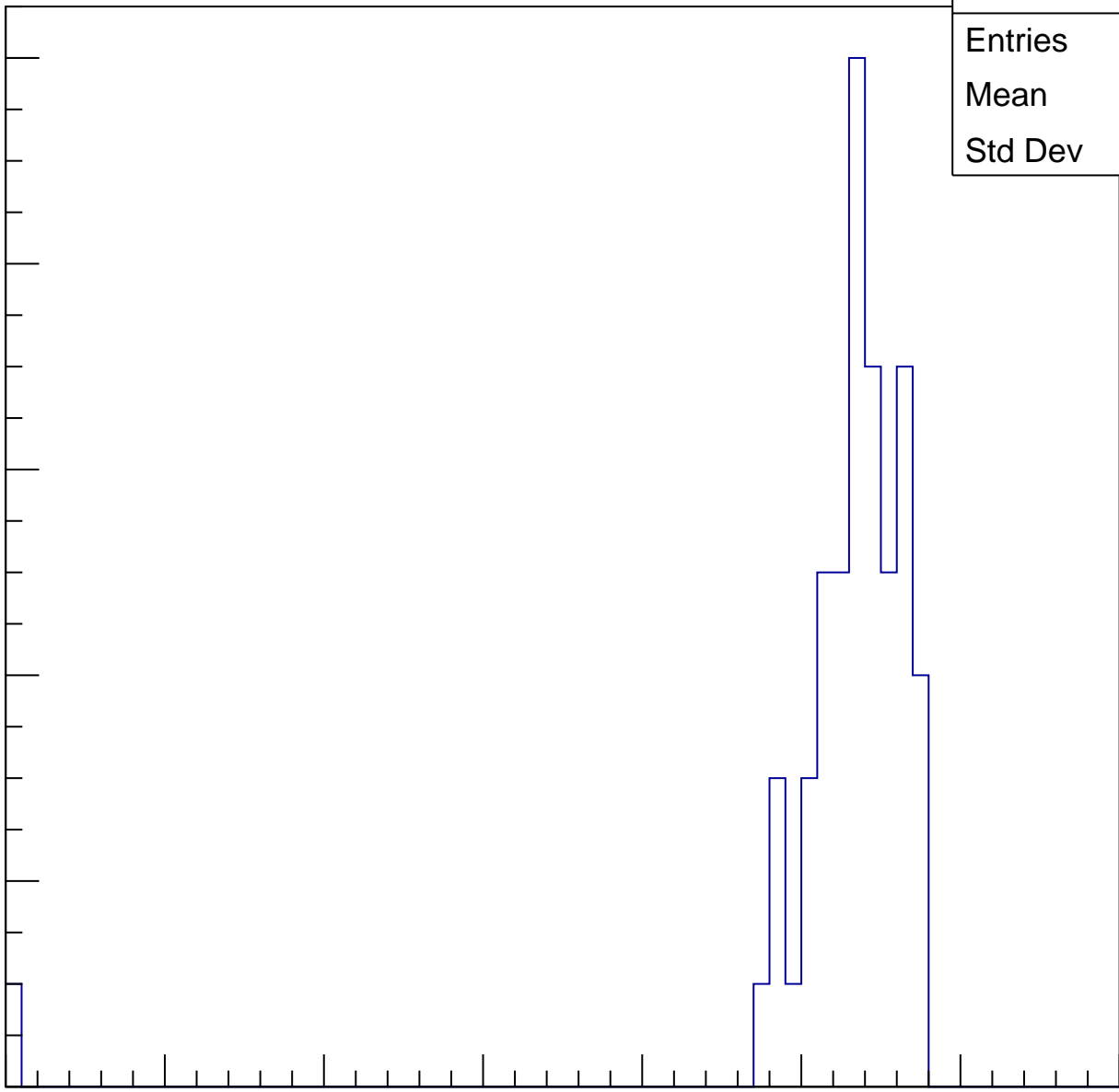
calib_packv5_041523_1651.root, FC#0, port C2

Entries	52
Mean	52.08
Std Dev	7.718

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl



B1L103S, U6-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	58.34
Std Dev	2.826

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

2

4

6

8

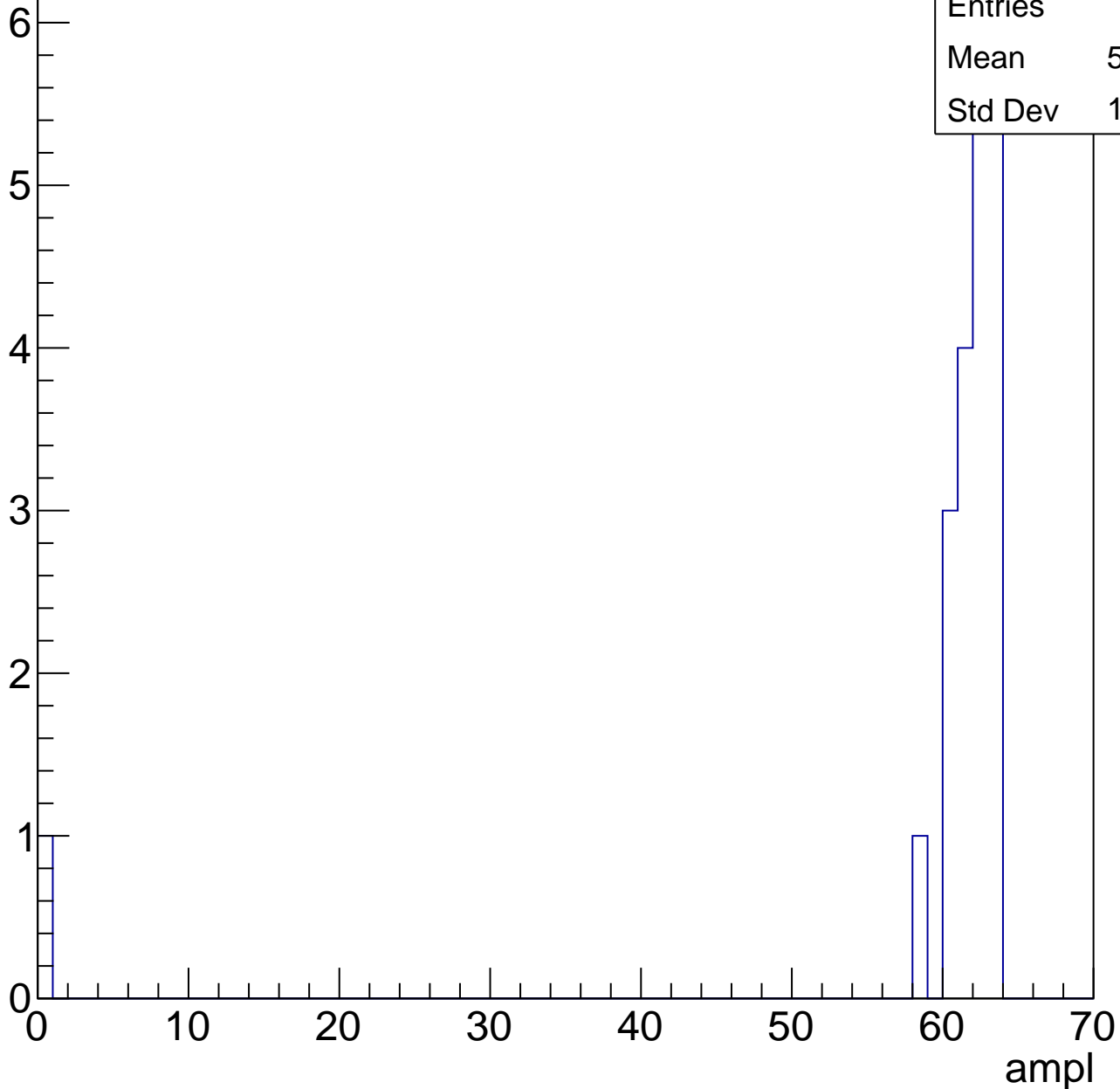
10

B1L103S, U6-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

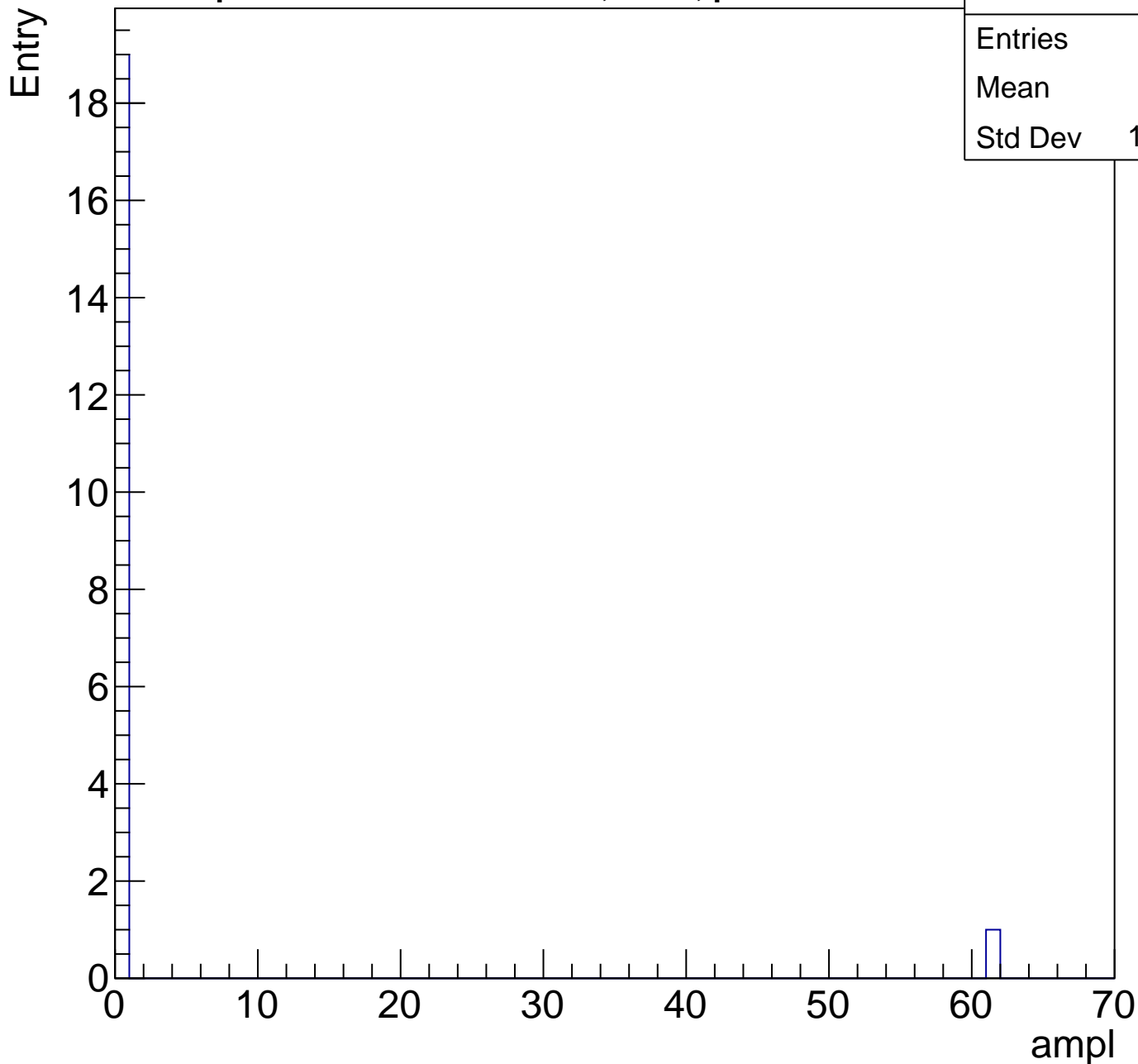
Entries	21
Mean	58.67
Std Dev	13.18



B1L103S, U6-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29



B1L103S, U6-ch7, adc0

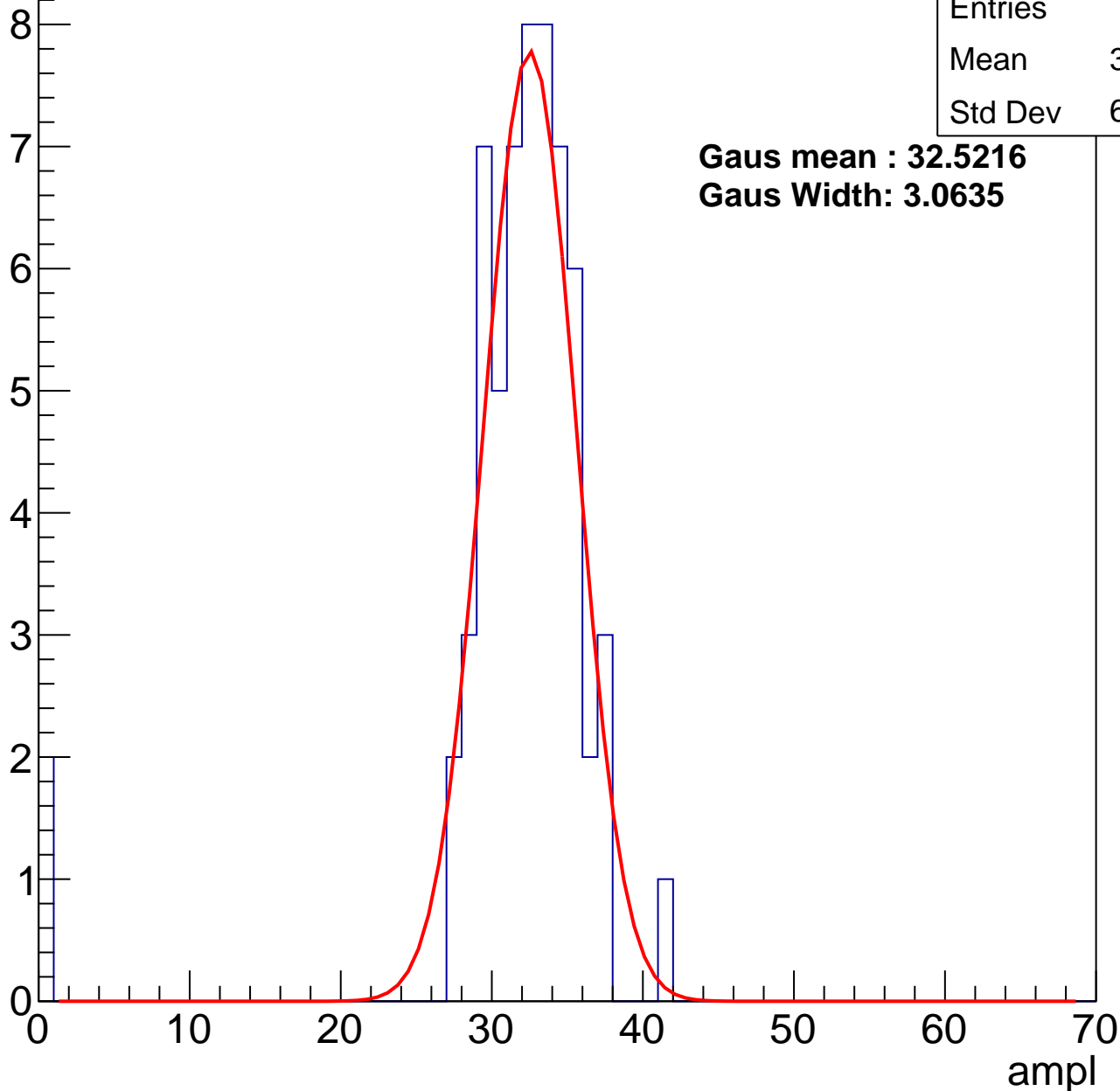
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	31.15
Std Dev	6.363

Gaus mean : 32.5216

Gaus Width: 3.0635



B1L103S, U6-ch7, adc1

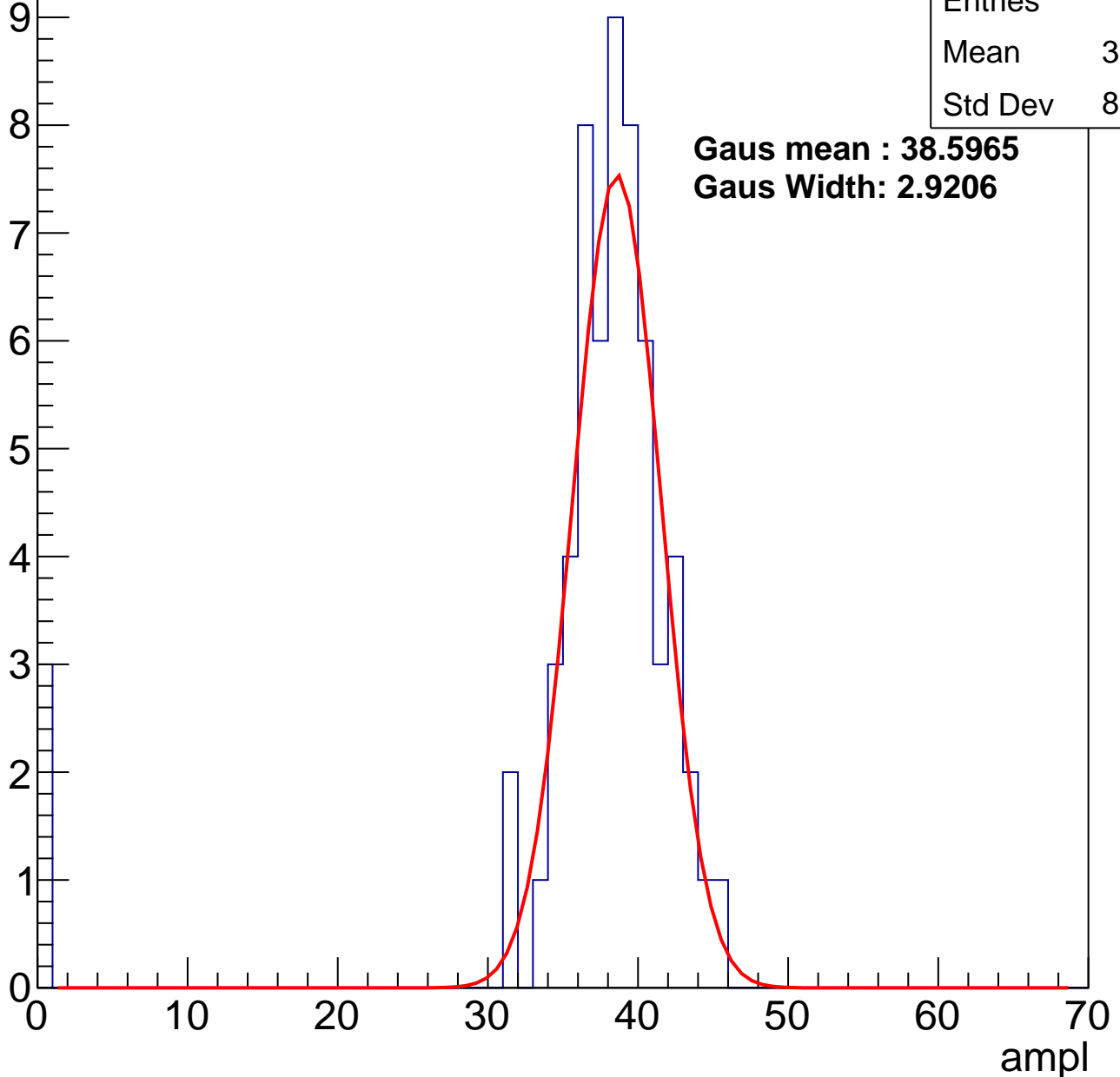
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	36.18
Std Dev	8.715

Gaus mean : 38.5965

Gaus Width: 2.9206



B1L103S, U6-ch7, adc2

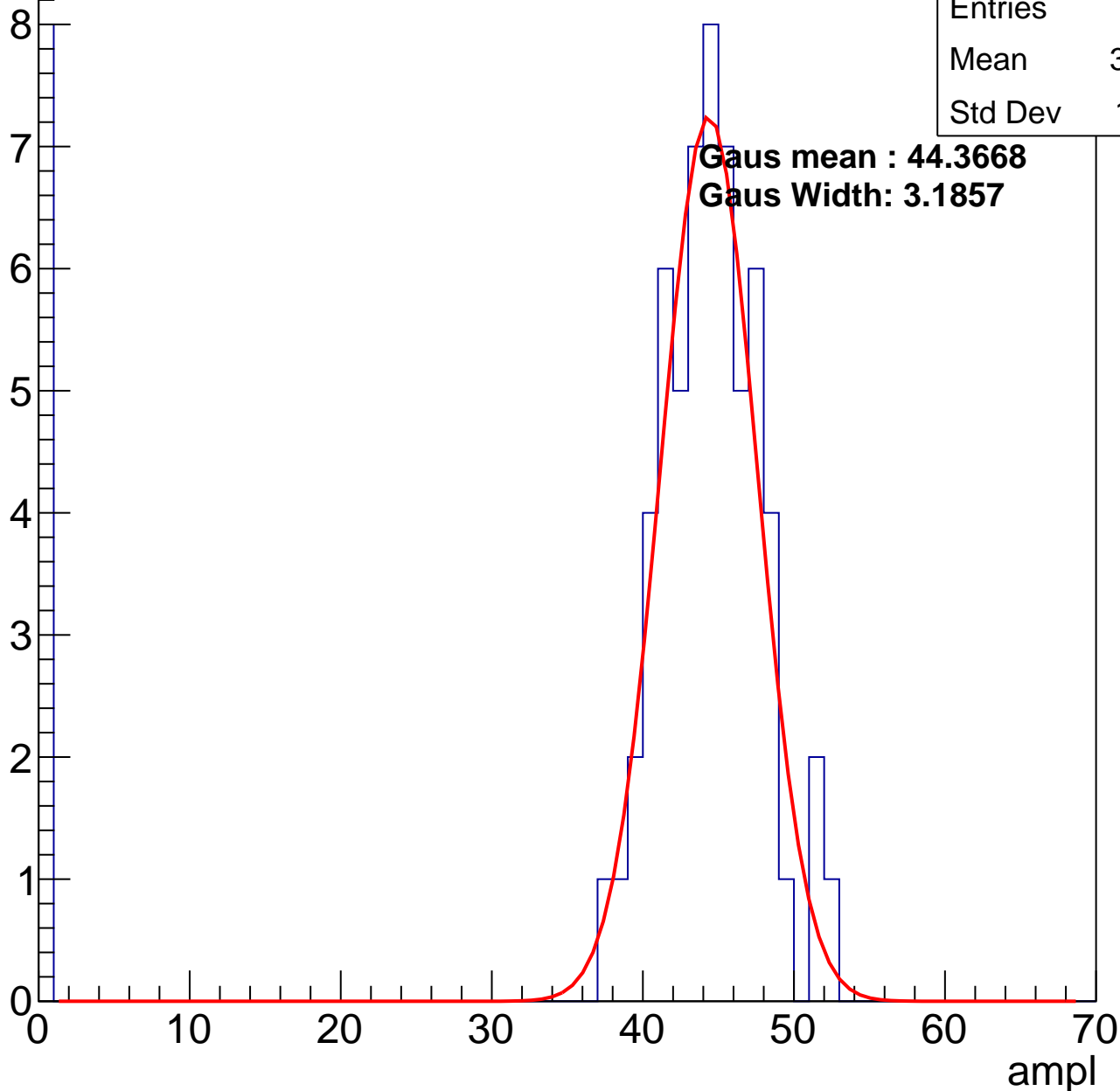
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.88
Std Dev	14.51

Gaus mean : 44.3668

Gaus Width: 3.1857

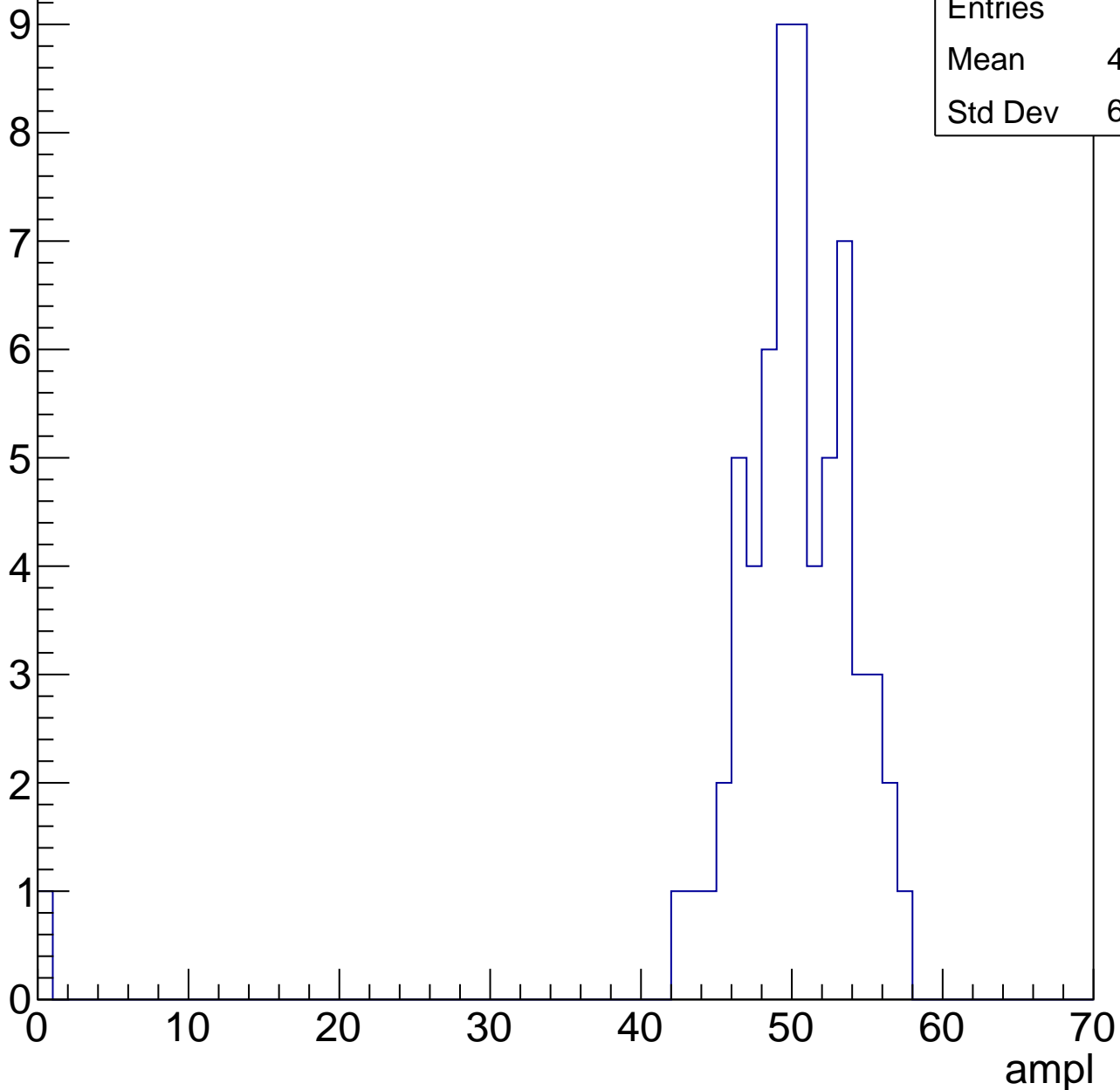


B1L103S, U6-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.17
Std Dev	6.999

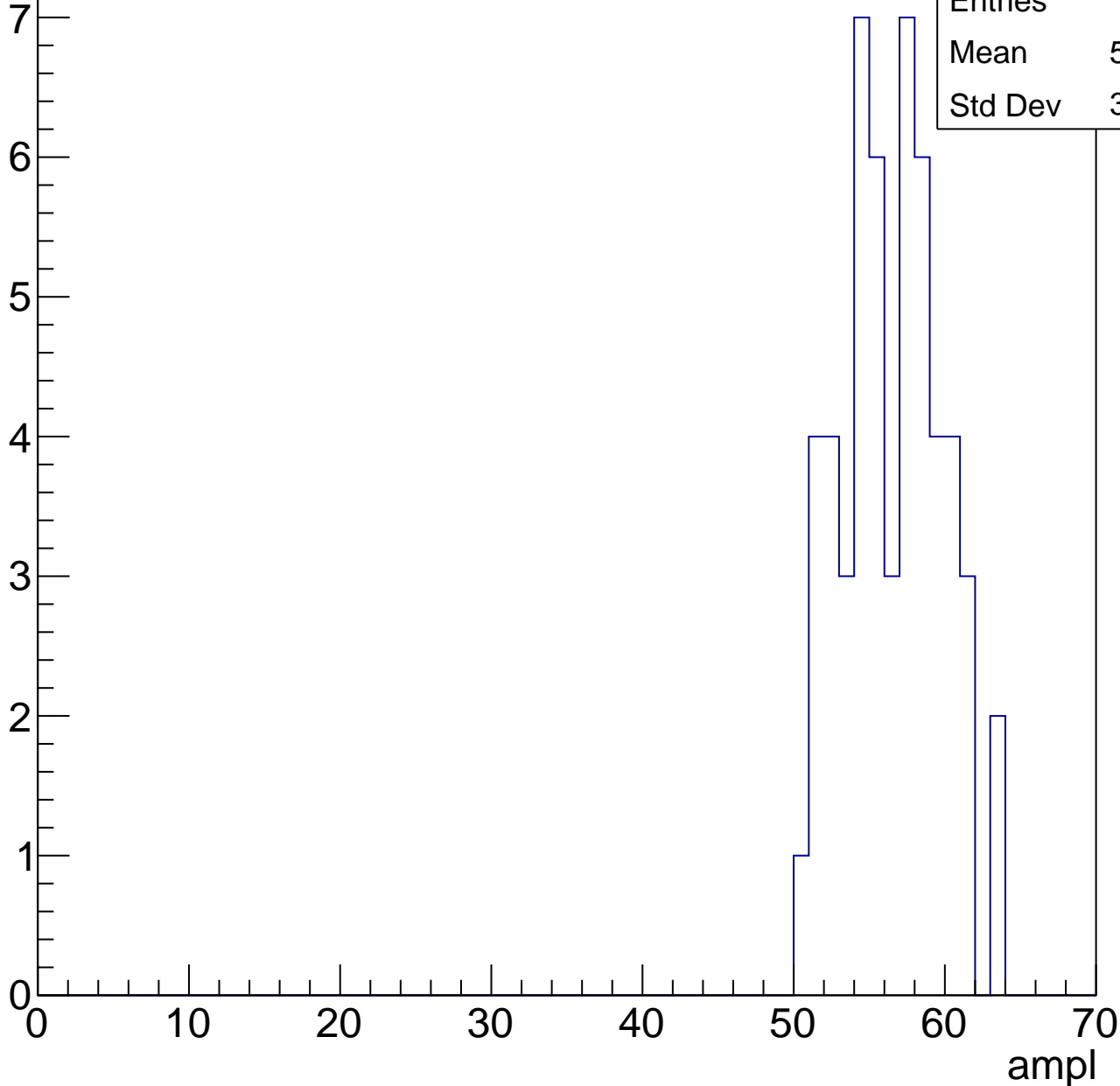


B1L103S, U6-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.09
Std Dev	3.233

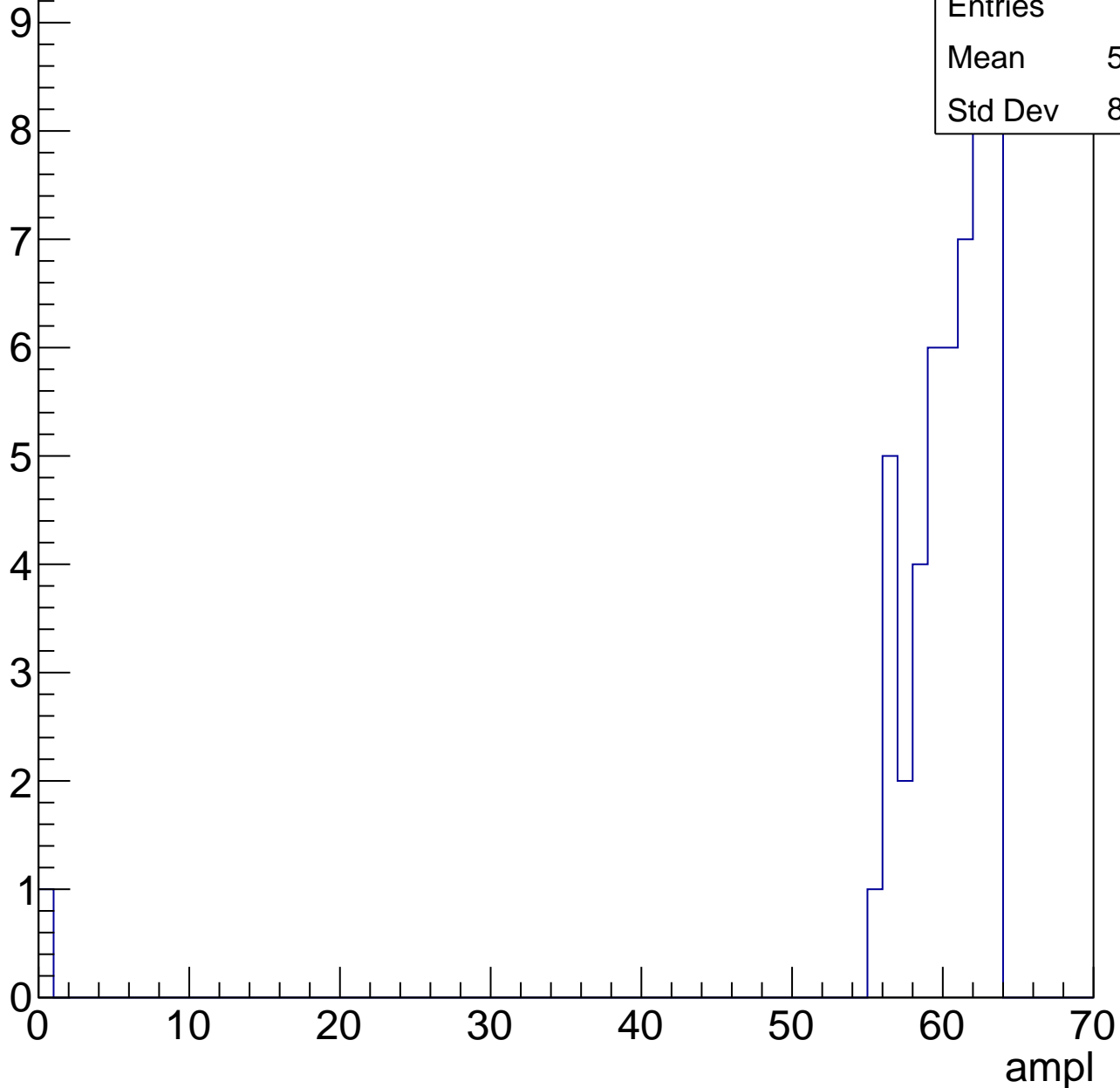


B1L103S, U6-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.86
Std Dev	8.802



B1L103S, U6-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

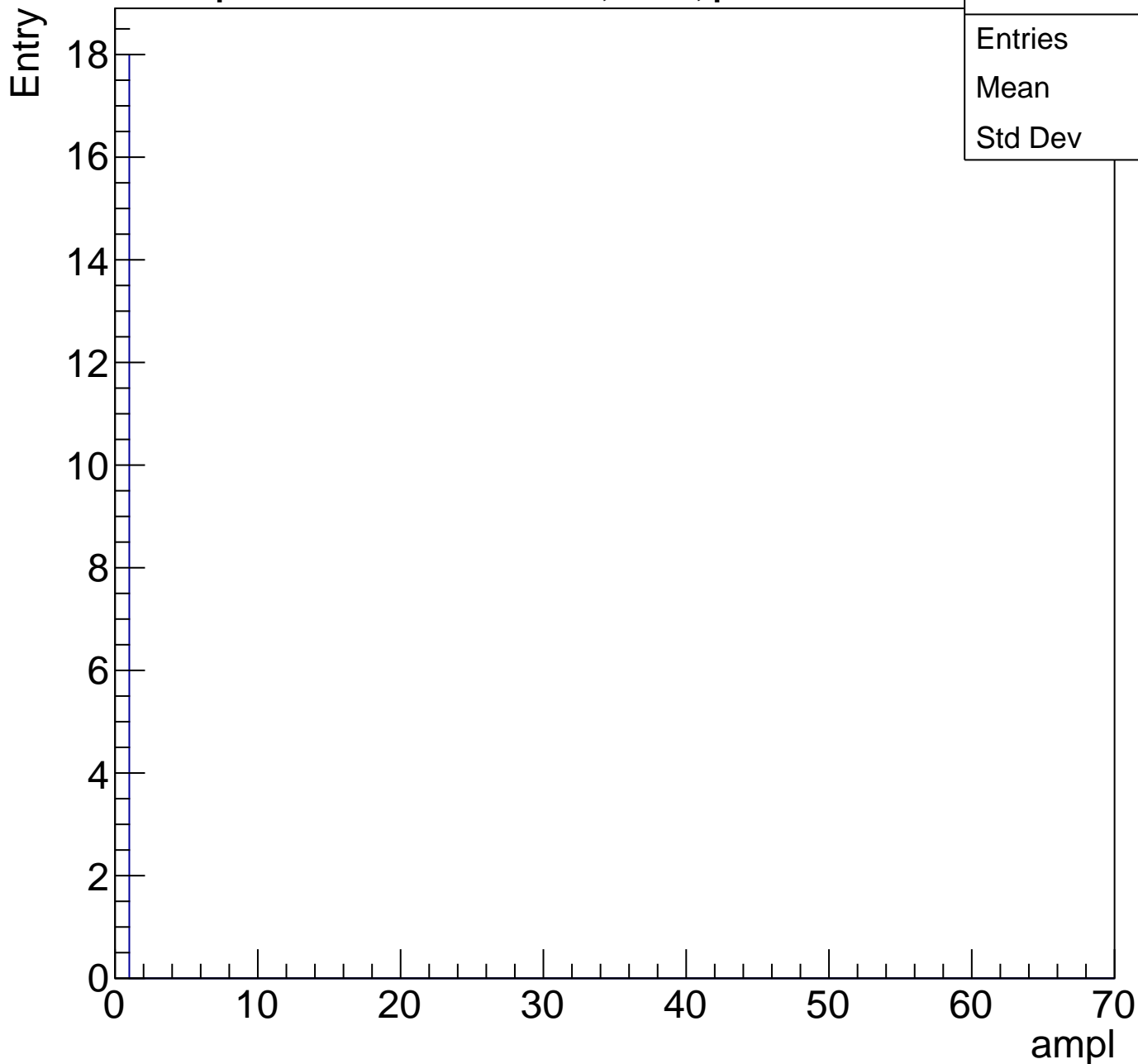
Entry



B1L103S, U6-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0



B1L103S, U6-ch8, adc0

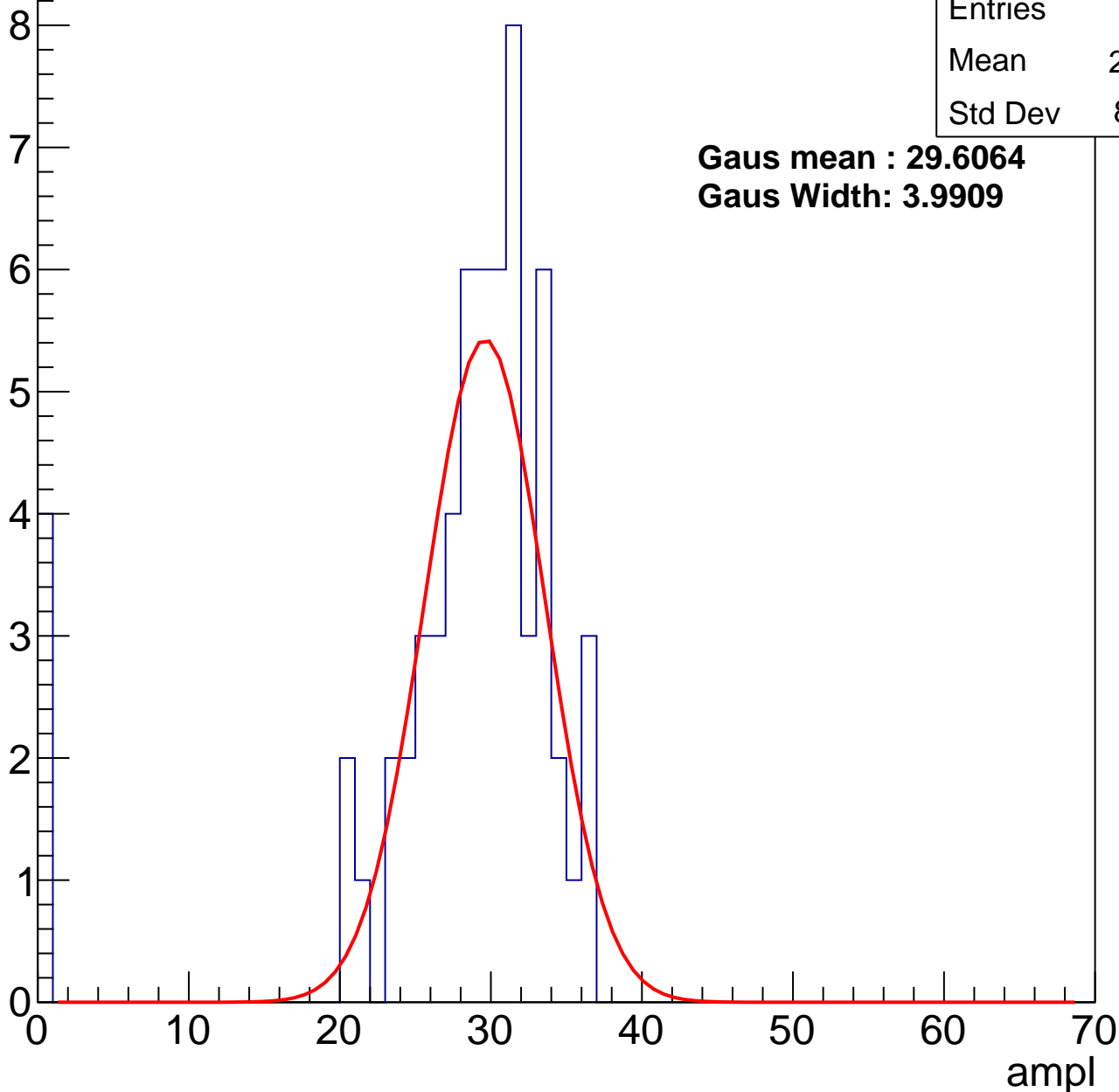
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	27.27
Std Dev	8.051

Gaus mean : 29.6064

Gaus Width: 3.9909



B1L103S, U6-ch8, adc1

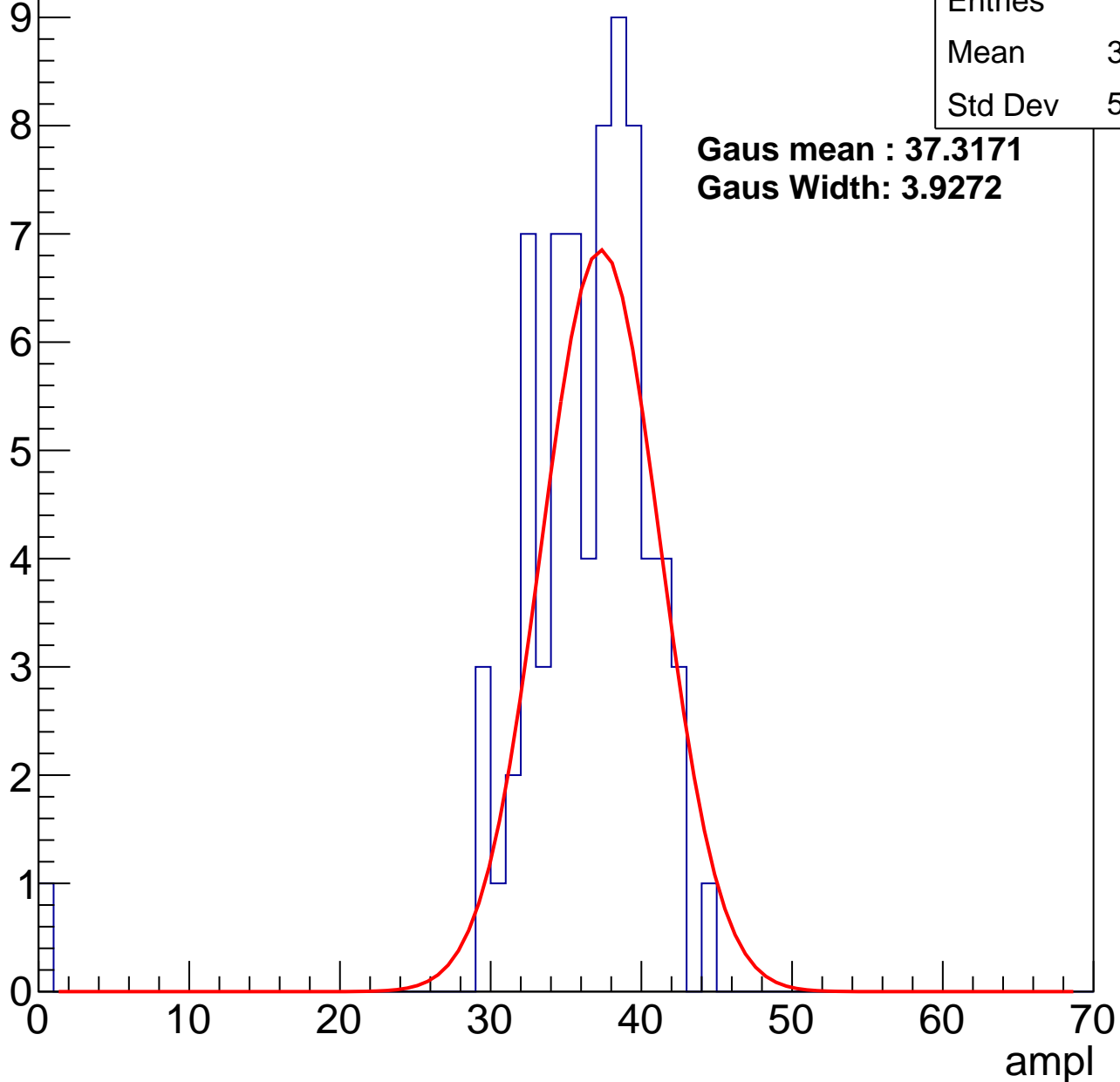
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.74
Std Dev	5.465

Gaus mean : 37.3171

Gaus Width: 3.9272



B1L103S, U6-ch8, adc2

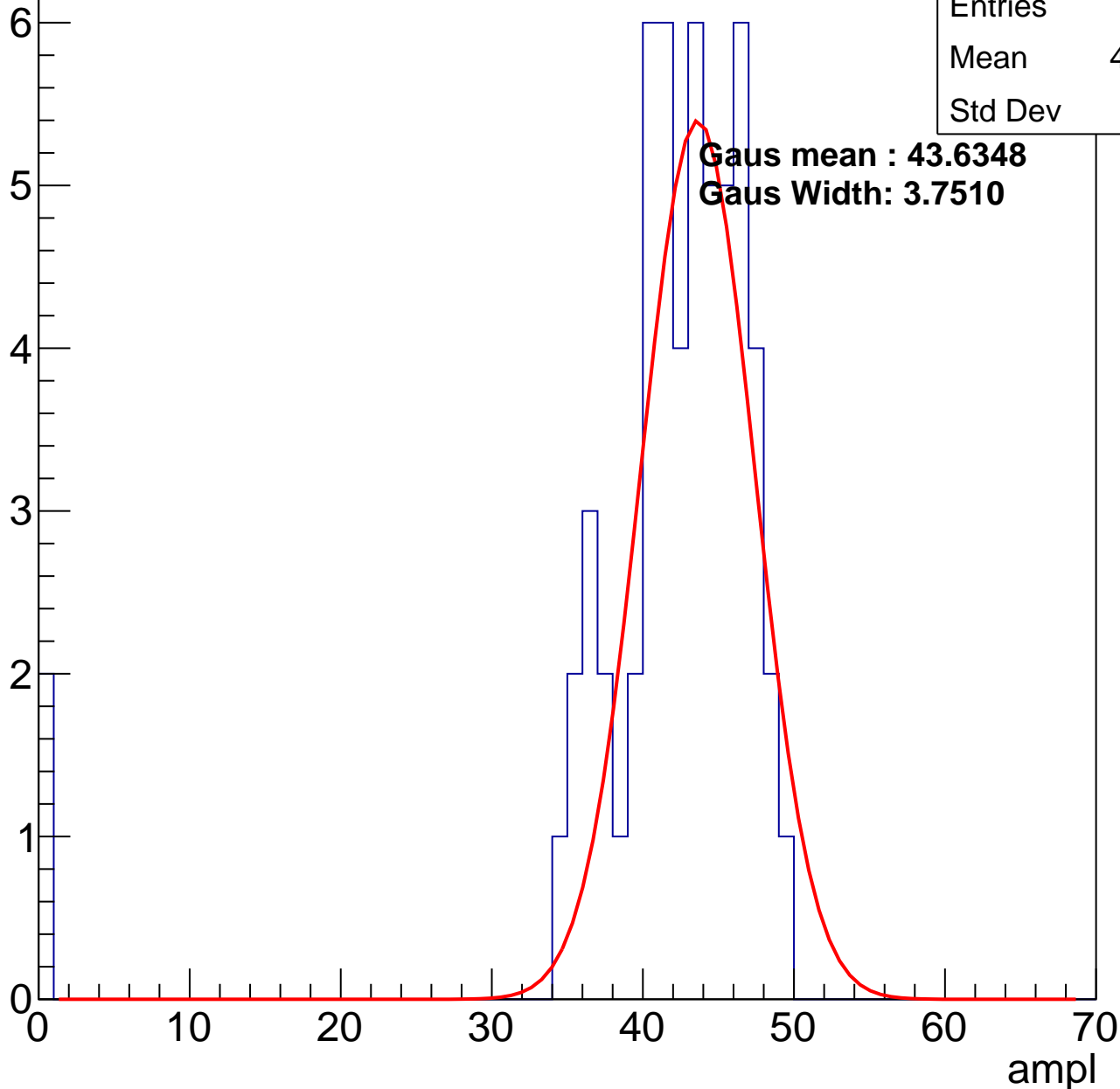
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.83
Std Dev	8.53

Gaus mean : 43.6348

Gaus Width: 3.7510

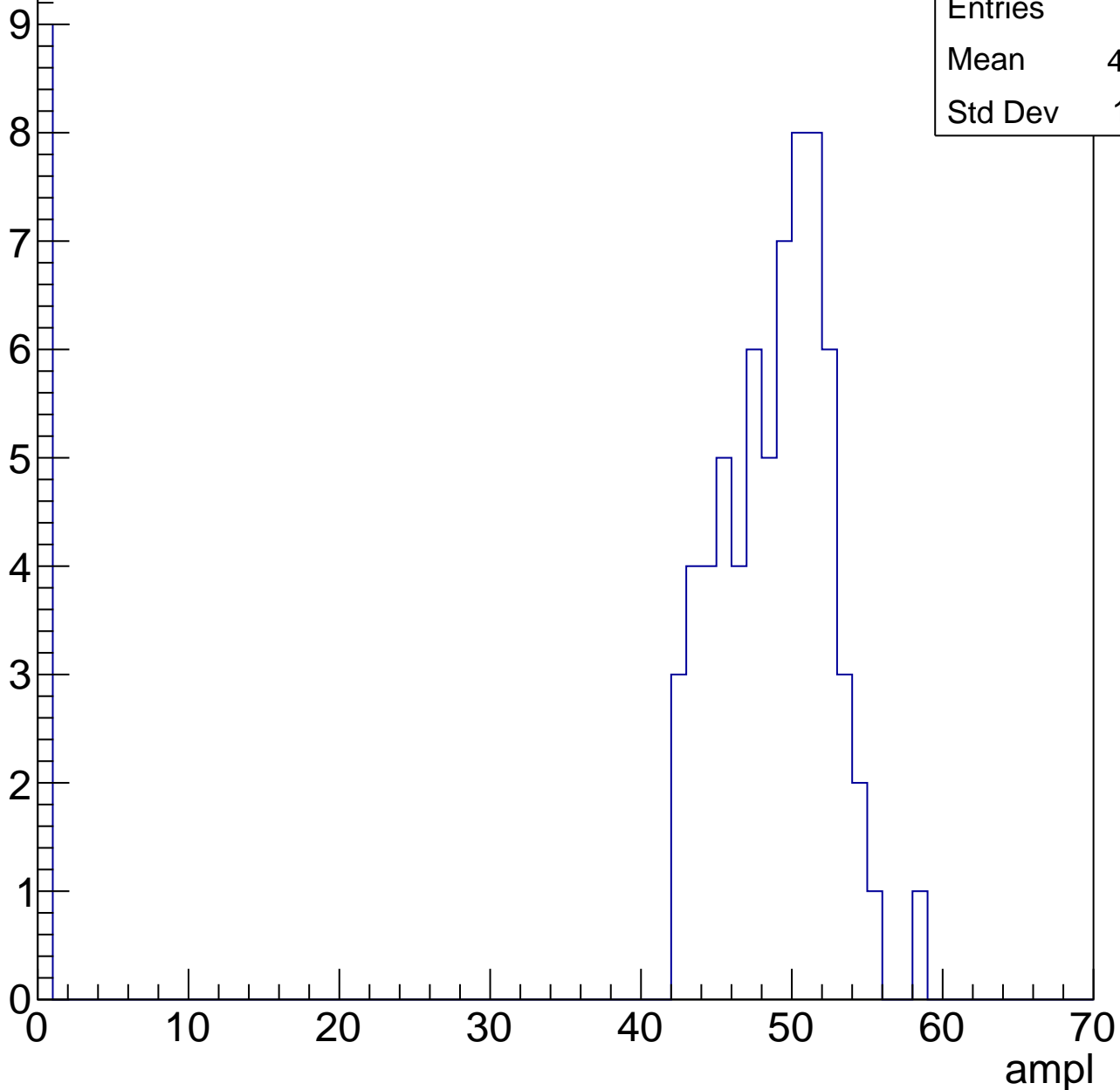


B1L103S, U6-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

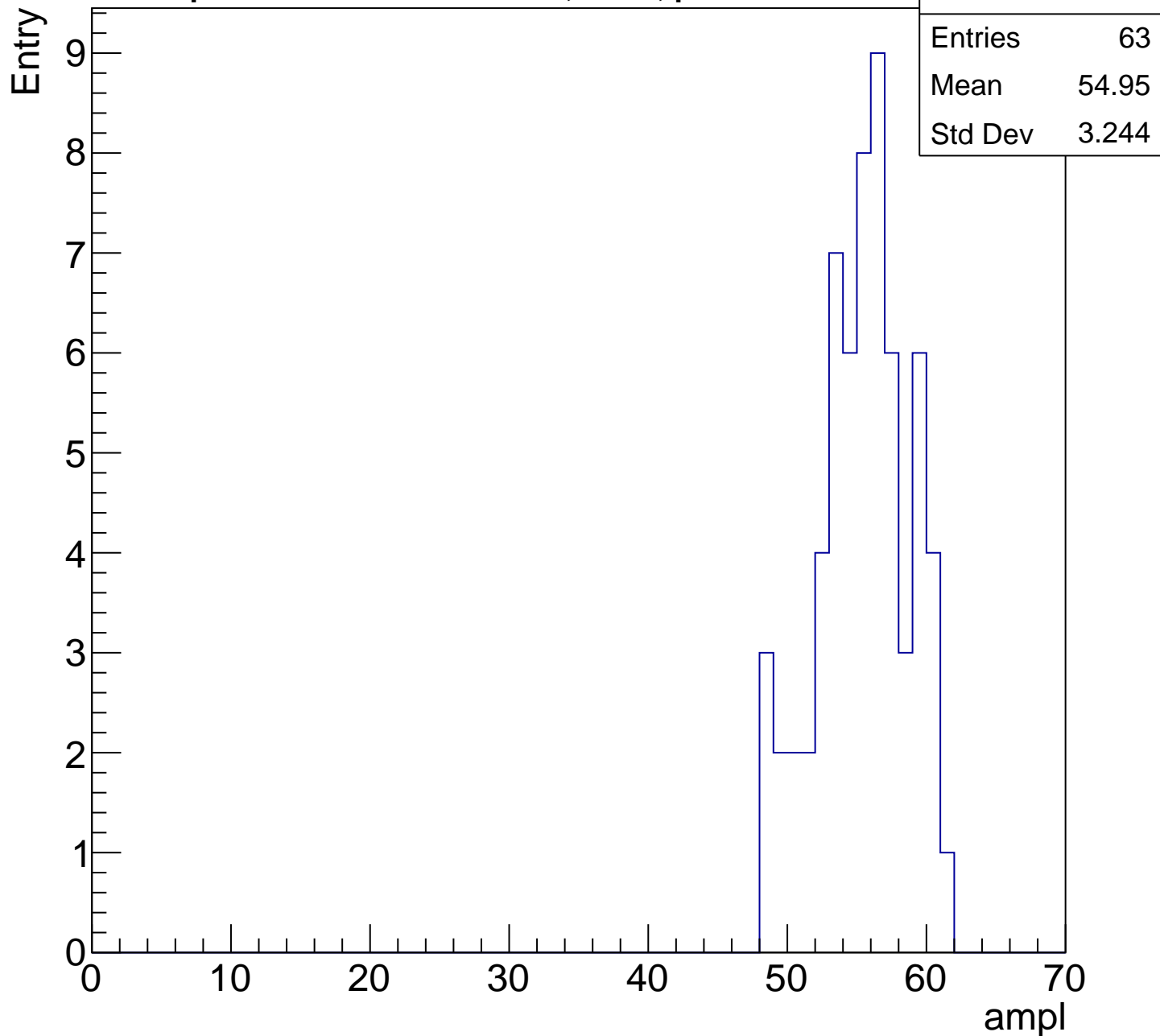
Entry

Entries	76
Mean	42.74
Std Dev	16.01



B1L103S, U6-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

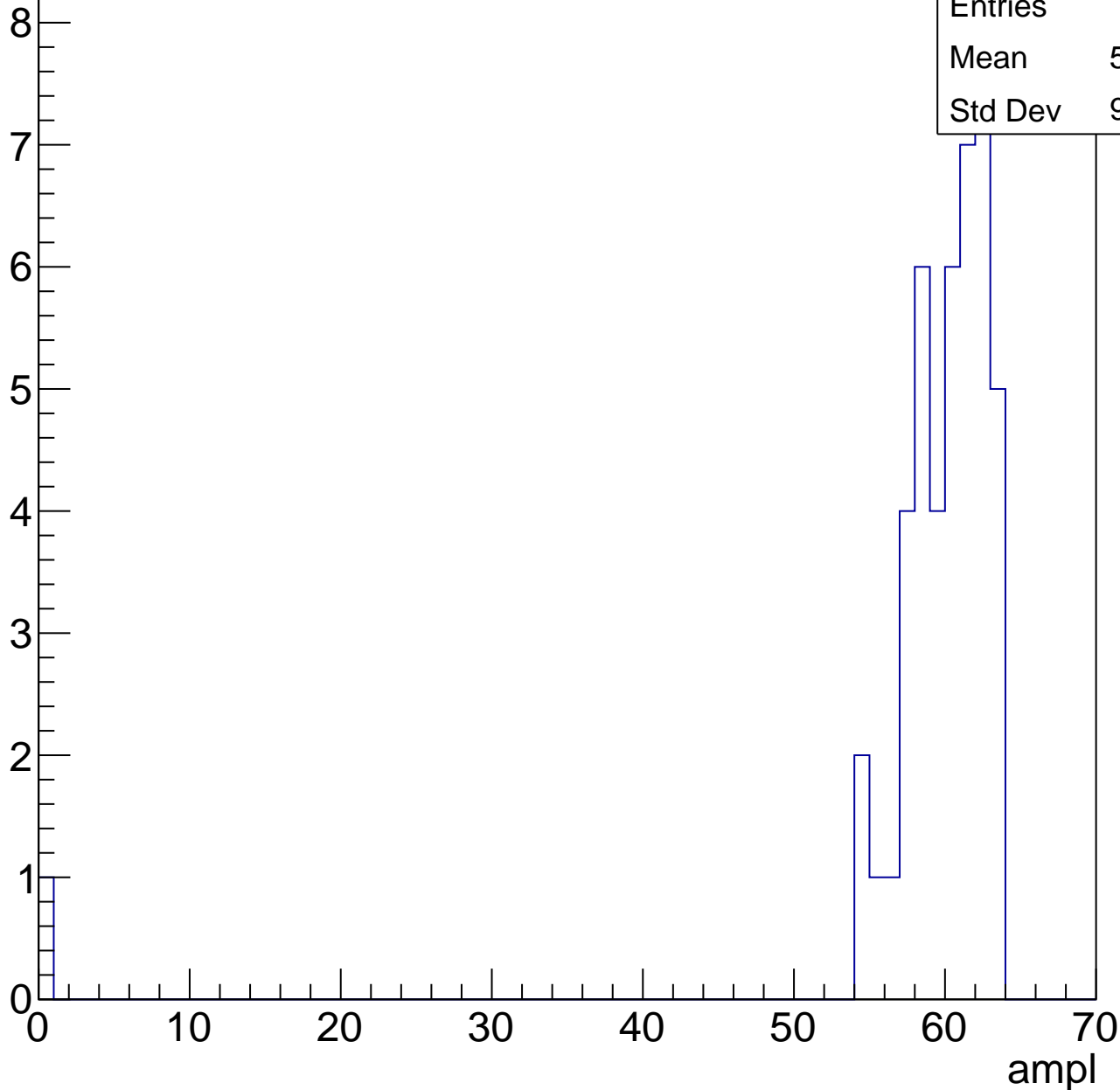


B1L103S, U6-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.42
Std Dev	9.127



B1L103S, U6-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

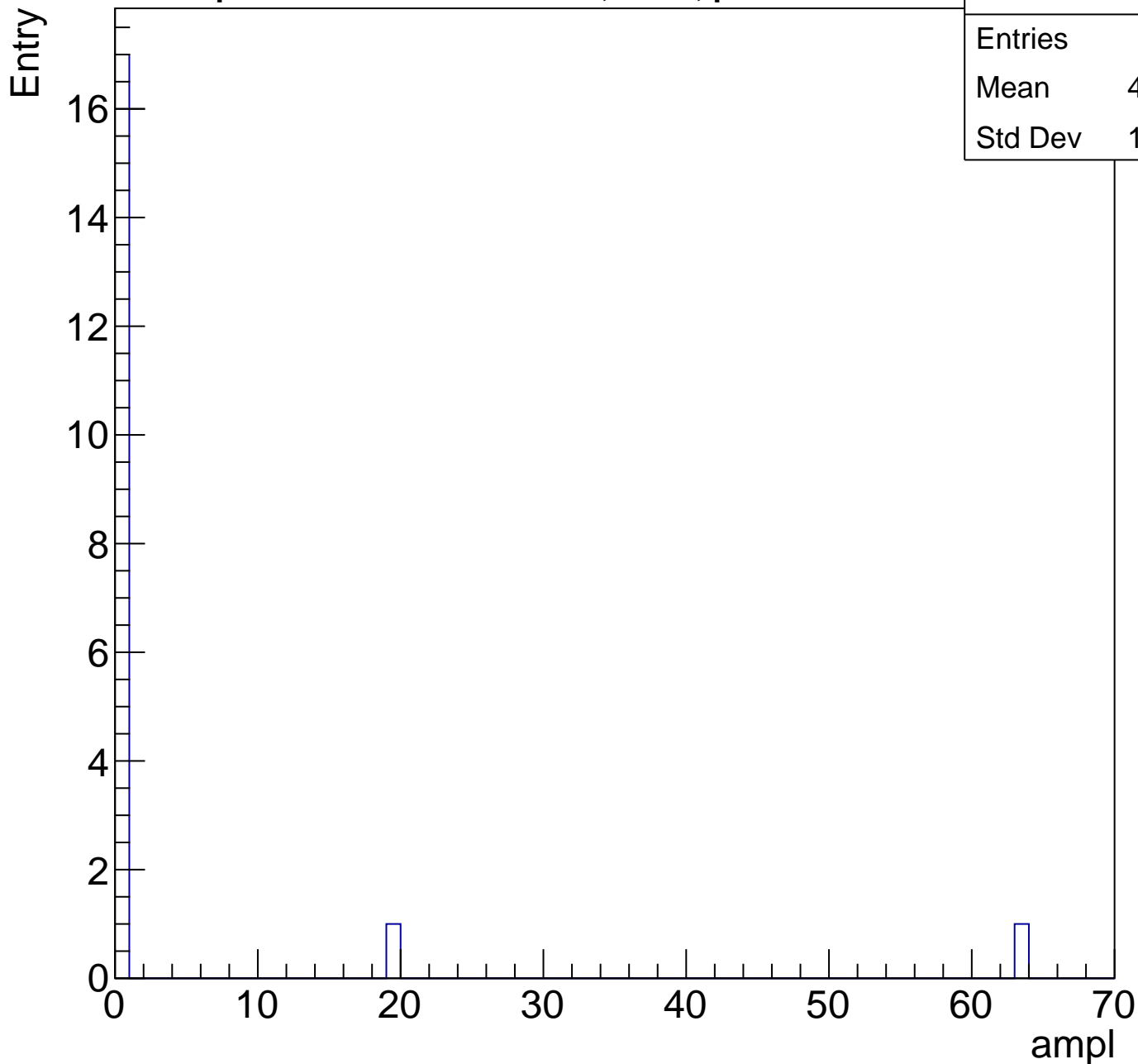
Entry



B1L103S, U6-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.316
Std Dev	14.47



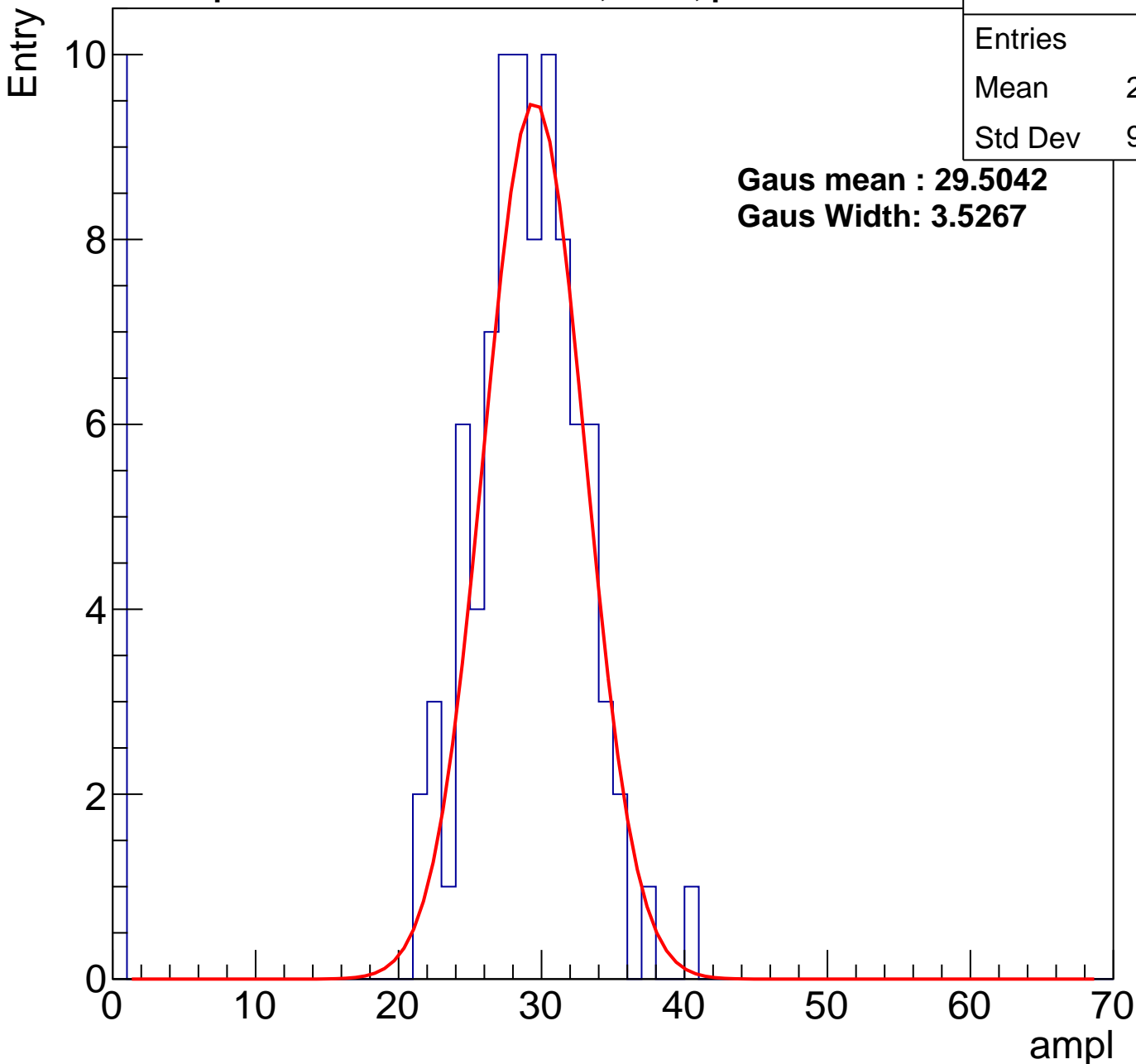
B1L103S, U6-ch9, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	25.78
Std Dev	9.342

Gaus mean : 29.5042

Gaus Width: 3.5267



B1L103S, U6-ch9, adc1

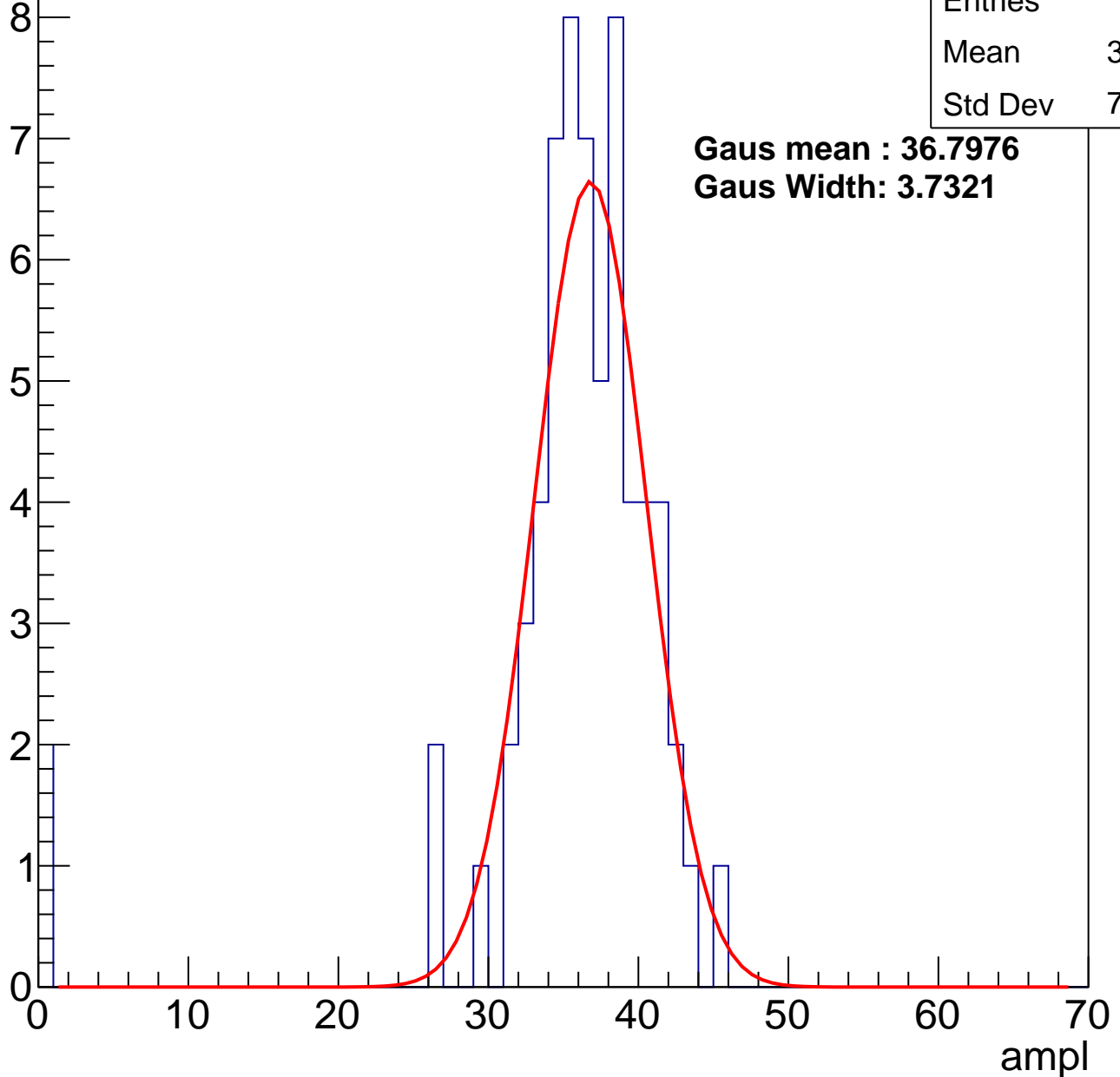
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.11
Std Dev	7.231

Gaus mean : 36.7976

Gaus Width: 3.7321



B1L103S, U6-ch9, adc2

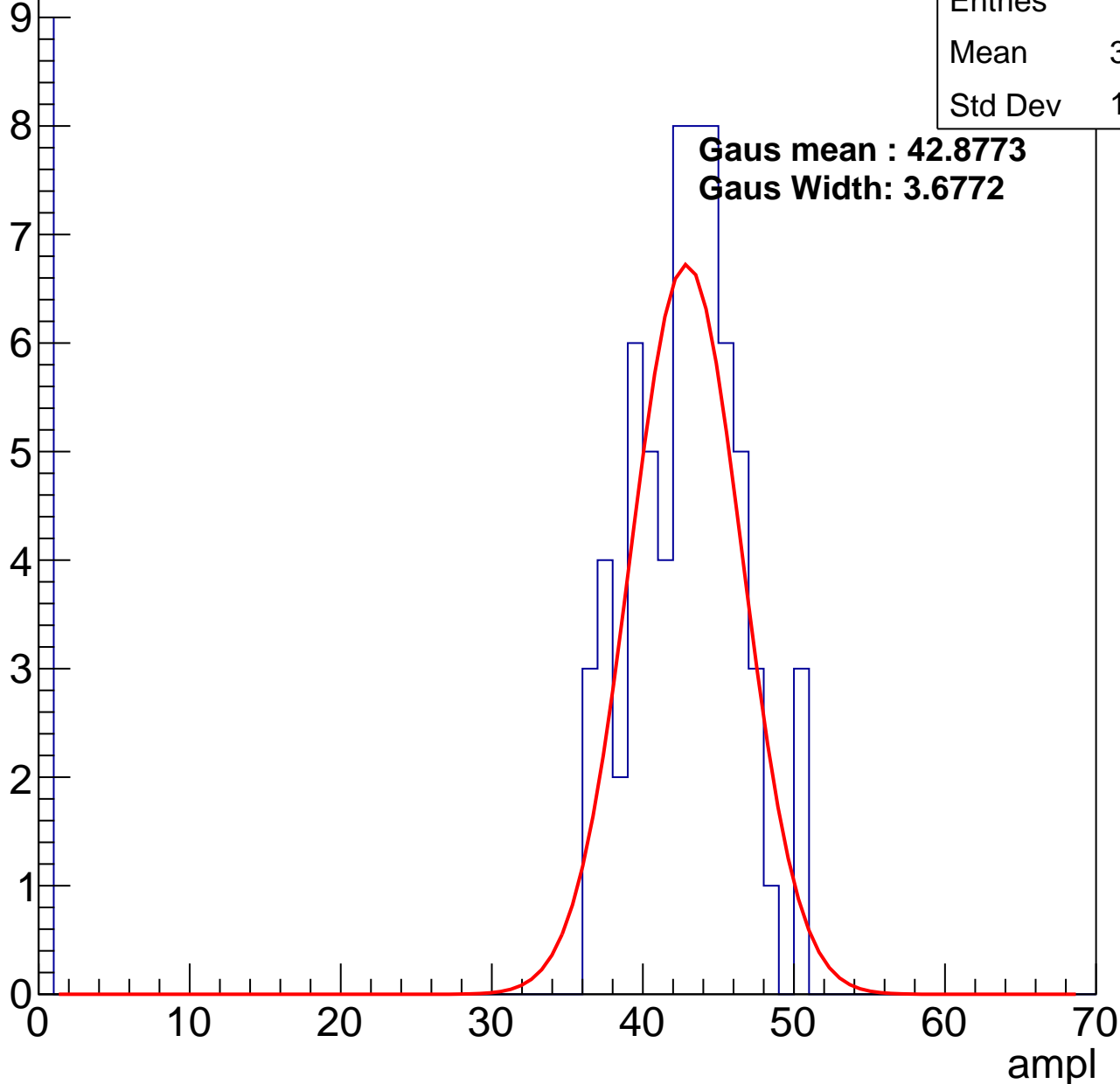
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	37.35
Std Dev	14.16

Gaus mean : 42.8773

Gaus Width: 3.6772

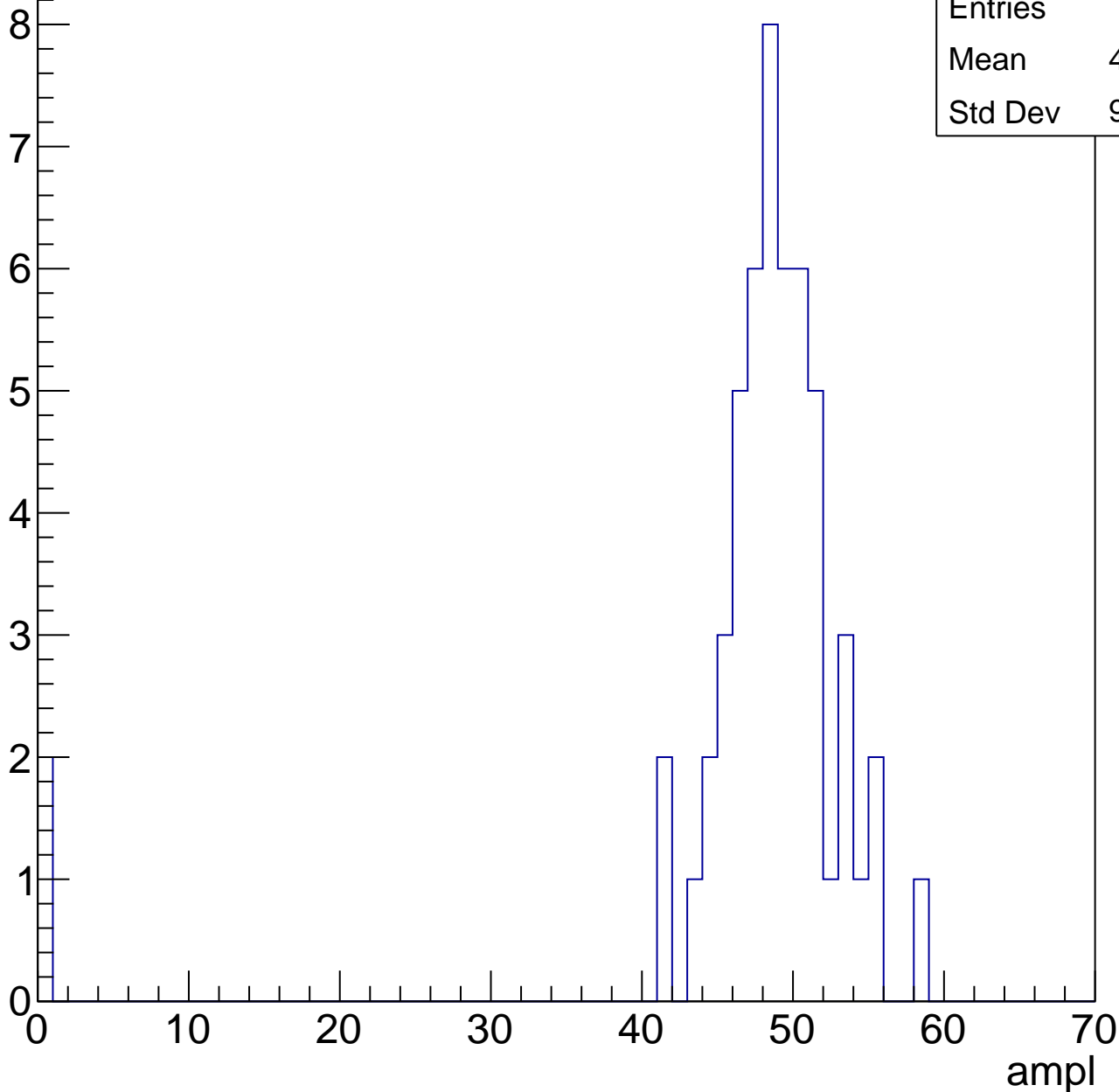


B1L103S, U6-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.78
Std Dev	9.752

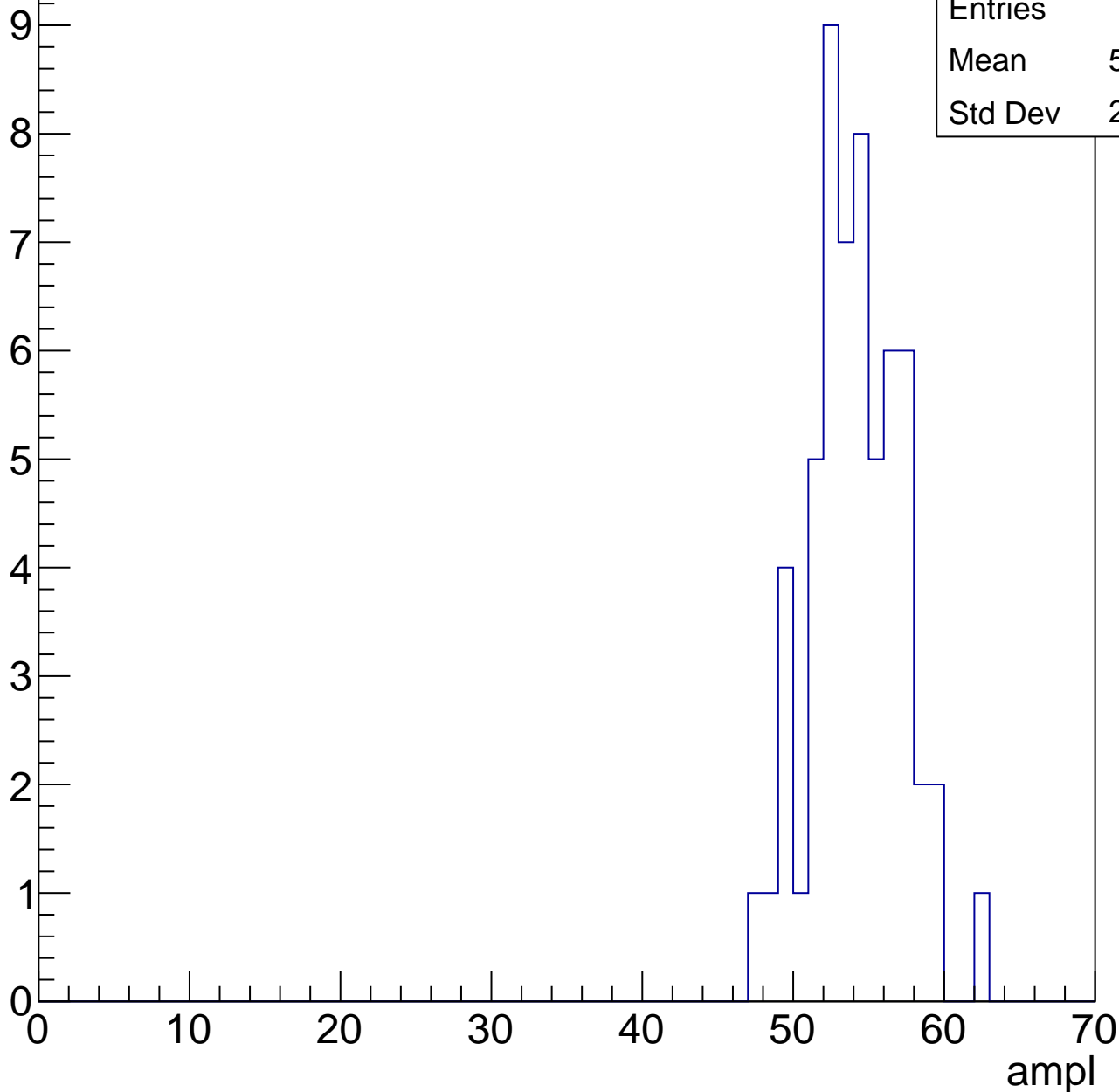


B1L103S, U6-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.72
Std Dev	2.976

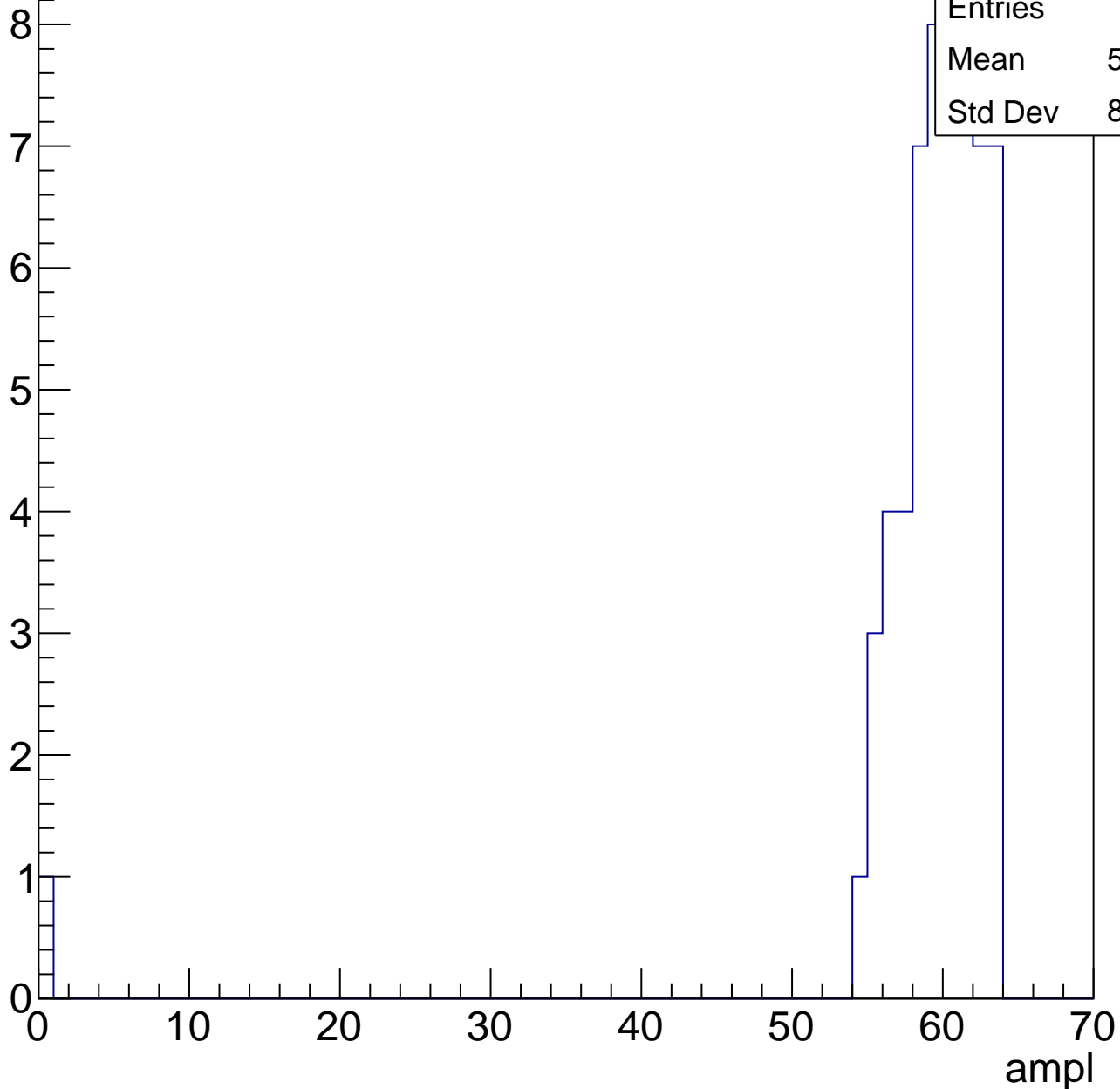


B1L103S, U6-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

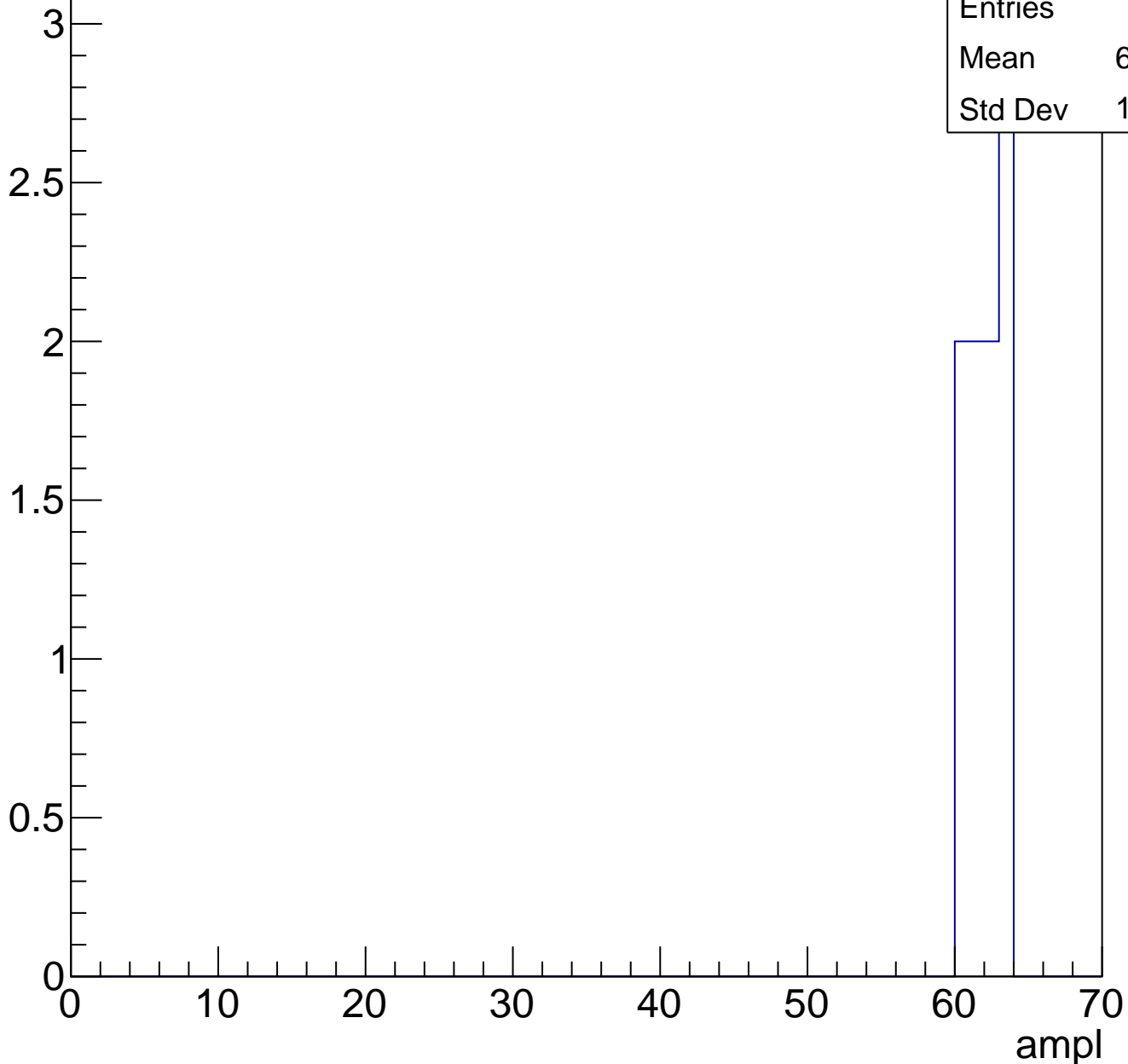
Entries	58
Mean	58.48
Std Dev	8.108



B1L103S, U6-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U6-ch10, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	27.06
Std Dev	7.634

Gaus mean : 28.4398

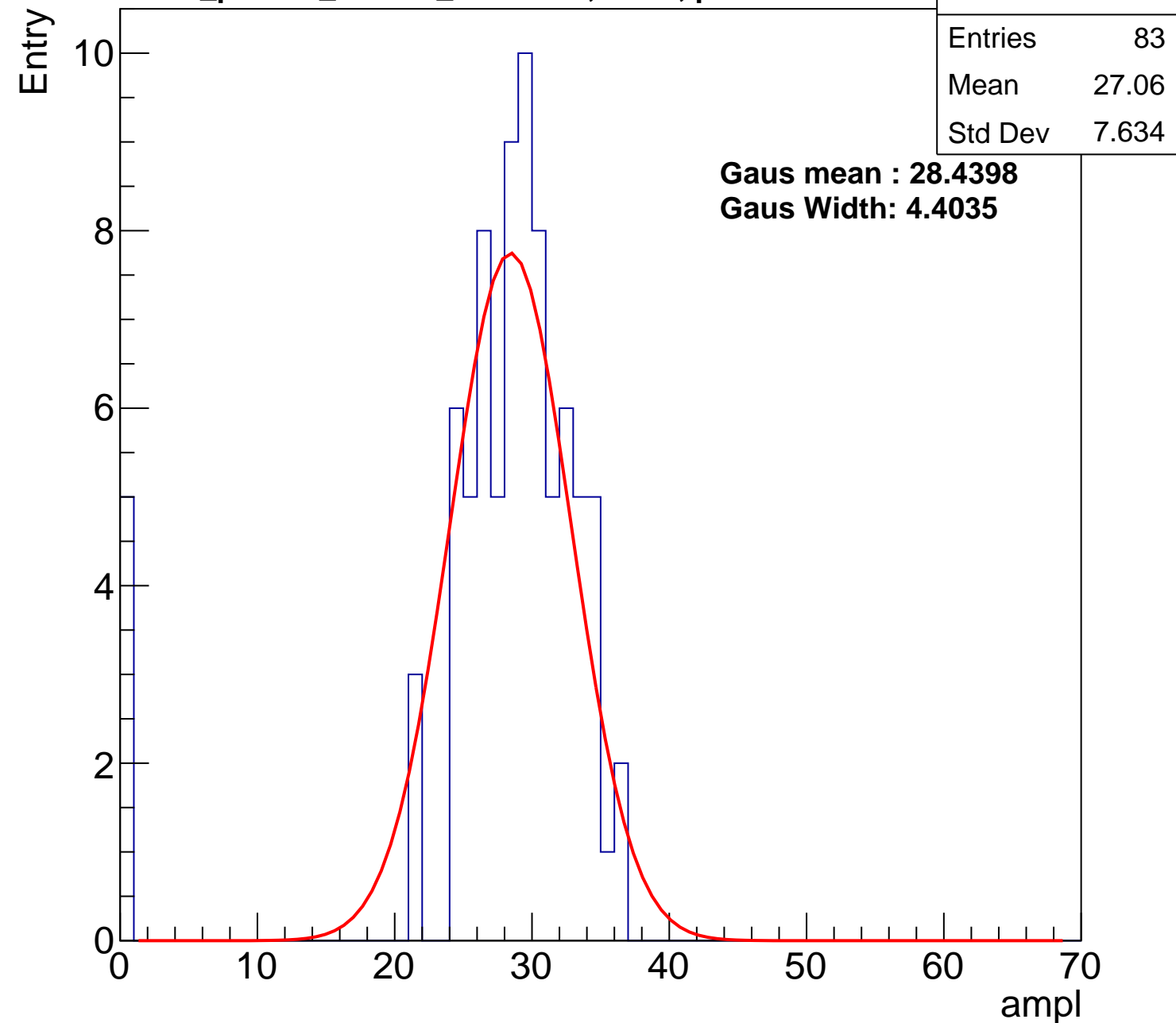
Gaus Width: 4.4035

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch10, adc1

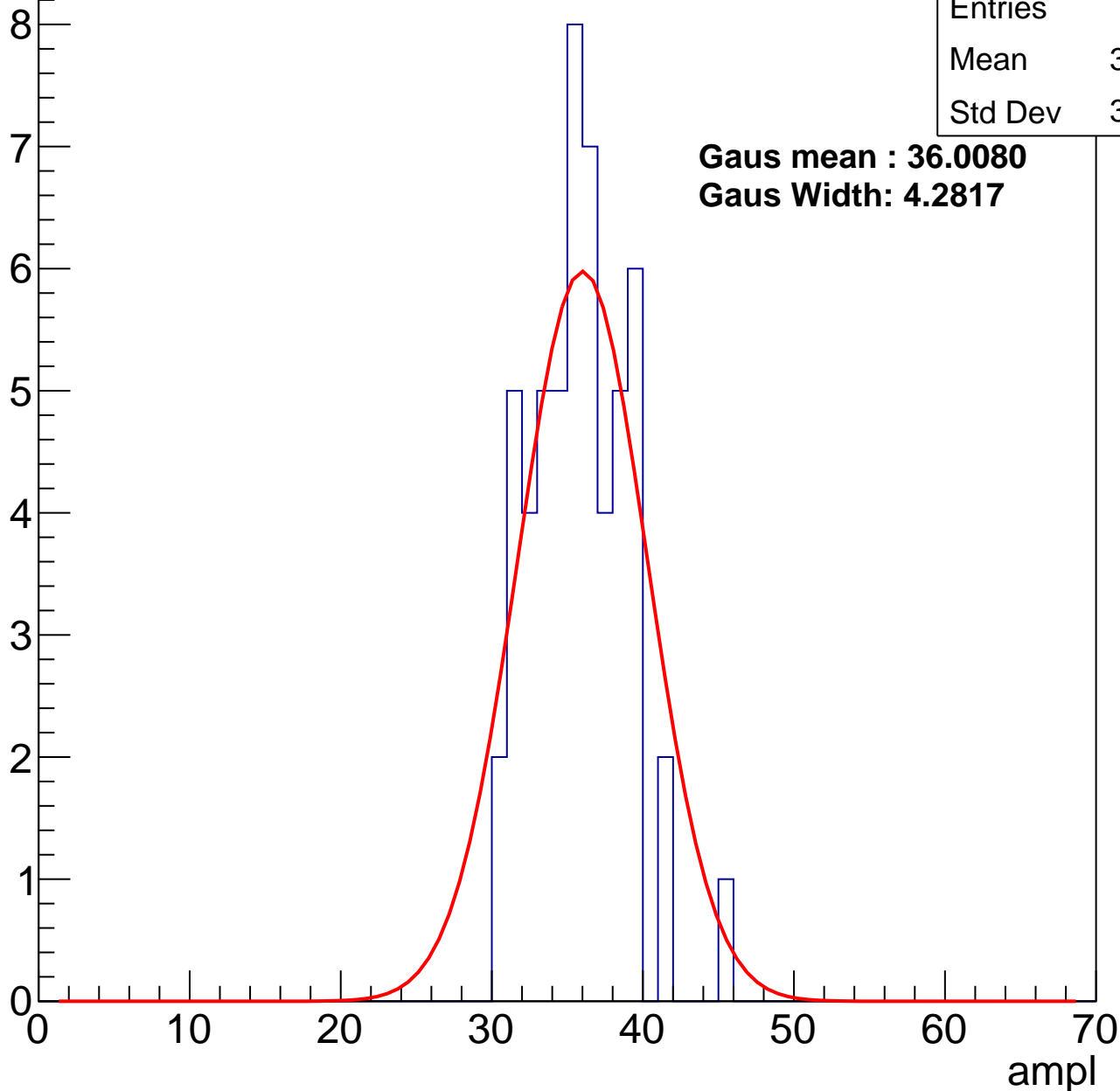
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	35.35
Std Dev	3.104

Gaus mean : 36.0080

Gaus Width: 4.2817



B1L103S, U6-ch10, adc2

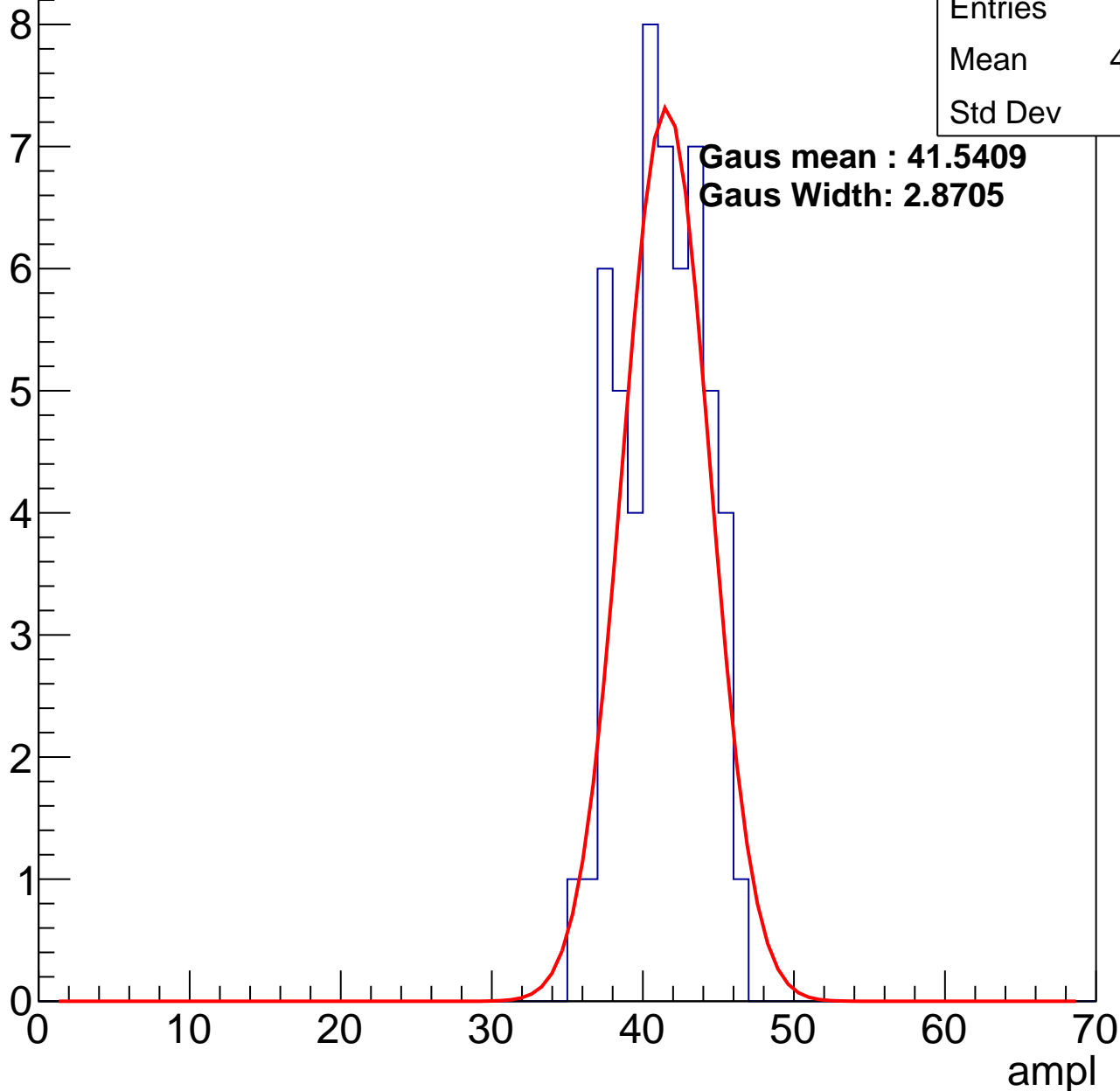
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	40.82
Std Dev	2.67

Gaus mean : 41.5409

Gaus Width: 2.8705

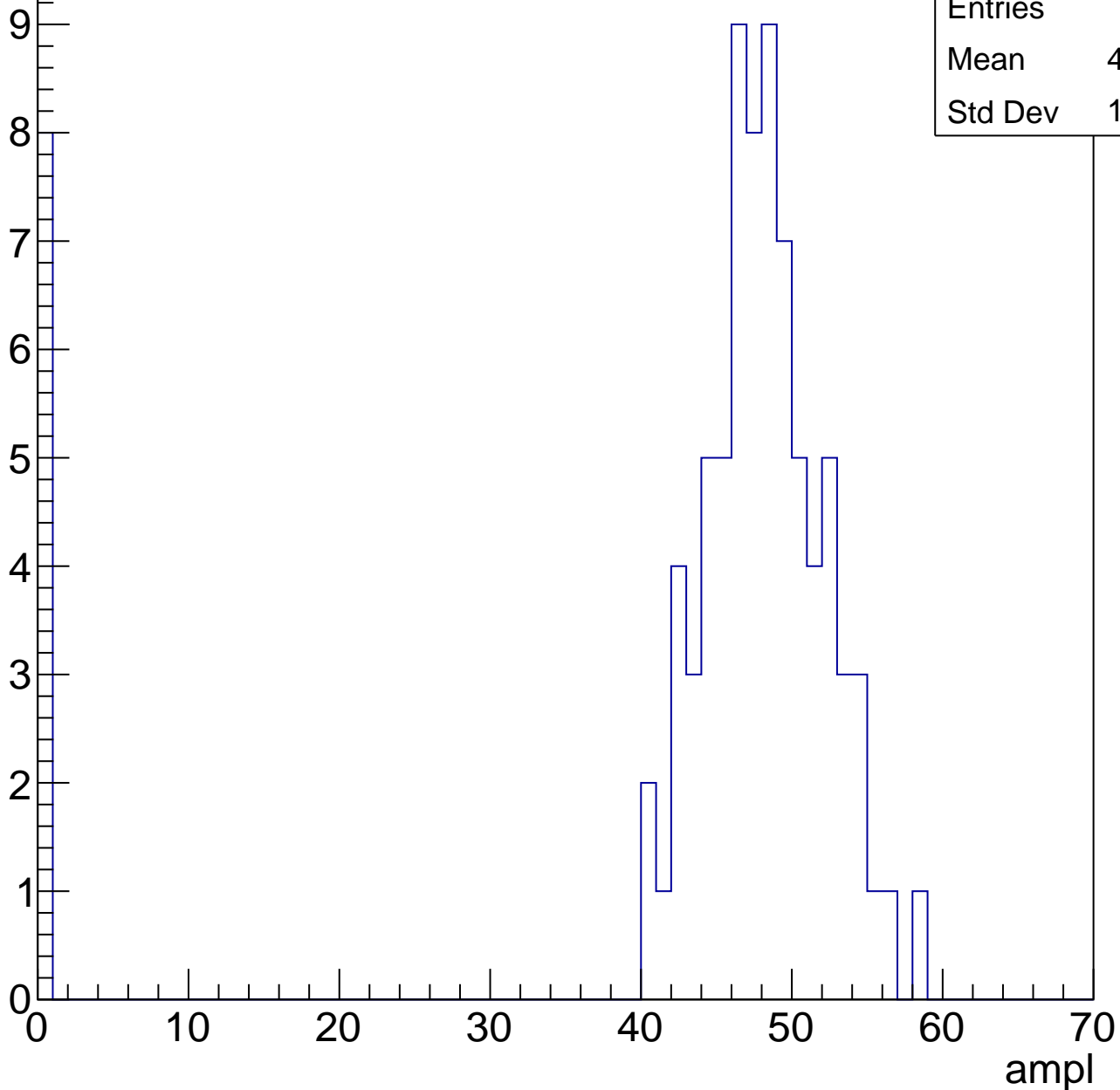


B1L103S, U6-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	43.24
Std Dev	14.49

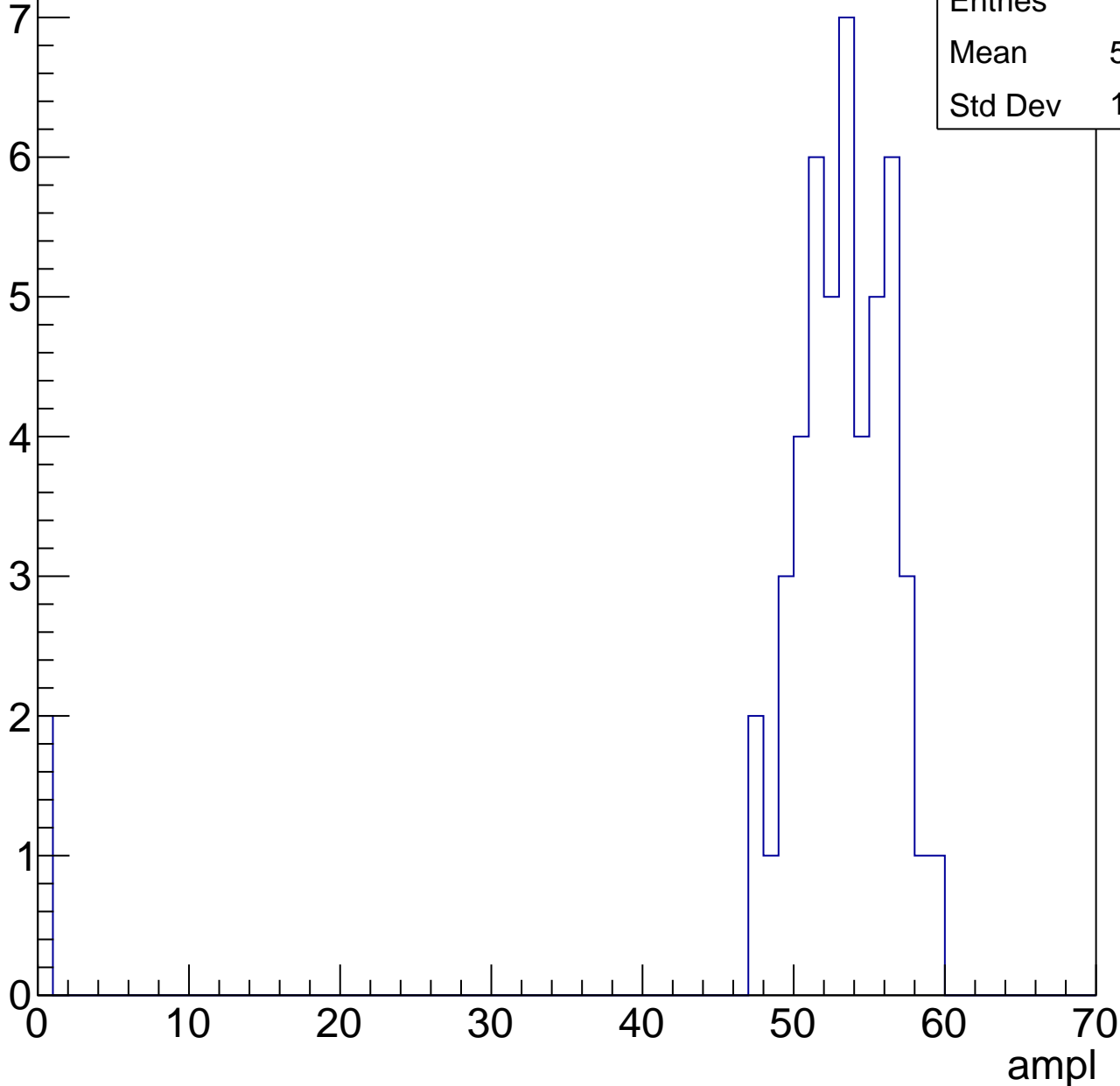


B1L103S, U6-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	50.82
Std Dev	10.75



B1L103S, U6-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

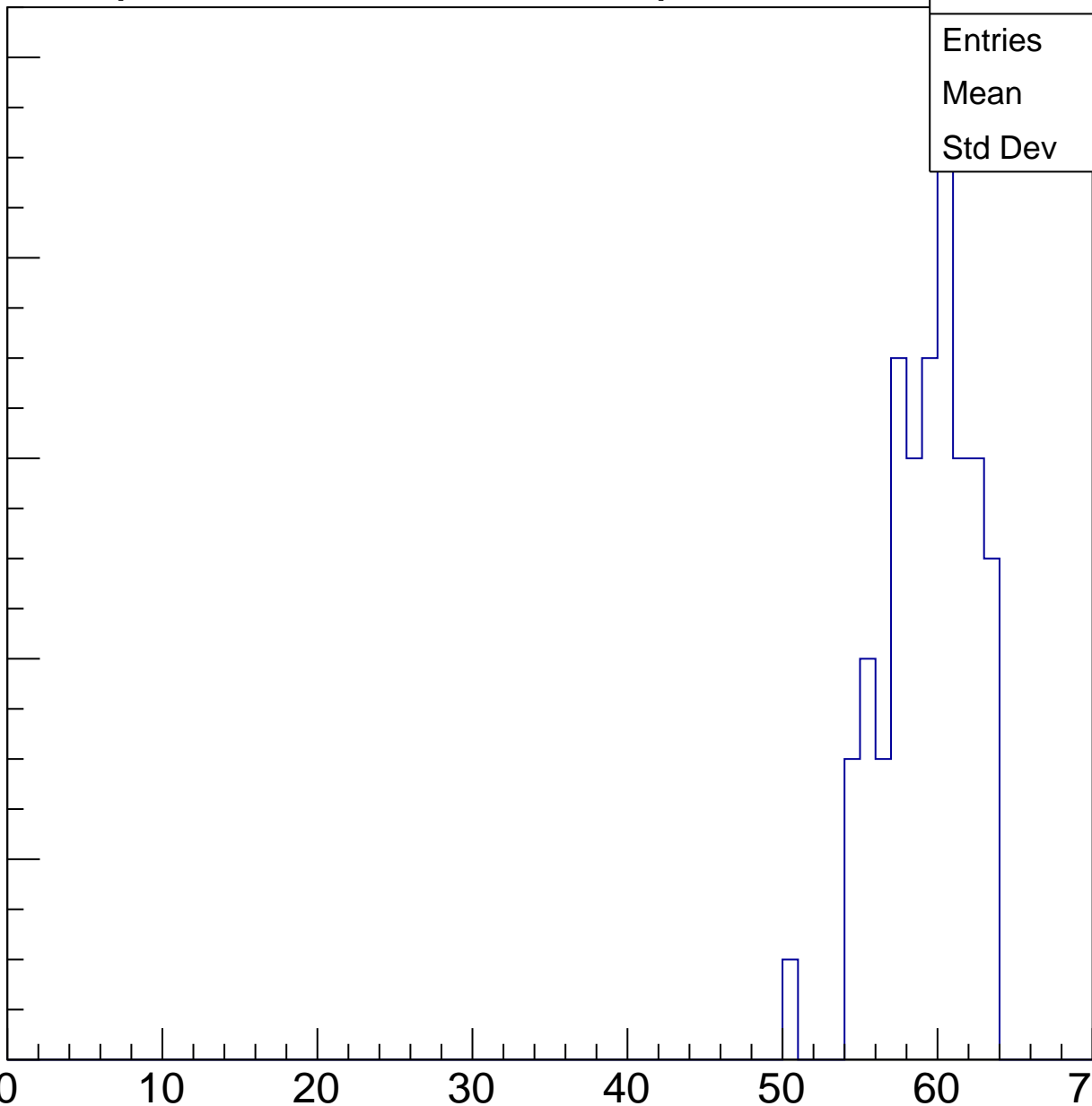
0

Entries	58
---------	----

Mean	58.84
------	-------

Std Dev	2.778
---------	-------

ampl

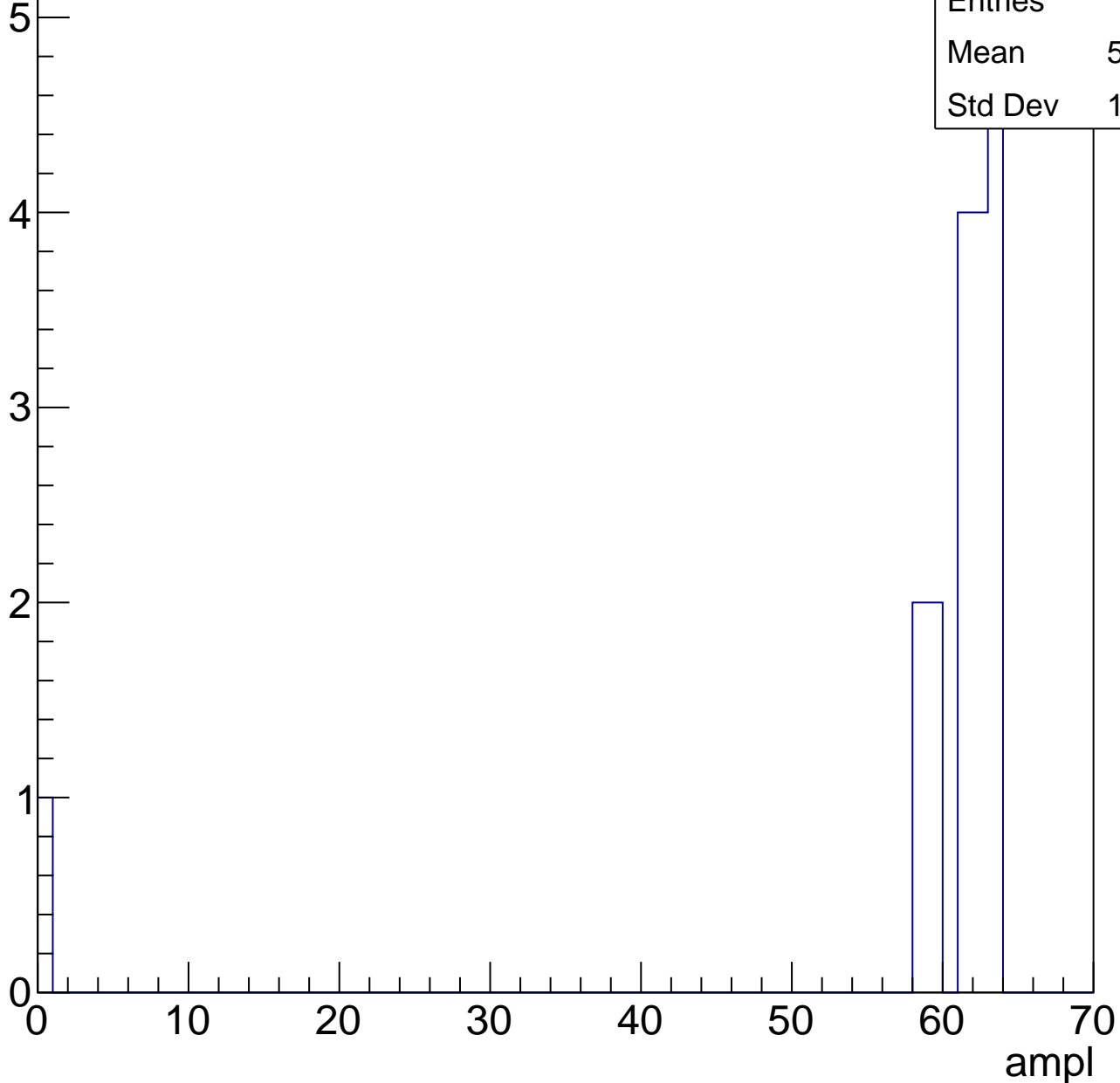


B1L103S, U6-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

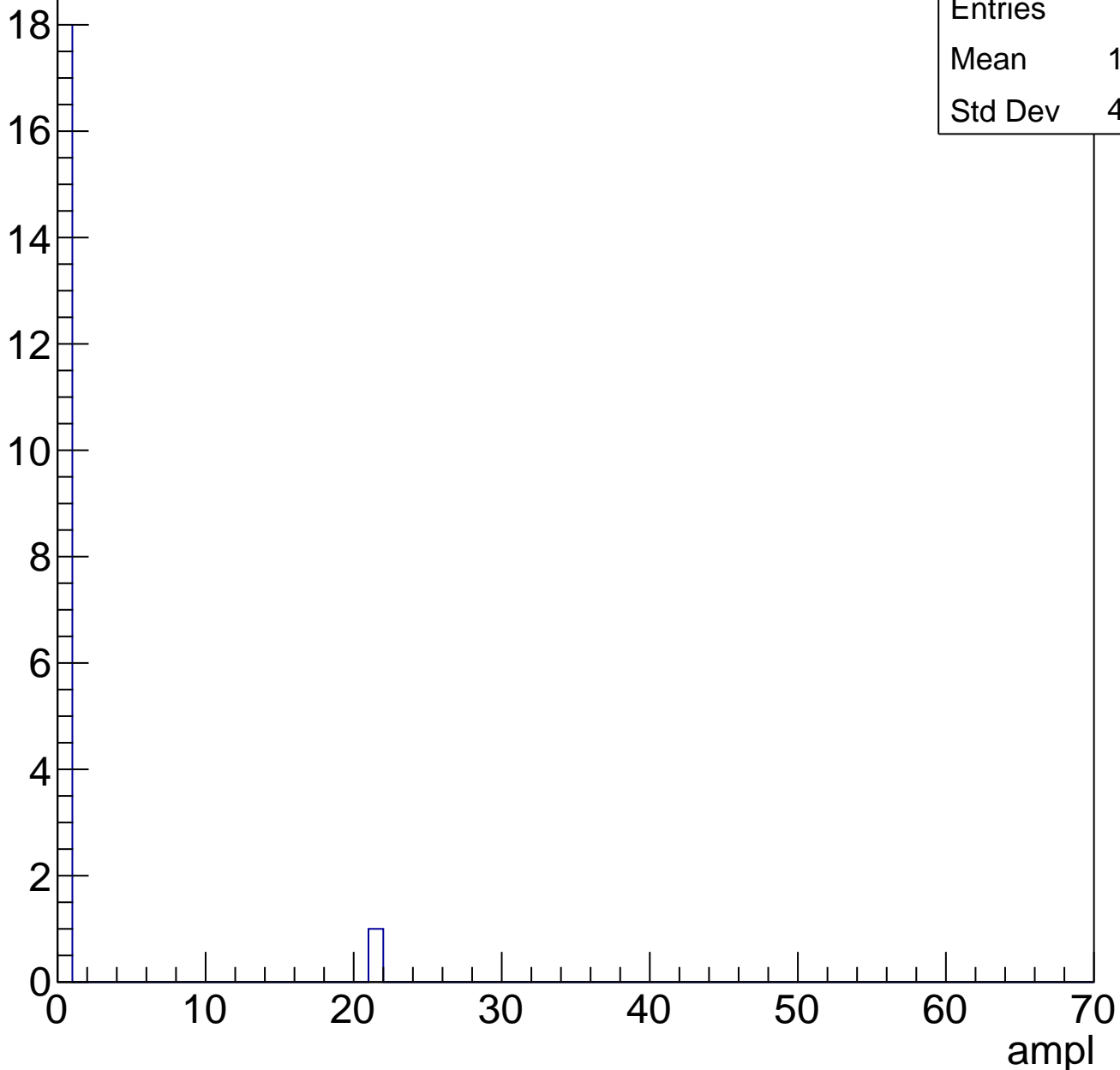
Entries	18
Mean	57.83
Std Dev	14.12



B1L103S, U6-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.105
Std Dev	4.689

B1L103S, U6-ch11, adc0

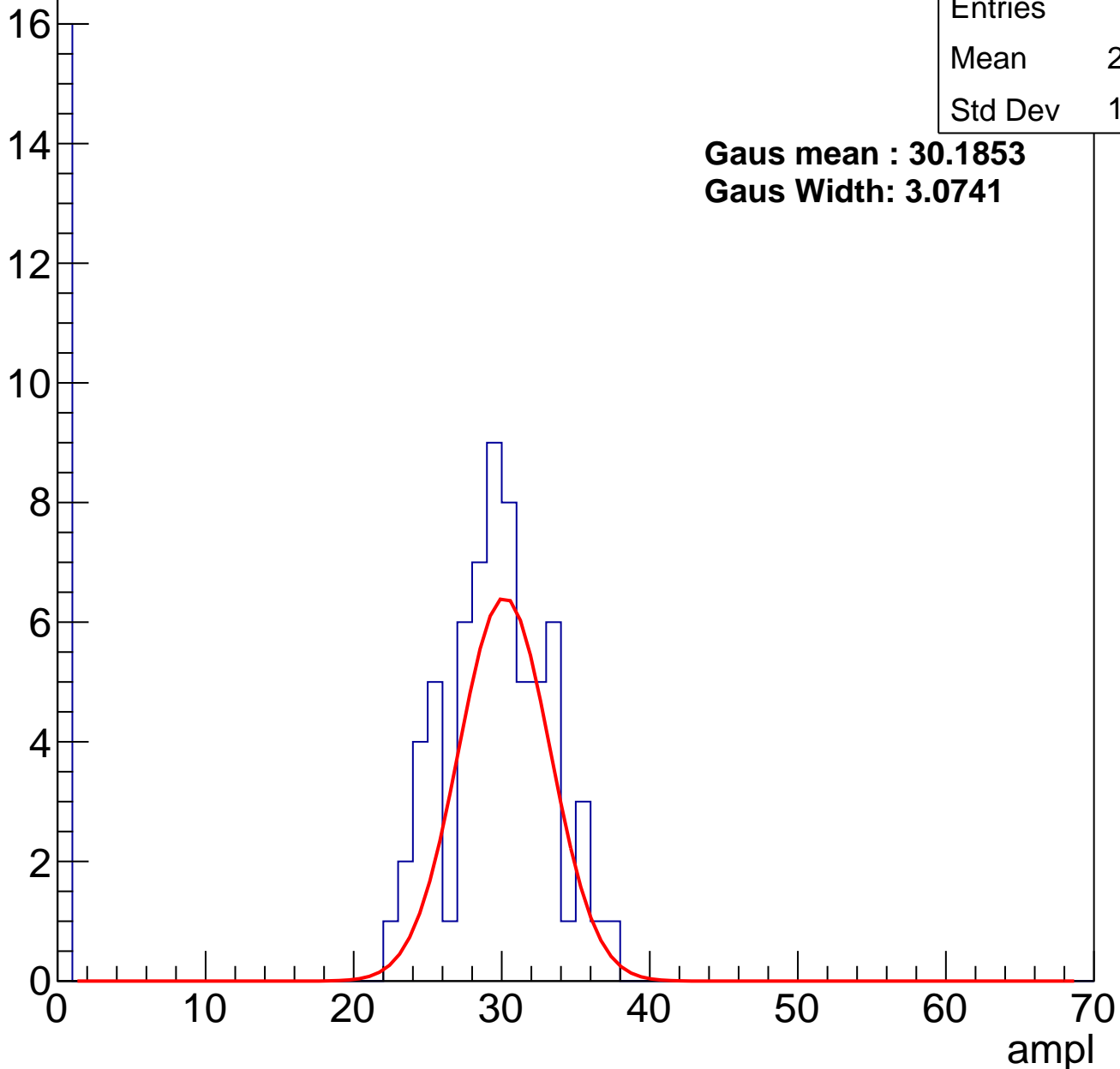
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.44
Std Dev	12.02

Gaus mean : 30.1853

Gaus Width: 3.0741

Entry



B1L103S, U6-ch11, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	31.21
Std Dev	13.14

Gaus mean : 37.0690

Gaus Width: 3.7417

Entry

12

10

8

6

4

2

0

0

10

20

30

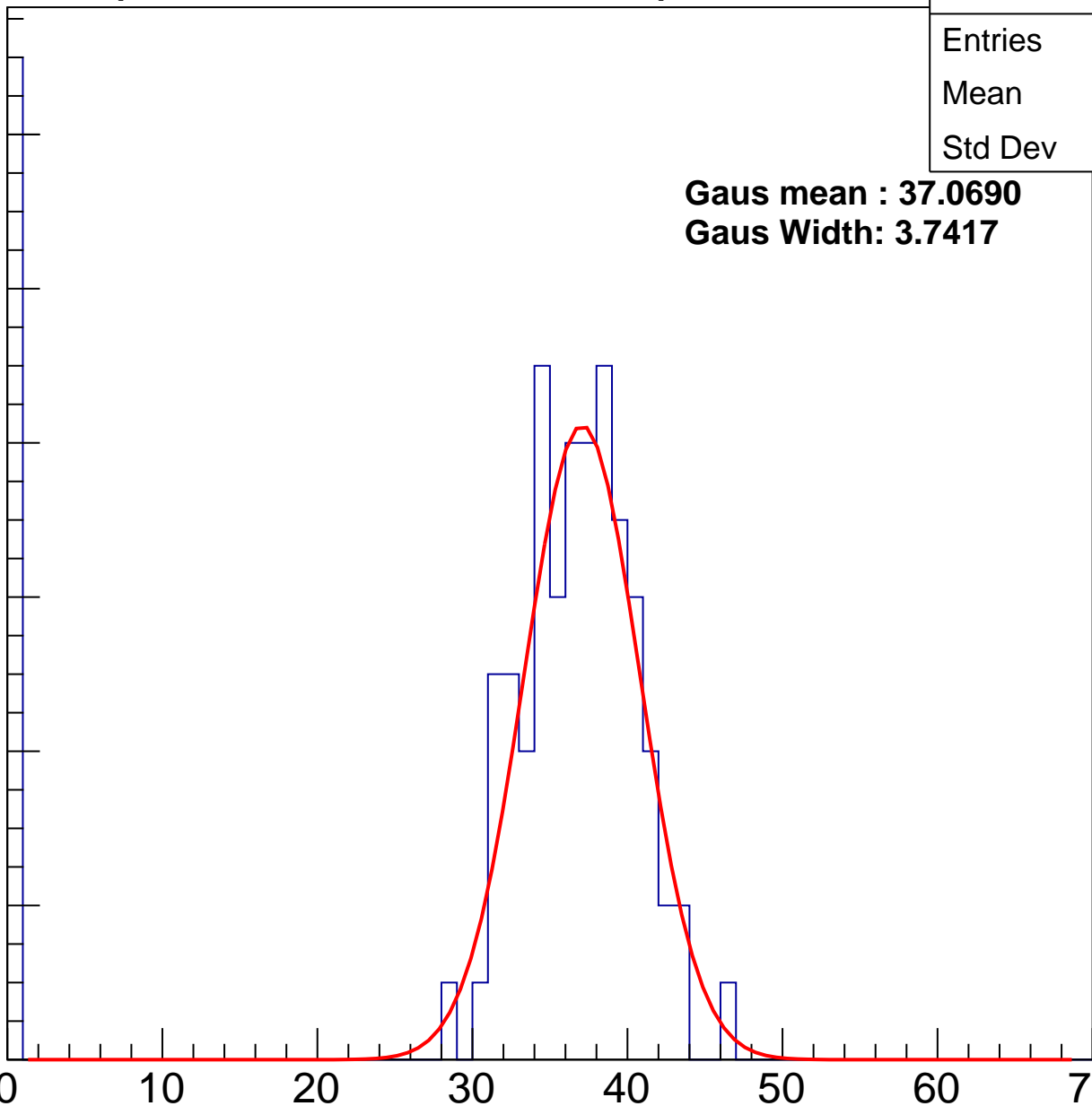
40

50

60

70

ampl



B1L103S, U6-ch11, adc2

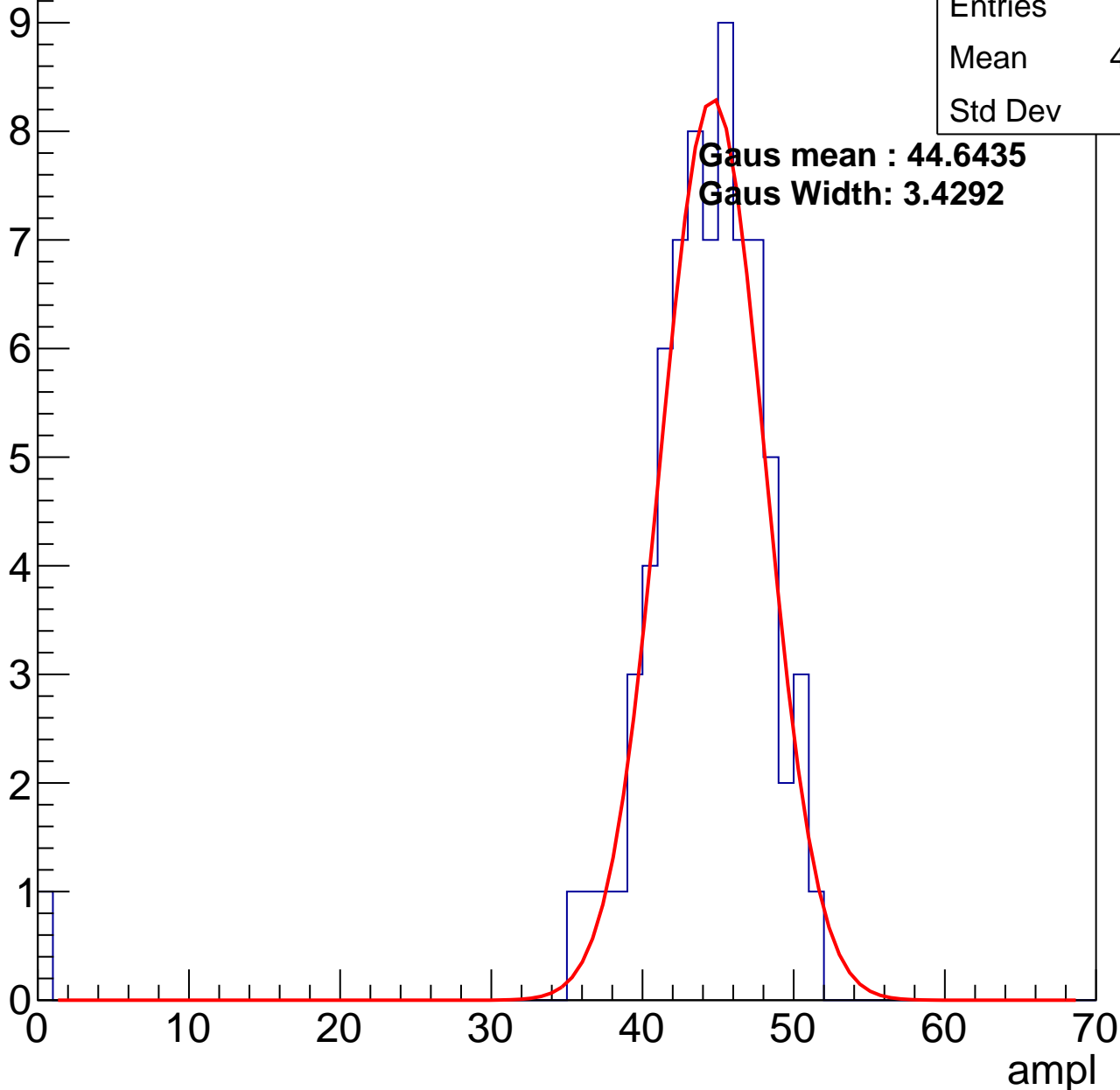
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	43.38
Std Dev	6.1

Gaus mean : 44.6435

Gaus Width: 3.4292

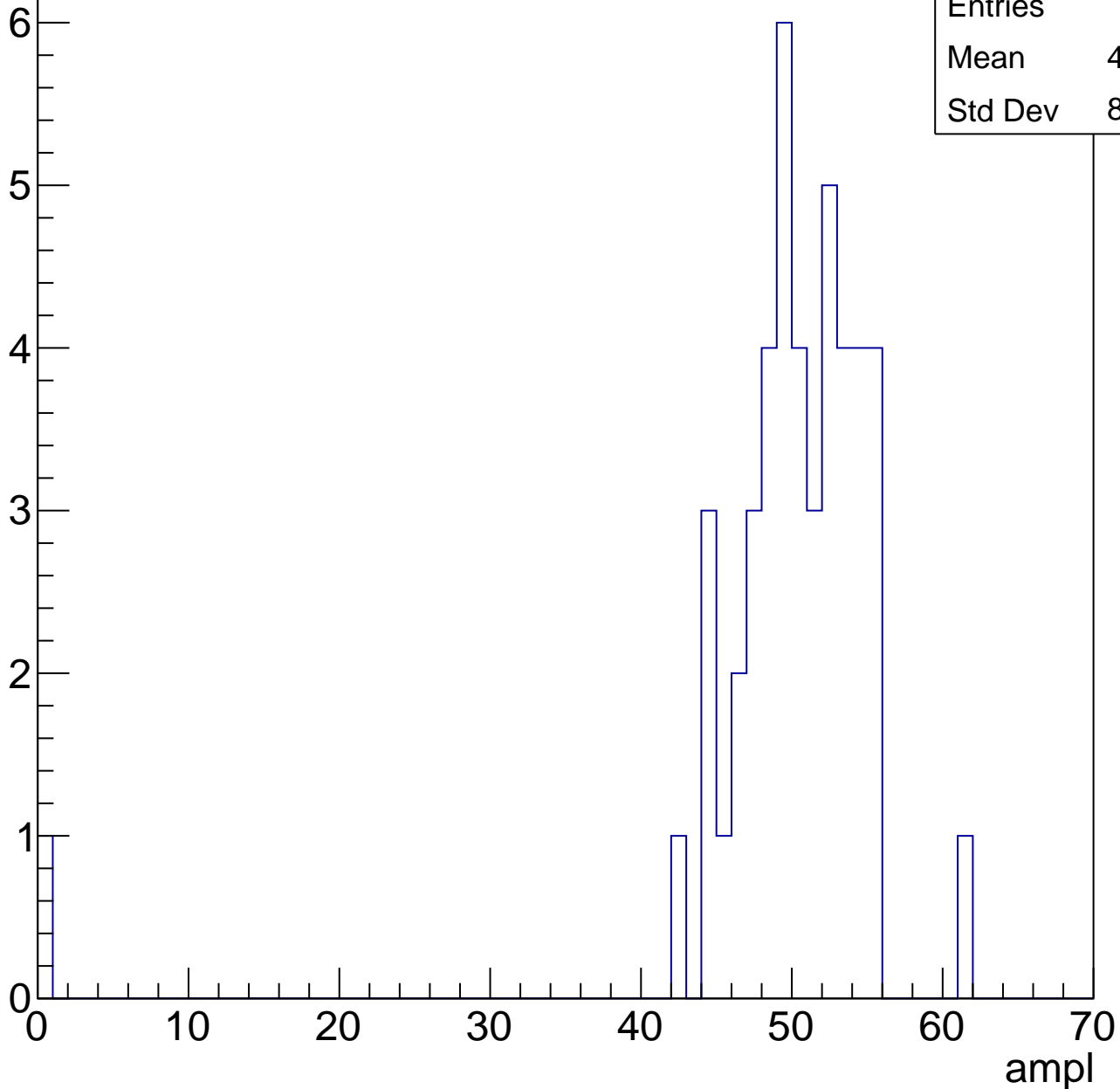


B1L103S, U6-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	49.13
Std Dev	8.192

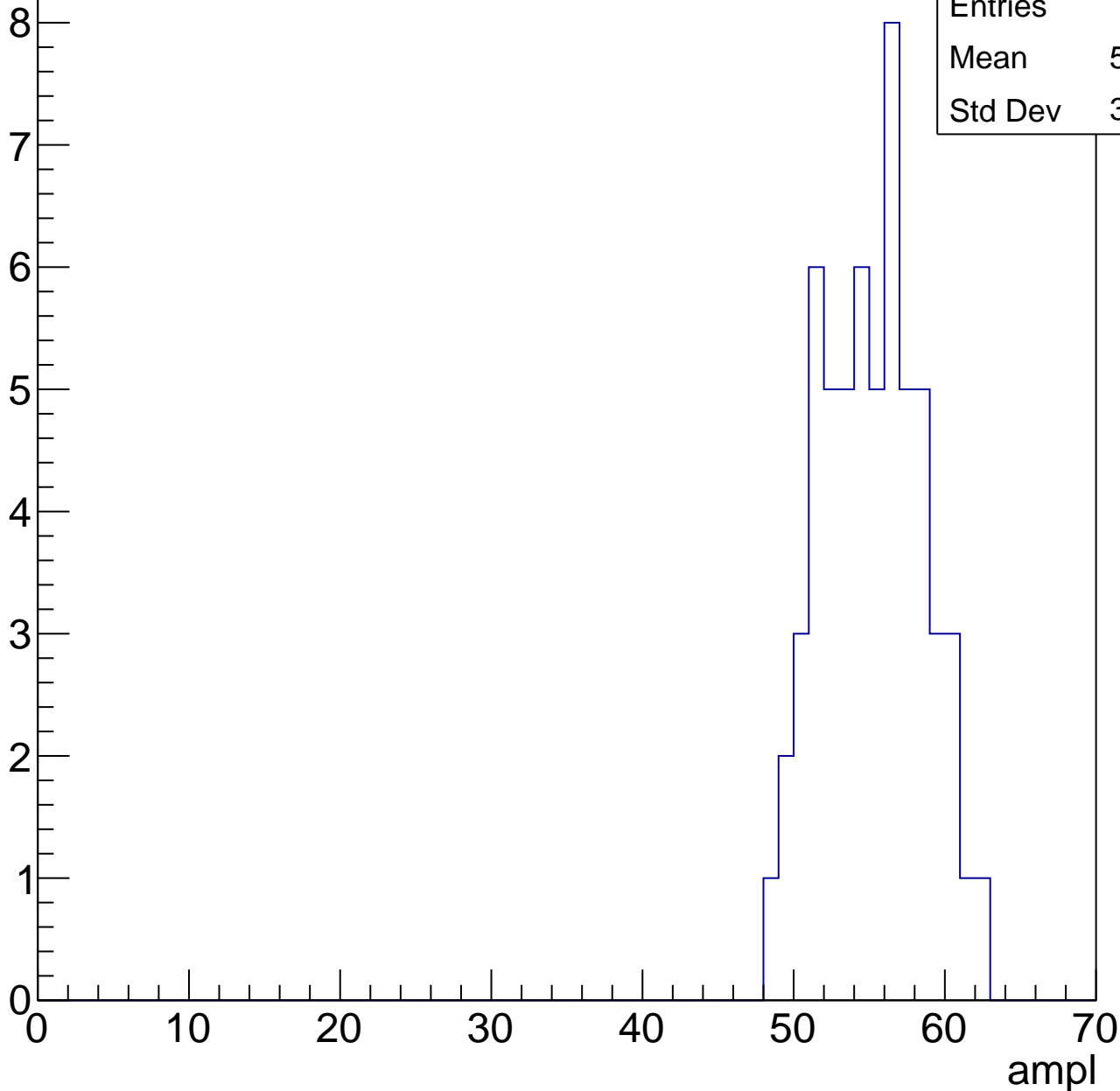


B1L103S, U6-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.73
Std Dev	3.293

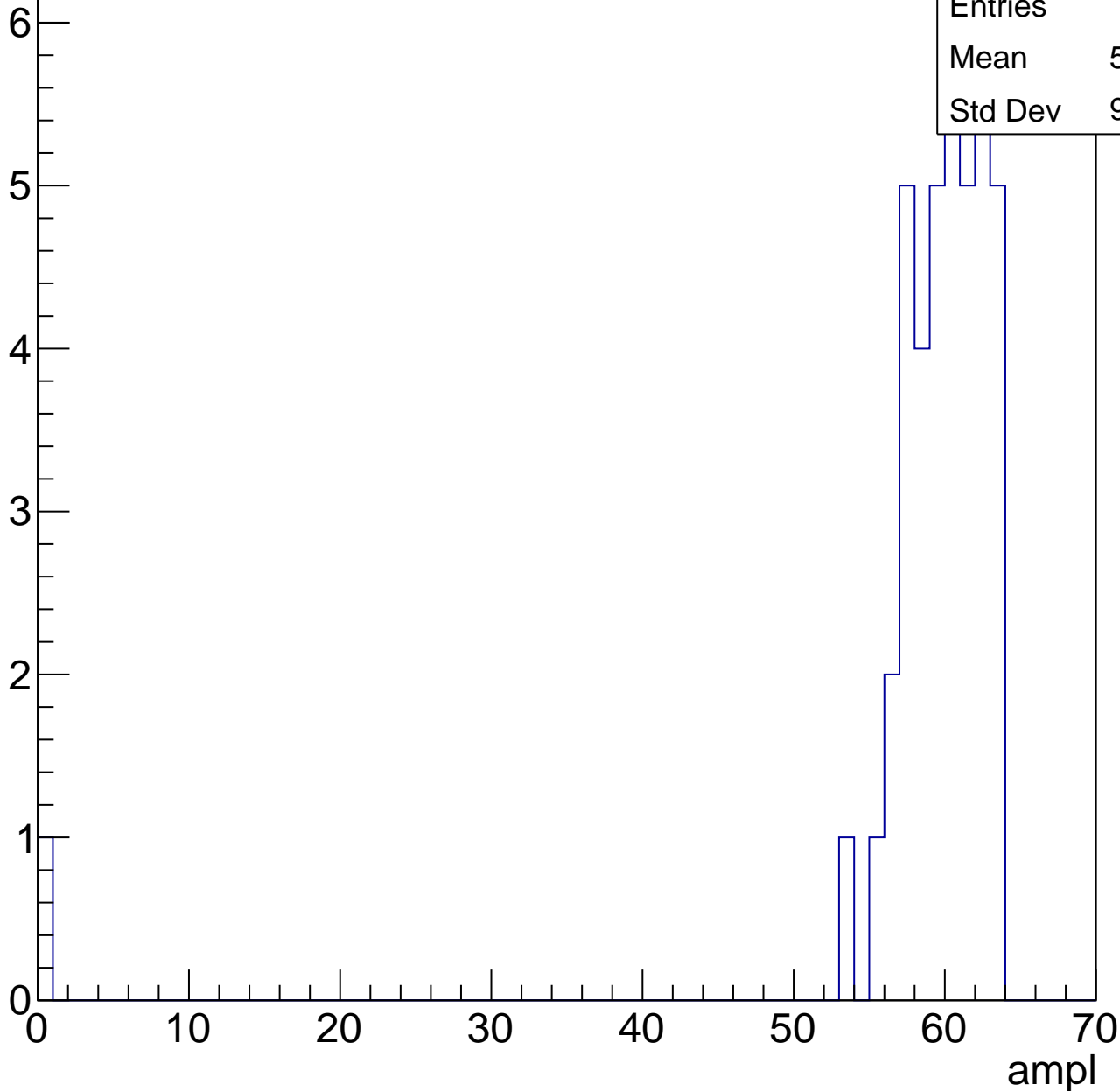


B1L103S, U6-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.15
Std Dev	9.506

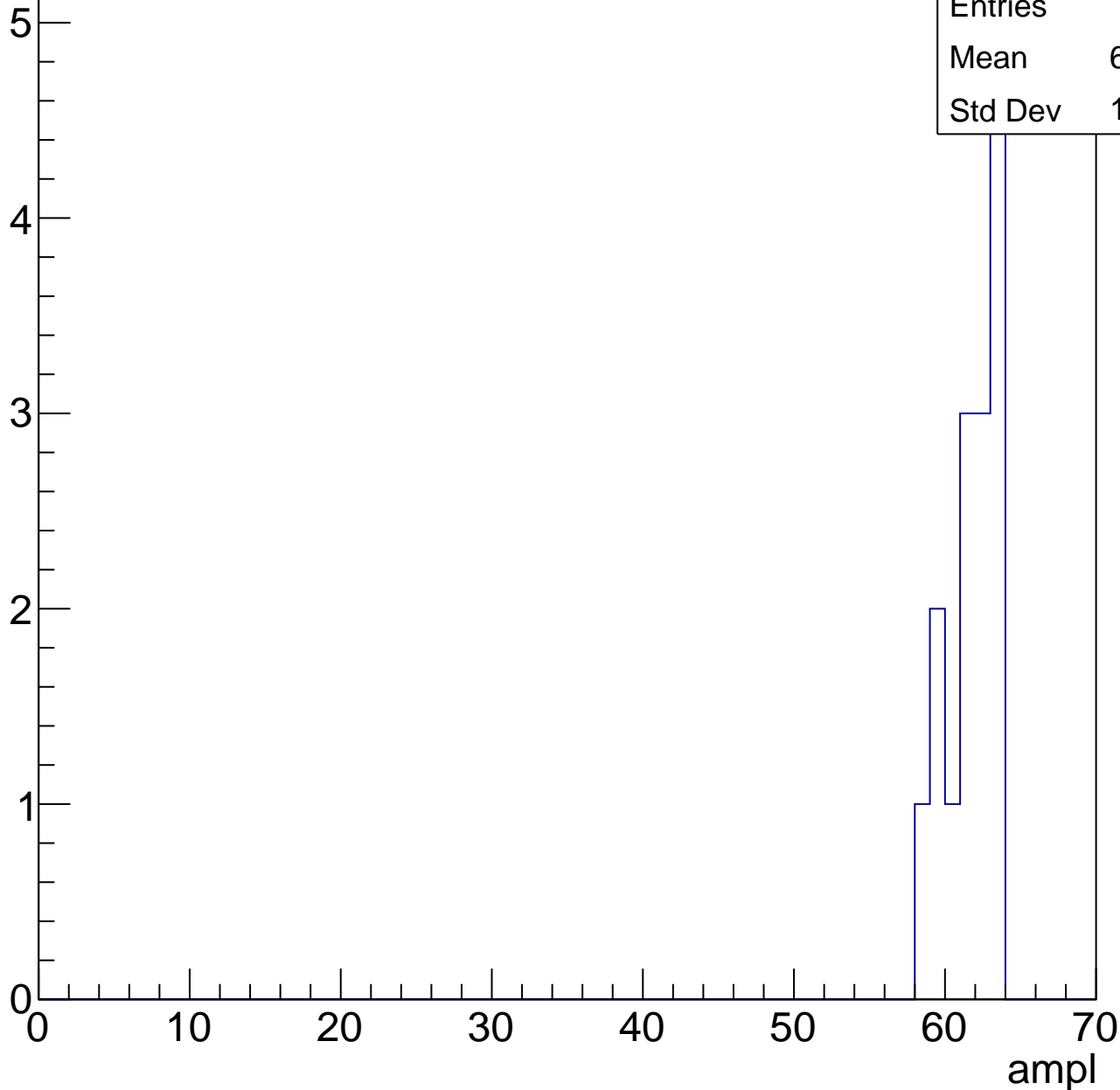


B1L103S, U6-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.33
Std Dev	1.619



B1L103S, U6-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch12, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	24.51
Std Dev	10.81

Gaus mean : 29.5996

Gaus Width: 4.2681

Entry

12

10

8

6

4

2

0

0

10

20

30

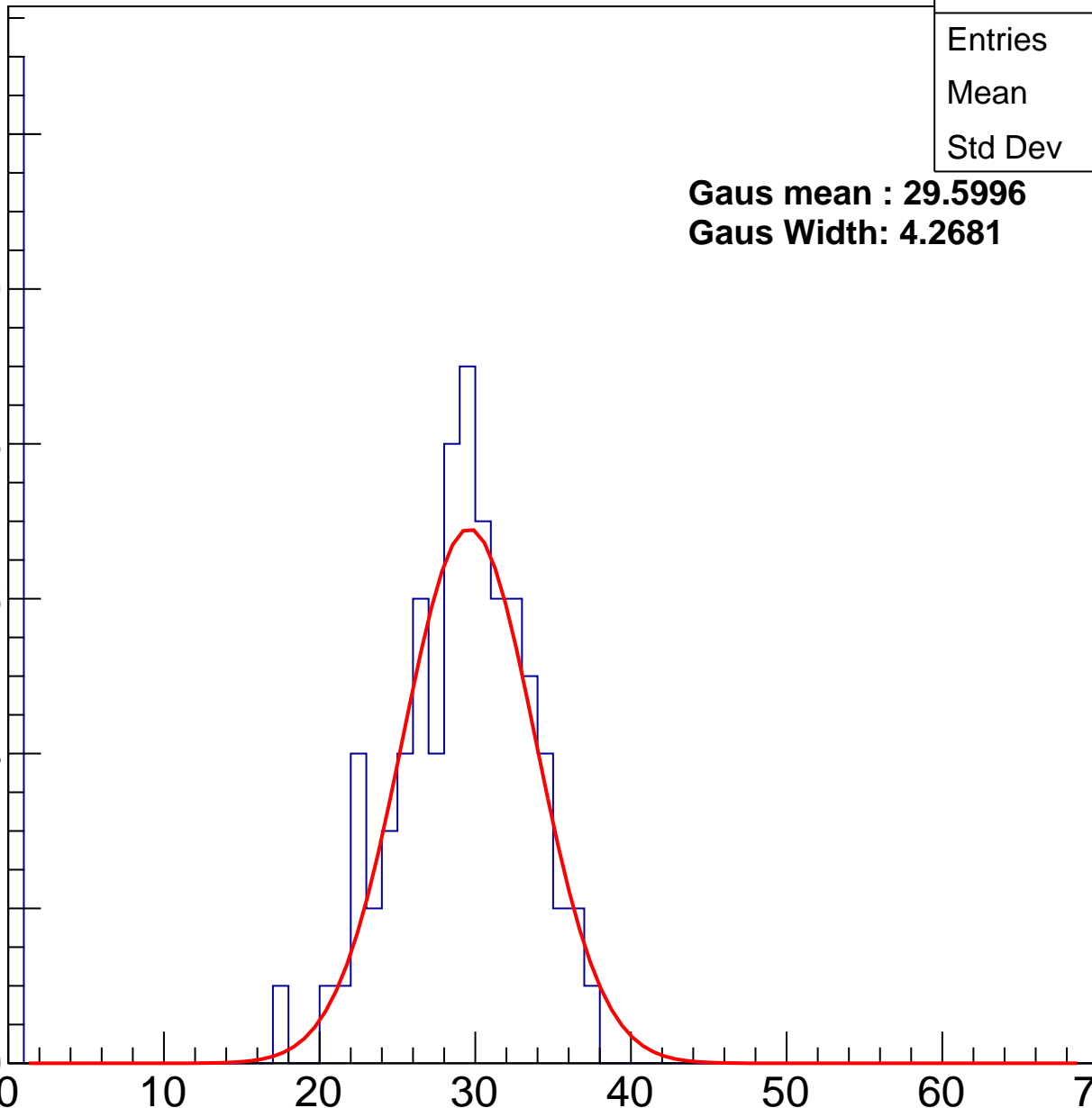
40

50

60

70

ampl



B1L103S, U6-ch12, adc1

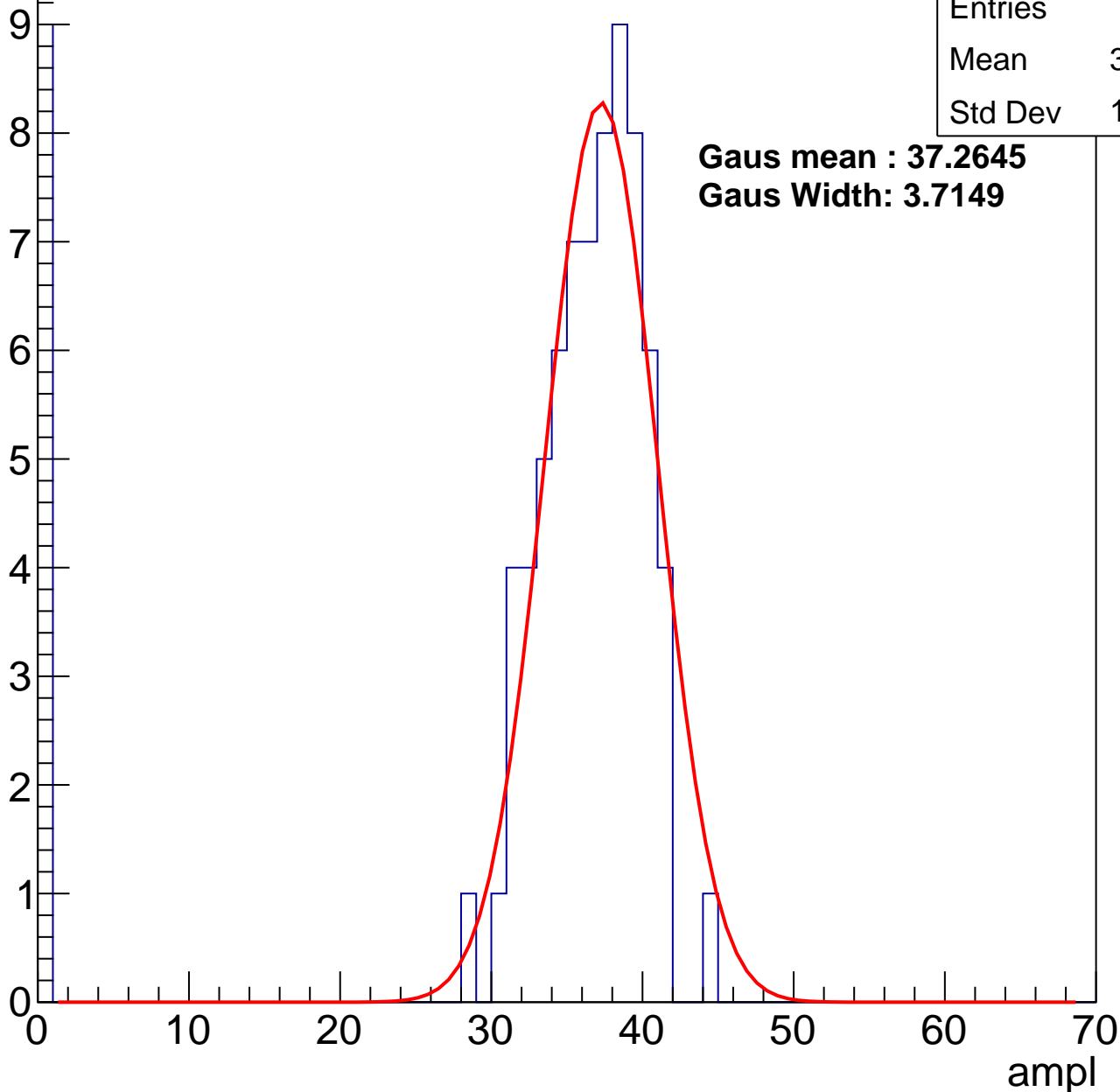
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	32.17
Std Dev	11.84

Gaus mean : 37.2645

Gaus Width: 3.7149



B1L103S, U6-ch12, adc2

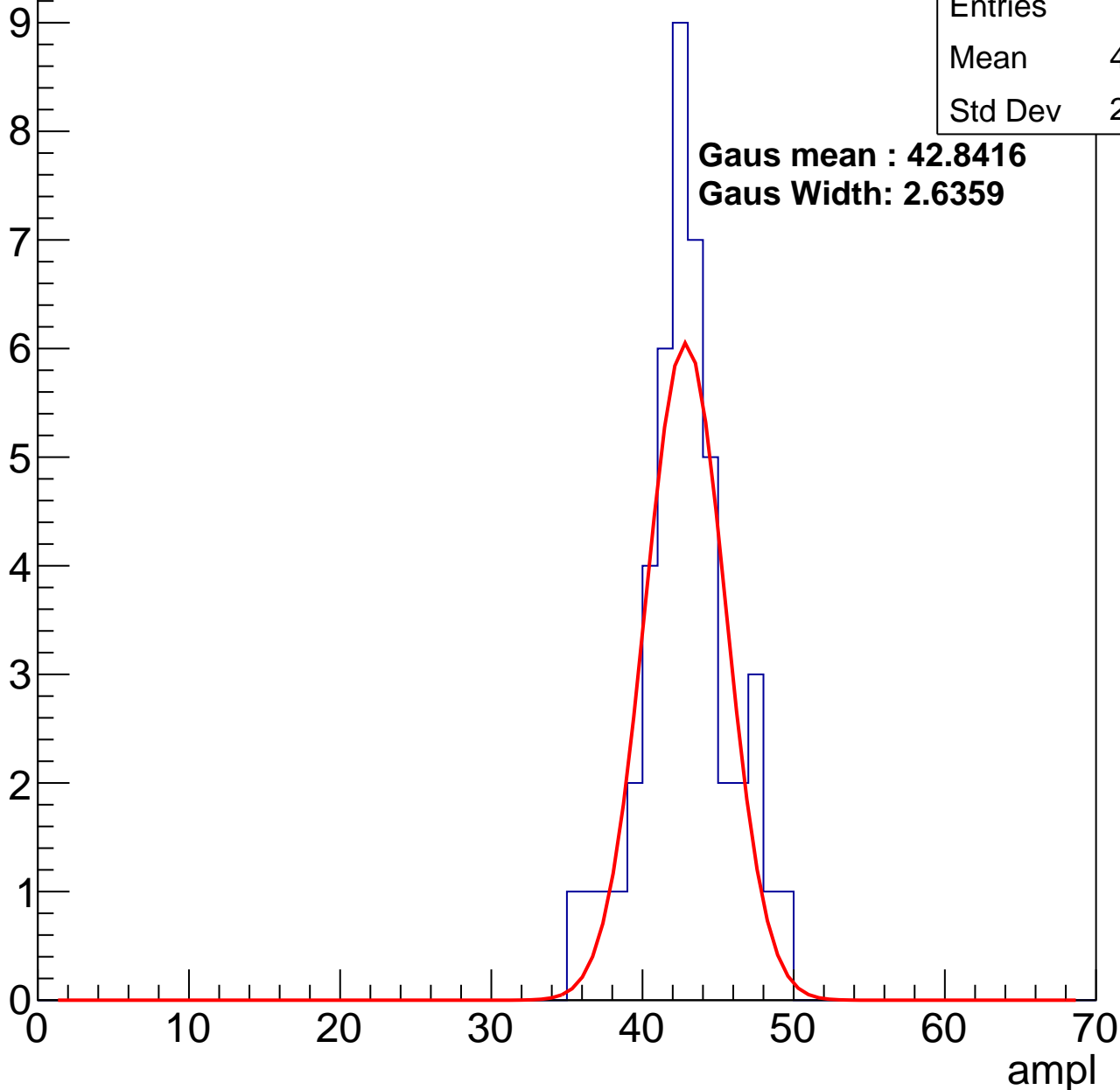
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	42.37
Std Dev	2.937

Gaus mean : 42.8416

Gaus Width: 2.6359

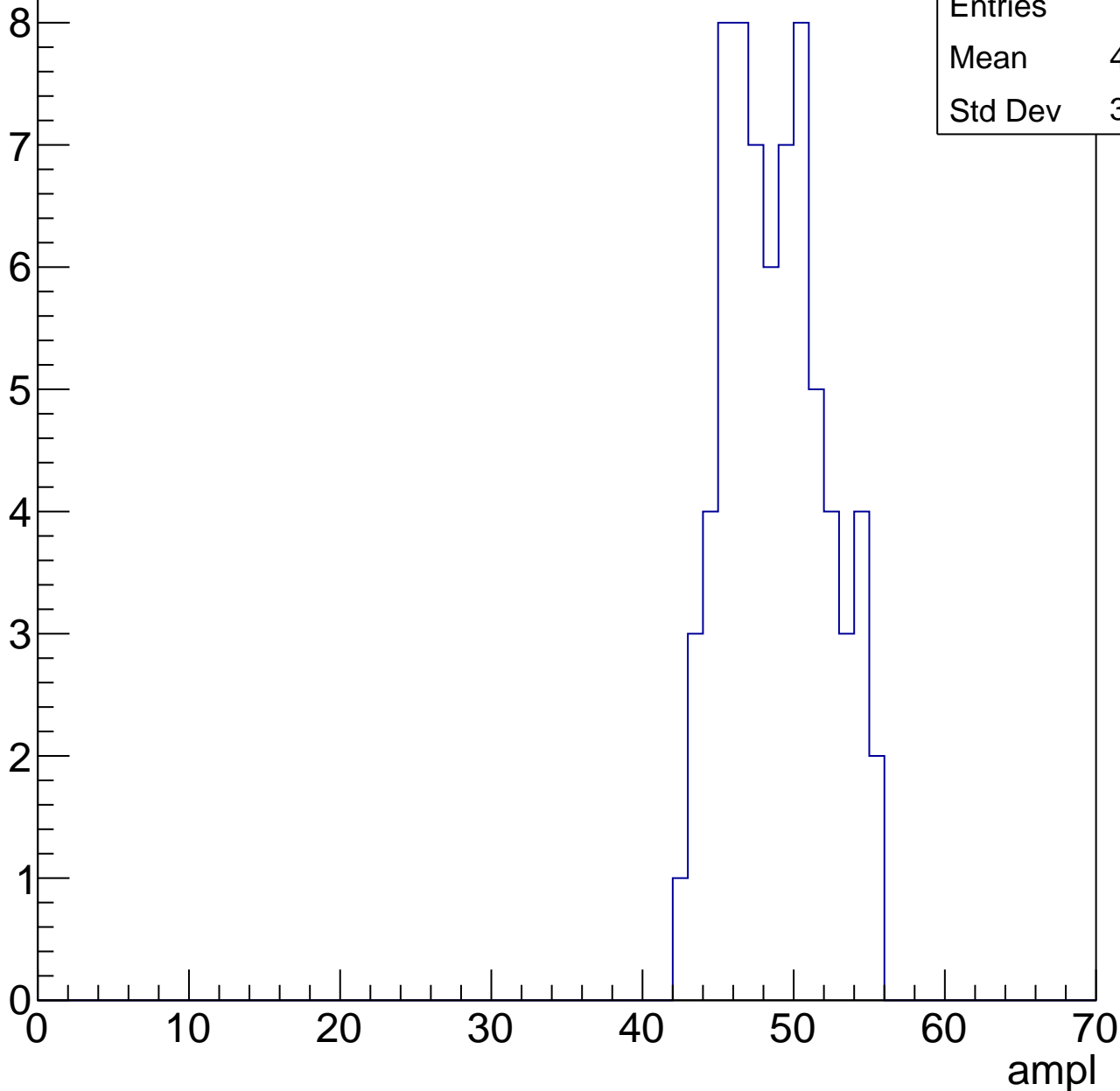


B1L103S, U6-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.33
Std Dev	3.259

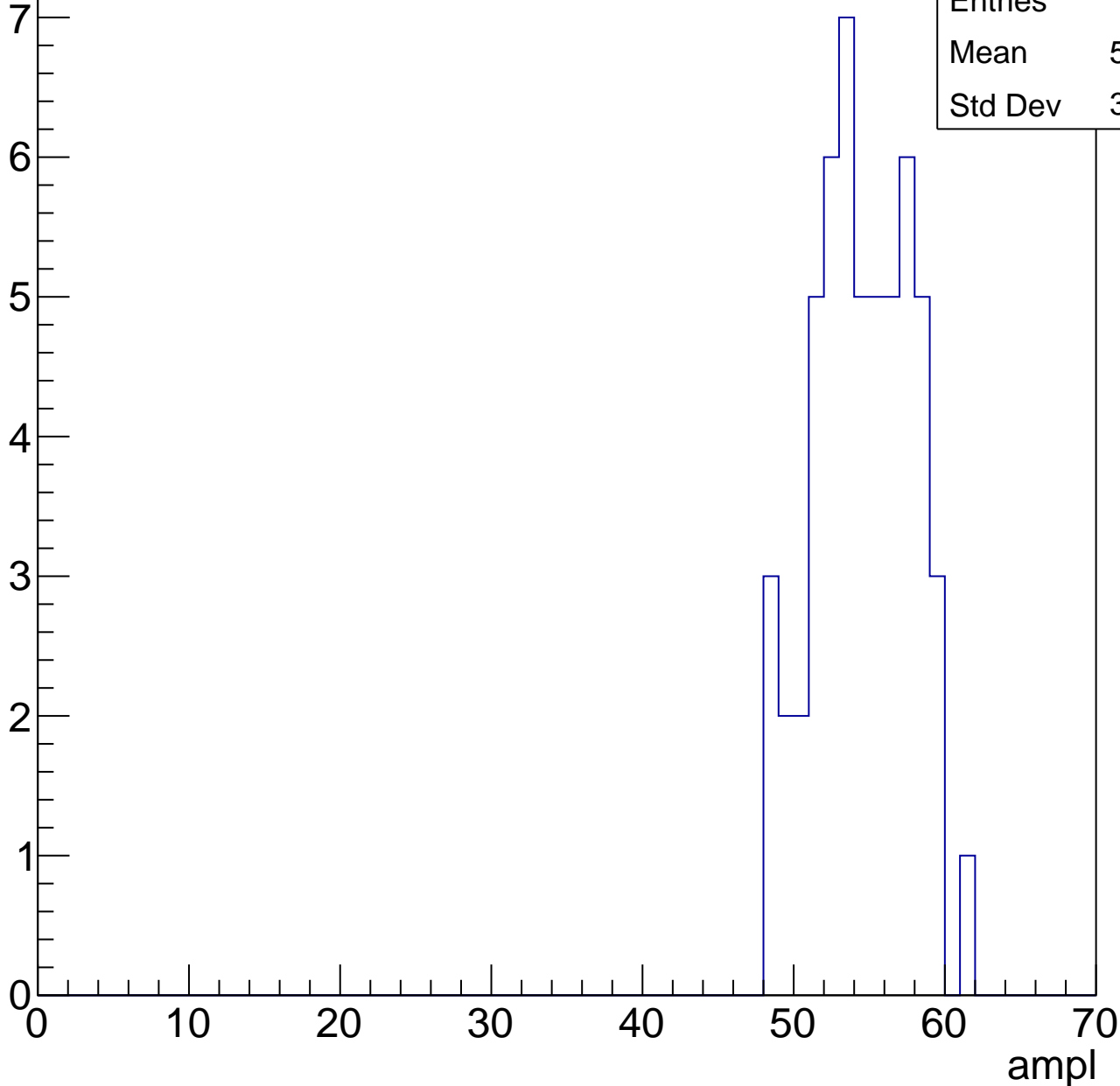


B1L103S, U6-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.09
Std Dev	3.158

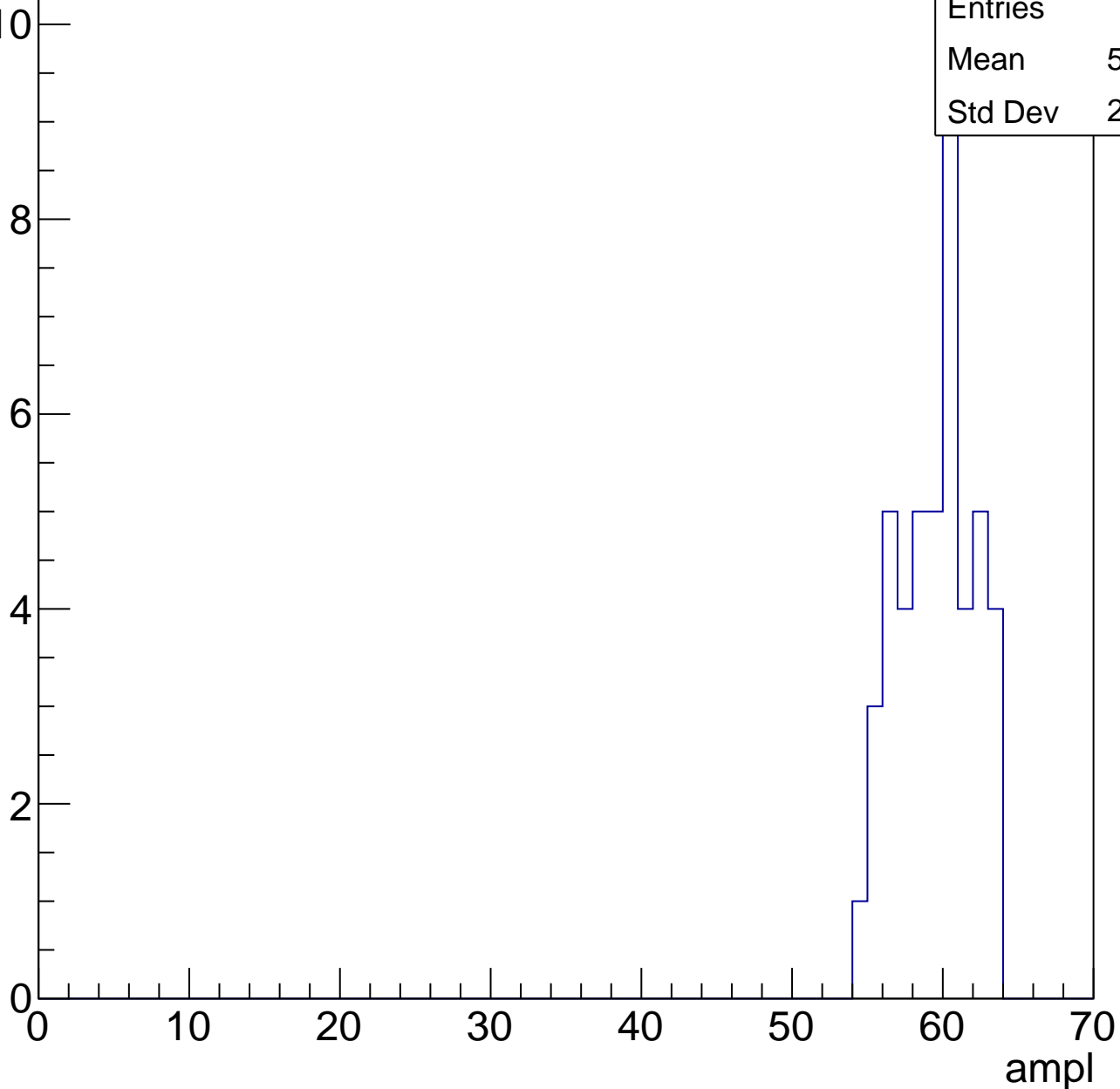


B1L103S, U6-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	59.09
Std Dev	2.439

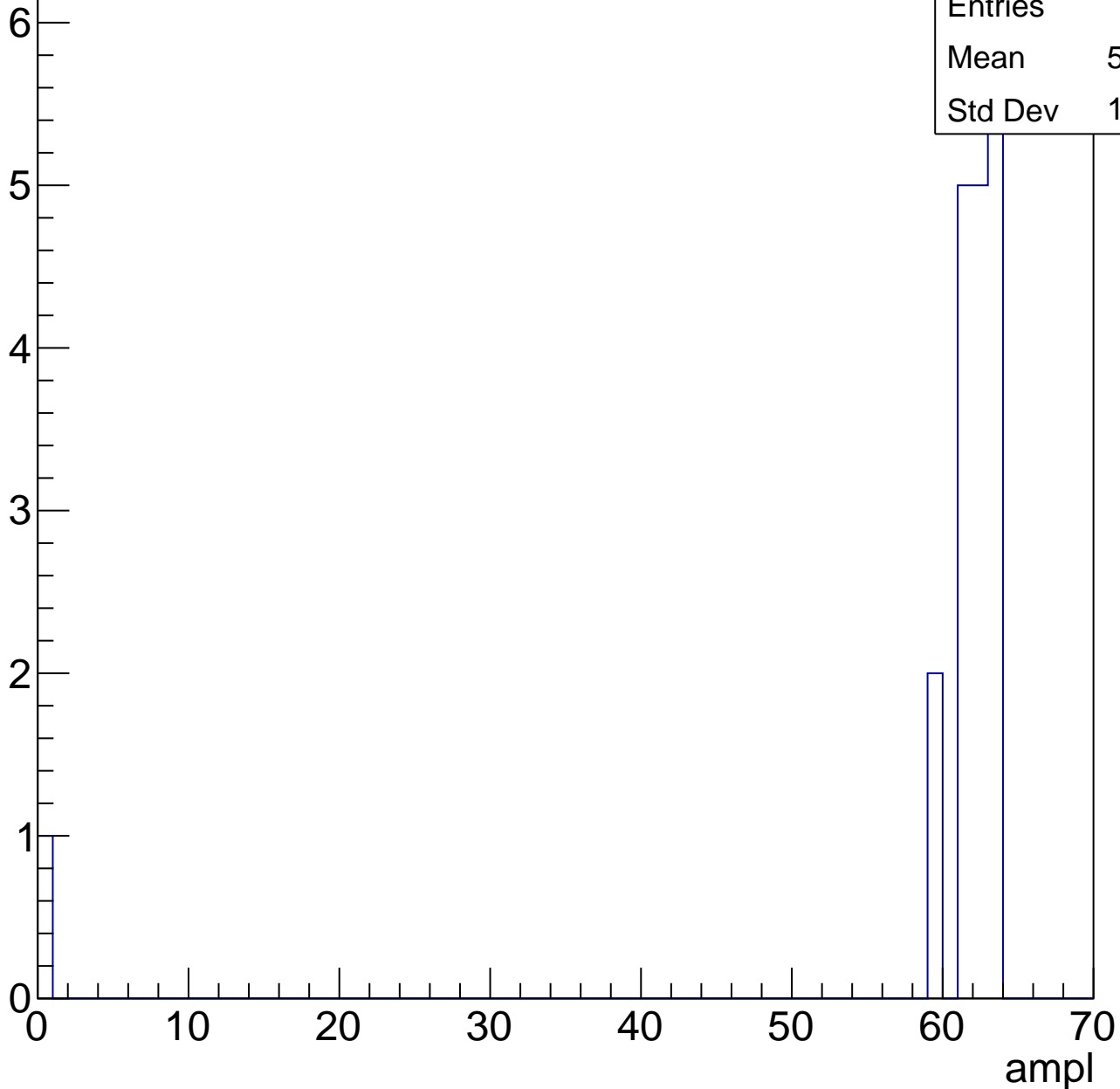


B1L103S, U6-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.47
Std Dev	13.83



B1L103S, U6-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch13, adc0

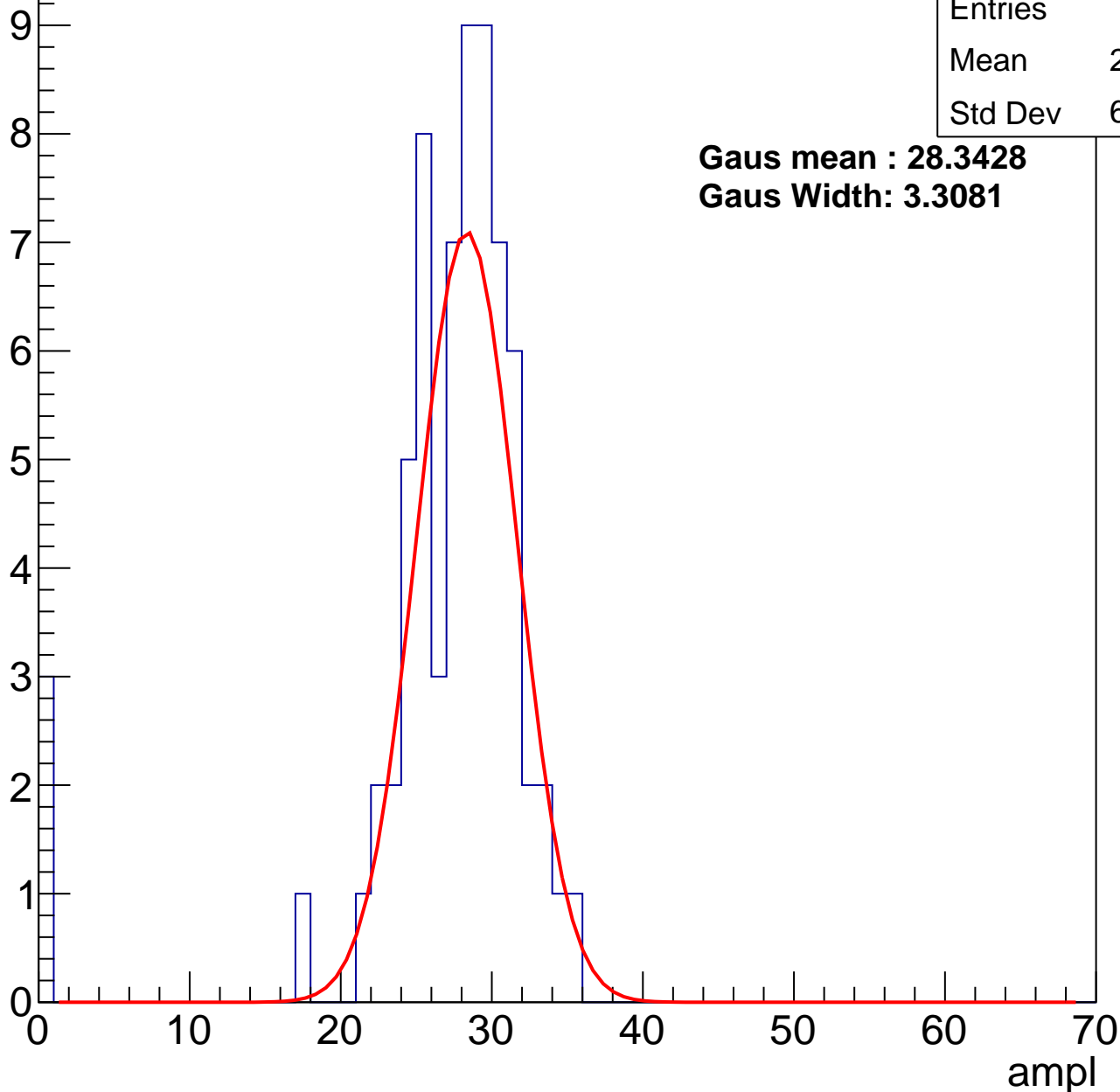
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	26.42
Std Dev	6.484

Gaus mean : 28.3428

Gaus Width: 3.3081



B1L103S, U6-ch13, adc1

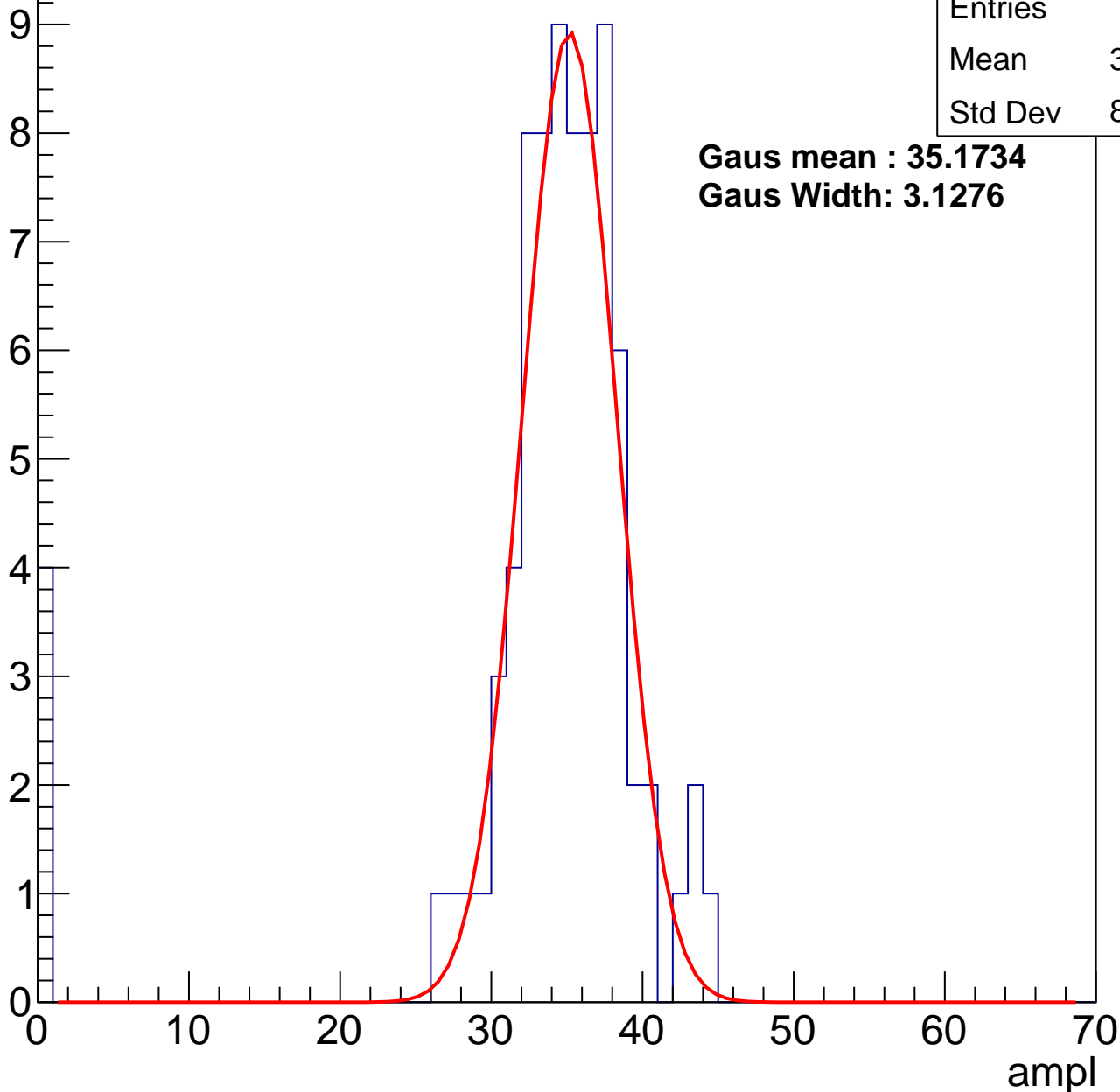
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	33.03
Std Dev	8.353

Gaus mean : 35.1734

Gaus Width: 3.1276



B1L103S, U6-ch13, adc2

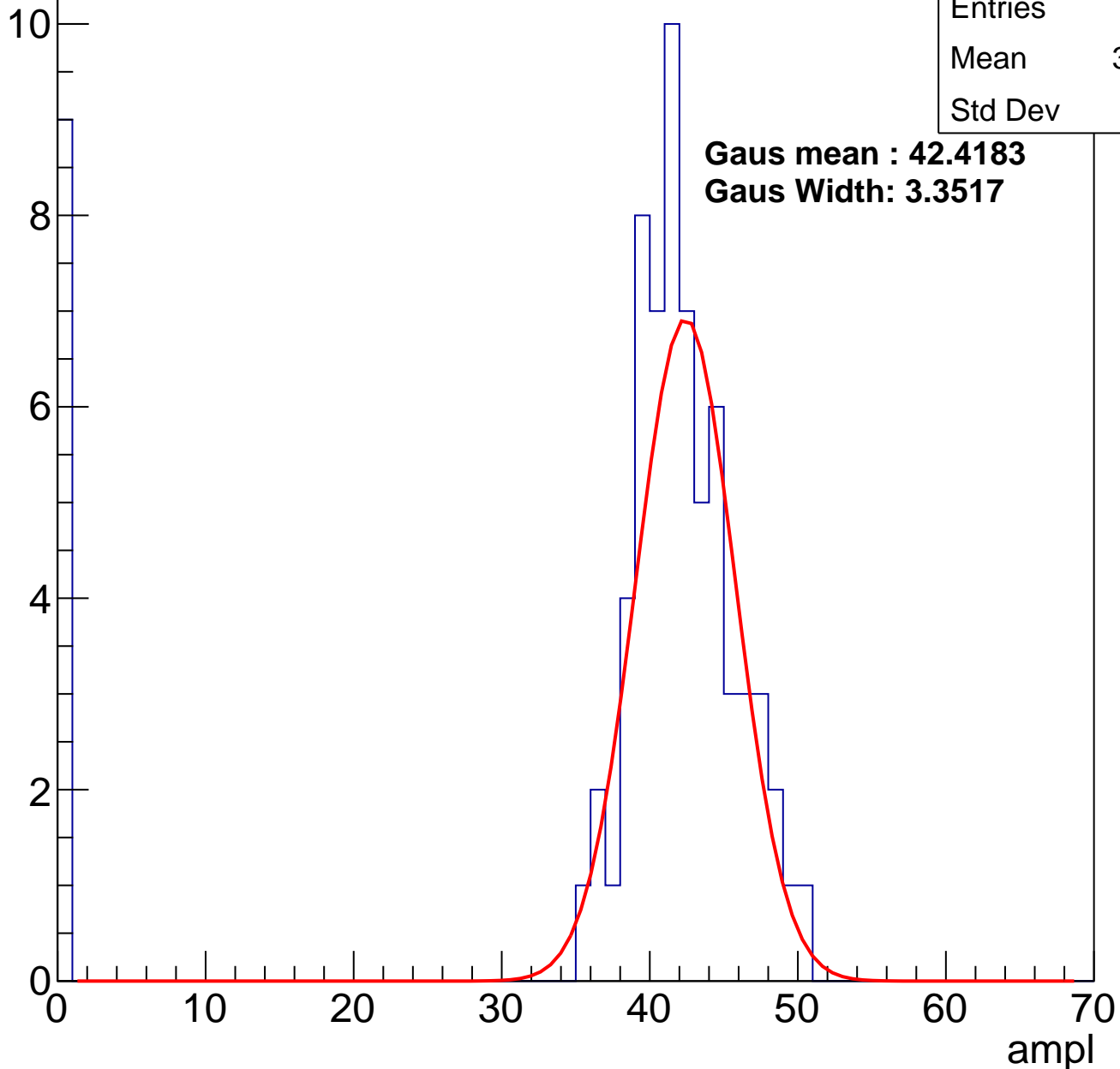
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	36.71
Std Dev	14.1

Gaus mean : 42.4183

Gaus Width: 3.3517

Entry

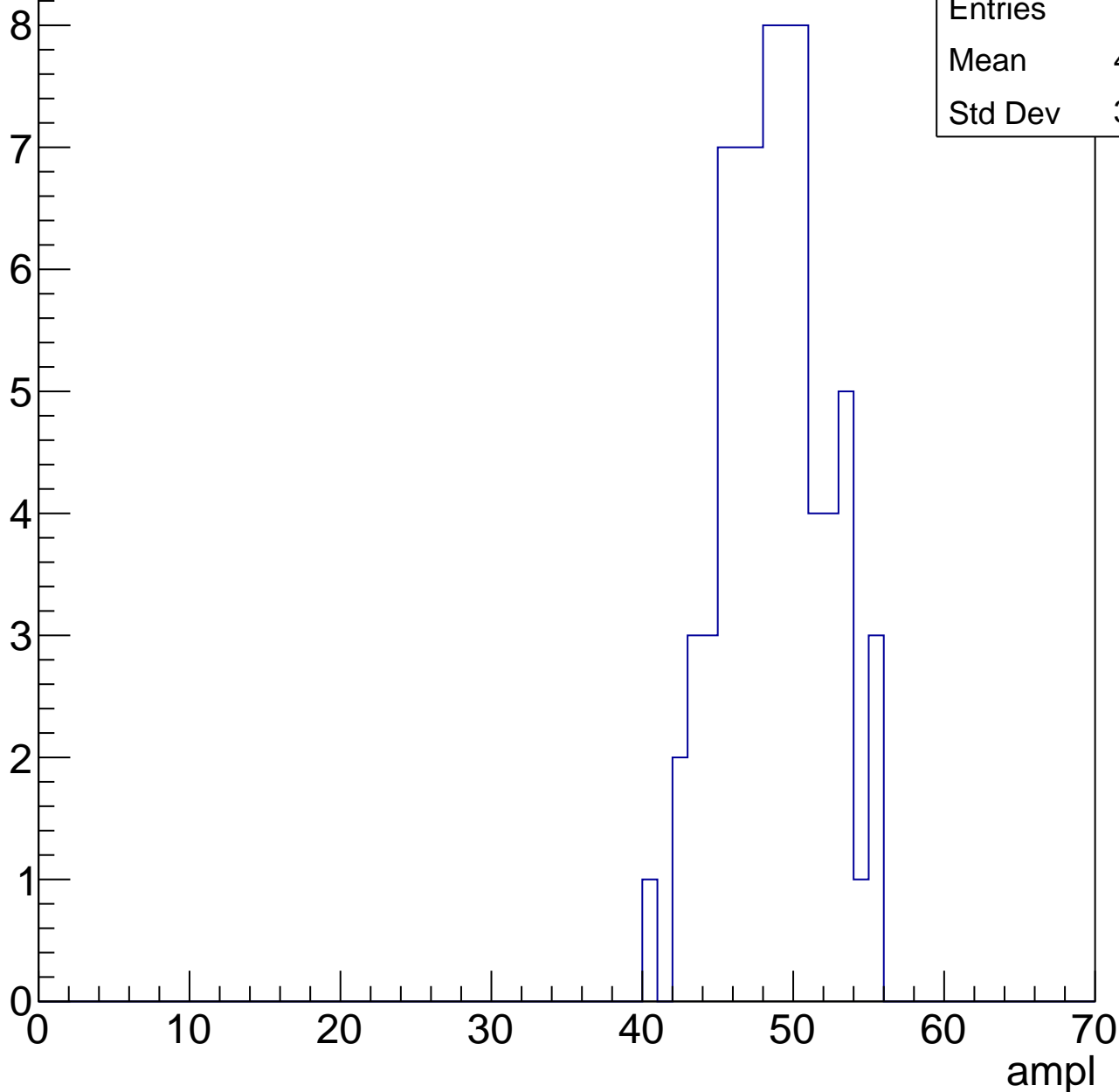


B1L103S, U6-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	48.21
Std Dev	3.361

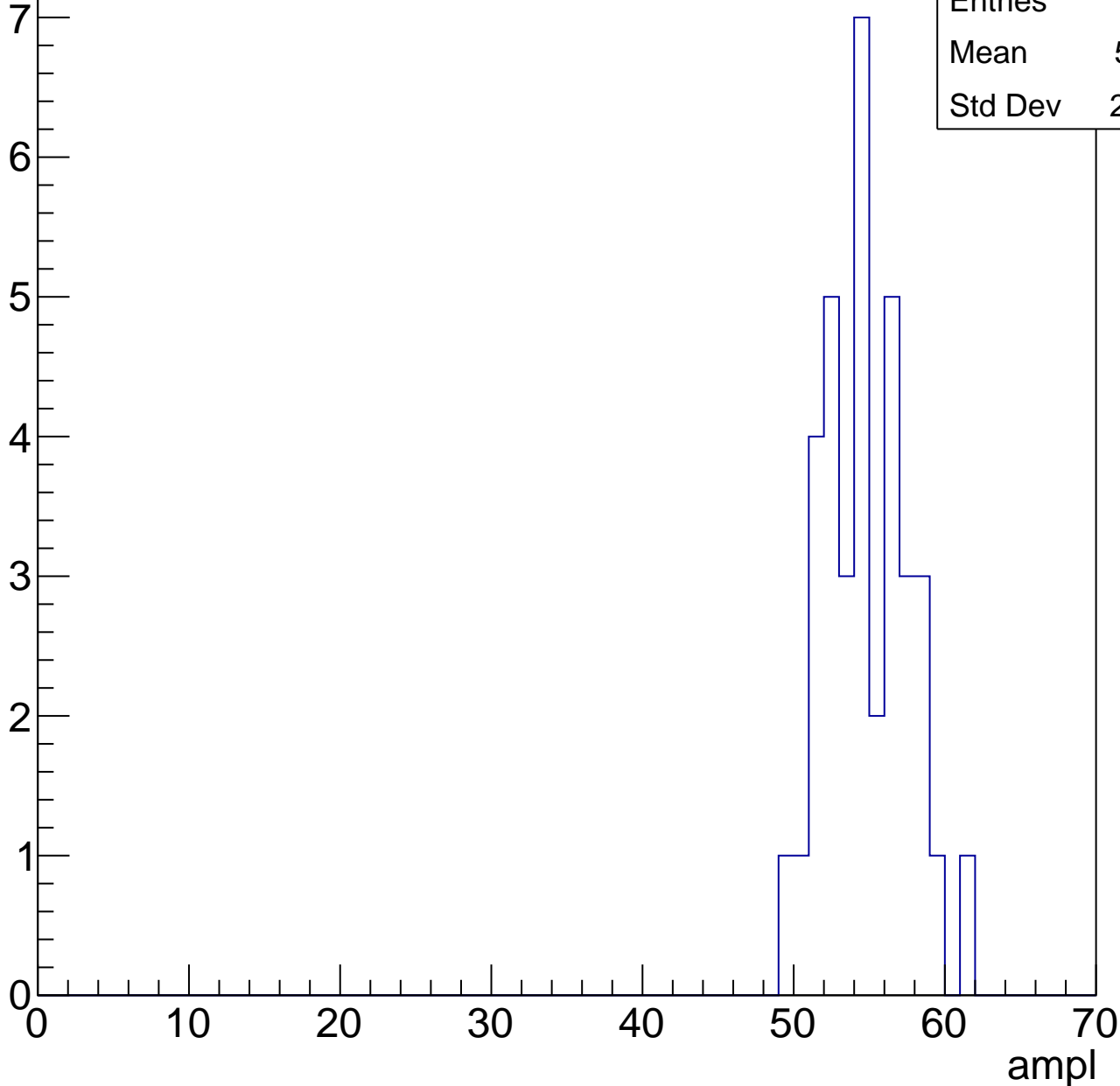


B1L103S, U6-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

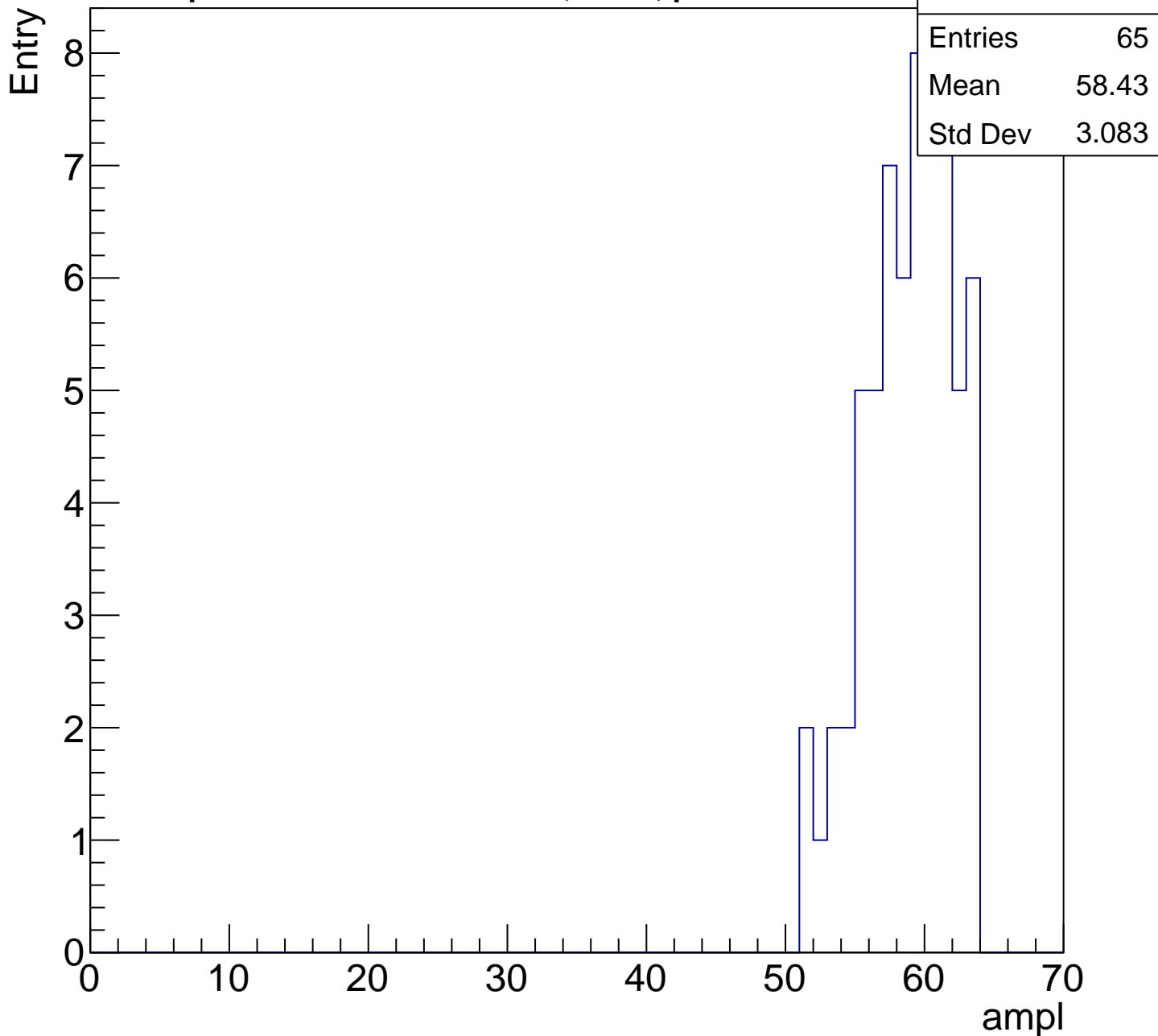
Entry

Entries	36
Mean	54.31
Std Dev	2.727



B1L103S, U6-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

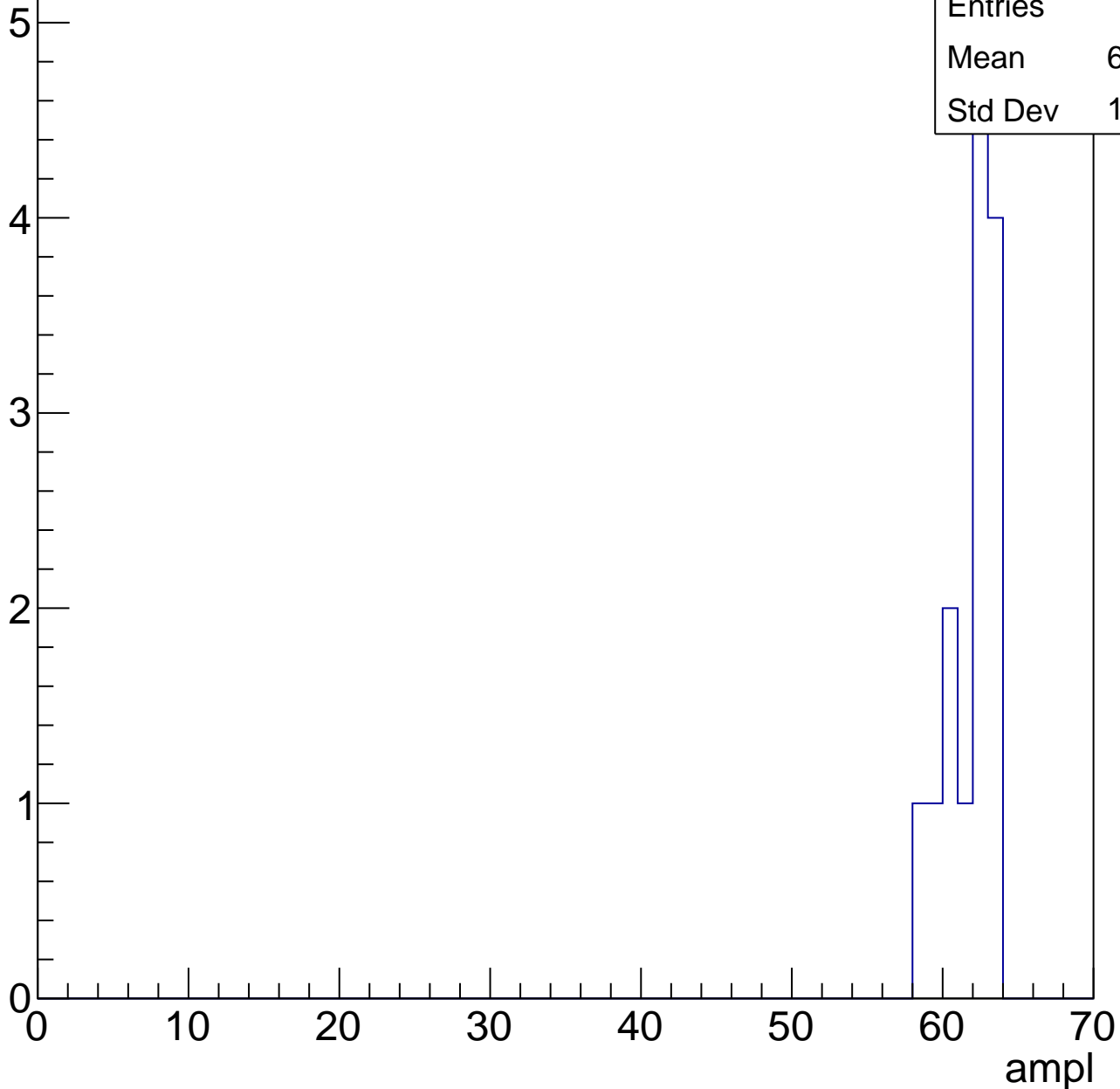


B1L103S, U6-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.43
Std Dev	1.545



B1L103S, U6-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch14, adc0

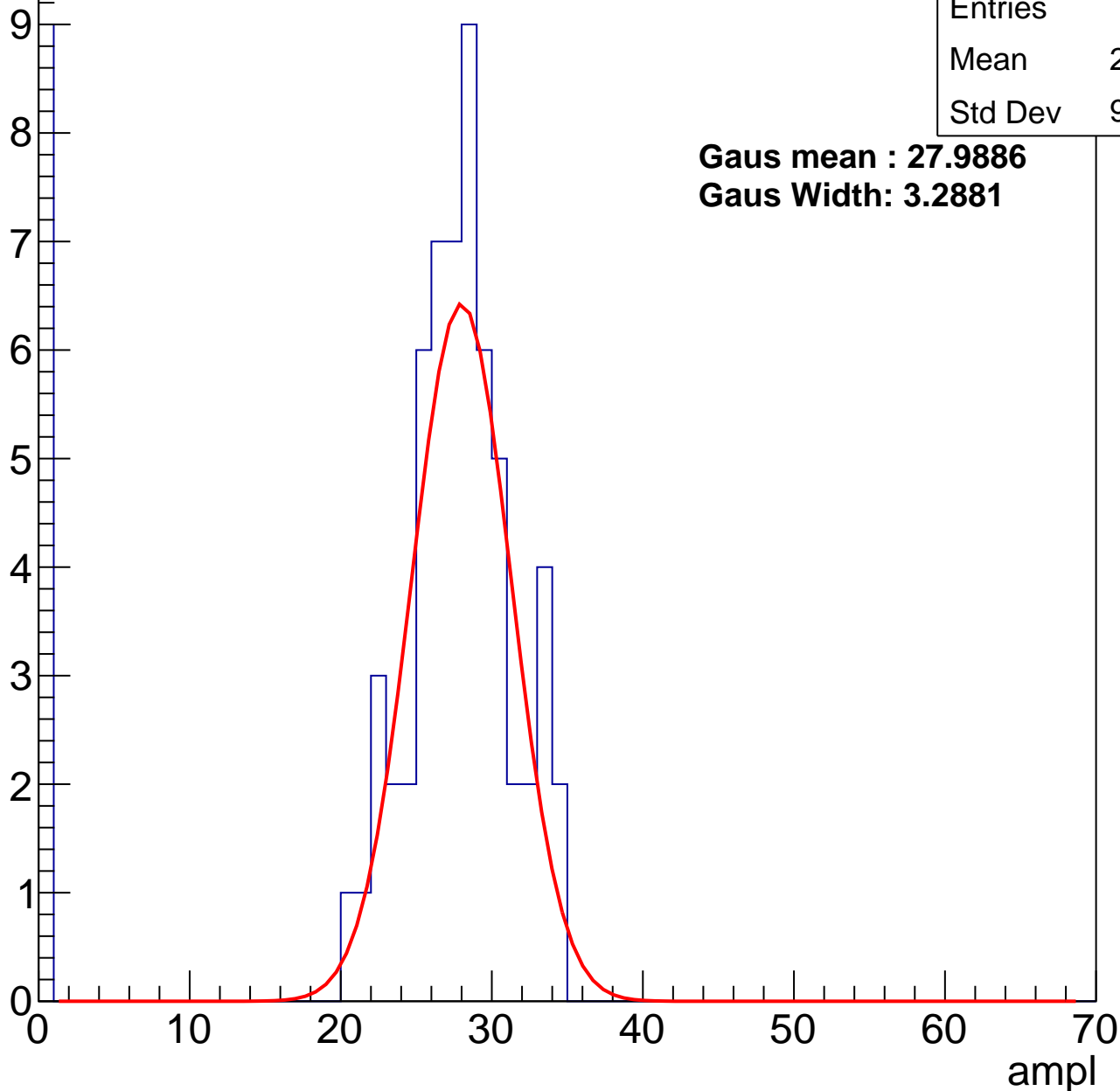
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	23.88
Std Dev	9.809

Gaus mean : 27.9886

Gaus Width: 3.2881



B1L103S, U6-ch14, adc1

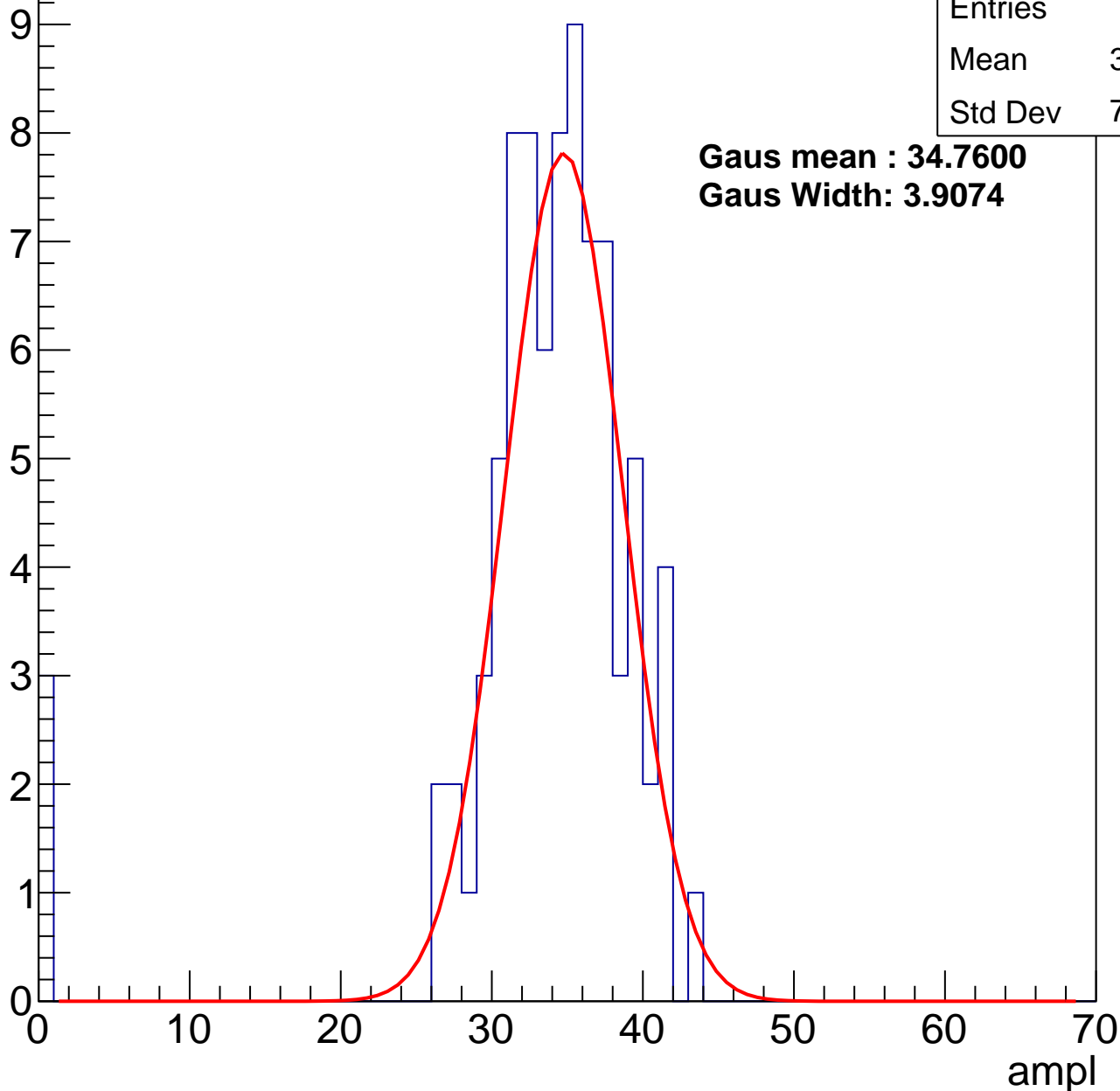
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	32.94
Std Dev	7.338

Gaus mean : 34.7600

Gaus Width: 3.9074



B1L103S, U6-ch14, adc2

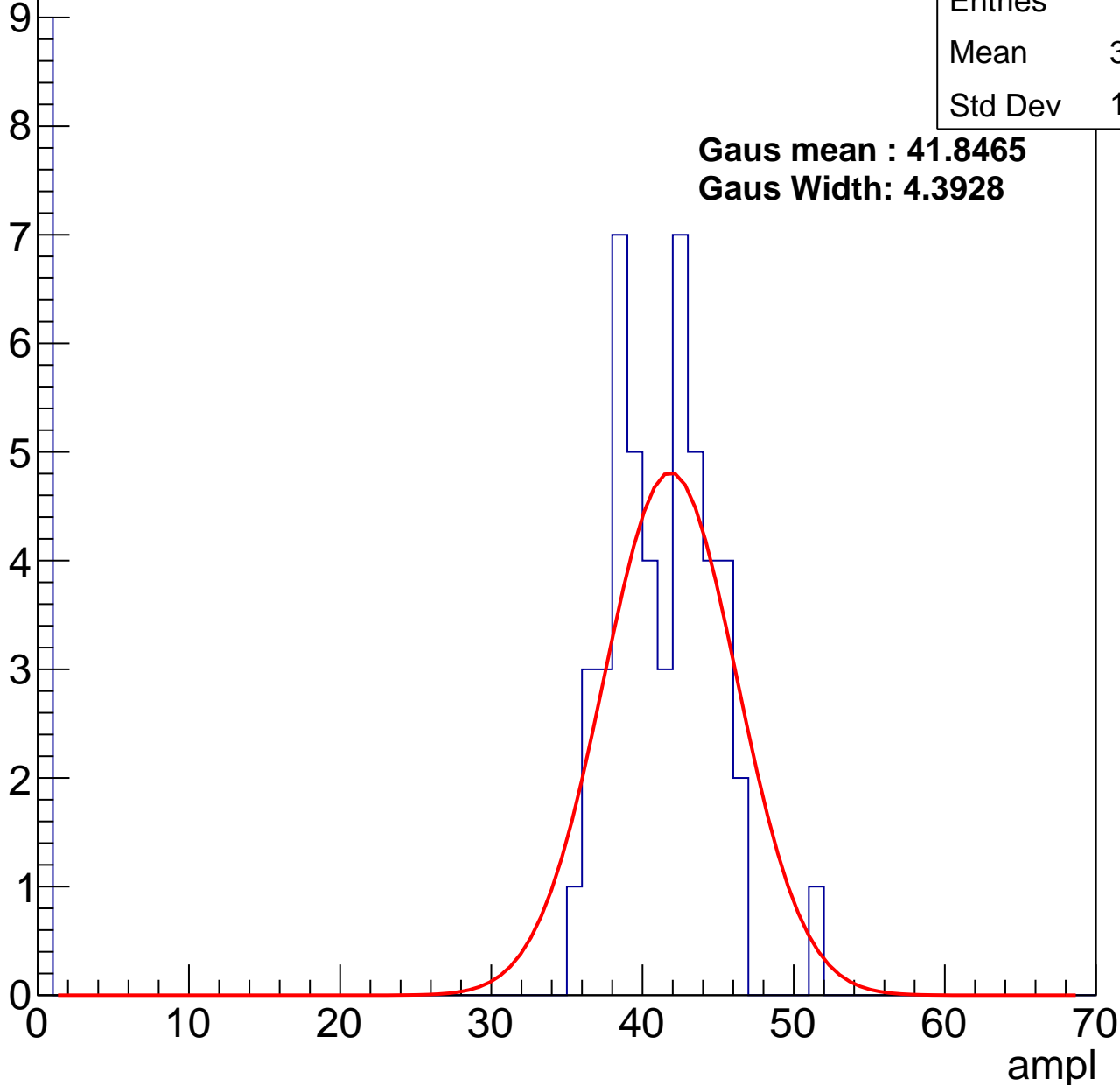
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	34.59
Std Dev	15.12

Gaus mean : 41.8465

Gaus Width: 4.3928

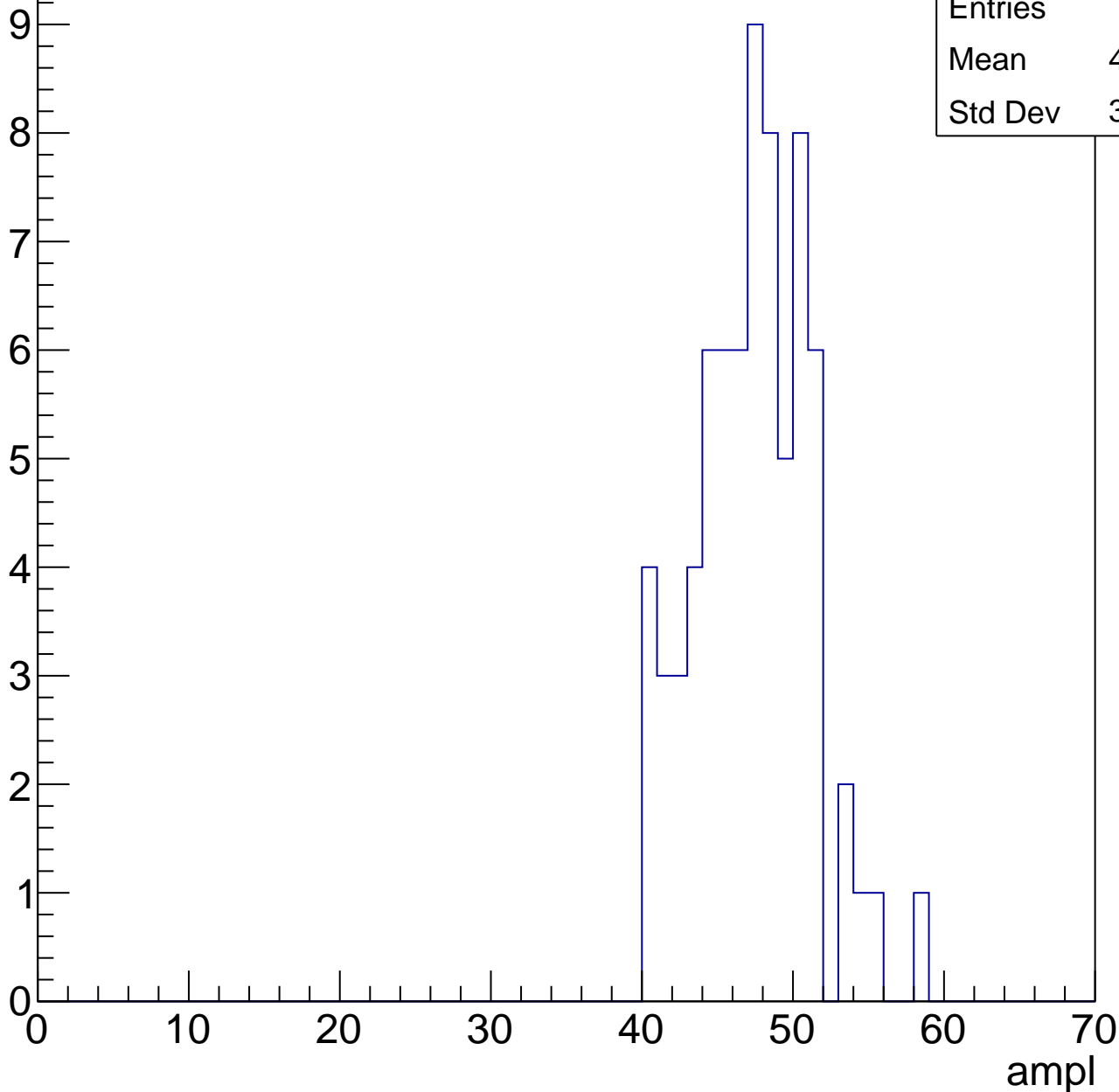


B1L103S, U6-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	46.88
Std Dev	3.752



B1L103S, U6-ch14, adc4

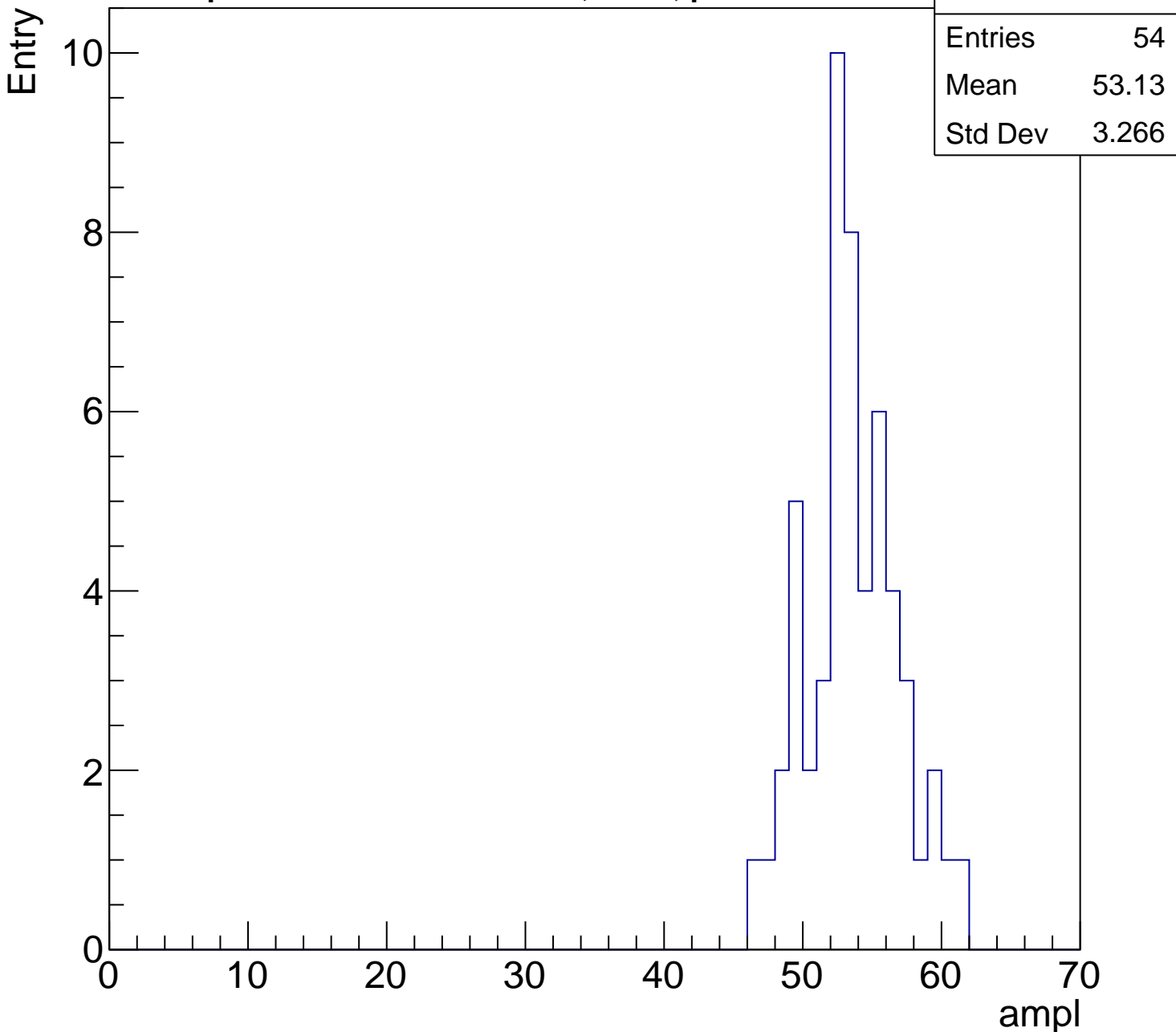
calib_packv5_041523_1651.root, FC#0, port C2

Entries	54
Mean	53.13
Std Dev	3.266

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

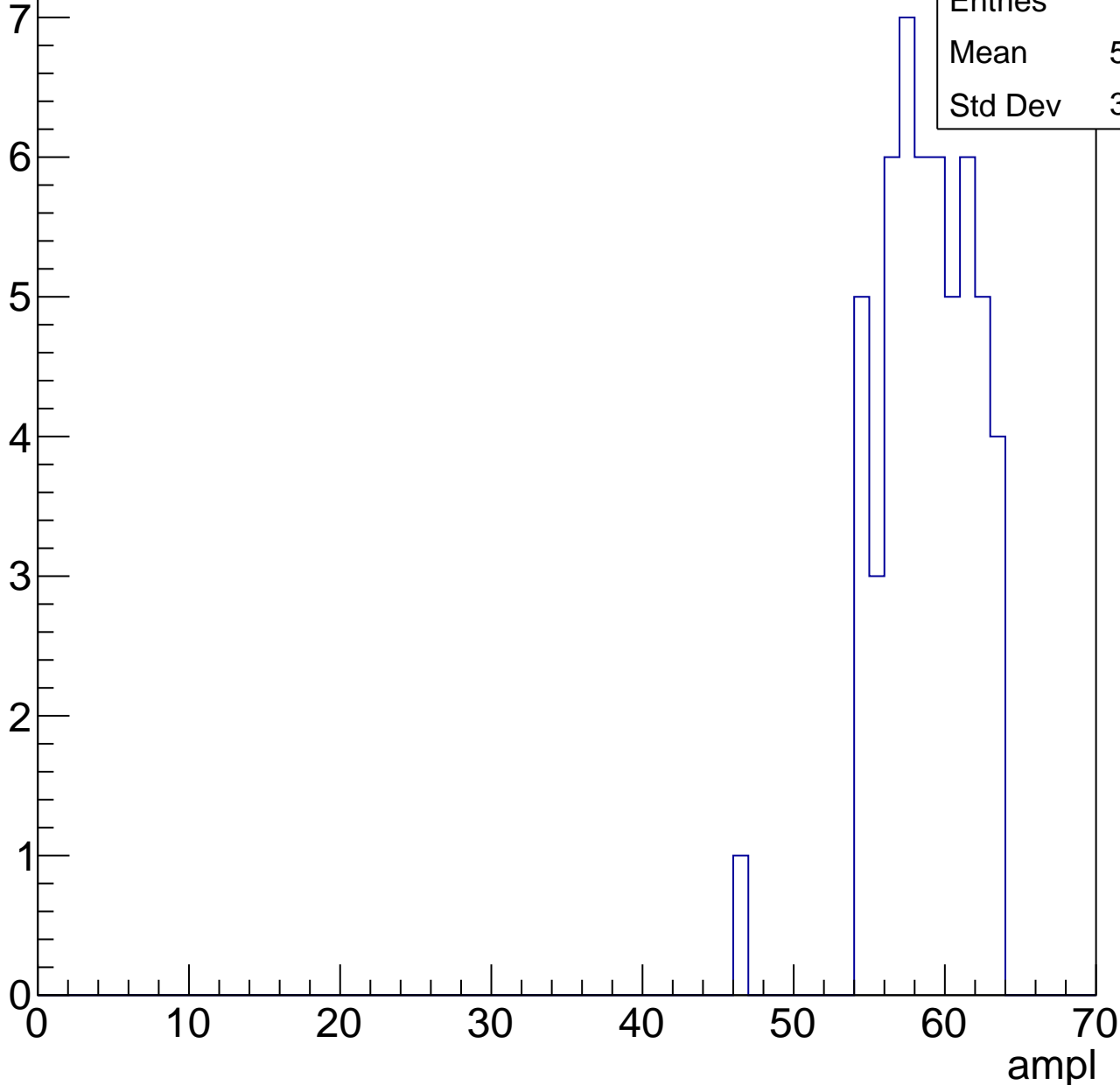


B1L103S, U6-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.26
Std Dev	3.158

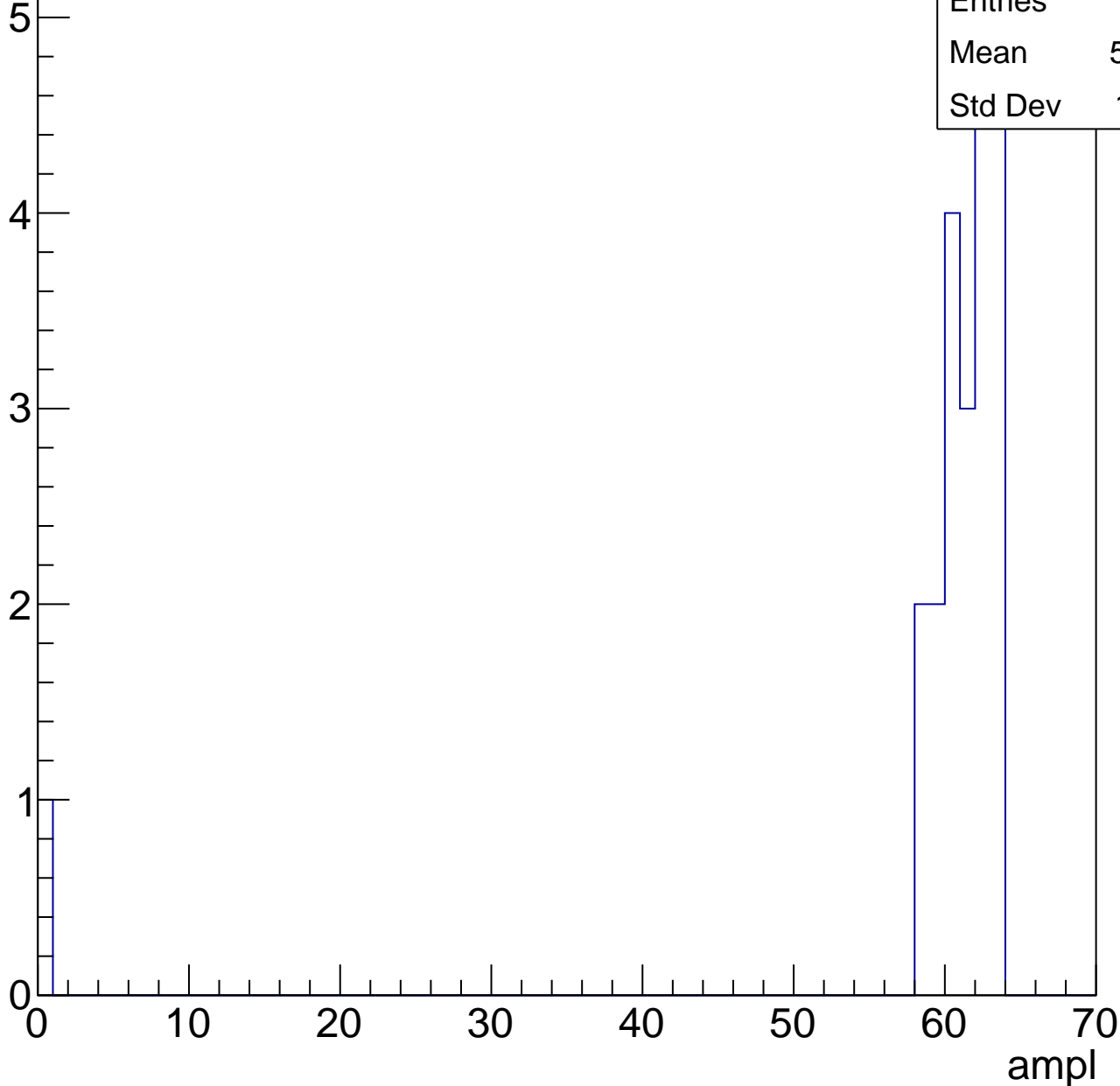


B1L103S, U6-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.27
Std Dev	12.81



B1L103S, U6-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

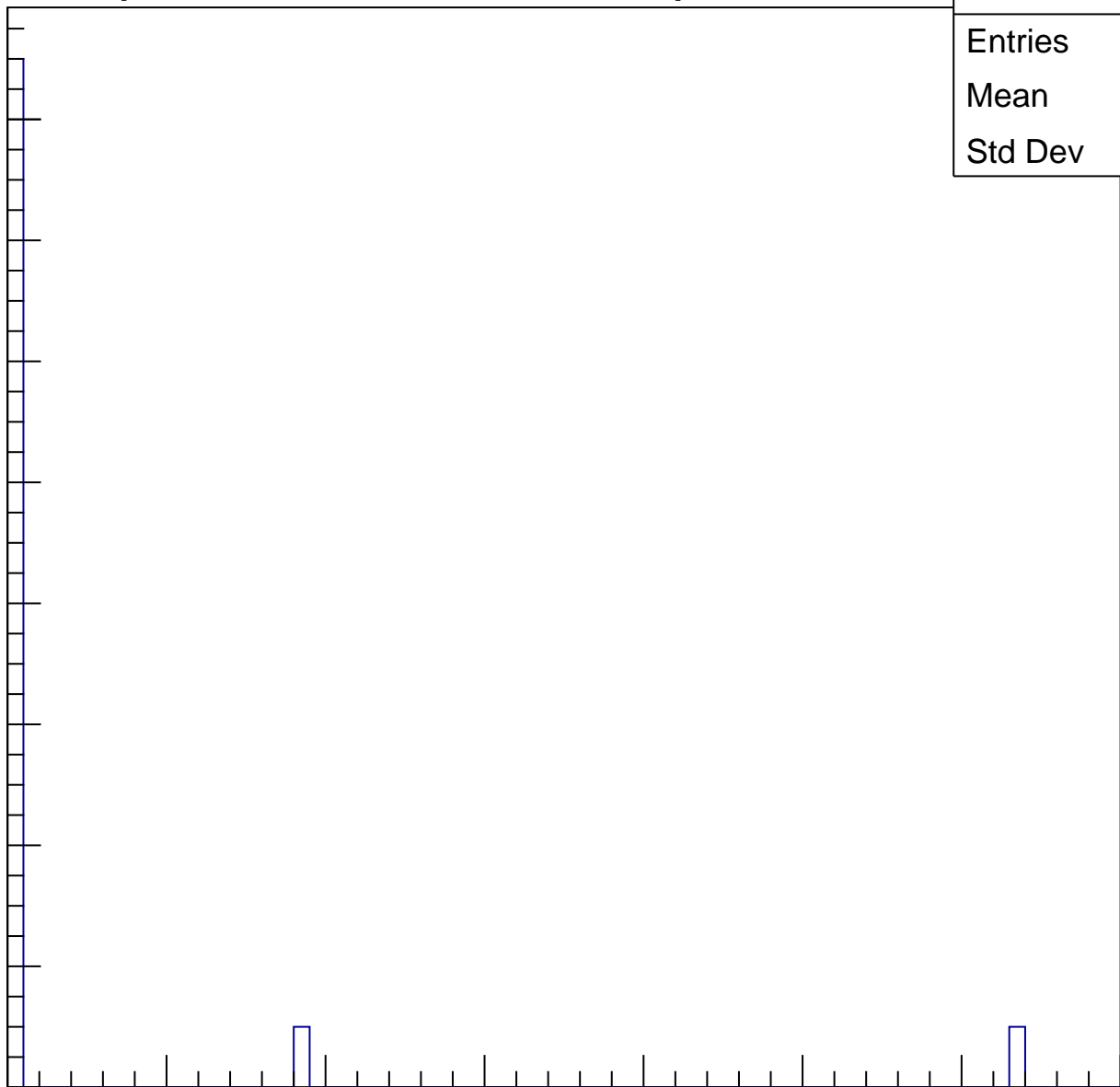
Entries	19
Mean	4.263
Std Dev	14.41

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch15, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	27.95
Std Dev	7.108

Gaus mean : 30.1842

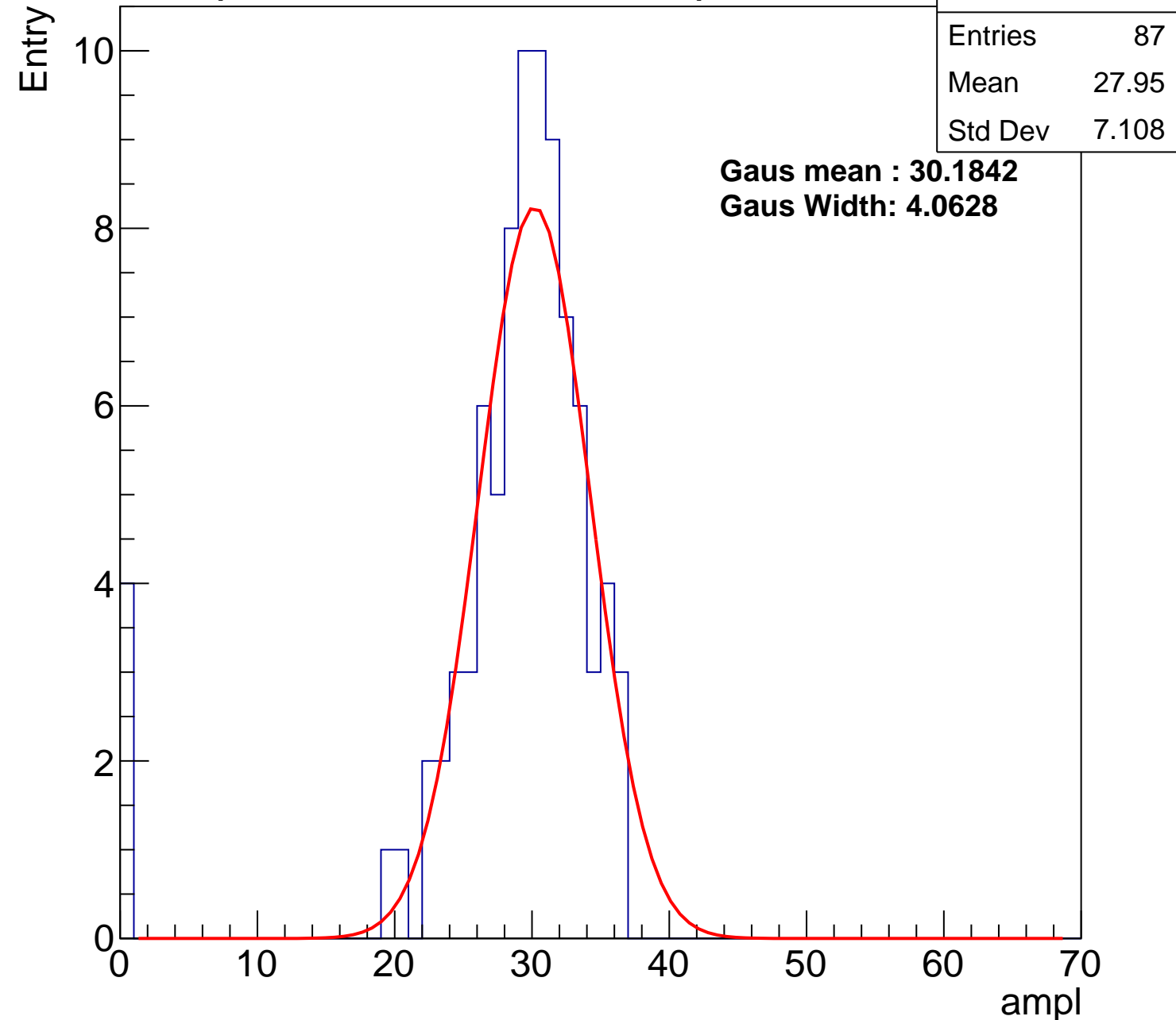
Gaus Width: 4.0628

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch15, adc1

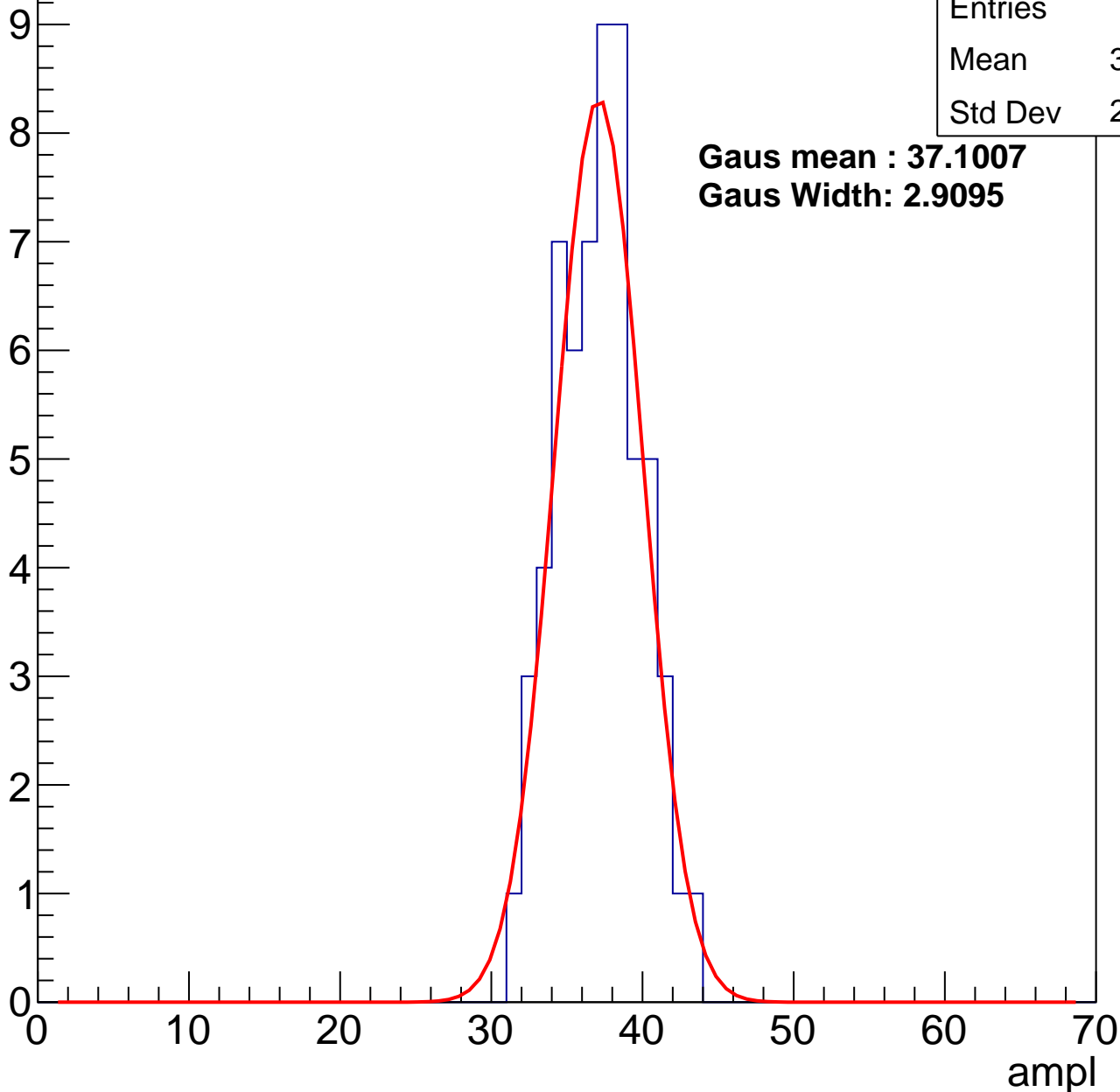
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	36.67
Std Dev	2.702

Gaus mean : 37.1007

Gaus Width: 2.9095



B1L103S, U6-ch15, adc2

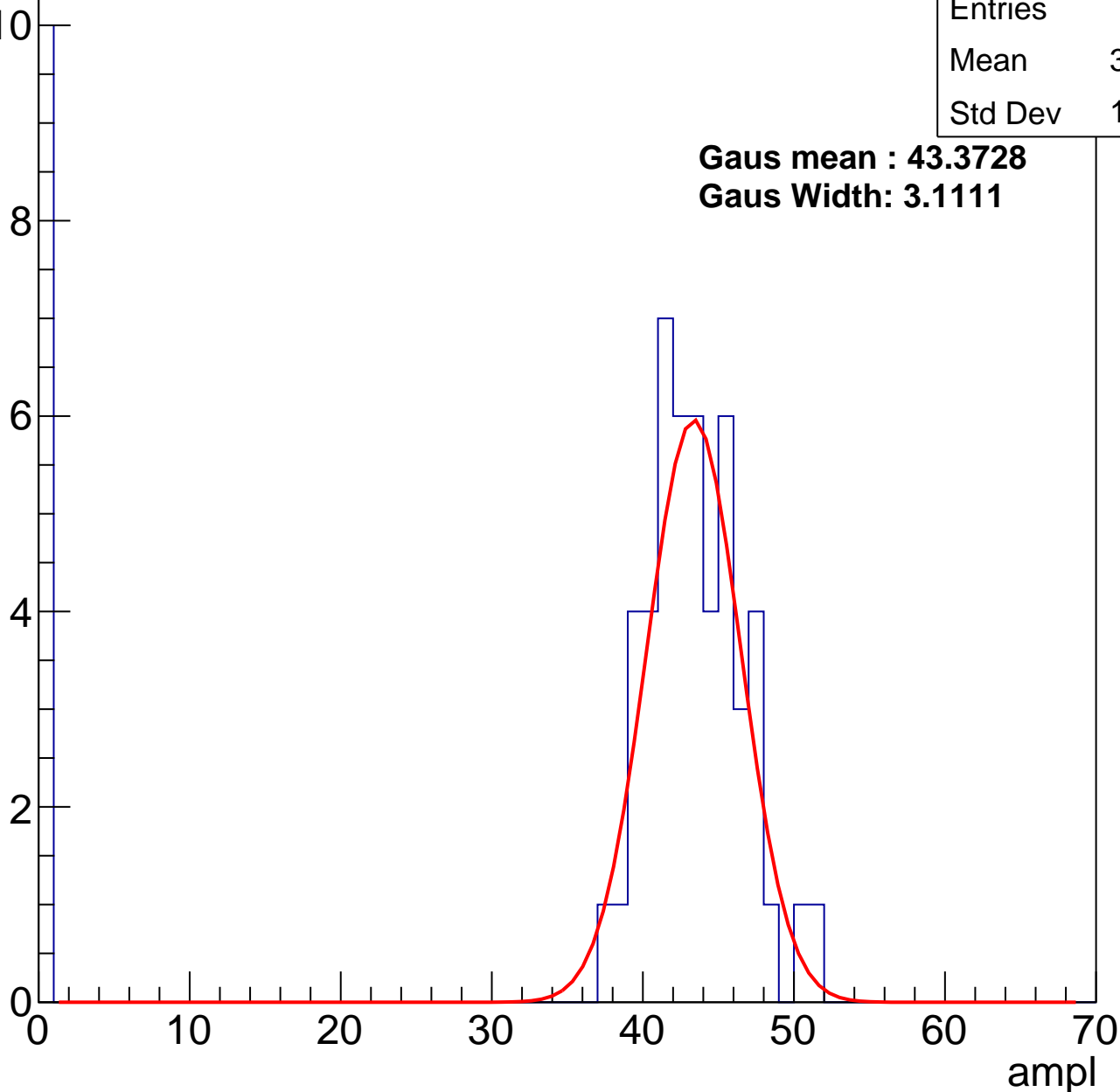
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.75
Std Dev	16.38

Gaus mean : 43.3728

Gaus Width: 3.1111

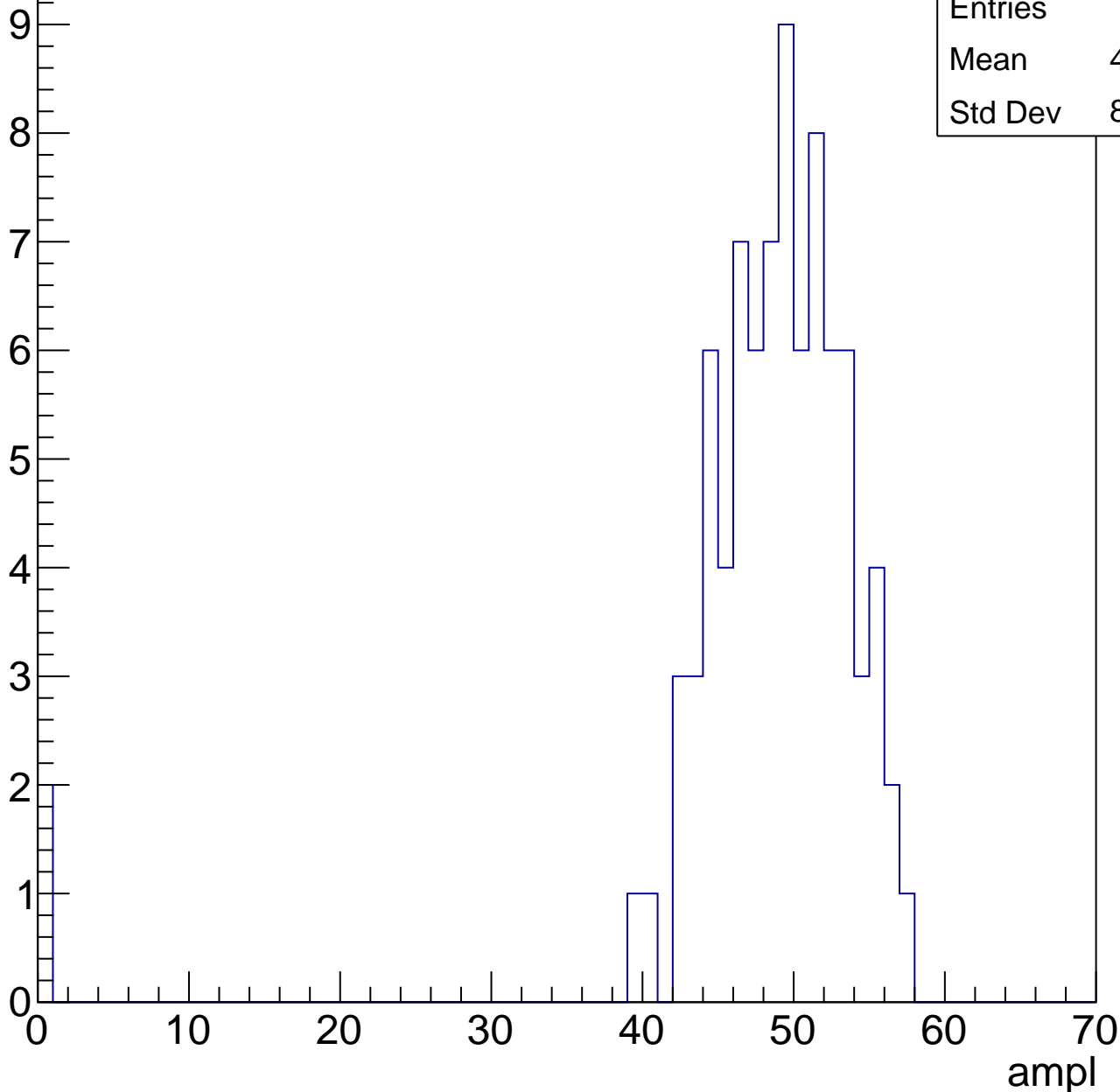


B1L103S, U6-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	47.62
Std Dev	8.369



B1L103S, U6-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

ampl

Entries	50
Mean	55.42
Std Dev	3.383

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

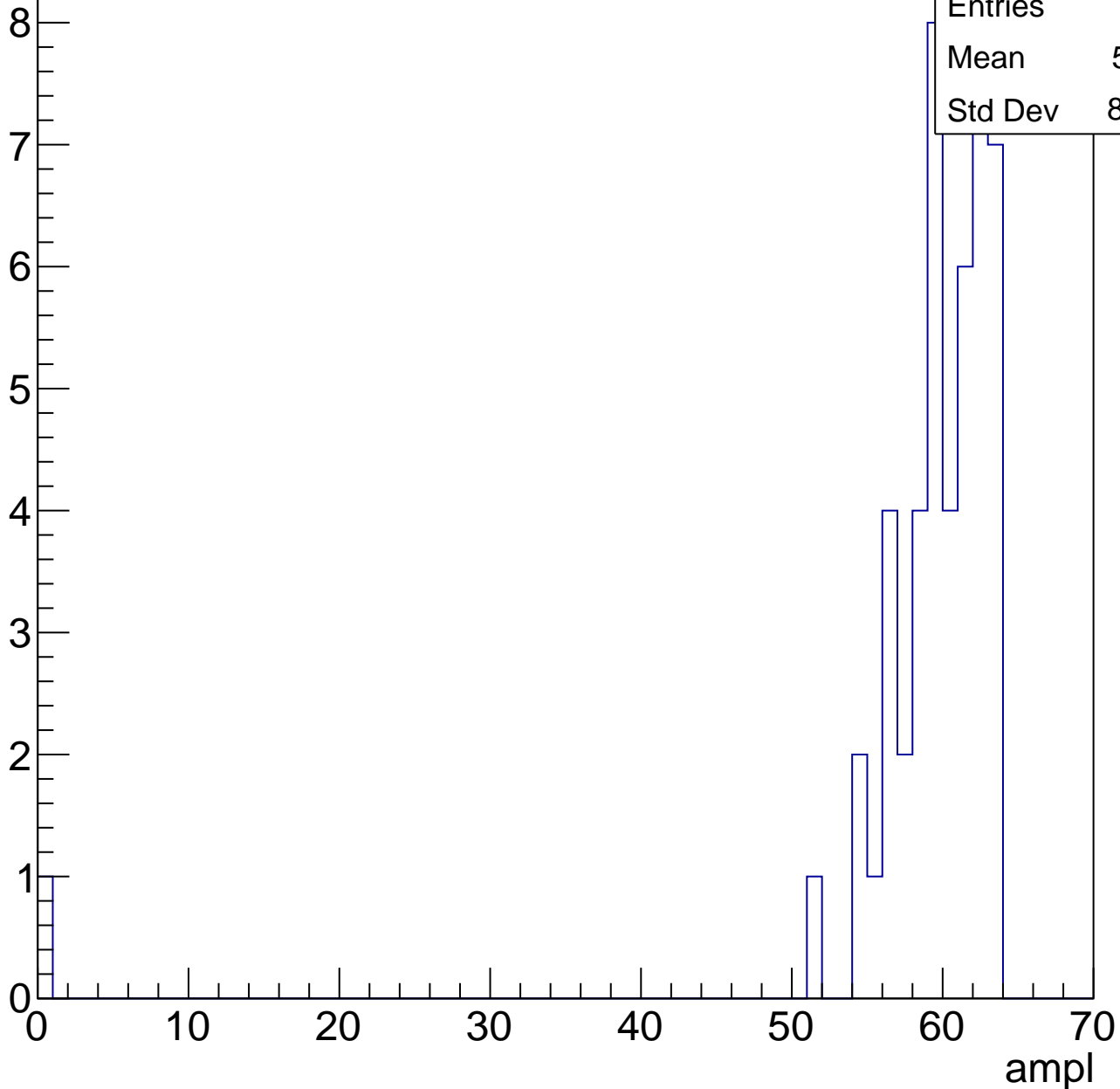
ampl

B1L103S, U6-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.31
Std Dev	8.954



B1L103S, U6-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

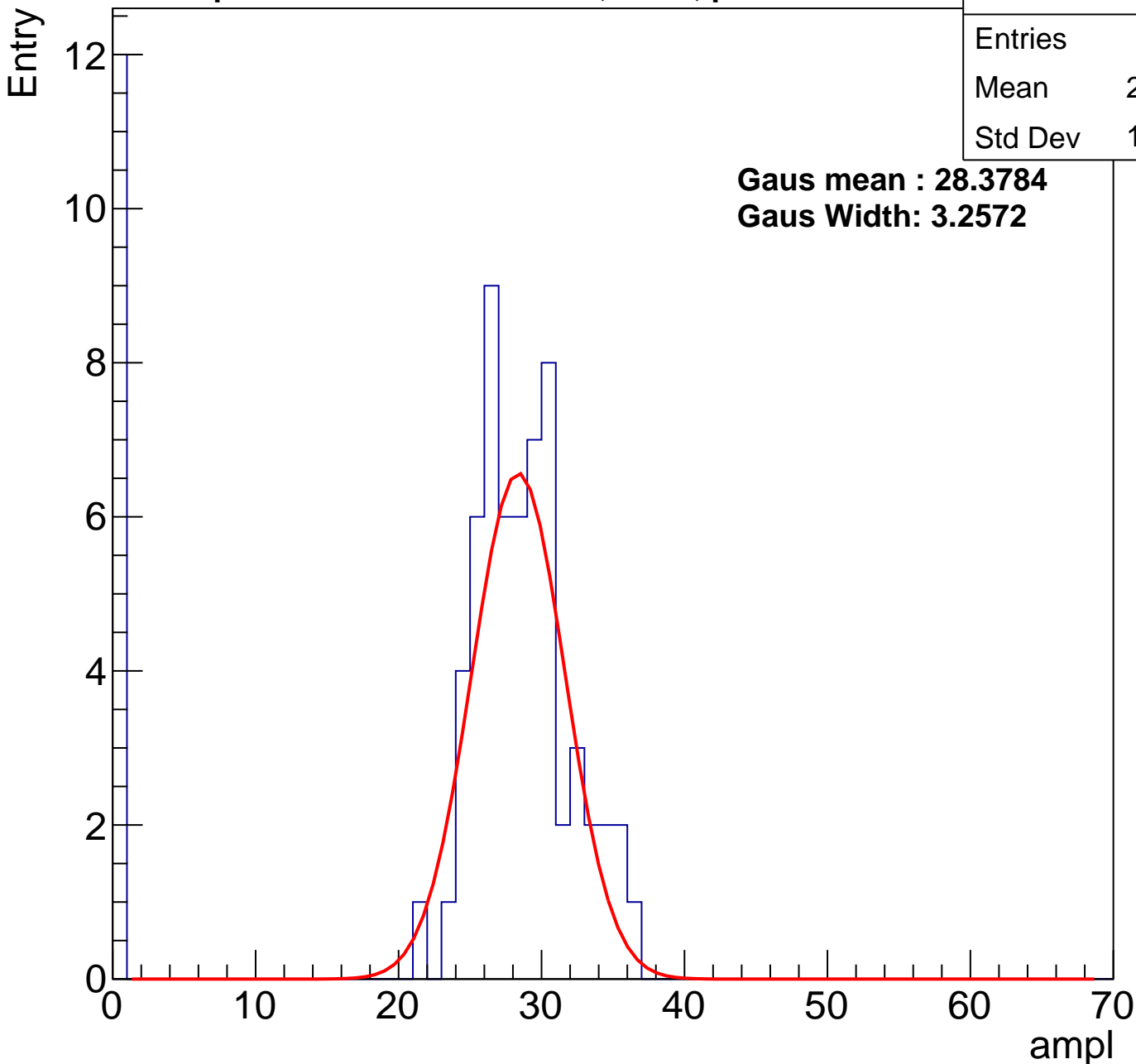
B1L103S, U6-ch16, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	23.54
Std Dev	10.93

Gaus mean : 28.3784

Gaus Width: 3.2572



B1L103S, U6-ch16, adc1

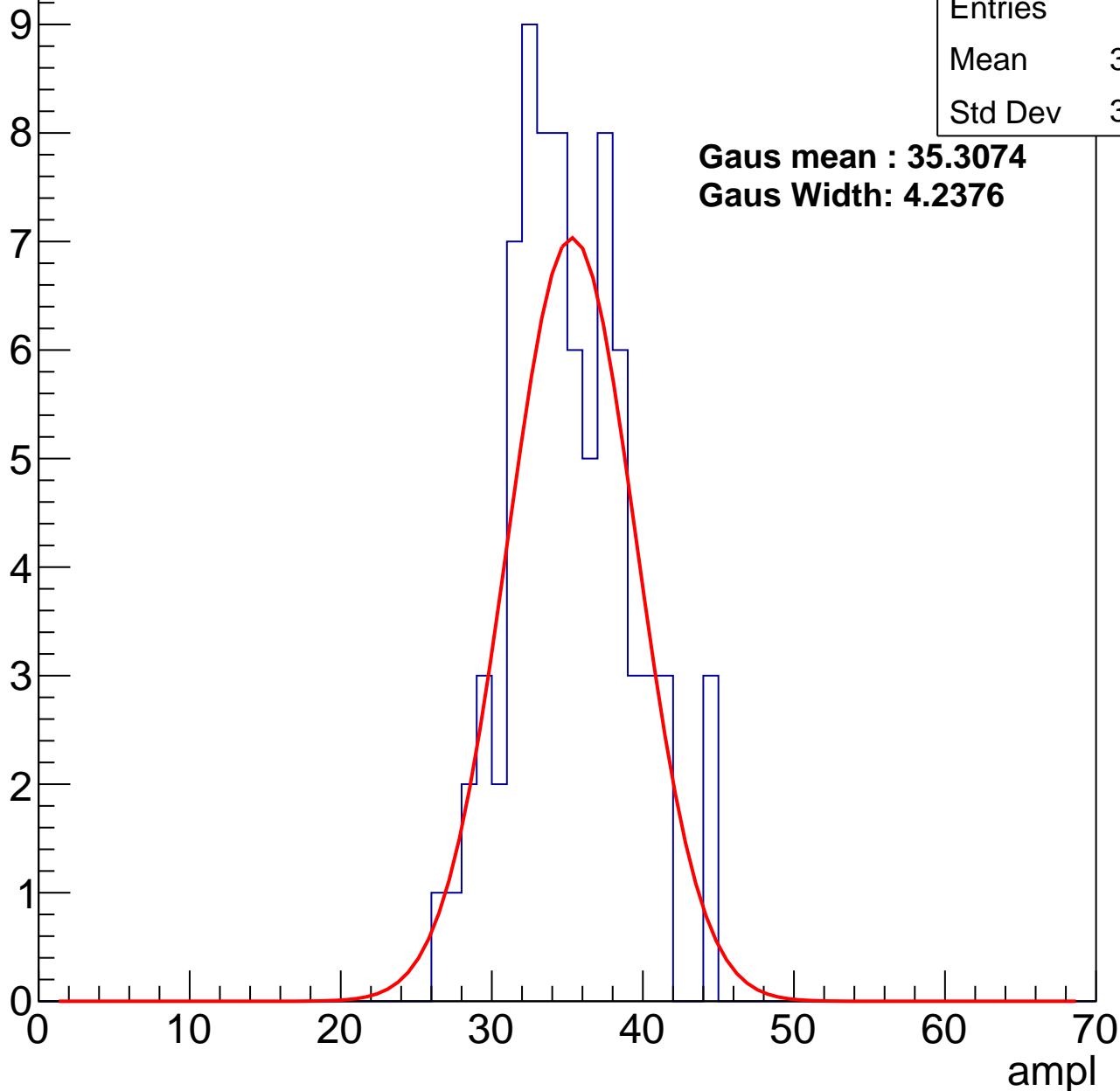
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.65
Std Dev	3.905

Gaus mean : 35.3074

Gaus Width: 4.2376



B1L103S, U6-ch16, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	33.68
Std Dev	15.3

Gaus mean : 41.7752

Gaus Width: 3.9048

Entry

10

8

6

4

2

0

0

10

20

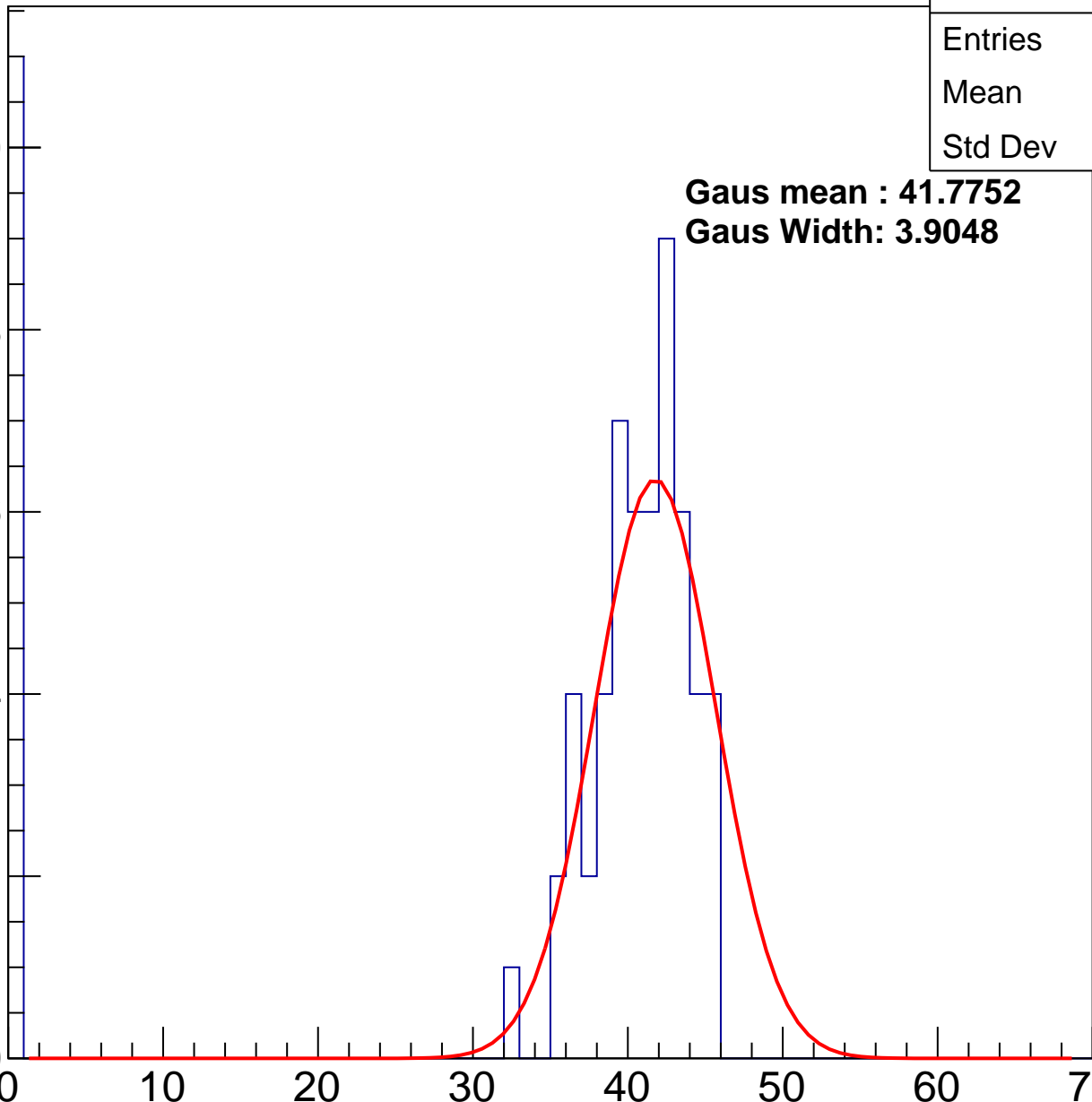
30

40

50

60

ampl

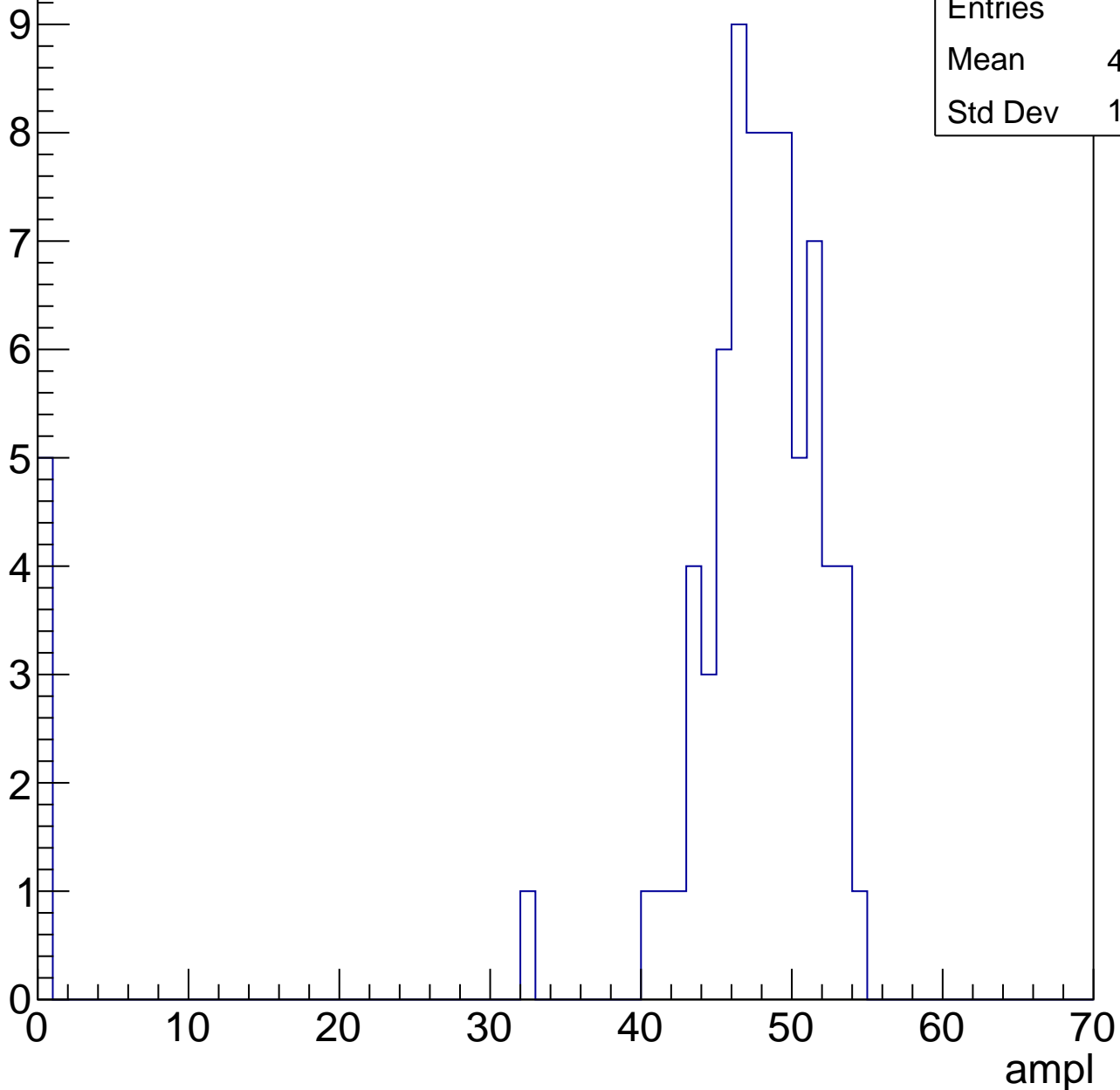


B1L103S, U6-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	44.42
Std Dev	12.29

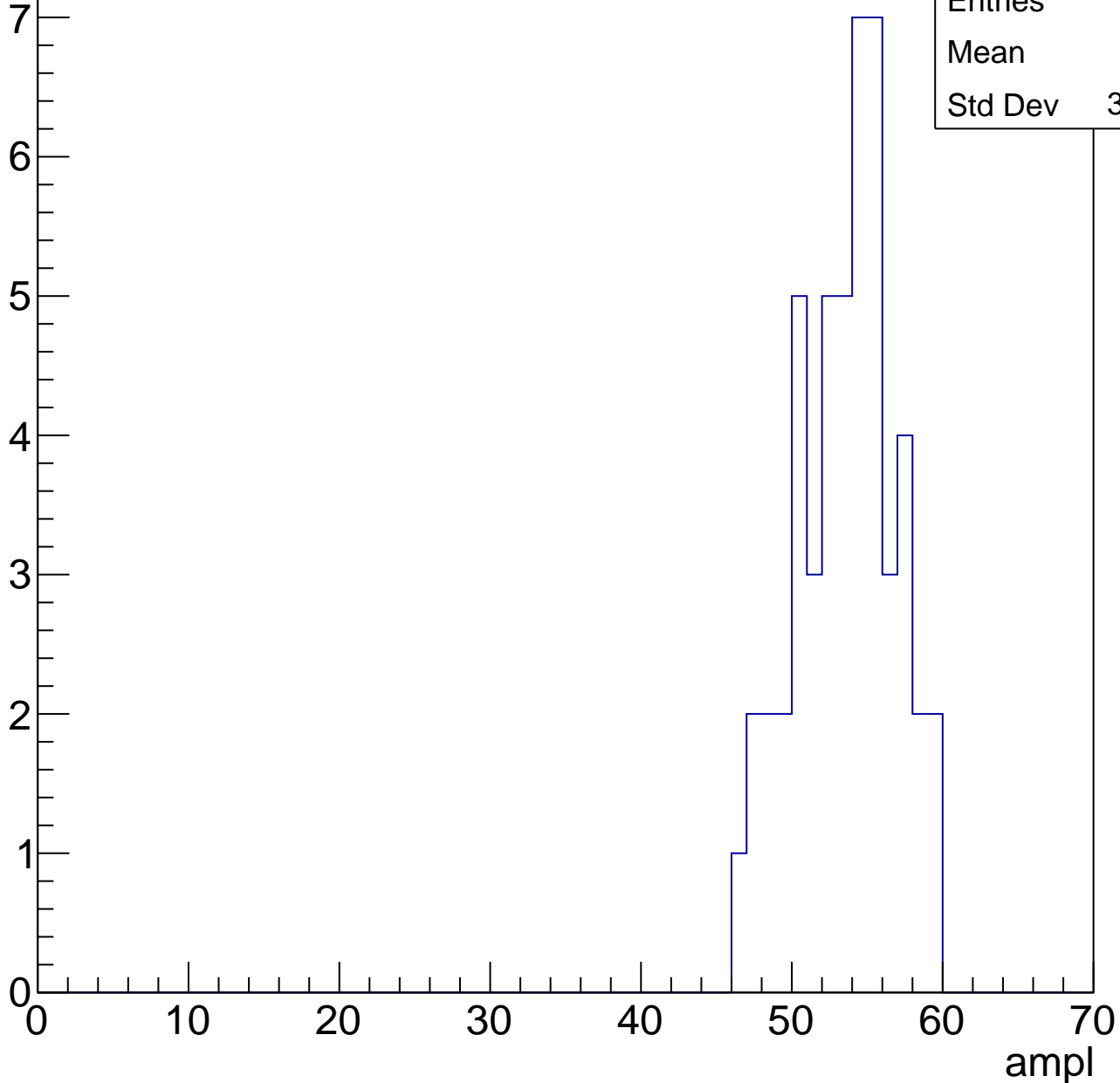


B1L103S, U6-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

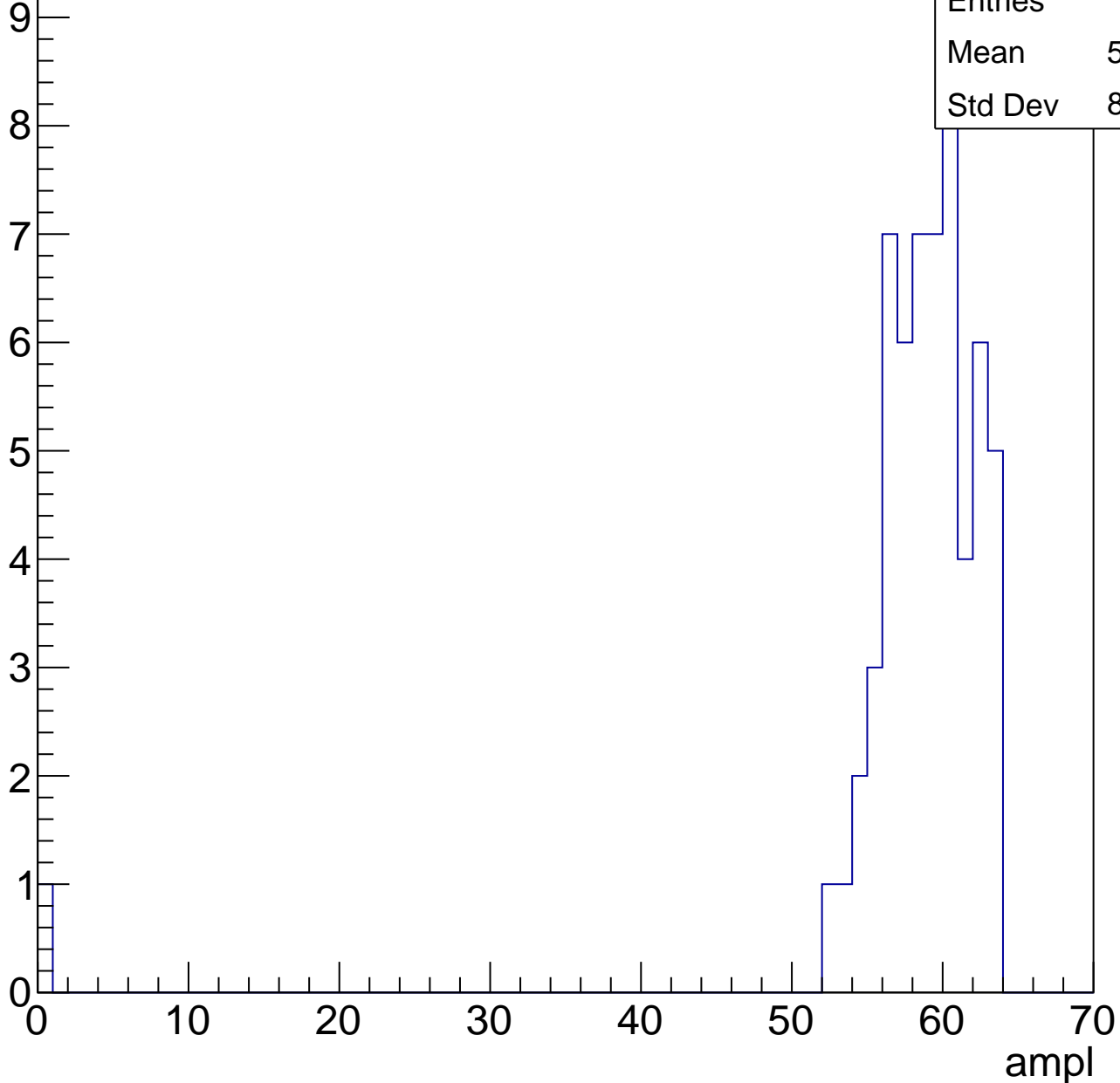
Entries	50
Mean	53.1
Std Dev	3.202



B1L103S, U6-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

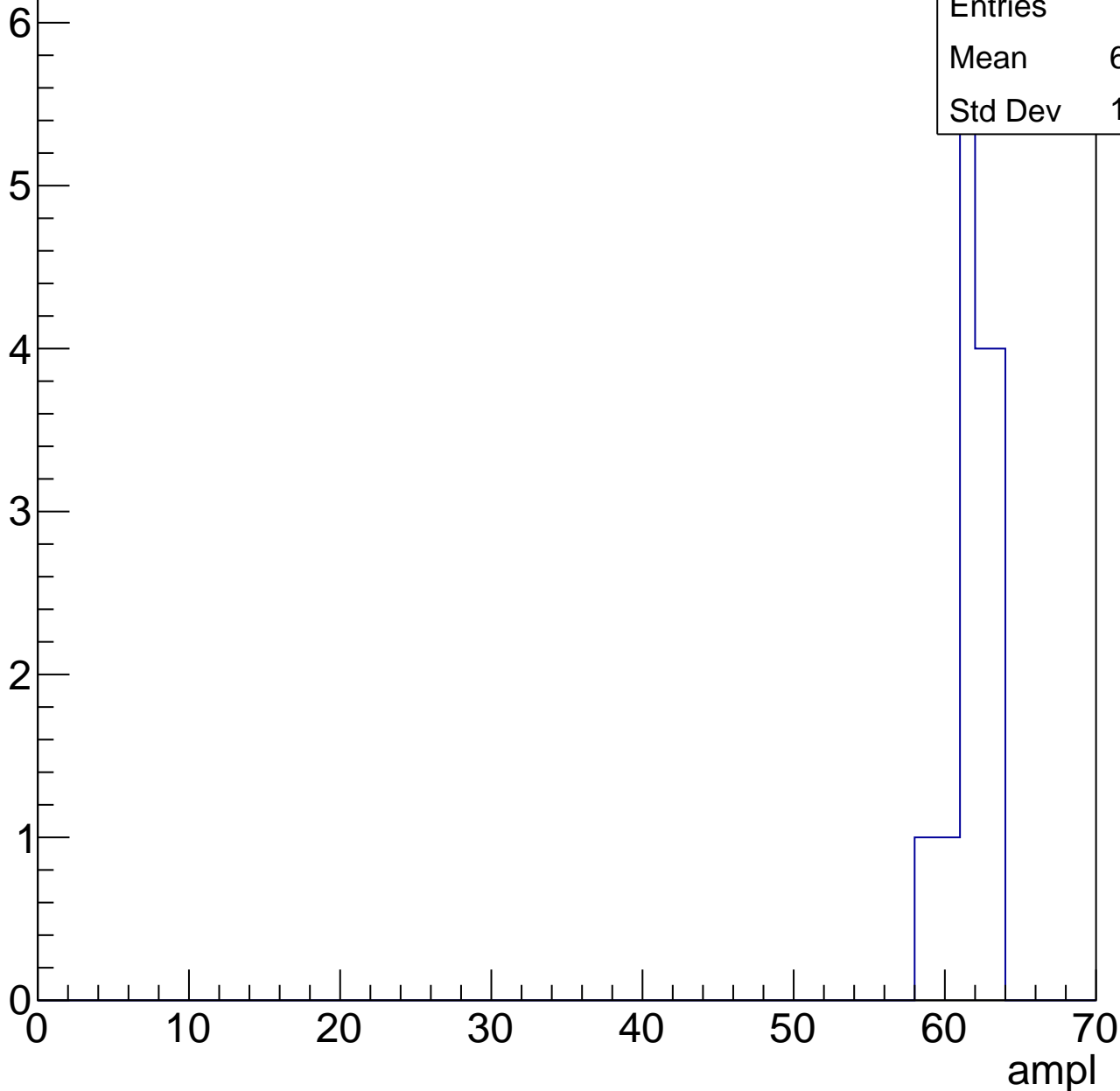


B1L103S, U6-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.369

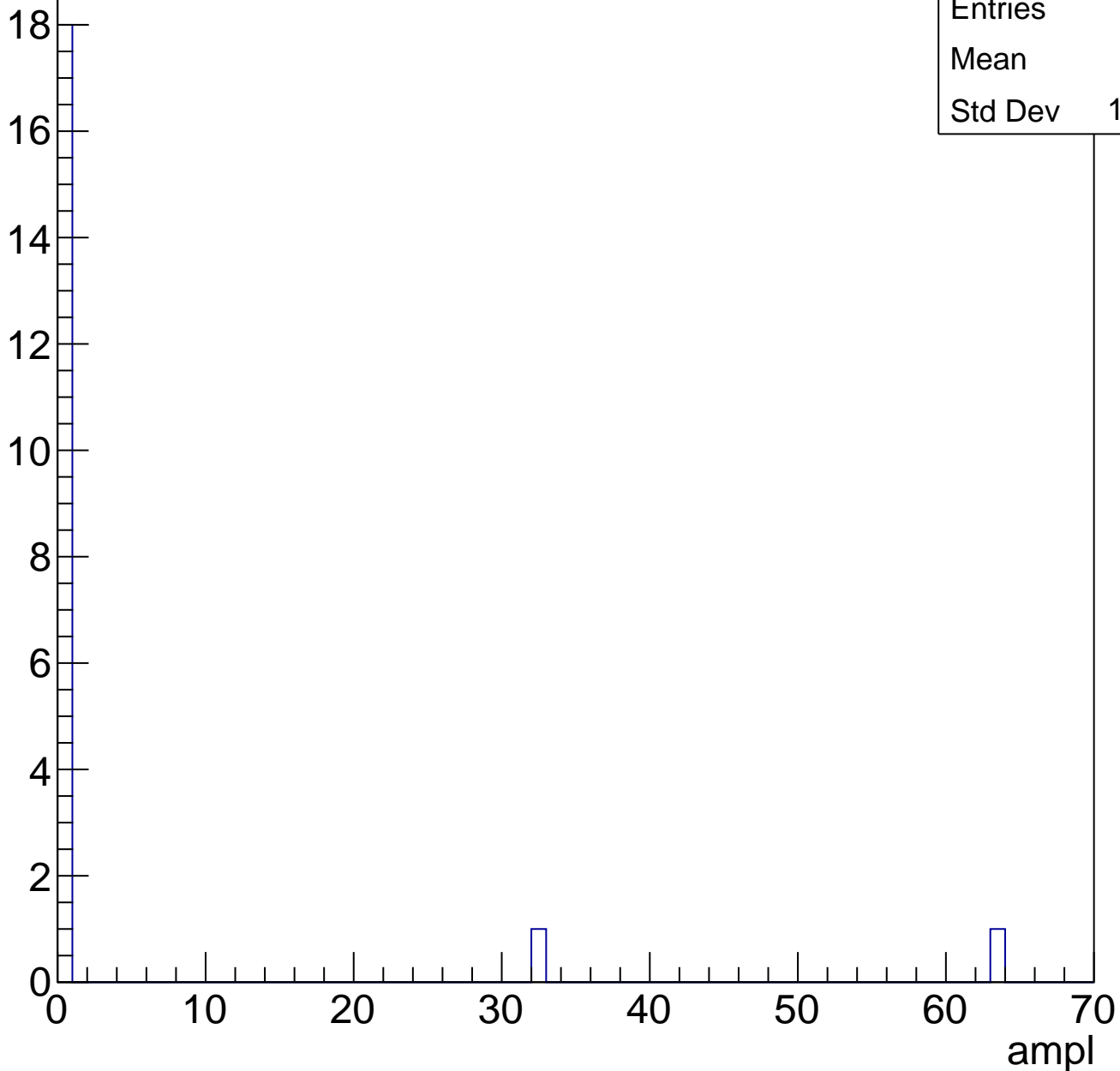


B1L103S, U6-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.75
Std Dev	15.07

Entry



B1L103S, U6-ch17, adc0

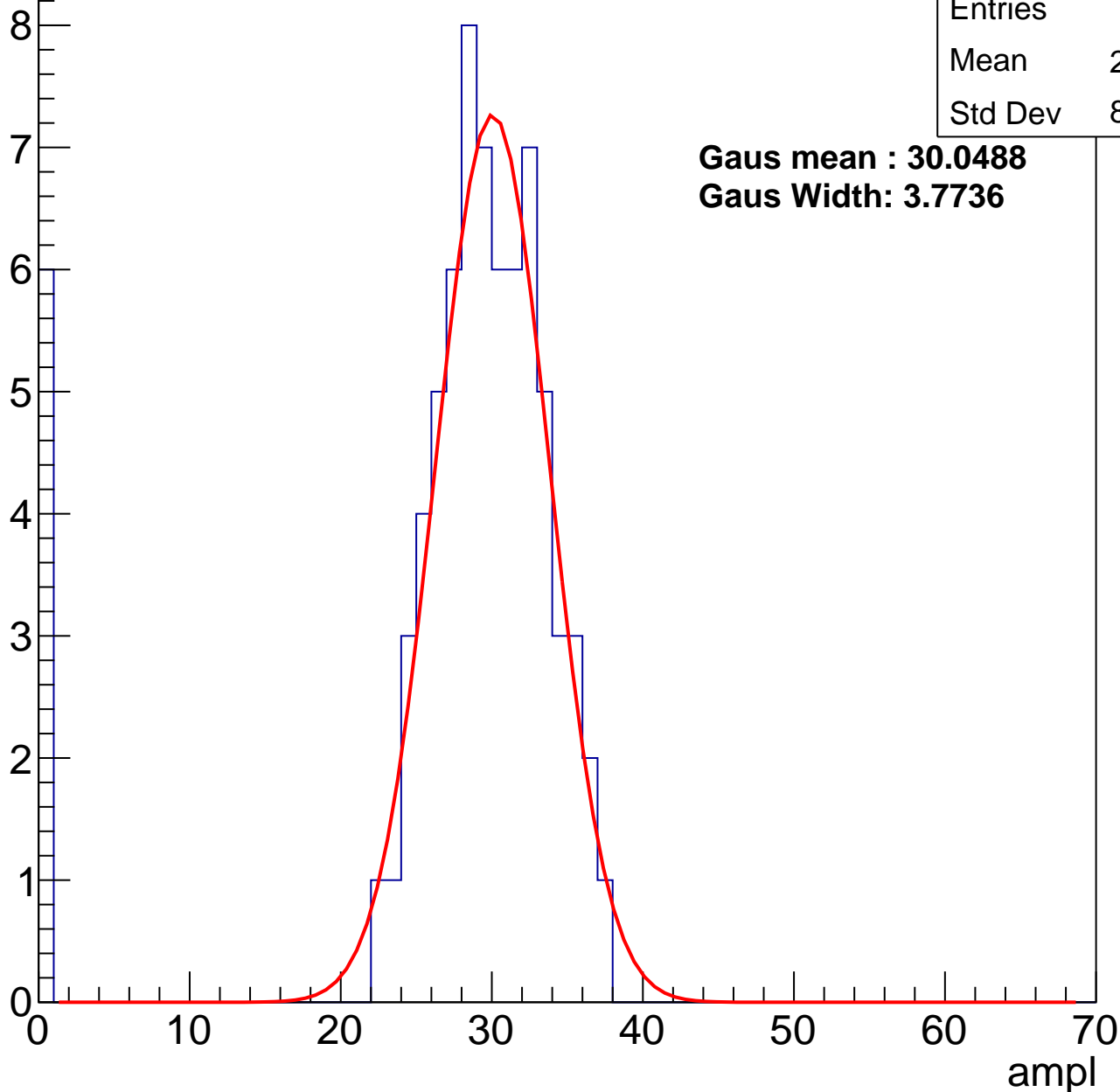
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	27.12
Std Dev	8.702

Gaus mean : 30.0488

Gaus Width: 3.7736



B1L103S, U6-ch17, adc1

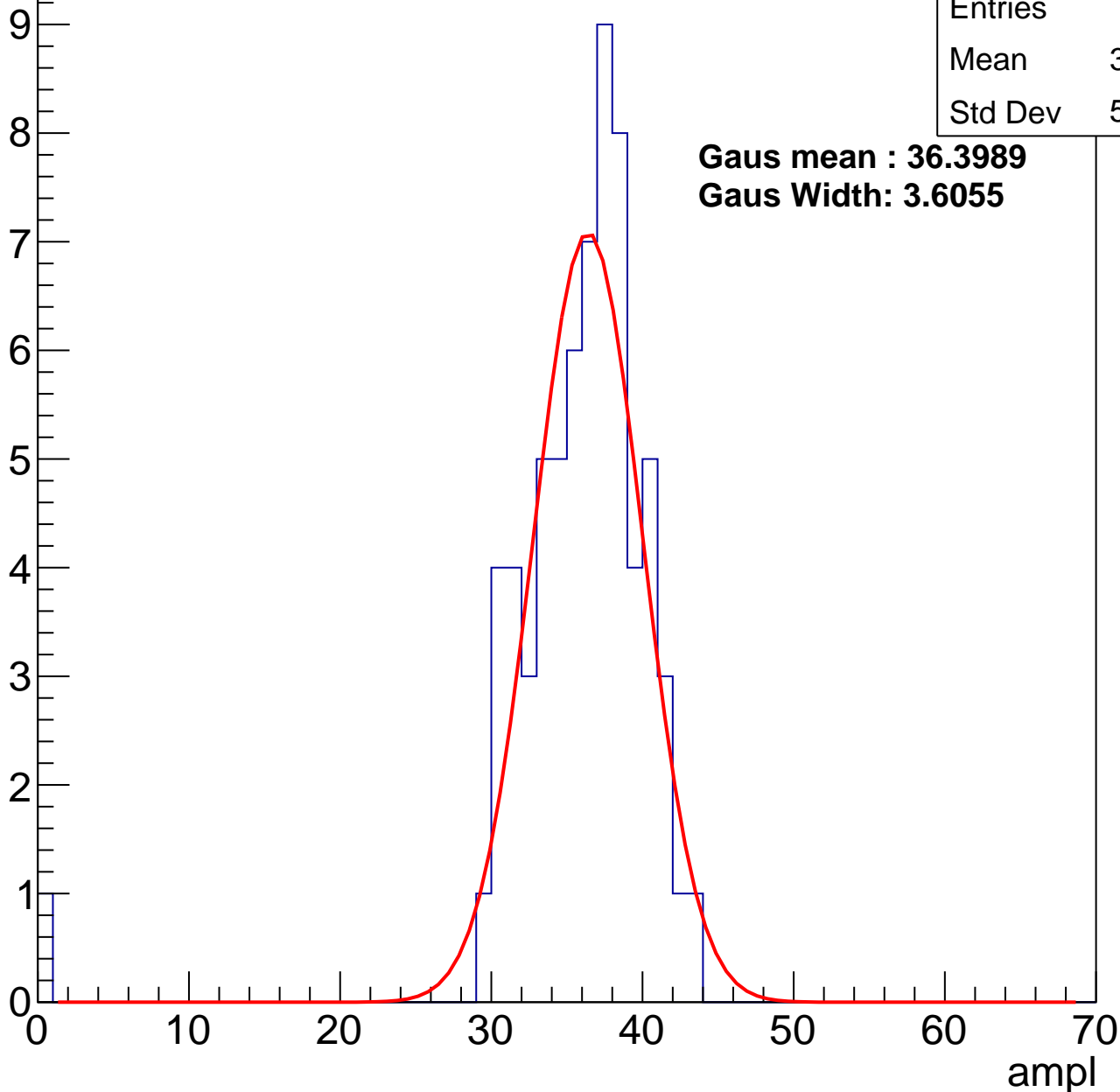
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.33
Std Dev	5.456

Gaus mean : 36.3989

Gaus Width: 3.6055



B1L103S, U6-ch17, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	36.94
Std Dev	14.75

Gaus mean : 43.2656

Gaus Width: 3.1809

Entry

10

8

6

4

2

0

0

10

20

30

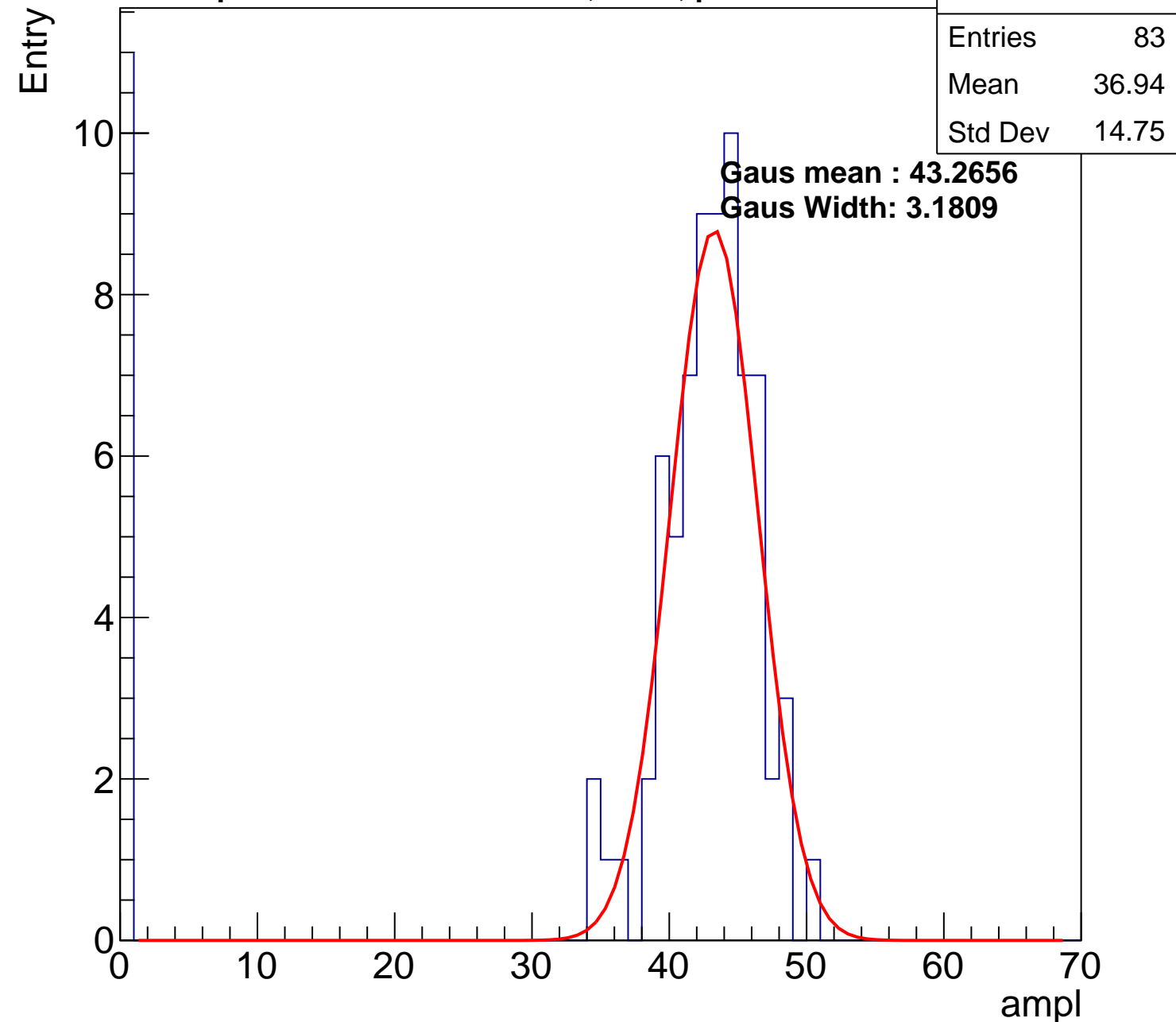
40

50

60

70

ampl

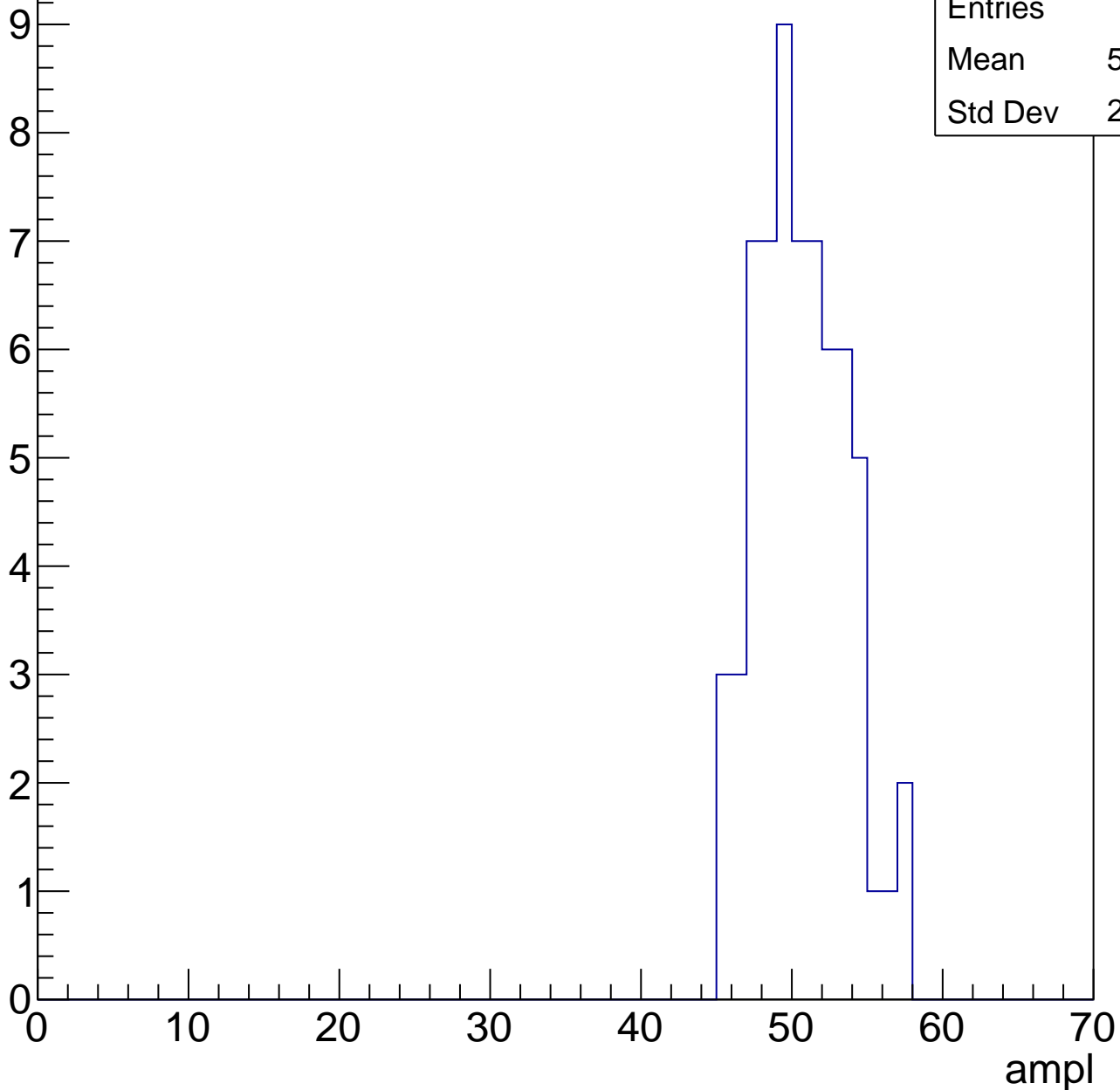


B1L103S, U6-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	50.17
Std Dev	2.918

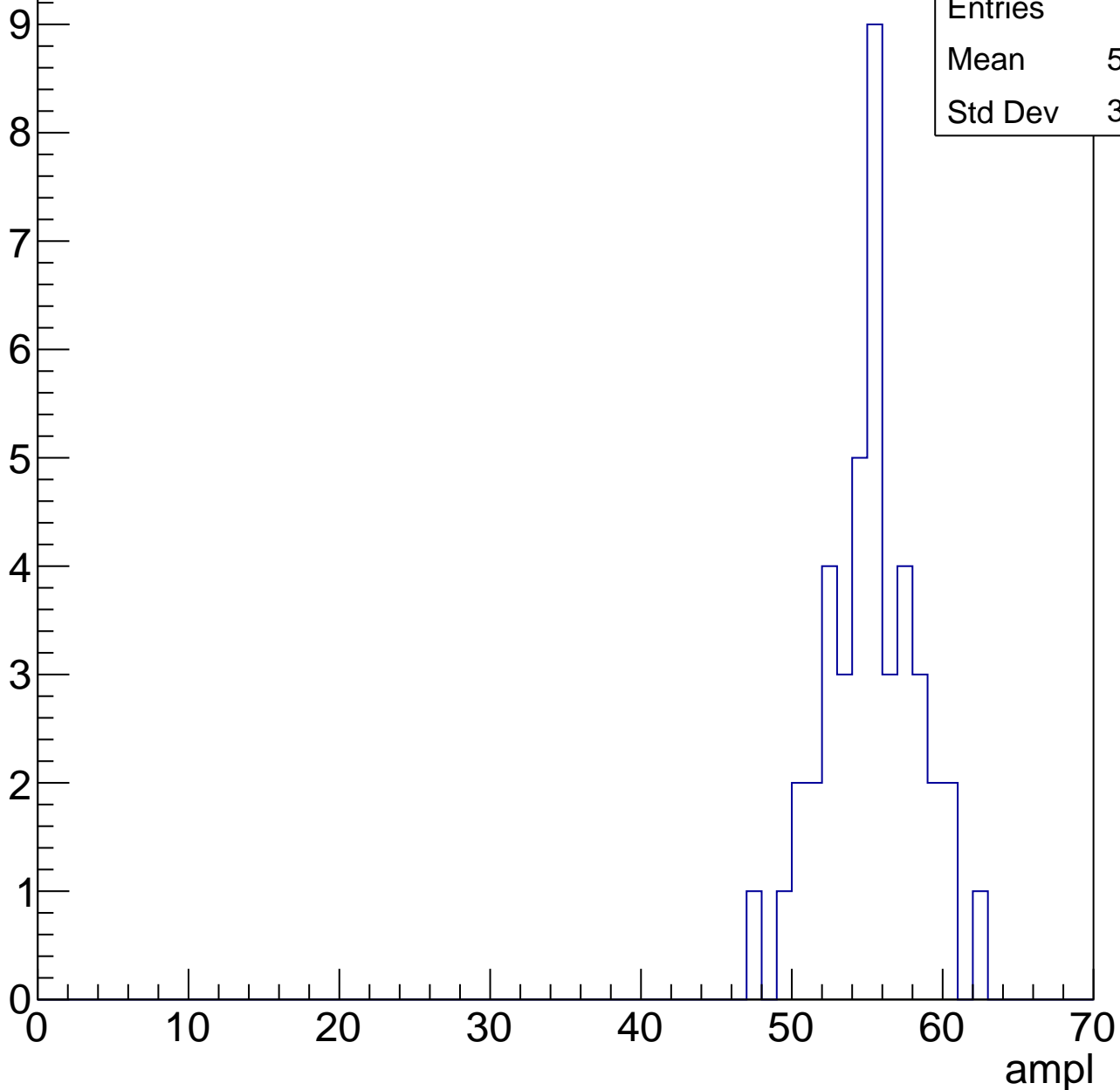


B1L103S, U6-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	54.76
Std Dev	3.123

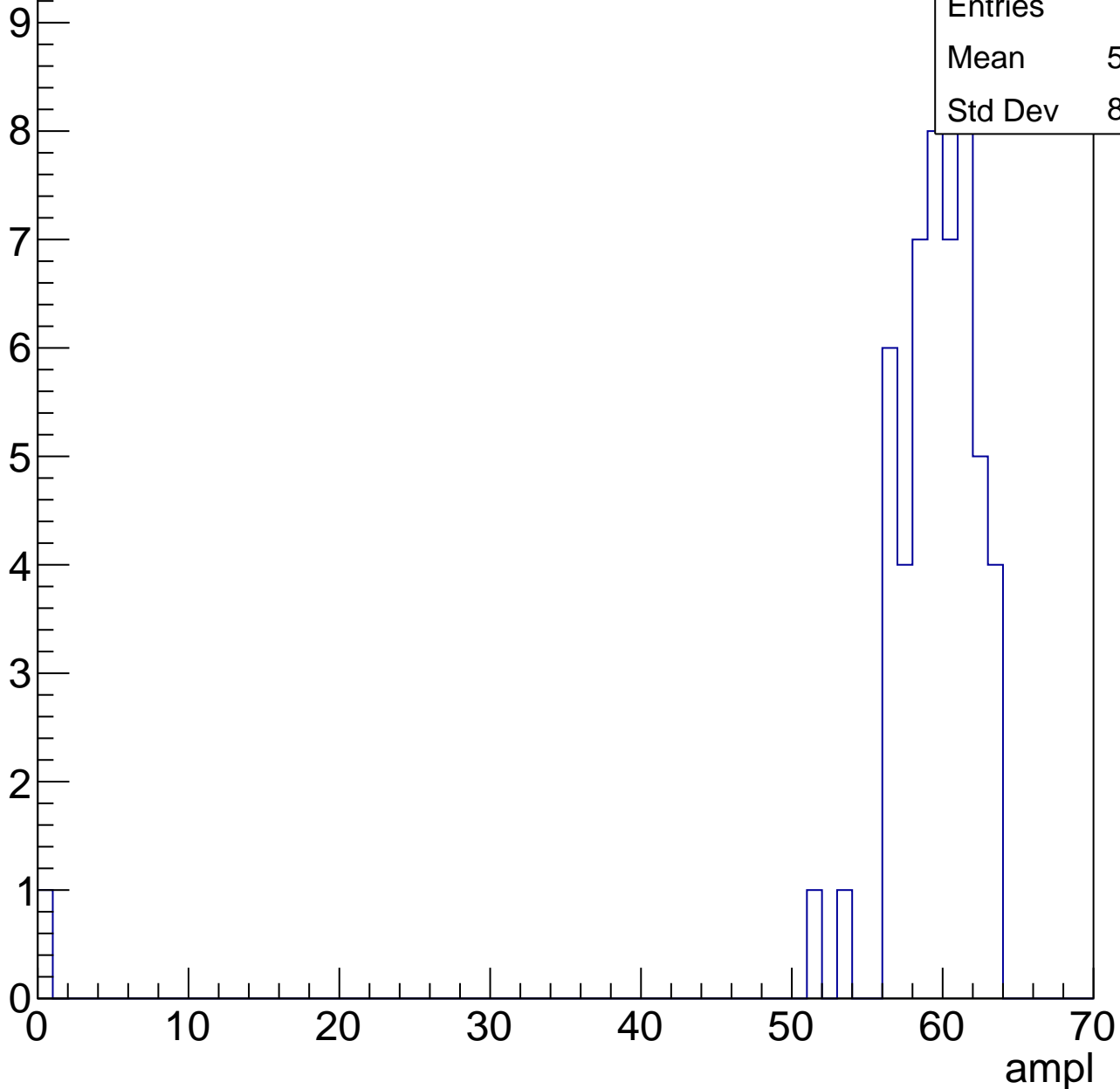


B1L103S, U6-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.06
Std Dev	8.426

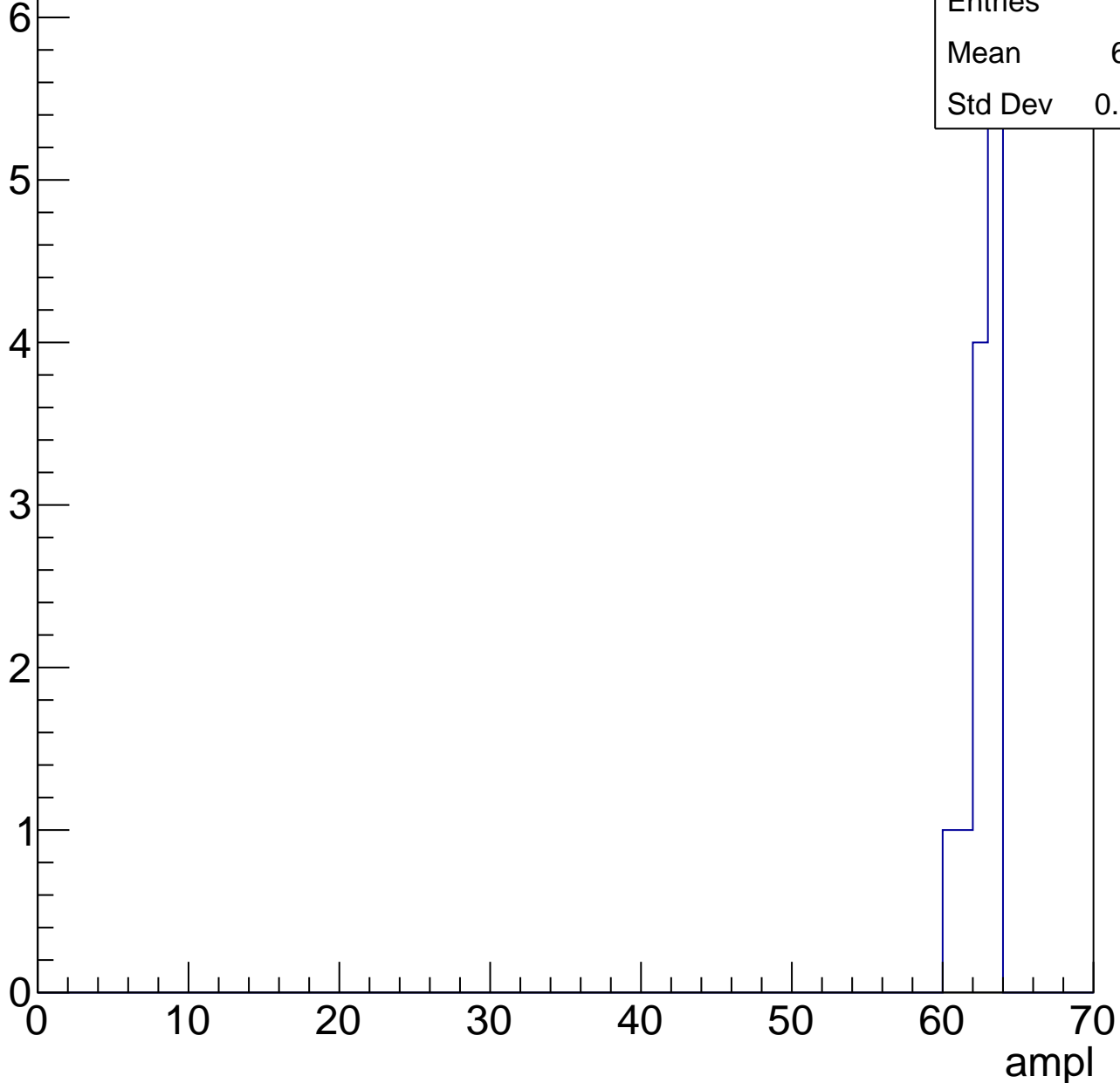


B1L103S, U6-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62.25
Std Dev	0.9242



B1L103S, U6-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	23.77
Std Dev	10.51

Gaus mean : 27.9829

Gaus Width: 4.0141

Entry

10

8

6

4

2

0

0

10

20

30

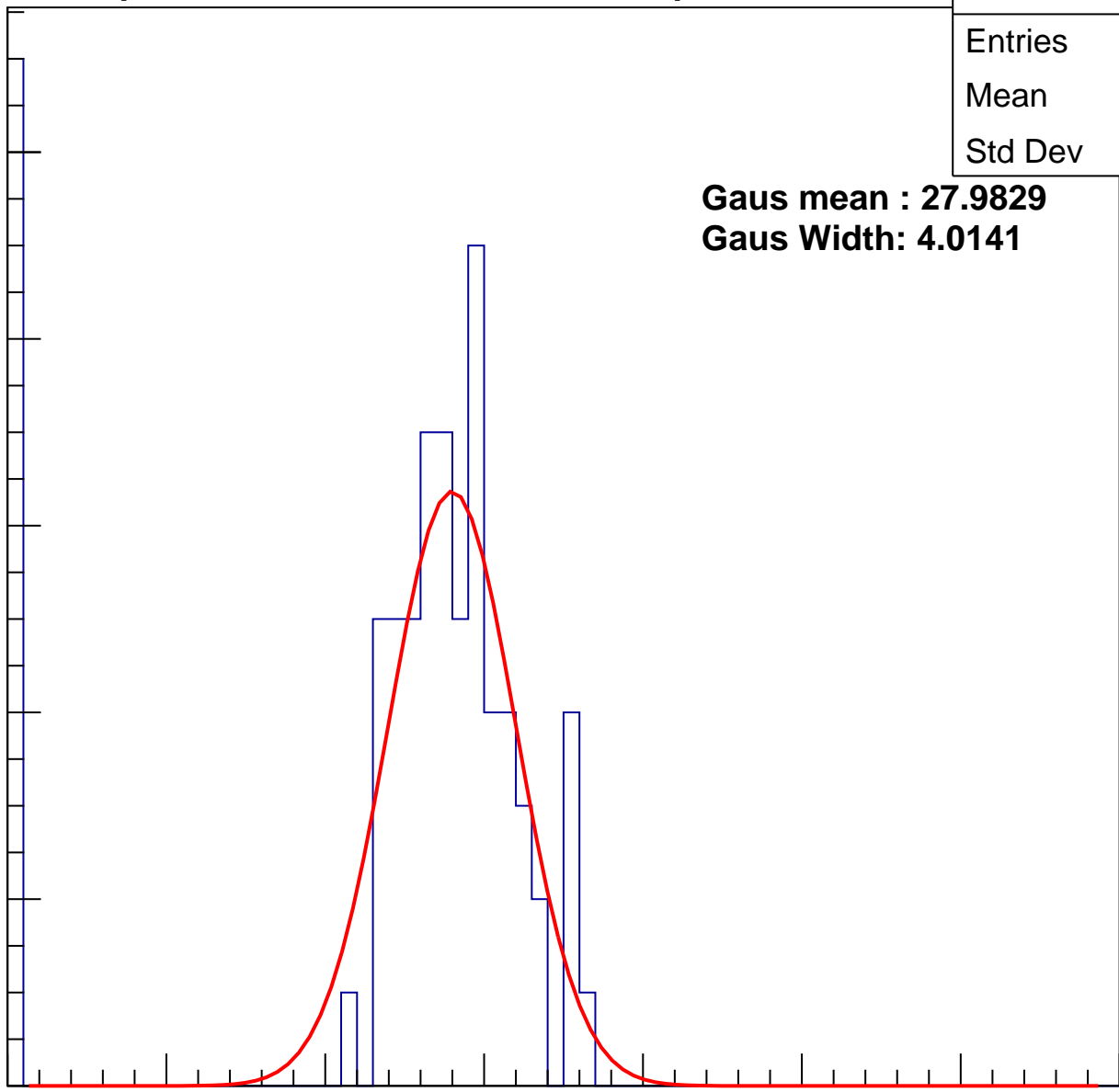
40

50

60

70

ampl



B1L103S, U6-ch18, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	32.89
Std Dev	8.8

Gaus mean : 35.0475

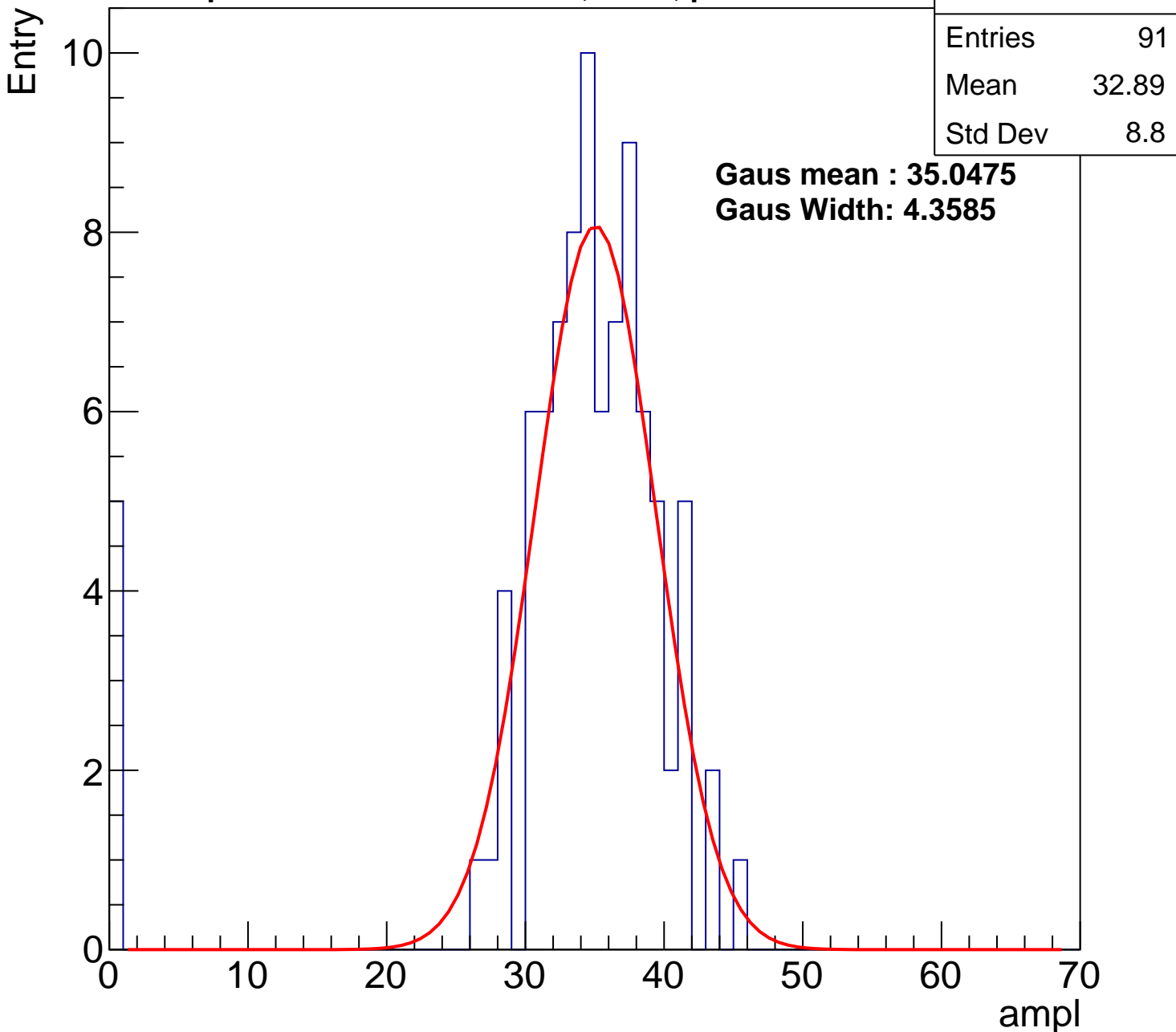
Gaus Width: 4.3585

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch18, adc2

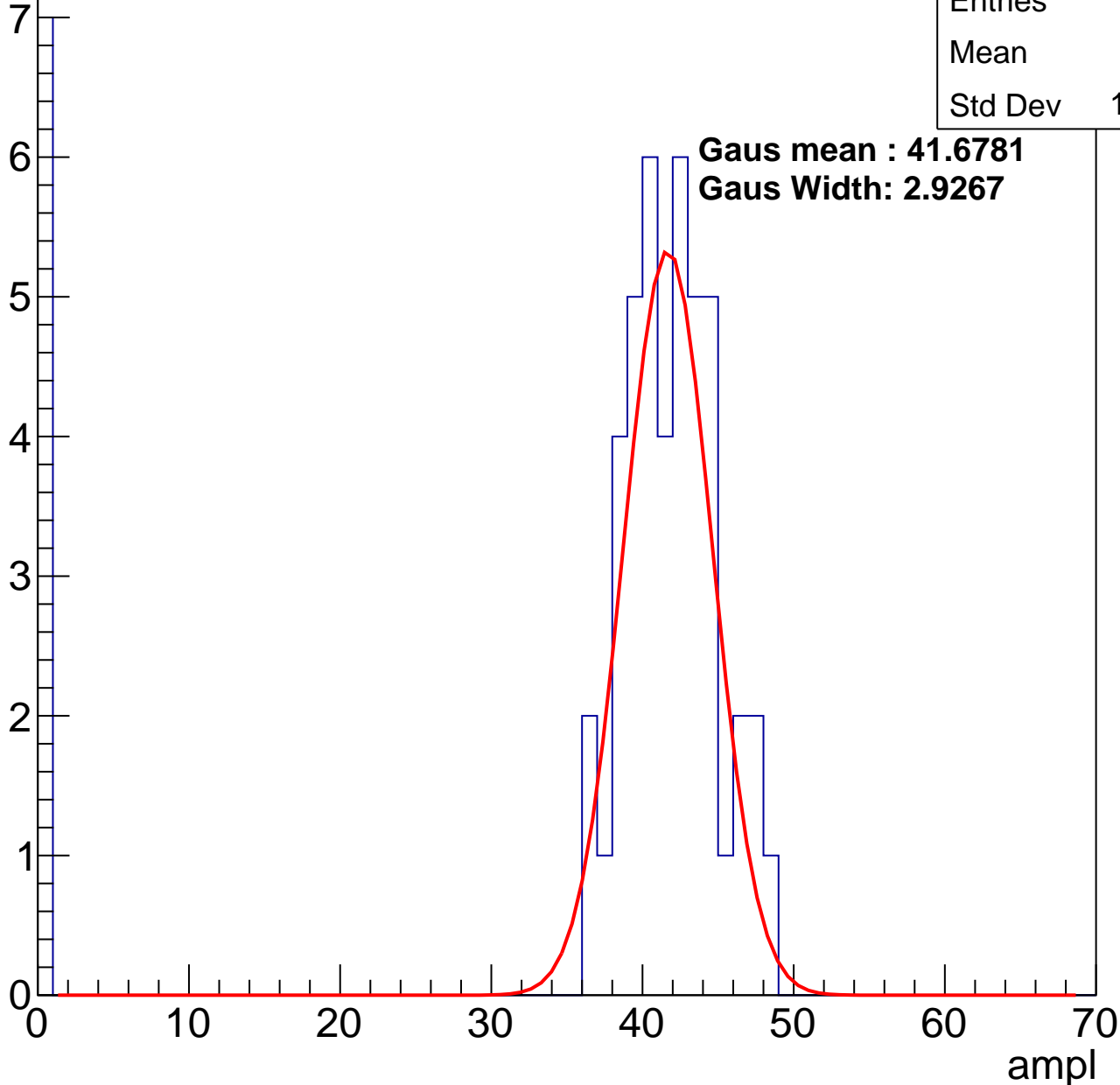
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	35.8
Std Dev	14.54

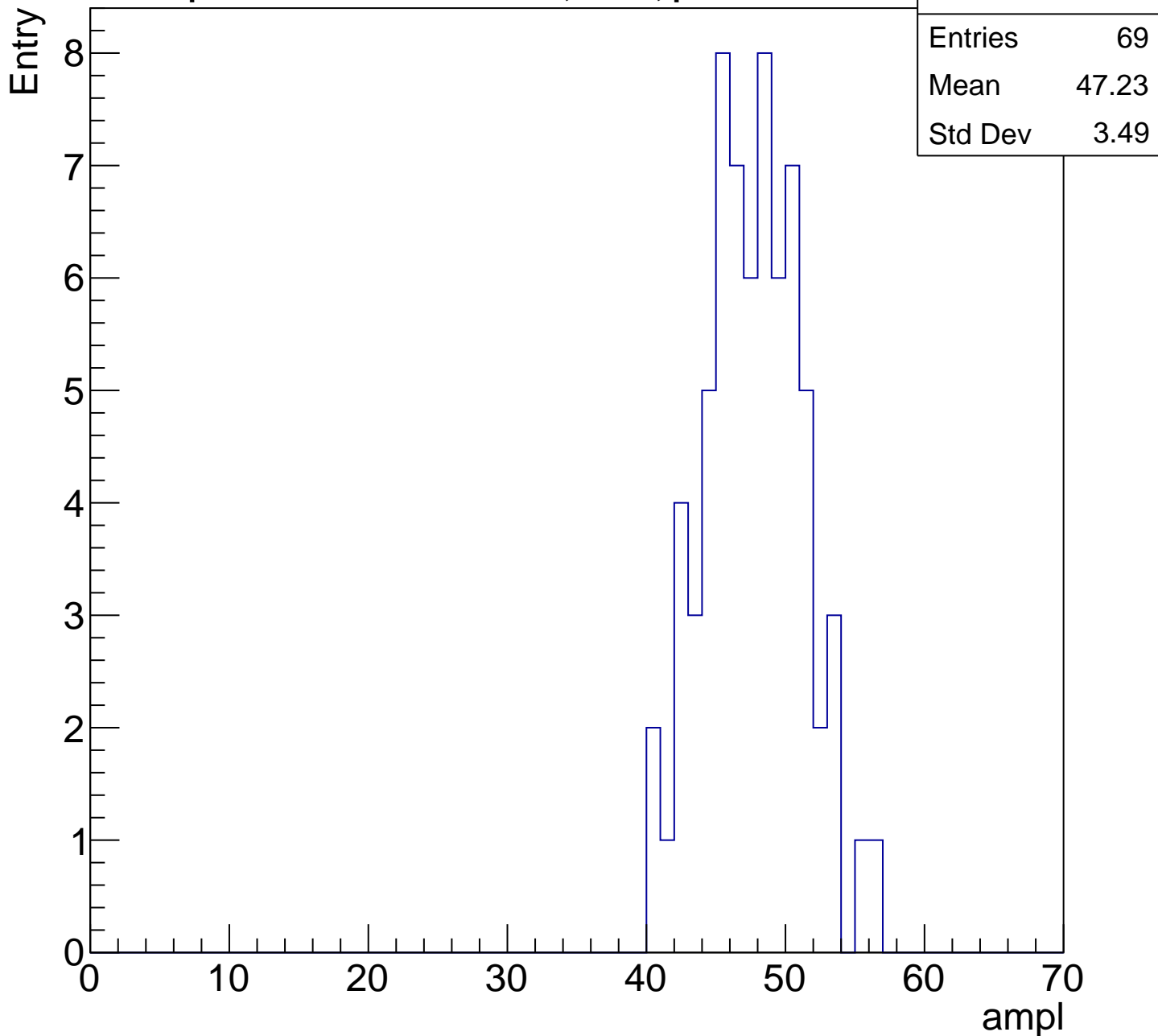
Gaus mean : 41.6781

Gaus Width: 2.9267



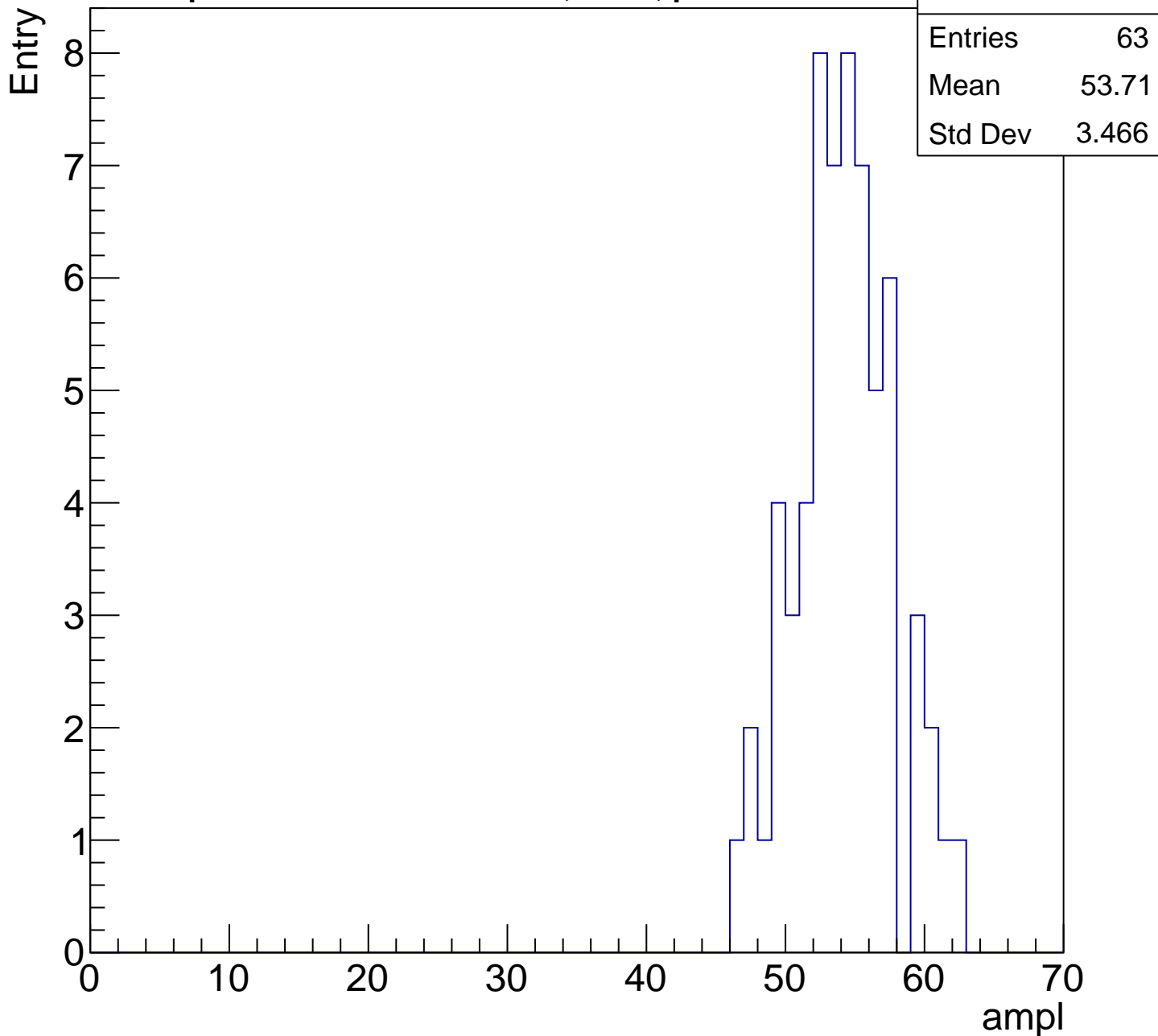
B1L103S, U6-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

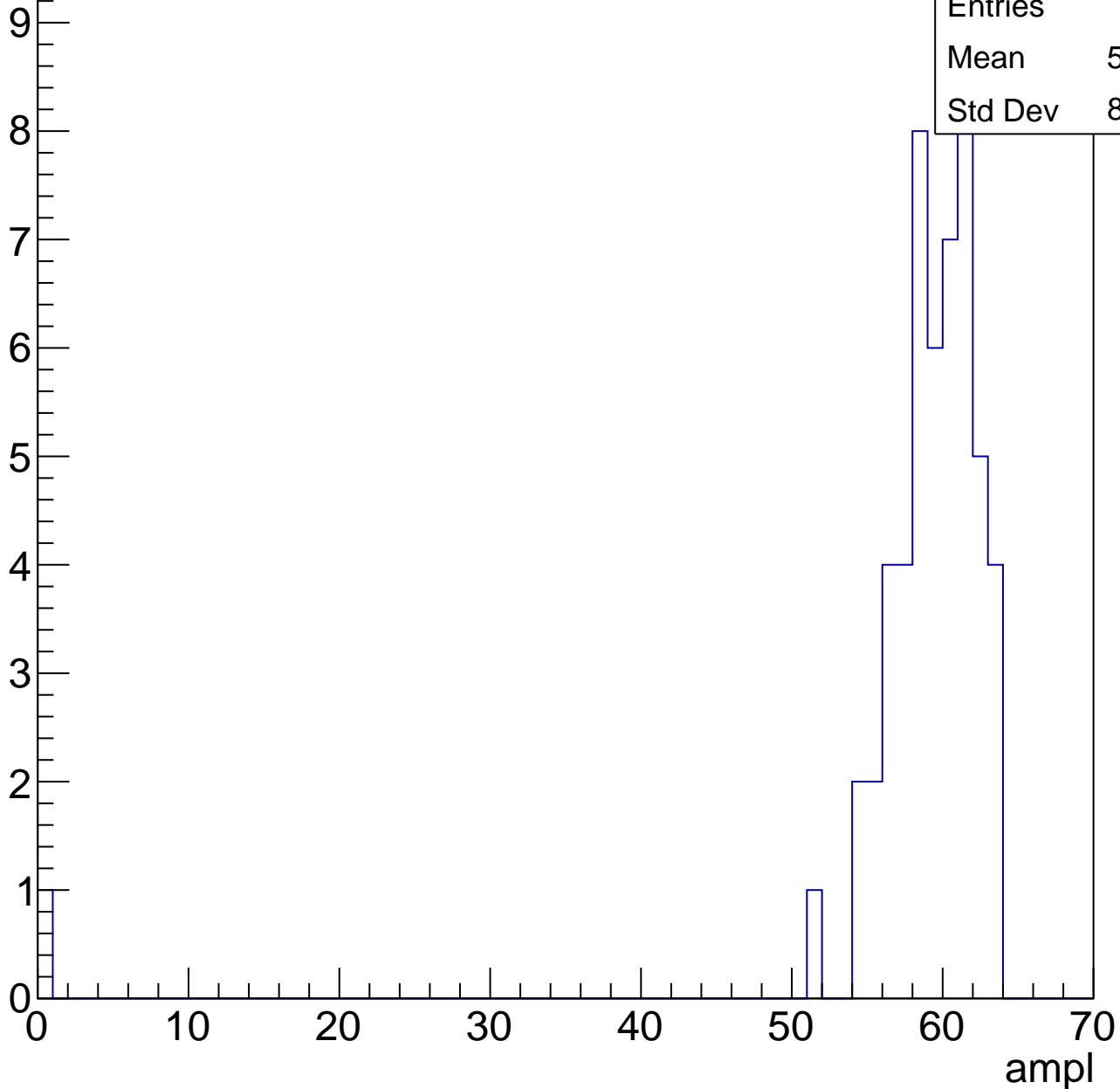


B1L103S, U6-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.92
Std Dev	8.443

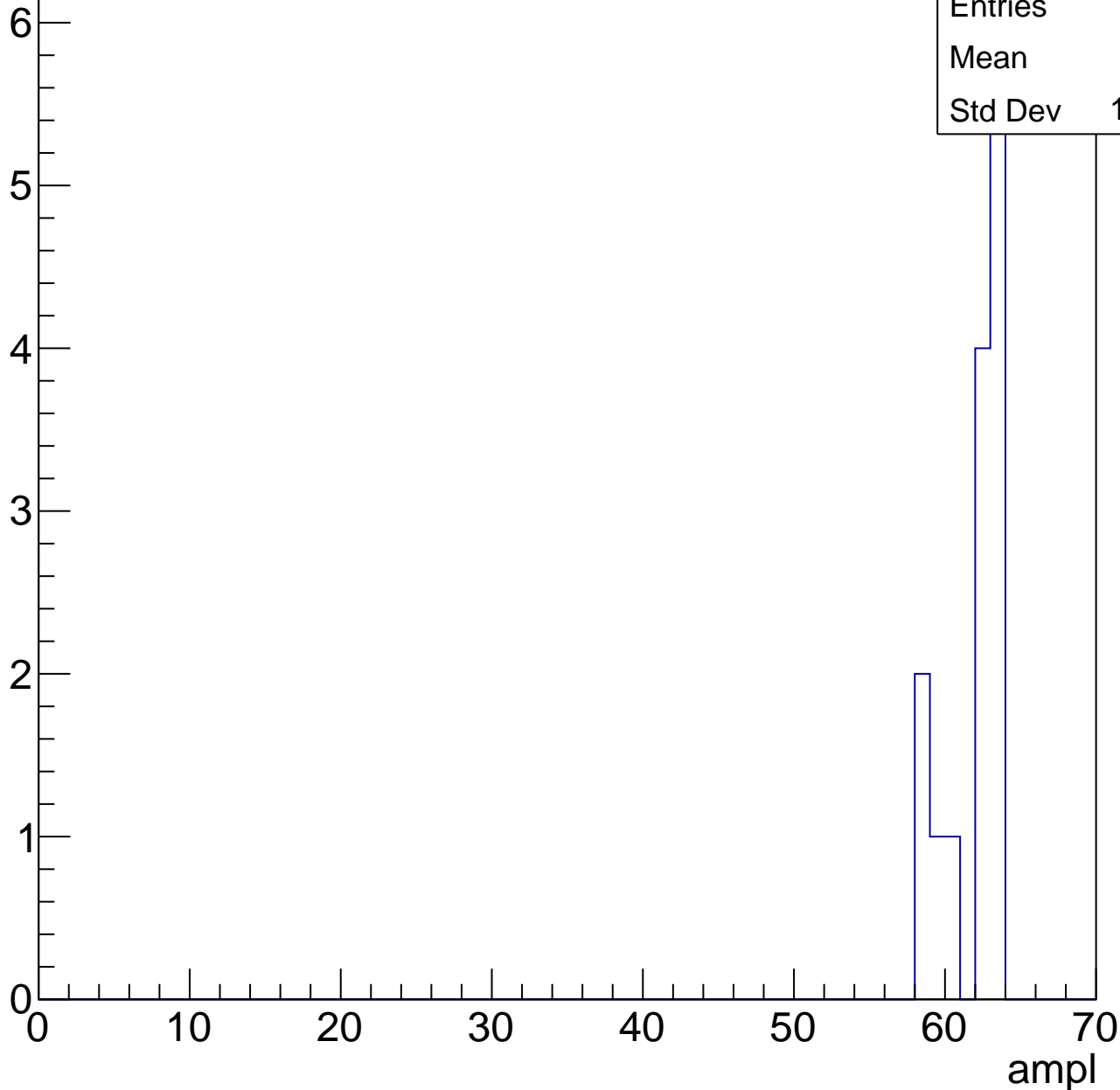


B1L103S, U6-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.5
Std Dev	1.842



B1L103S, U6-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

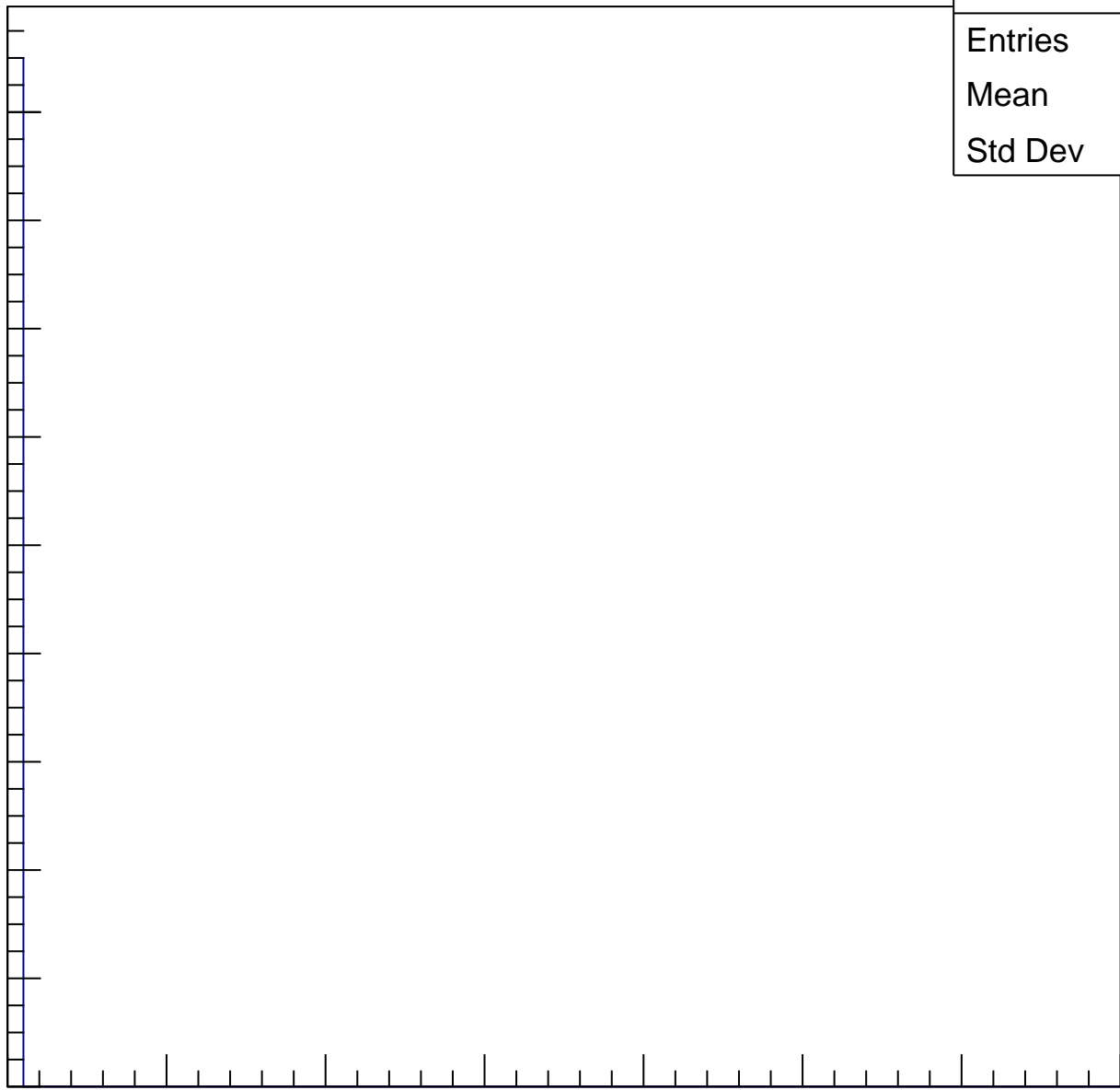
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch19, adc0

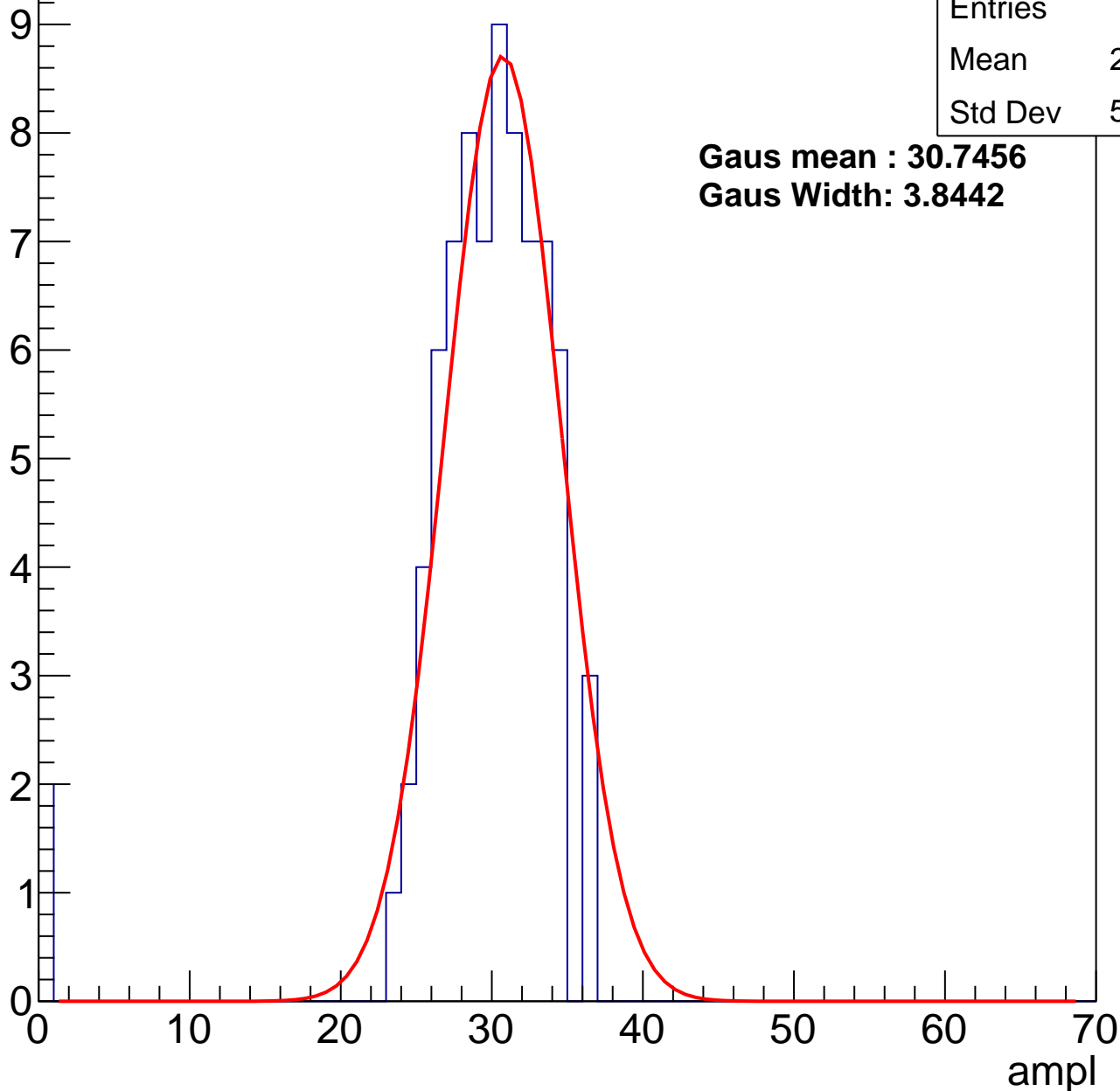
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	28.94
Std Dev	5.623

Gaus mean : 30.7456

Gaus Width: 3.8442



B1L103S, U6-ch19, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	36.86
Std Dev	2.977

Gaus mean : 37.3468

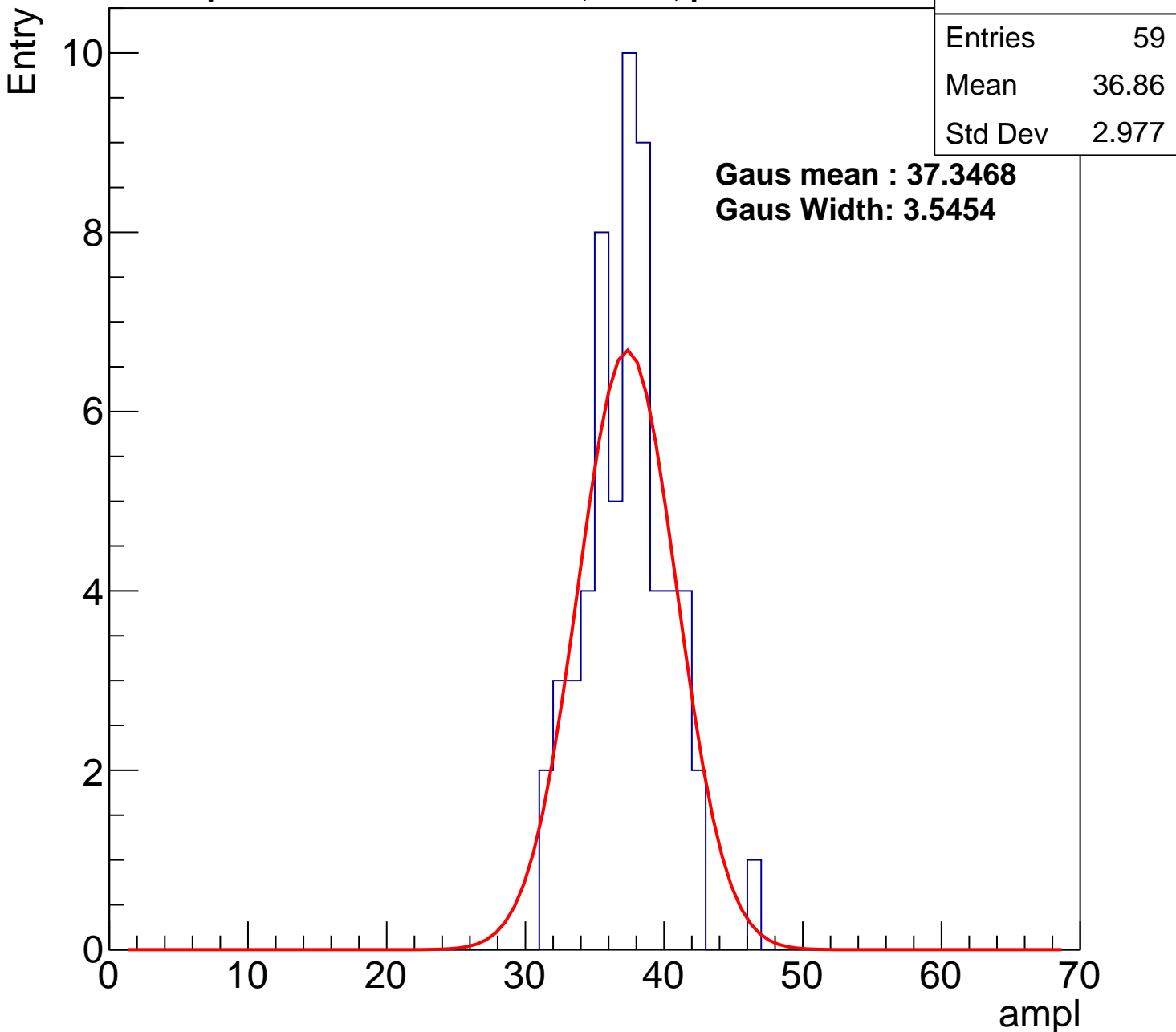
Gaus Width: 3.5454

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch19, adc2

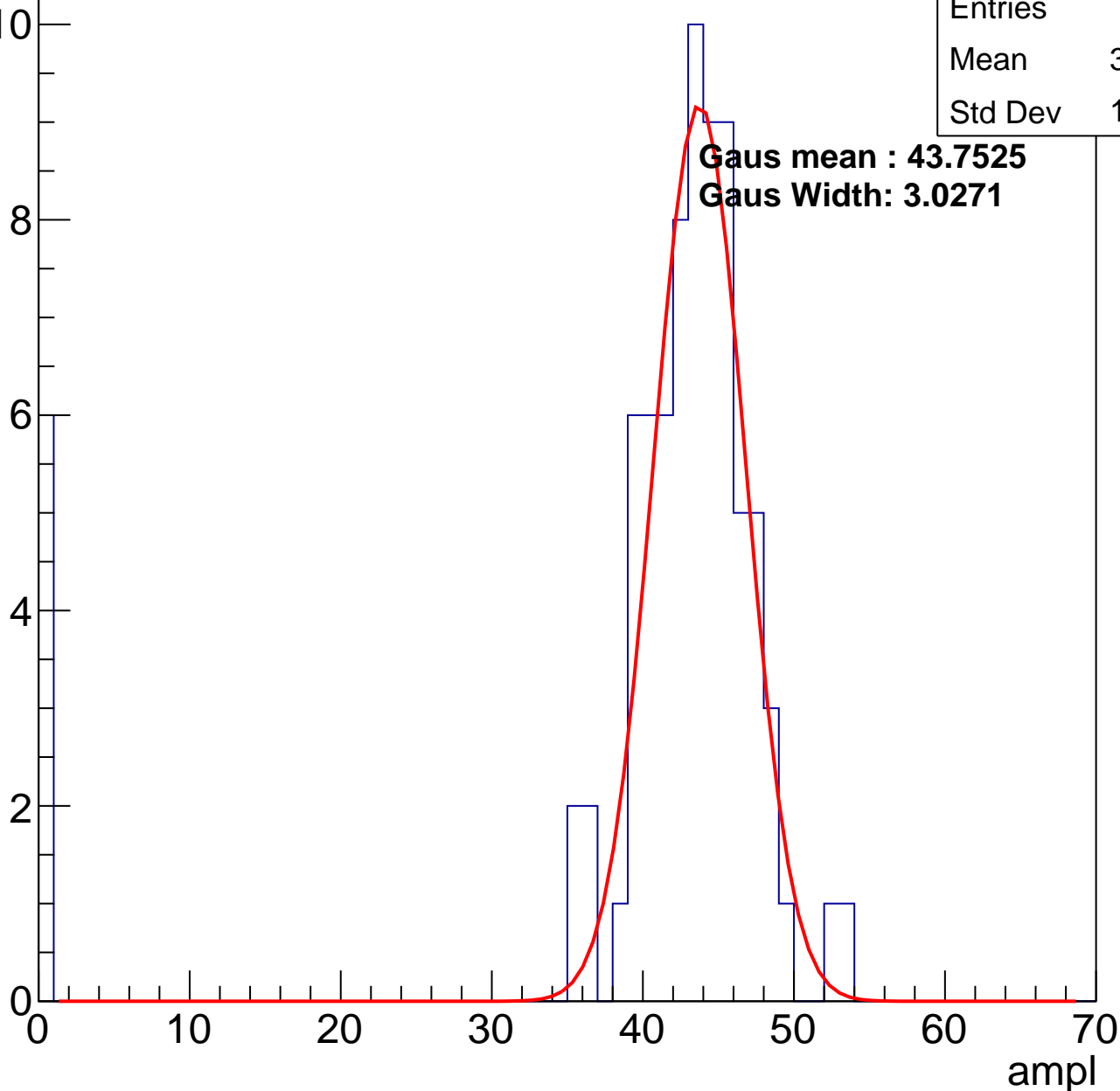
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	39.88
Std Dev	11.76

Gaus mean : 43.7525

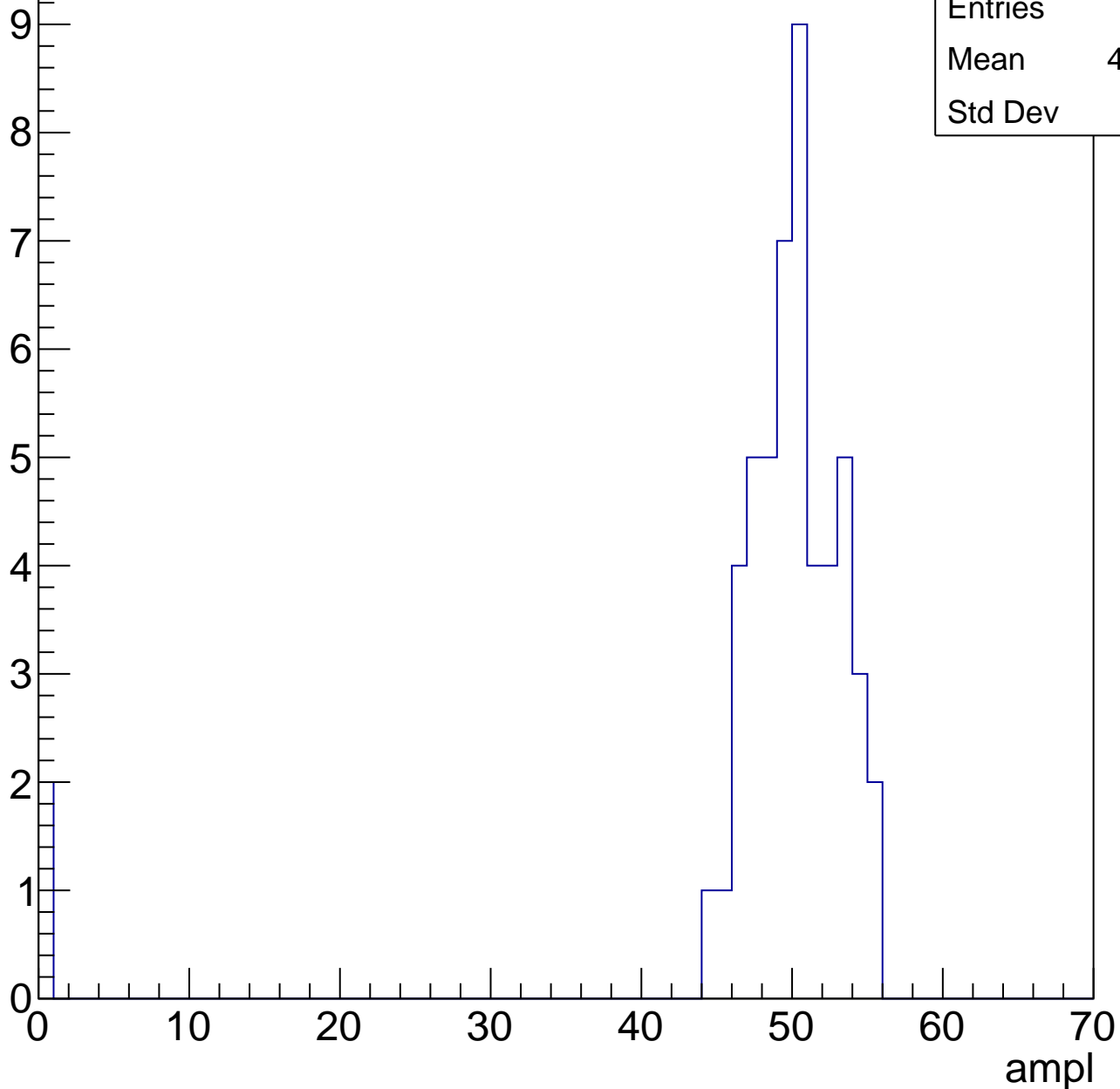
Gaus Width: 3.0271



B1L103S, U6-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

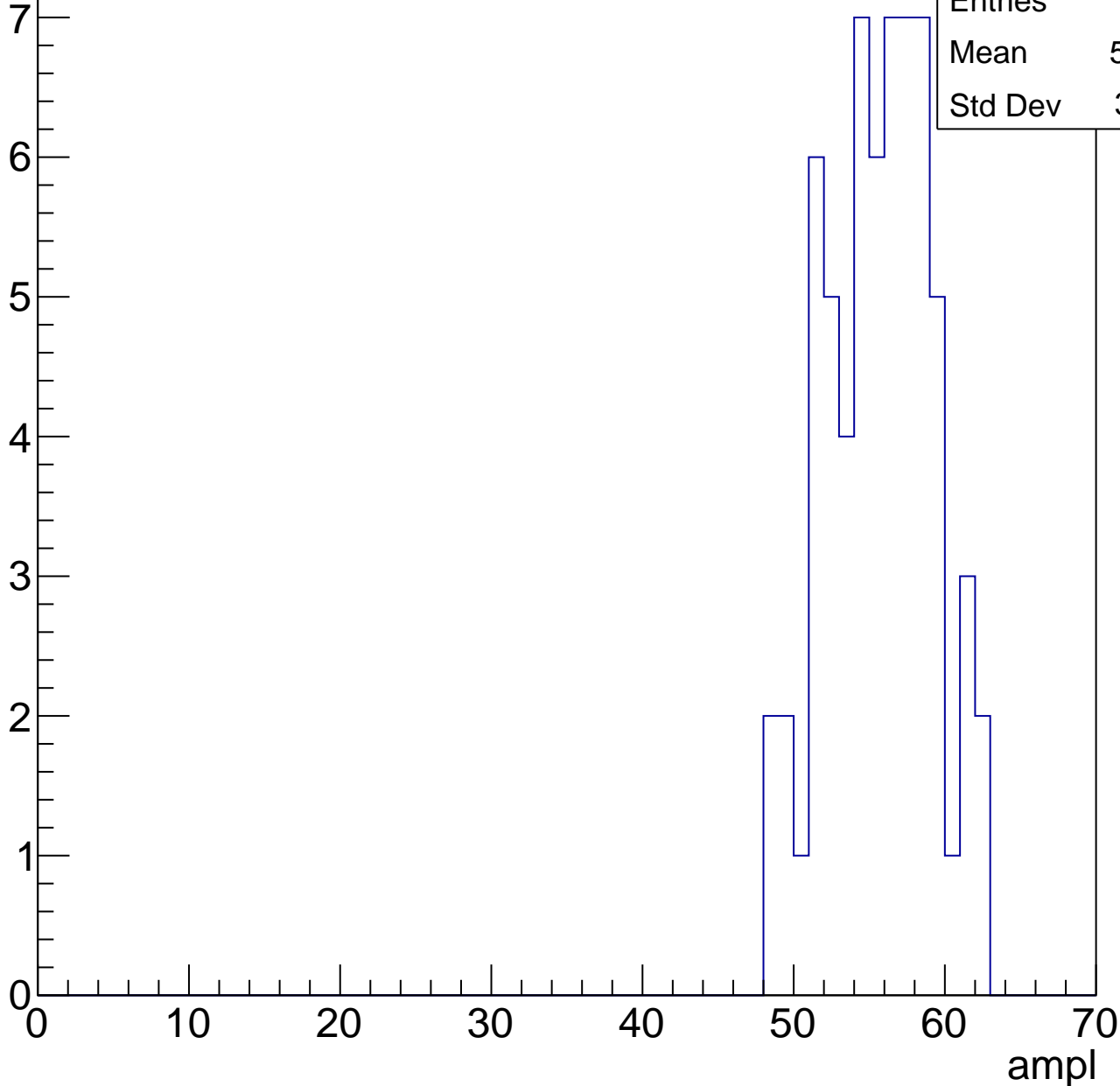


B1L103S, U6-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.22
Std Dev	3.431



B1L103S, U6-ch19, adc5

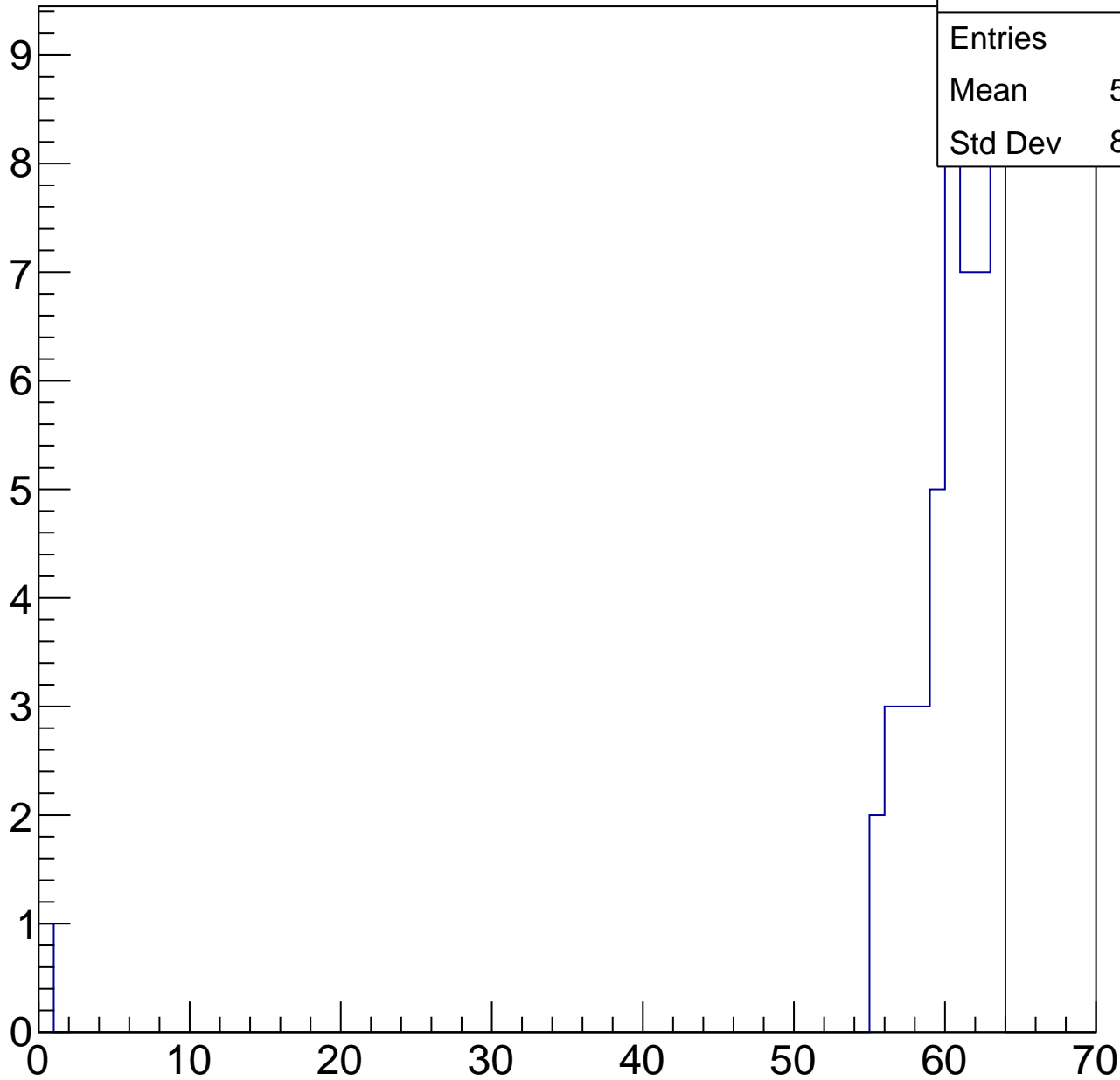
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.88
Std Dev	8.894

ampl



B1L103S, U6-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U6-ch20, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12
10
8
6
4
2
0

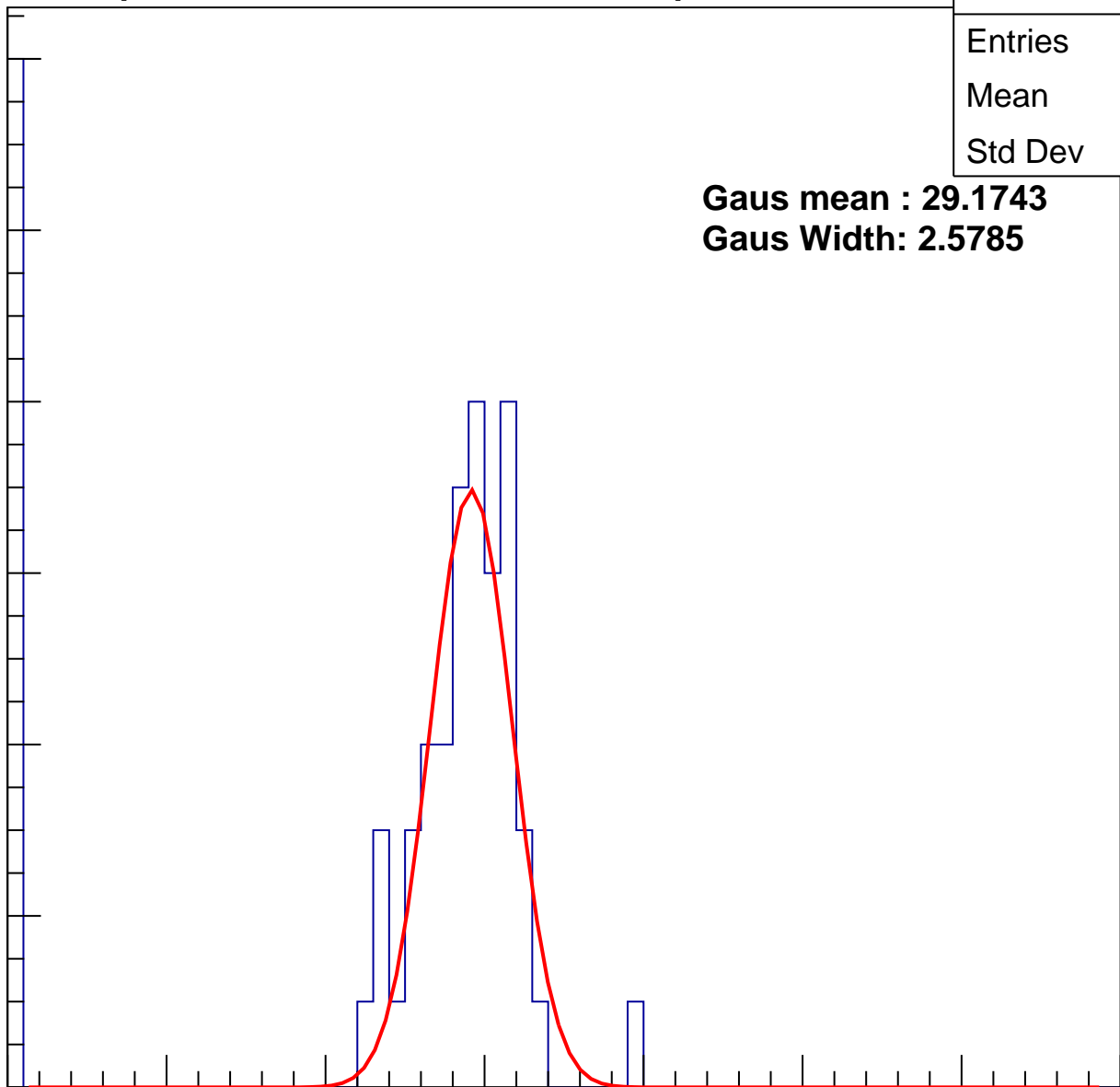
Entries	62
Mean	23
Std Dev	11.59

Gaus mean : 29.1743

Gaus Width: 2.5785

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch20, adc1

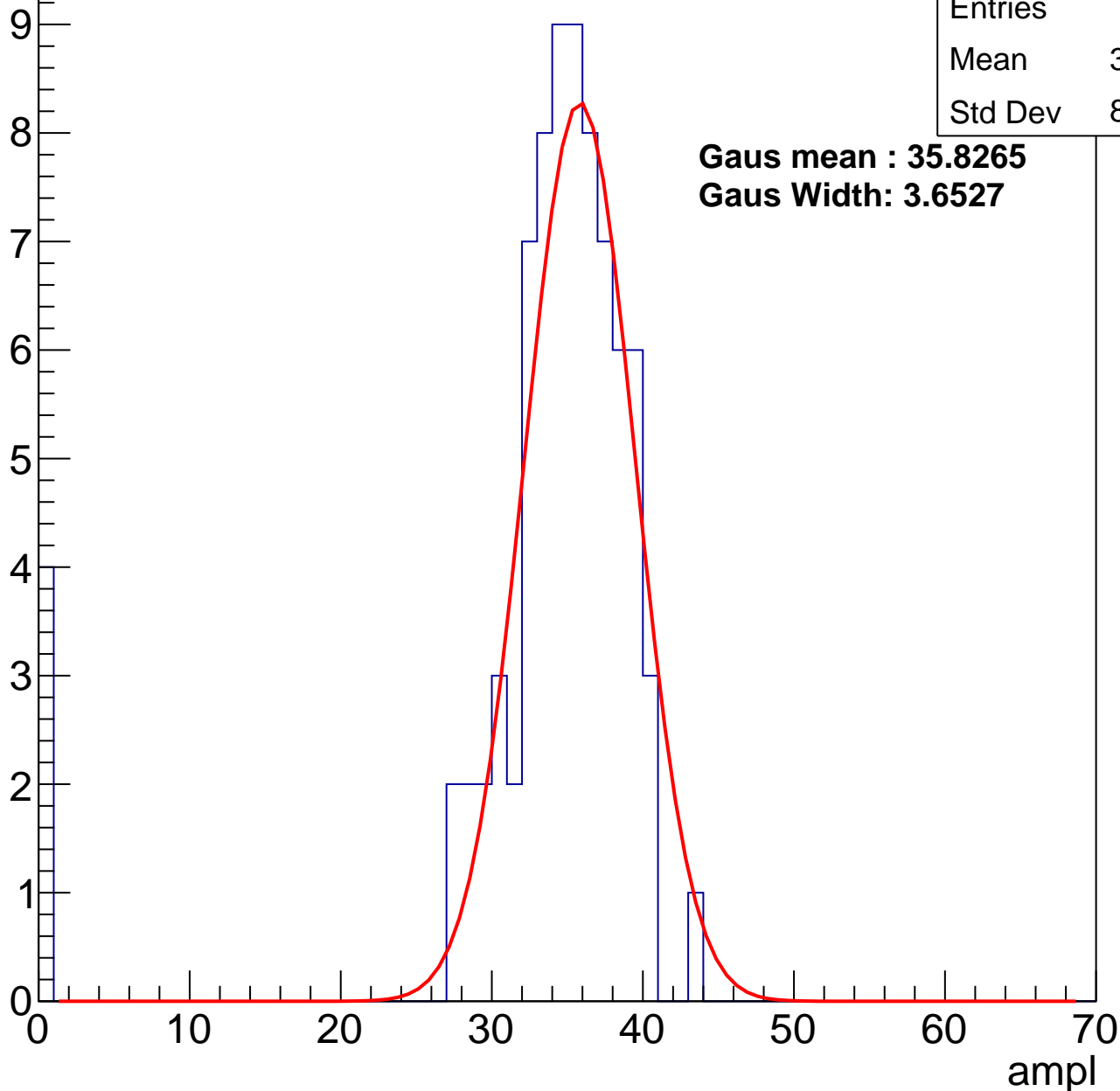
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	32.92
Std Dev	8.263

Gaus mean : 35.8265

Gaus Width: 3.6527



B1L103S, U6-ch20, adc2

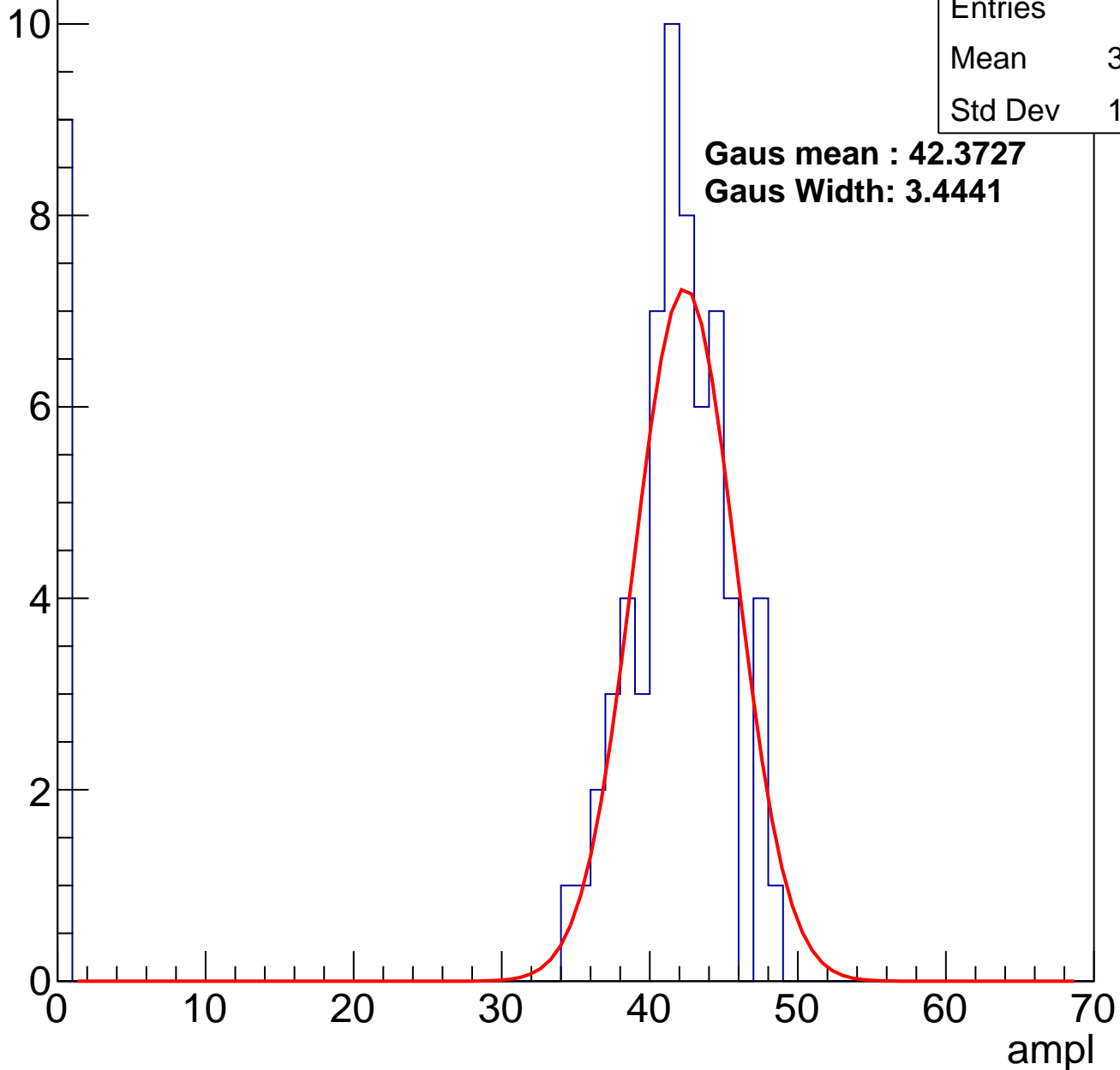
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	36.13
Std Dev	14.17

Gaus mean : 42.3727

Gaus Width: 3.4441

Entry

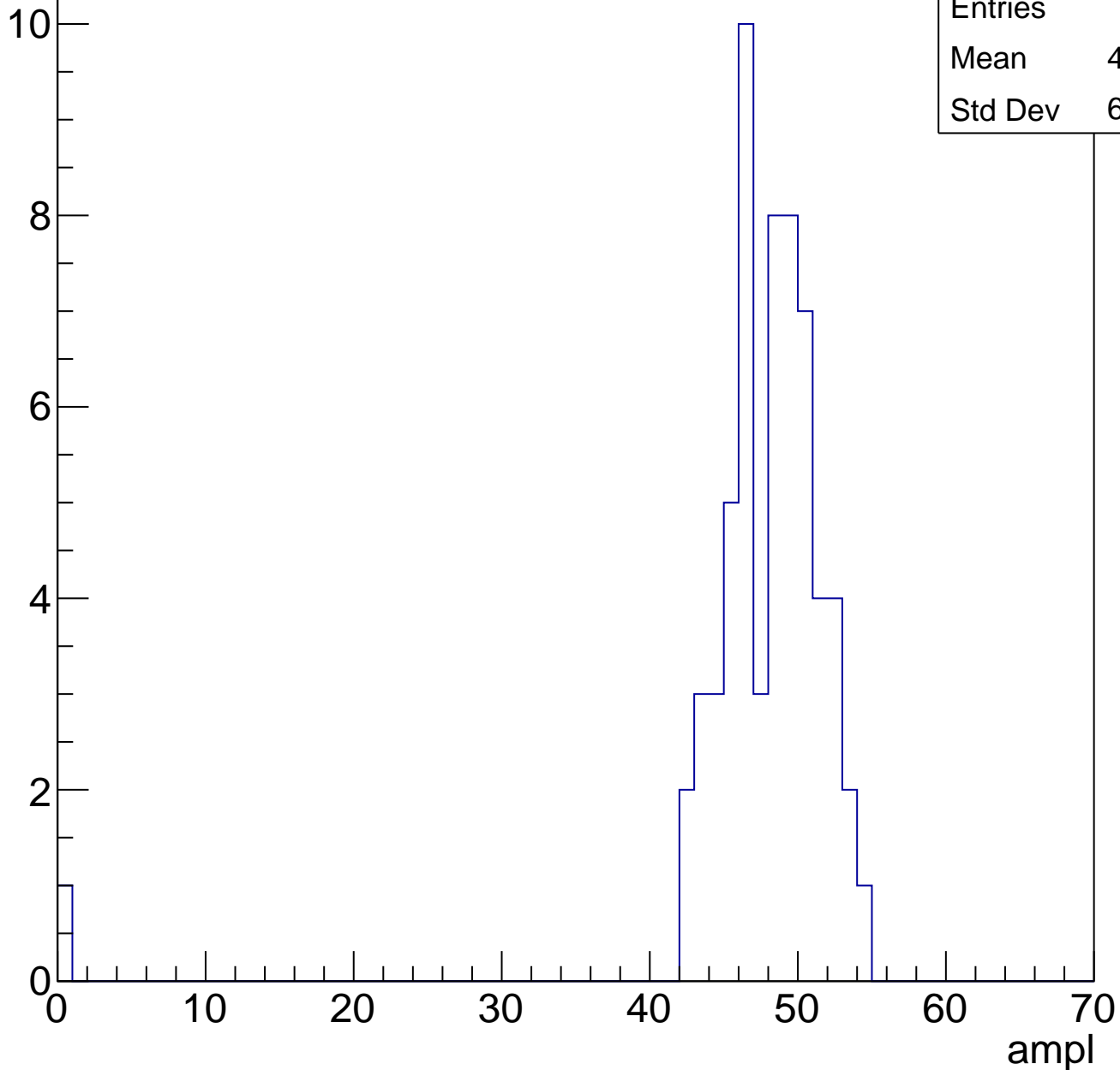


B1L103S, U6-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	47.03
Std Dev	6.717

Entry

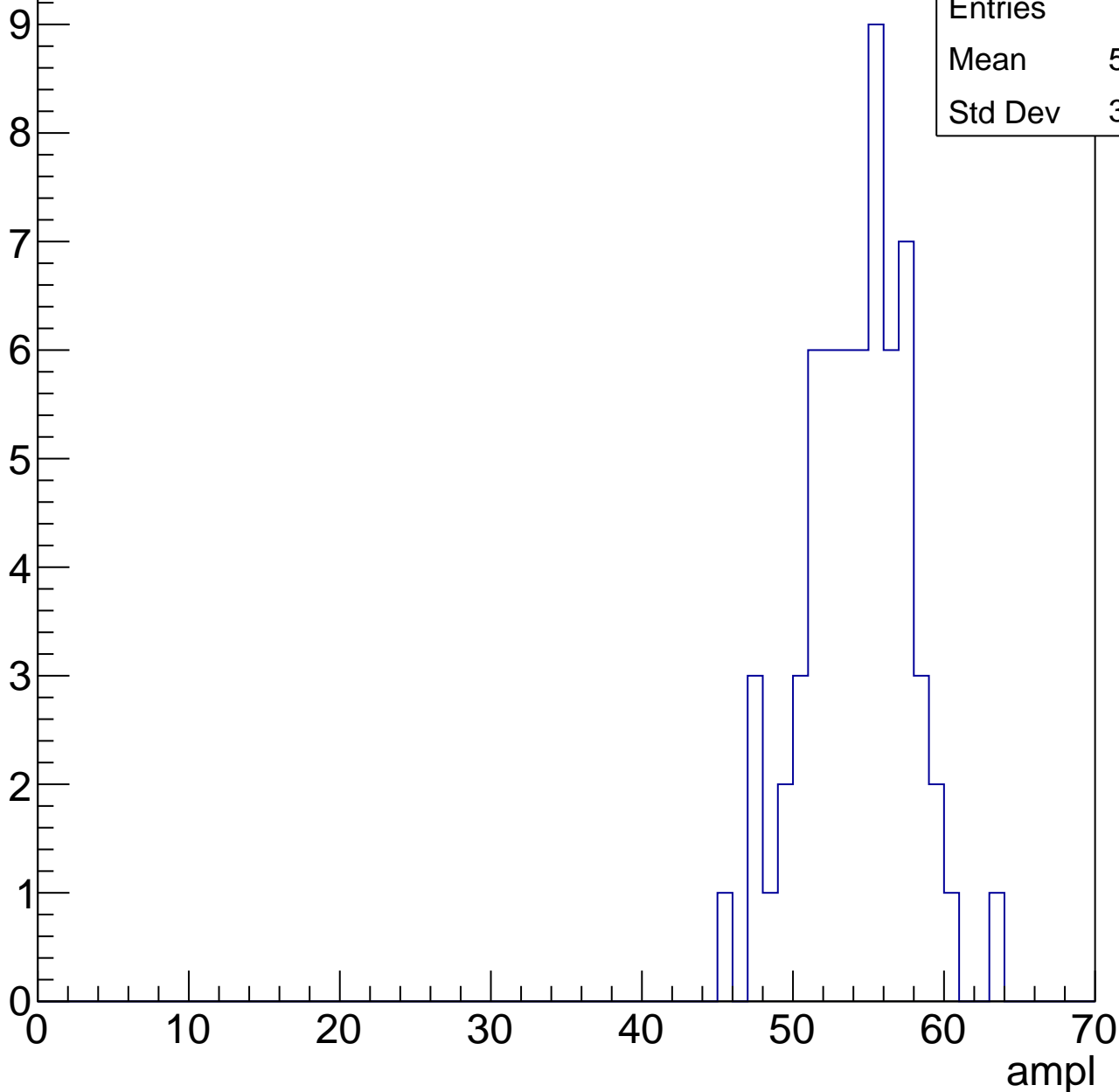


B1L103S, U6-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.76
Std Dev	3.458

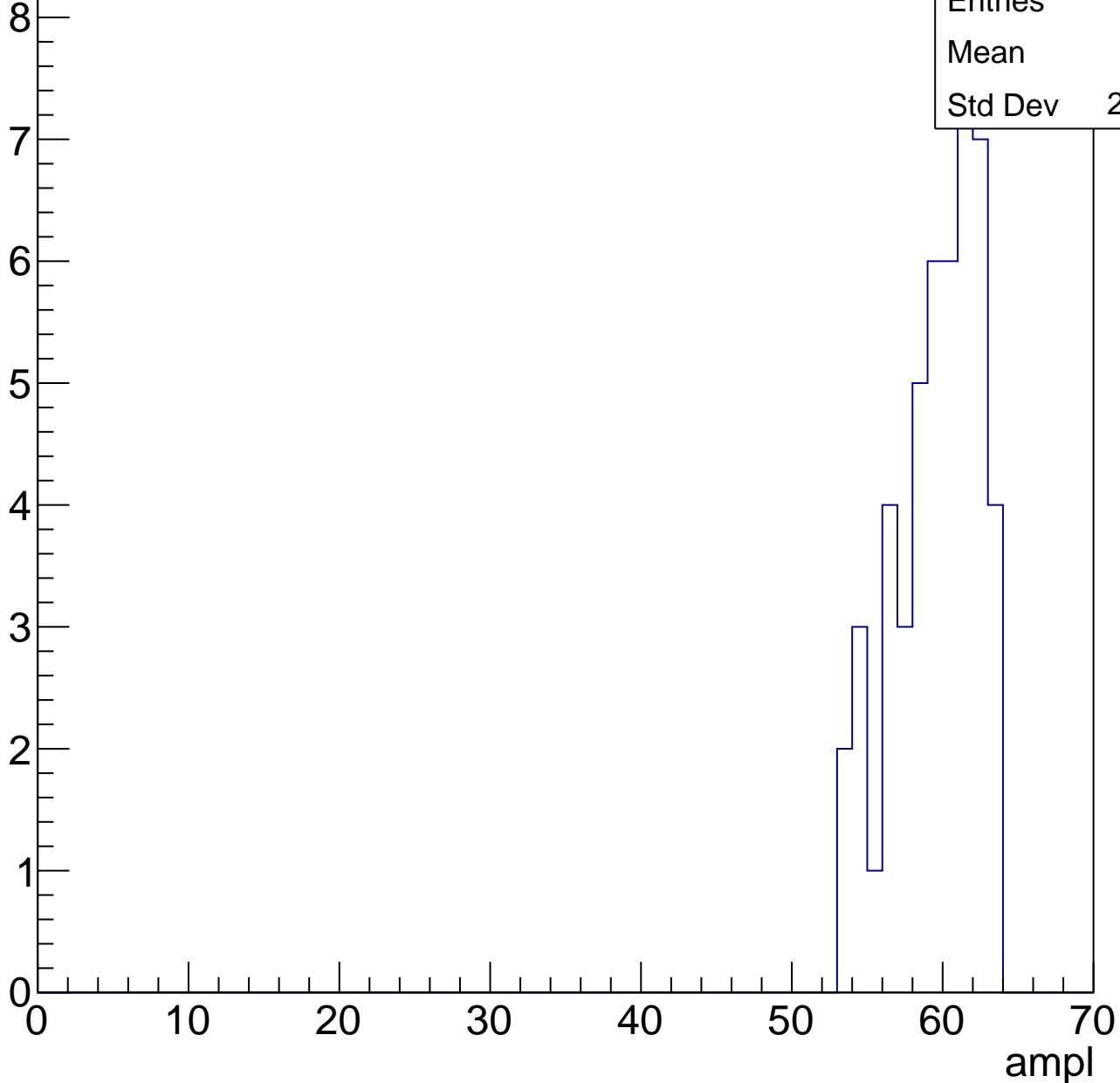


B1L103S, U6-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

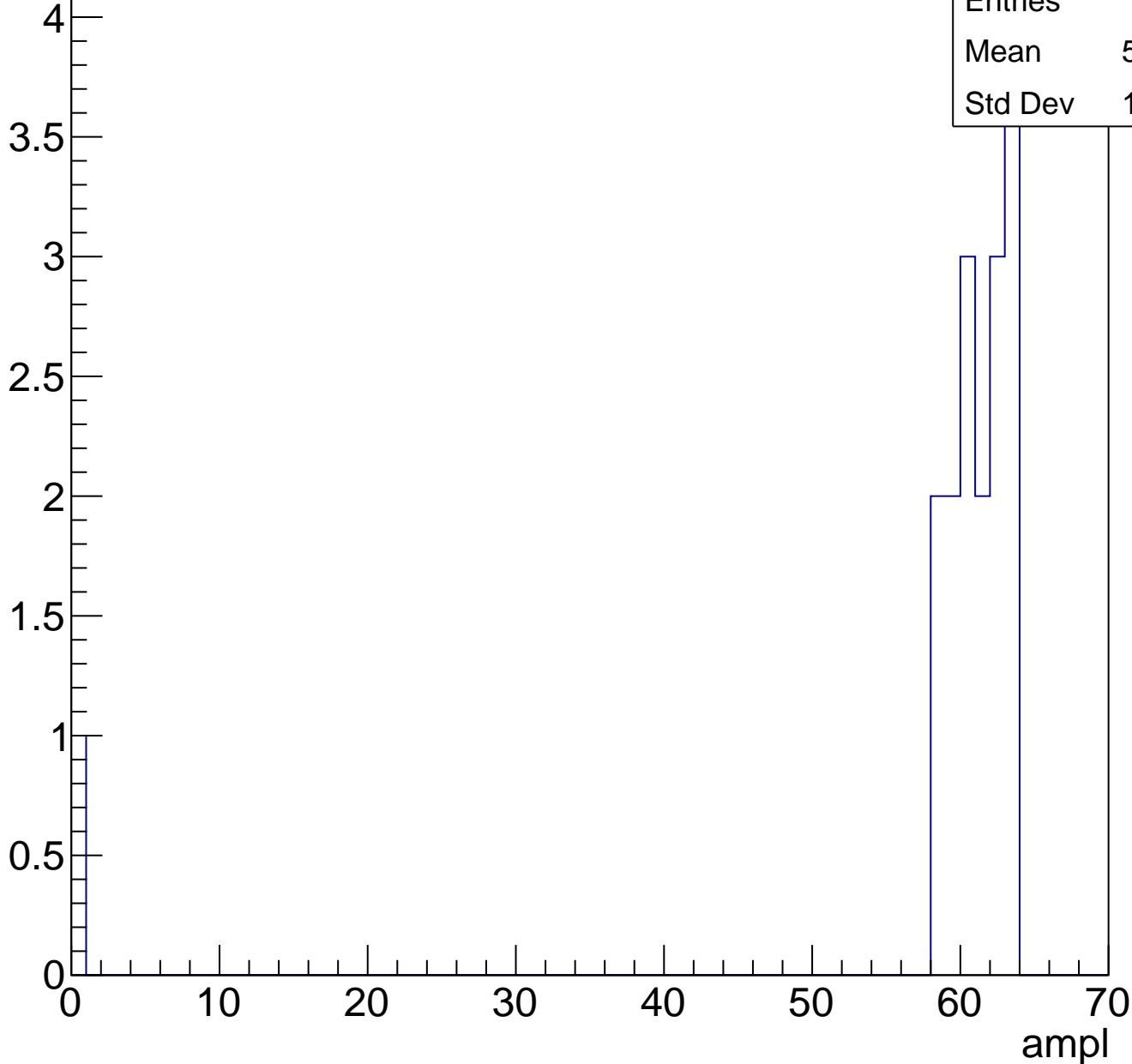
Entries	49
Mean	59.1
Std Dev	2.787



B1L103S, U6-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U6-ch21, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	25.8
Std Dev	11.32

Gaus mean : 30.8049

Gaus Width: 3.8052

Entry

12

10

8

6

4

2

0

0

10

20

30

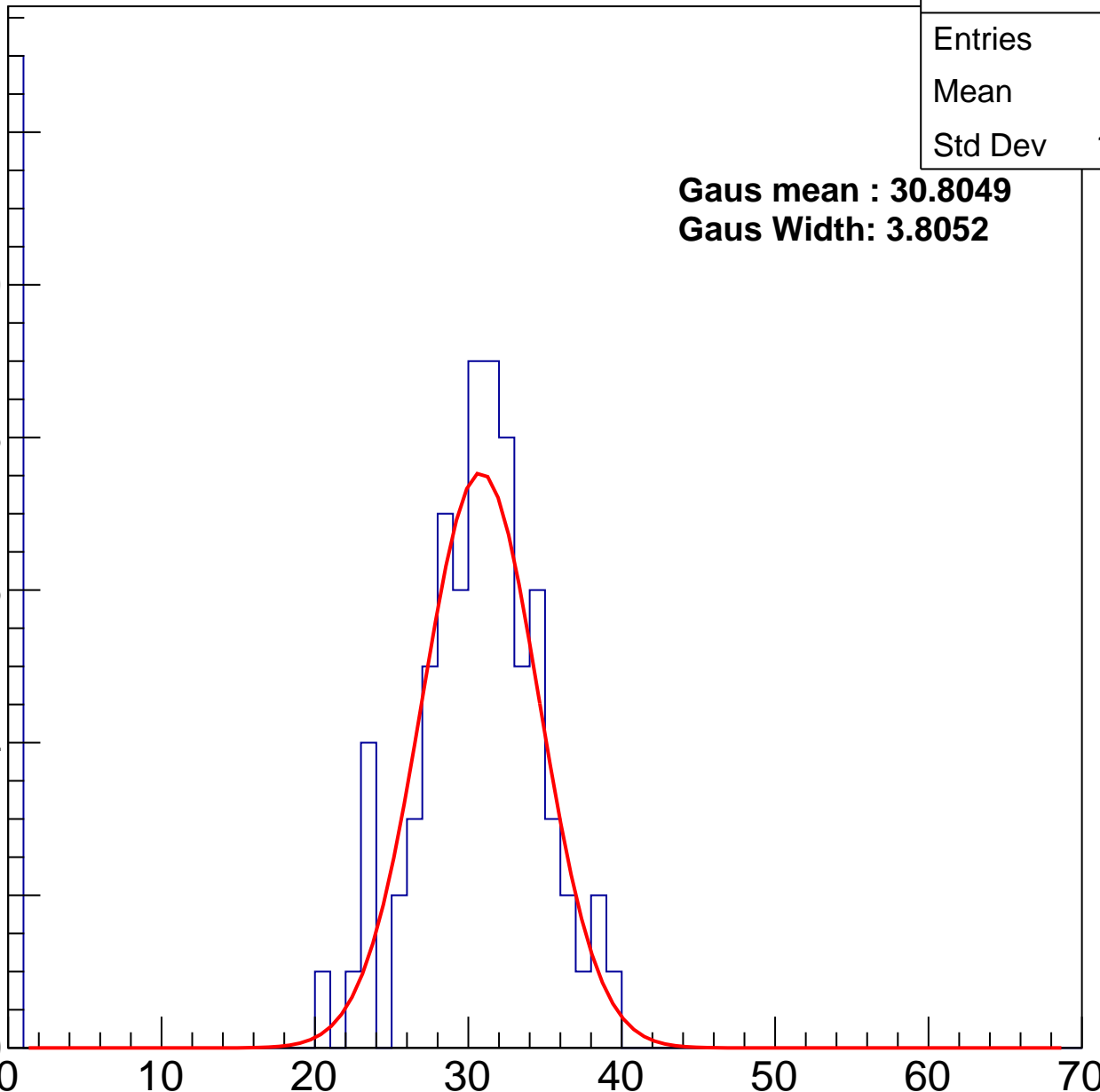
40

50

60

70

ampl



B1L103S, U6-ch21, adc1

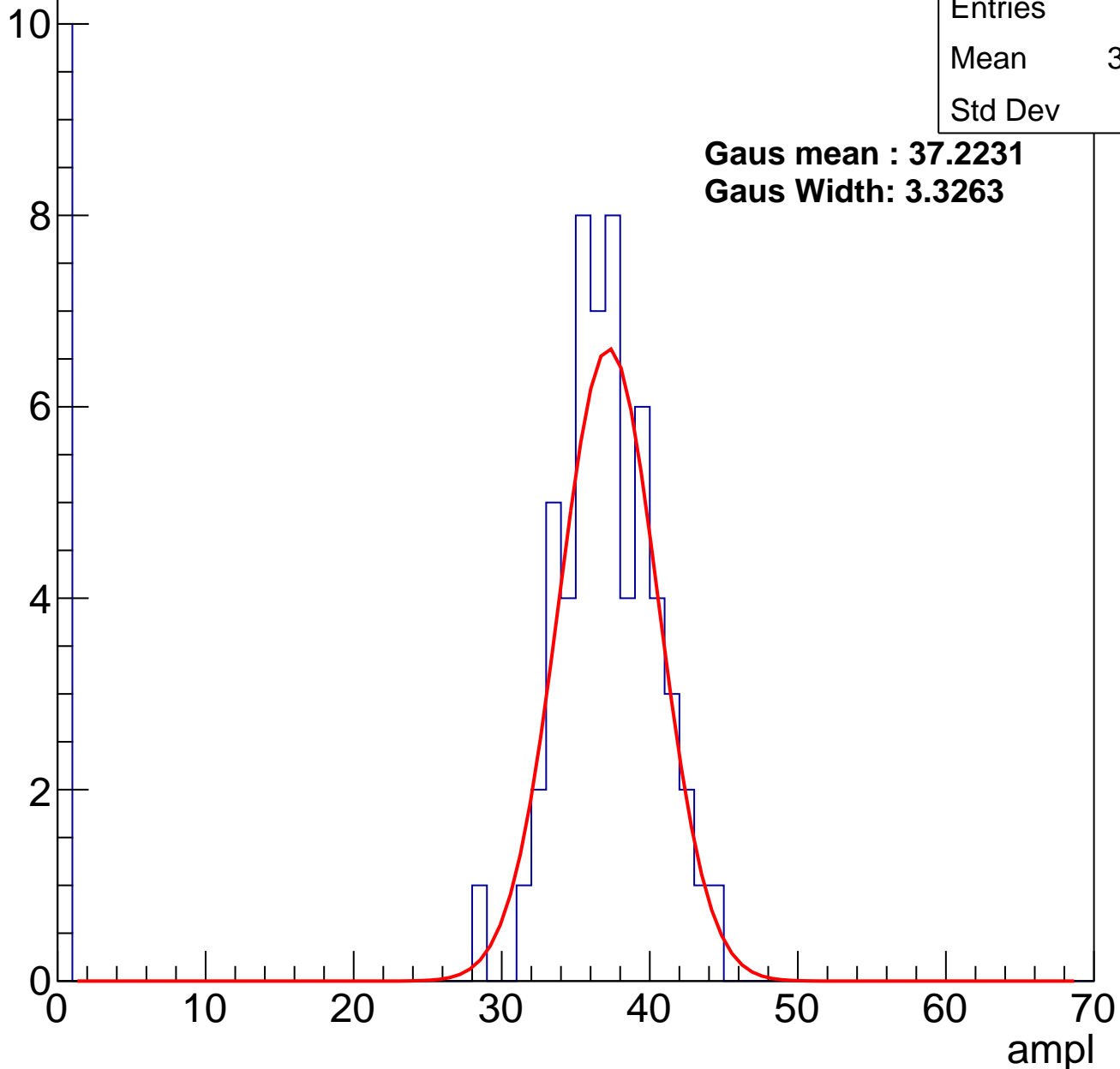
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	31.22
Std Dev	13.4

Gaus mean : 37.2231

Gaus Width: 3.3263

Entry



B1L103S, U6-ch21, adc2

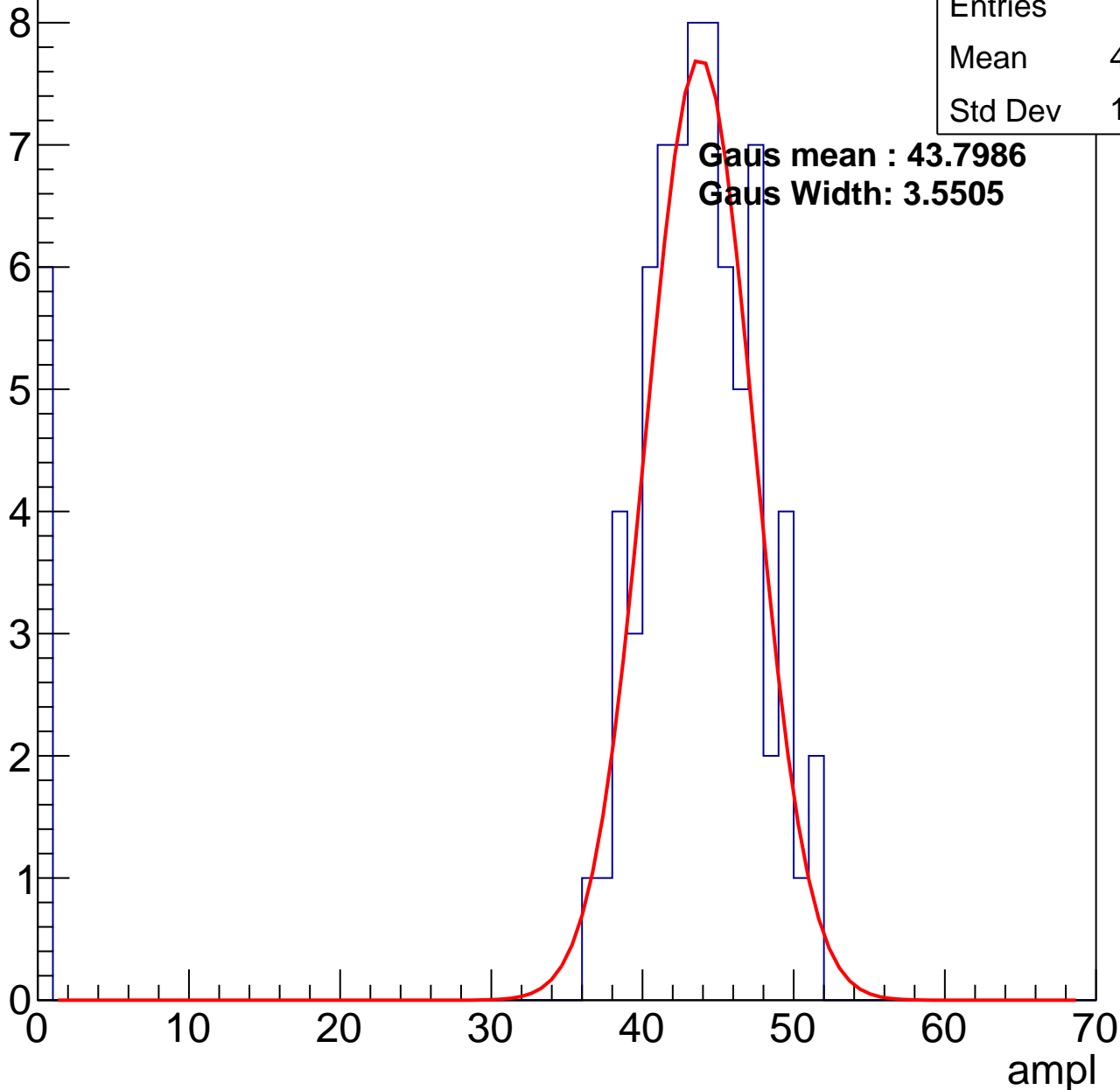
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.15
Std Dev	12.06

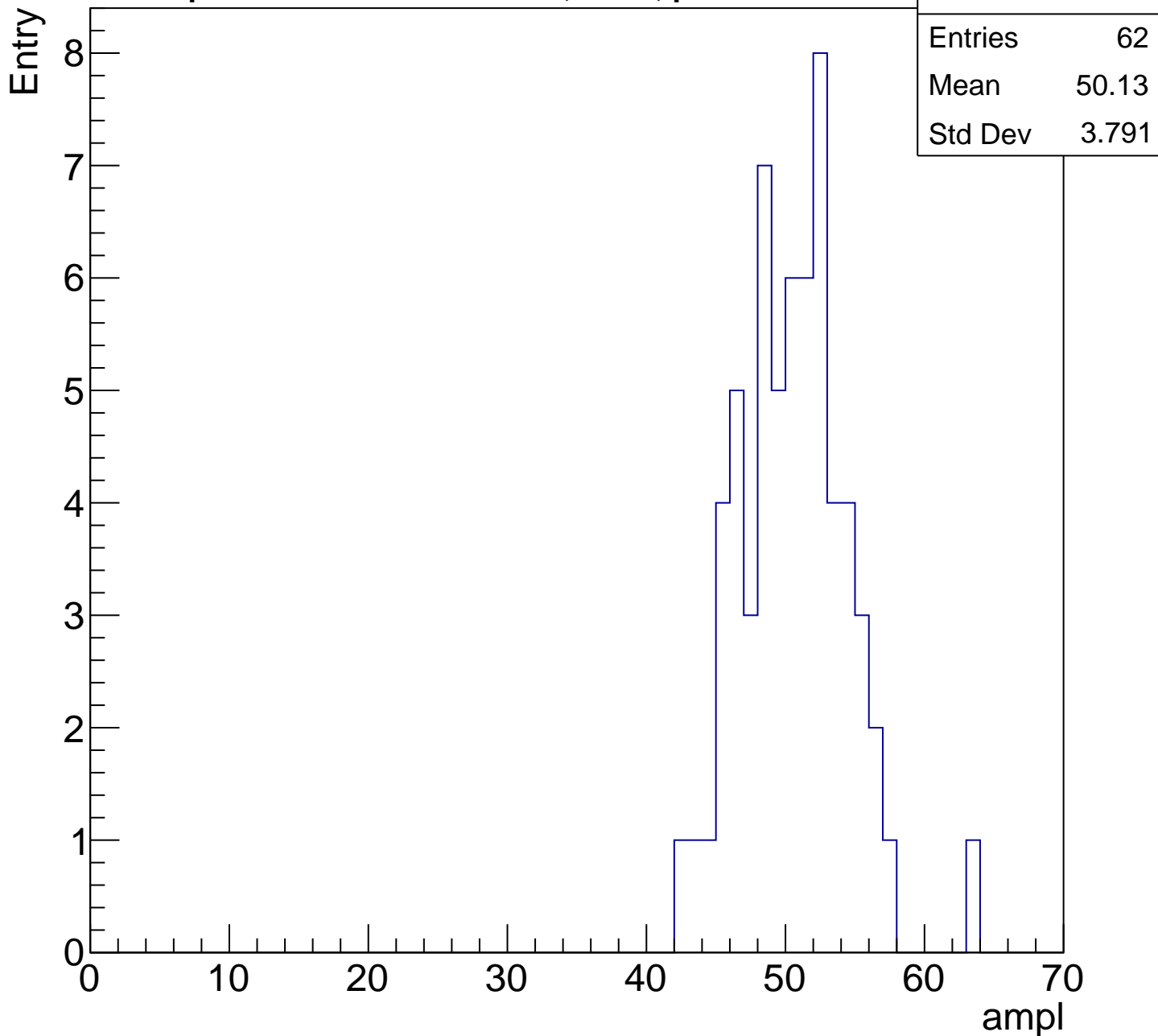
Gaus mean : 43.7986

Gaus Width: 3.5505



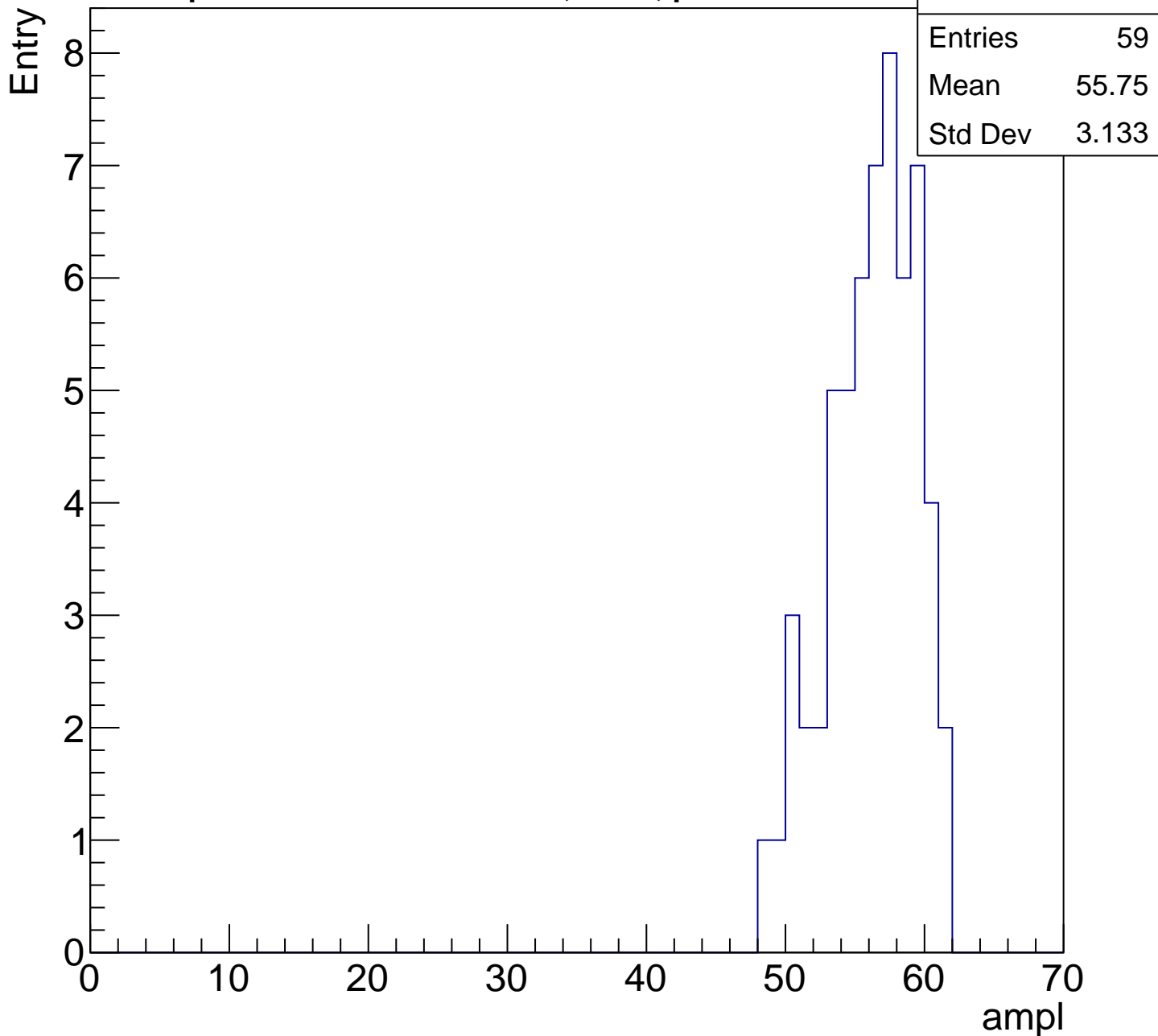
B1L103S, U6-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

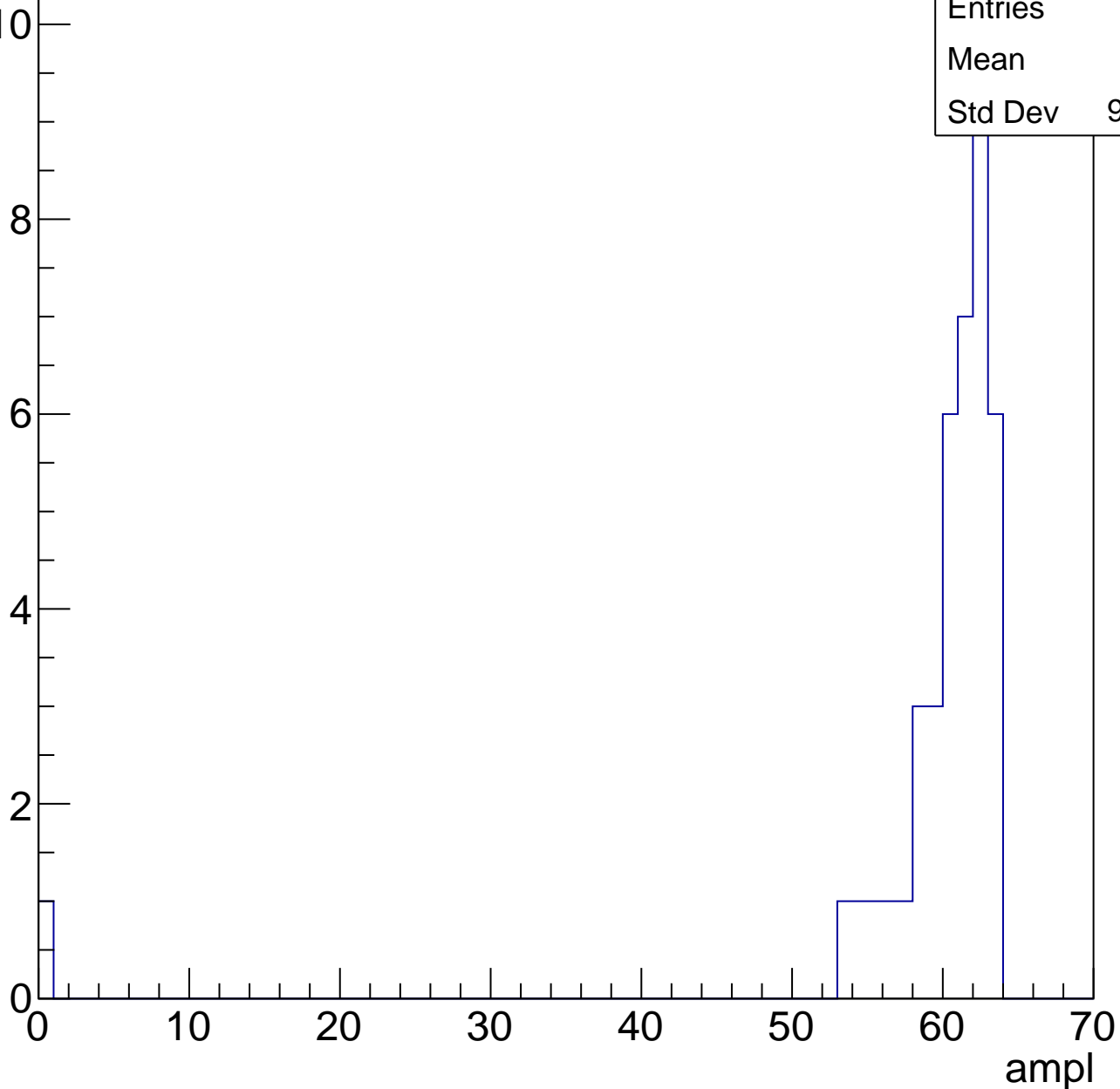


B1L103S, U6-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

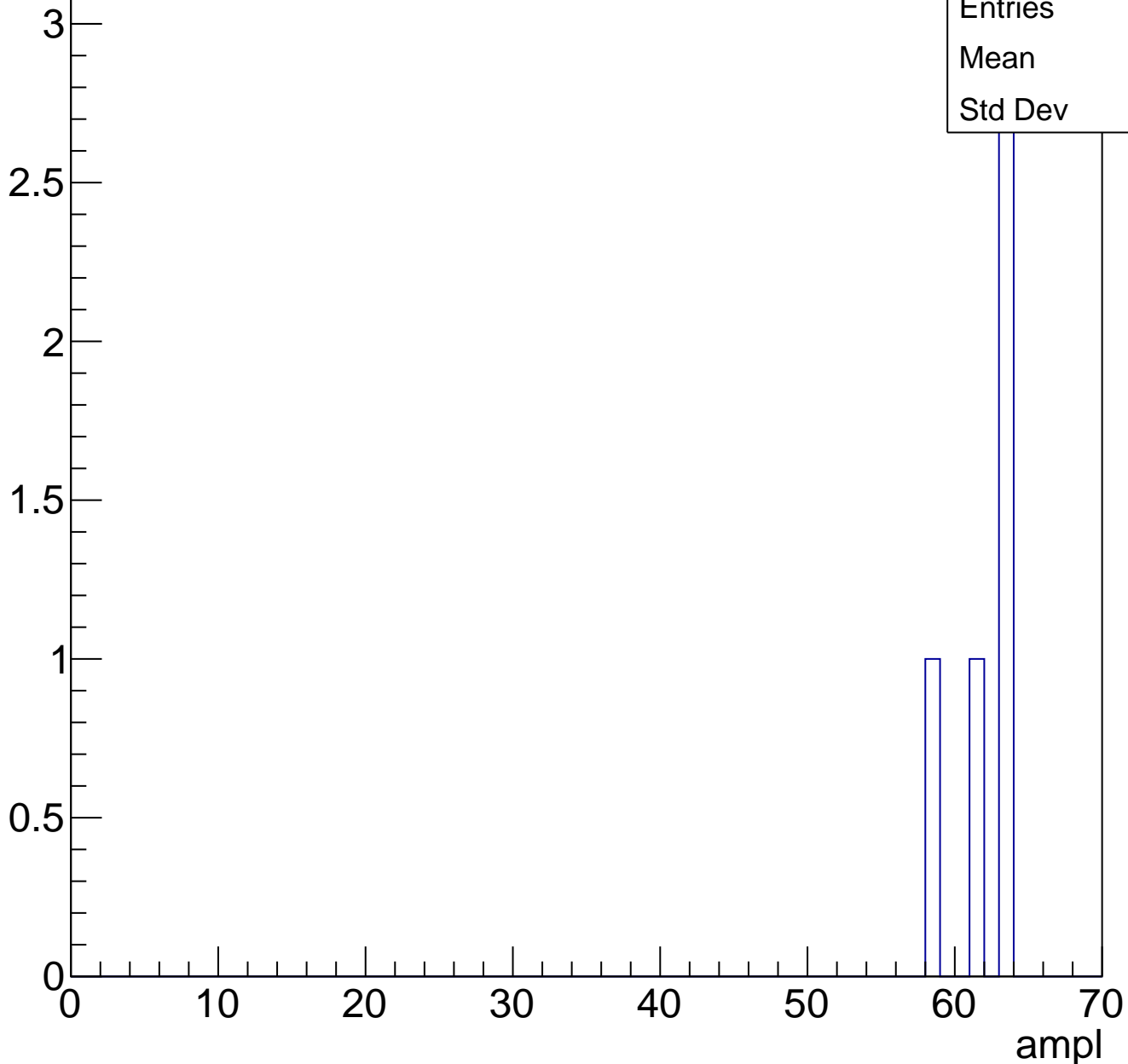
Entries	41
Mean	58.8
Std Dev	9.618



B1L103S, U6-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	61.6
Std Dev	1.96

B1L103S, U6-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch22, adc0

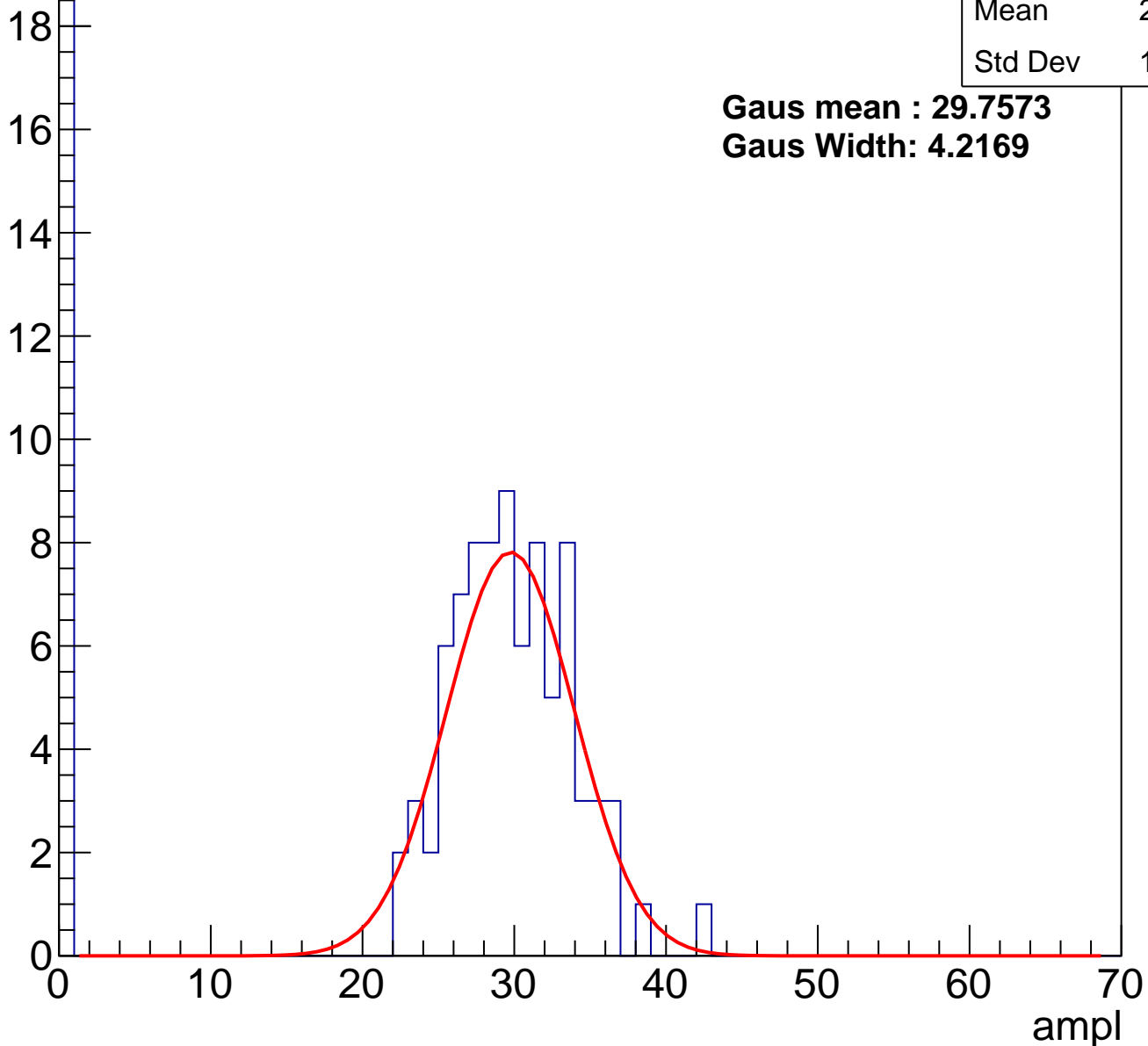
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	23.93
Std Dev	11.97

Gaus mean : 29.7573

Gaus Width: 4.2169

Entry



B1L103S, U6-ch22, adc1

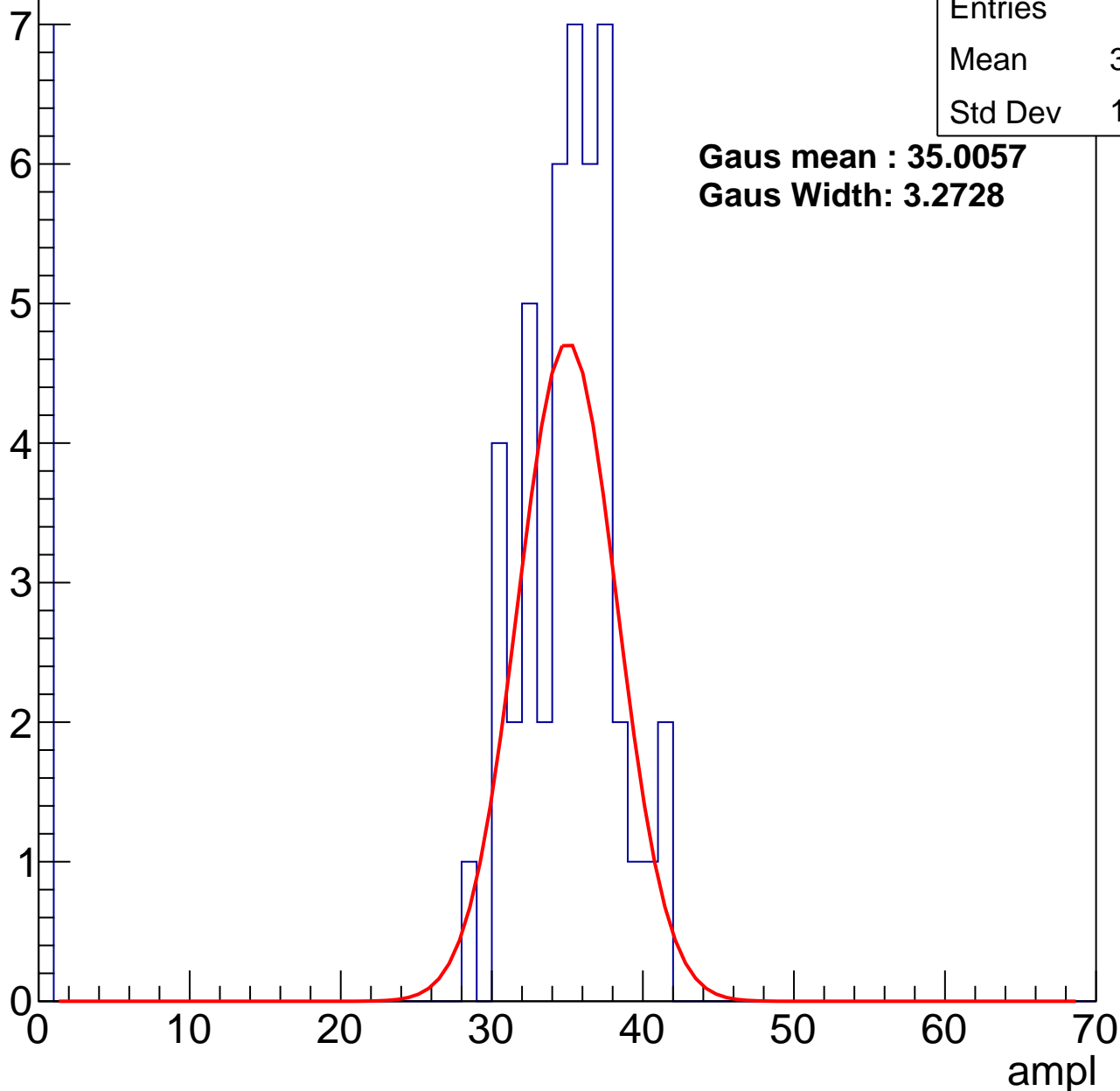
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	30.13
Std Dev	12.07

Gaus mean : 35.0057

Gaus Width: 3.2728



B1L103S, U6-ch22, adc2

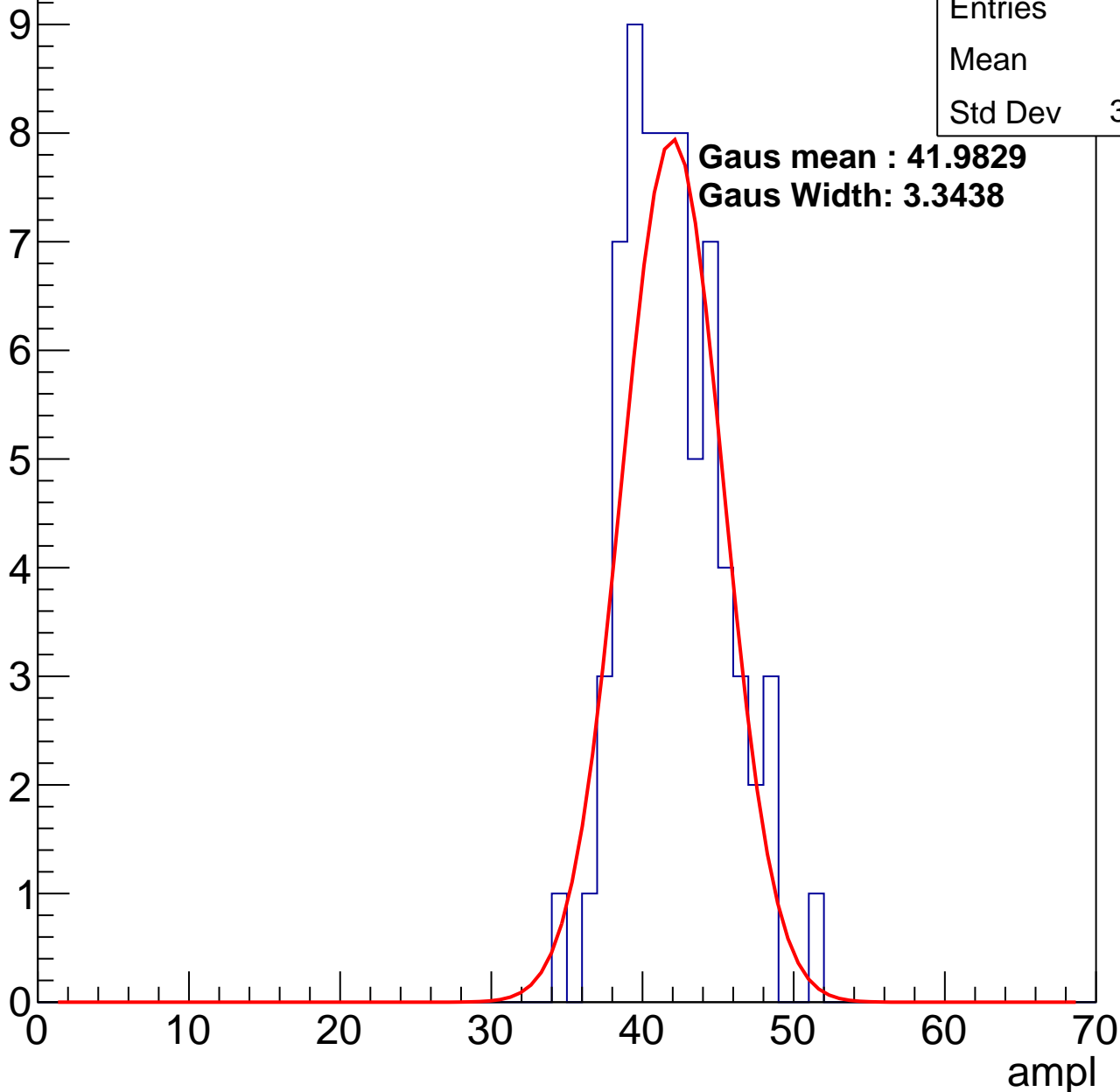
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	41.6
Std Dev	3.279

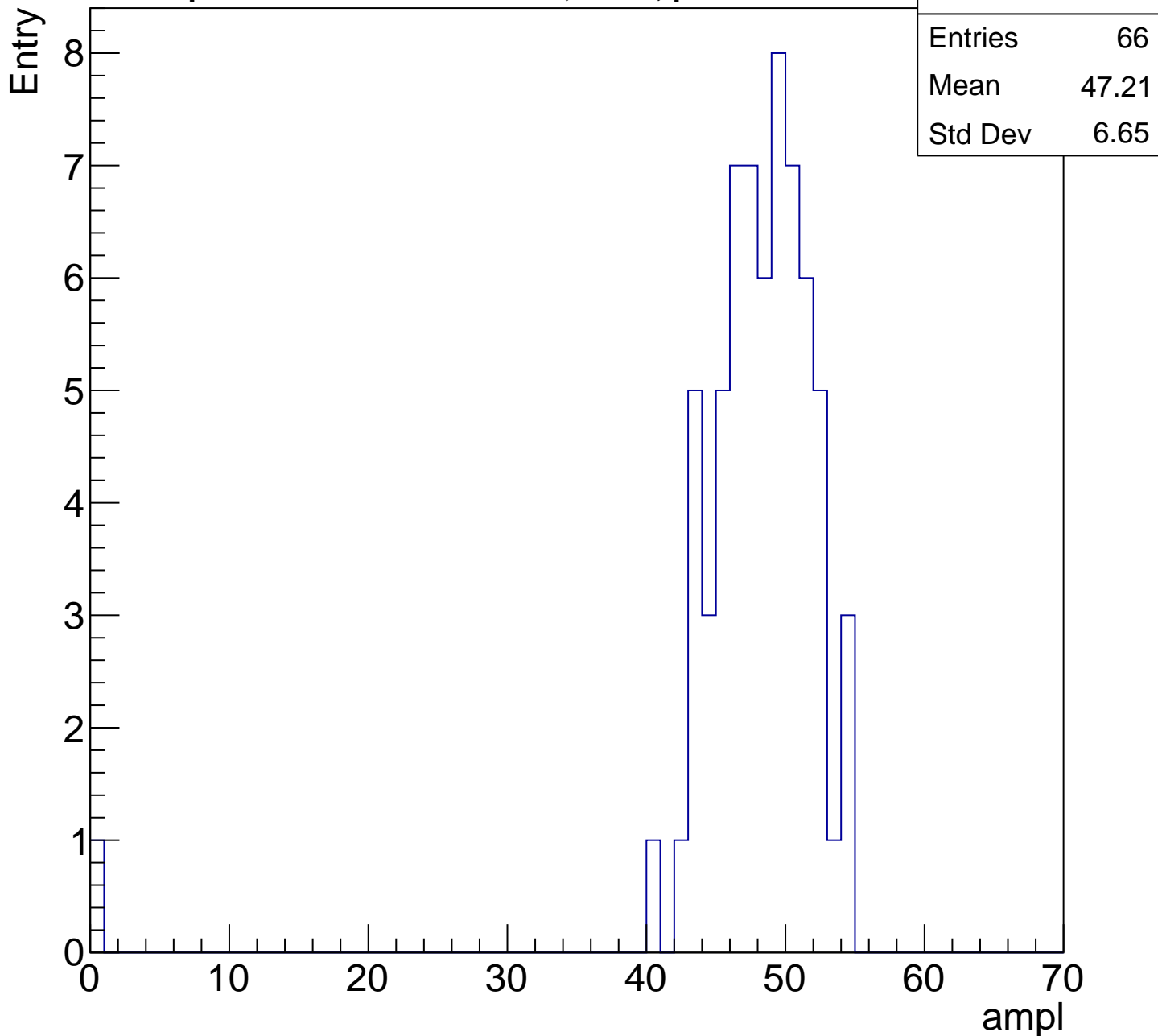
Gaus mean : 41.9829

Gaus Width: 3.3438



B1L103S, U6-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

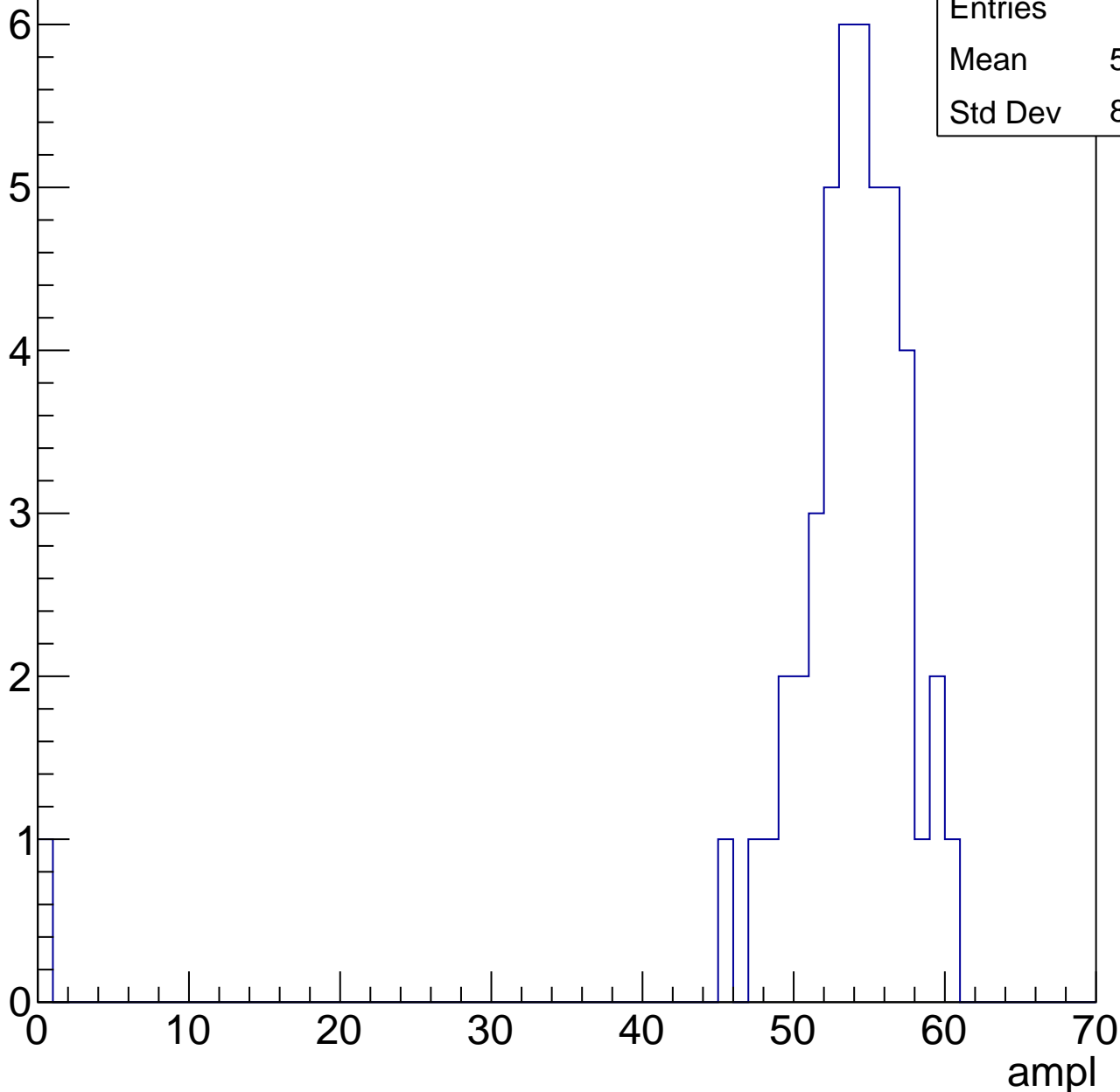


B1L103S, U6-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

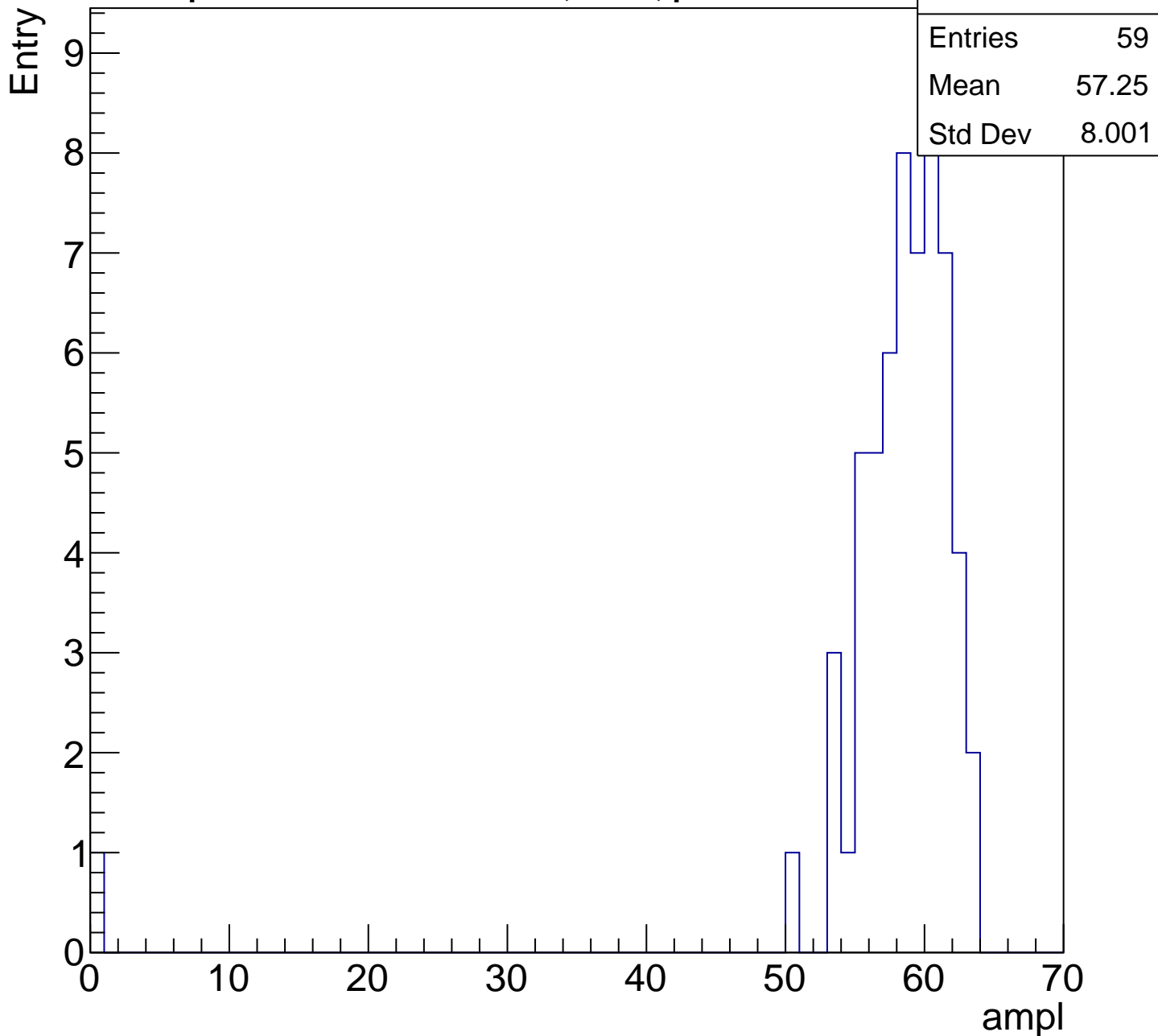
Entry

Entries	46
Mean	52.43
Std Dev	8.428



B1L103S, U6-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

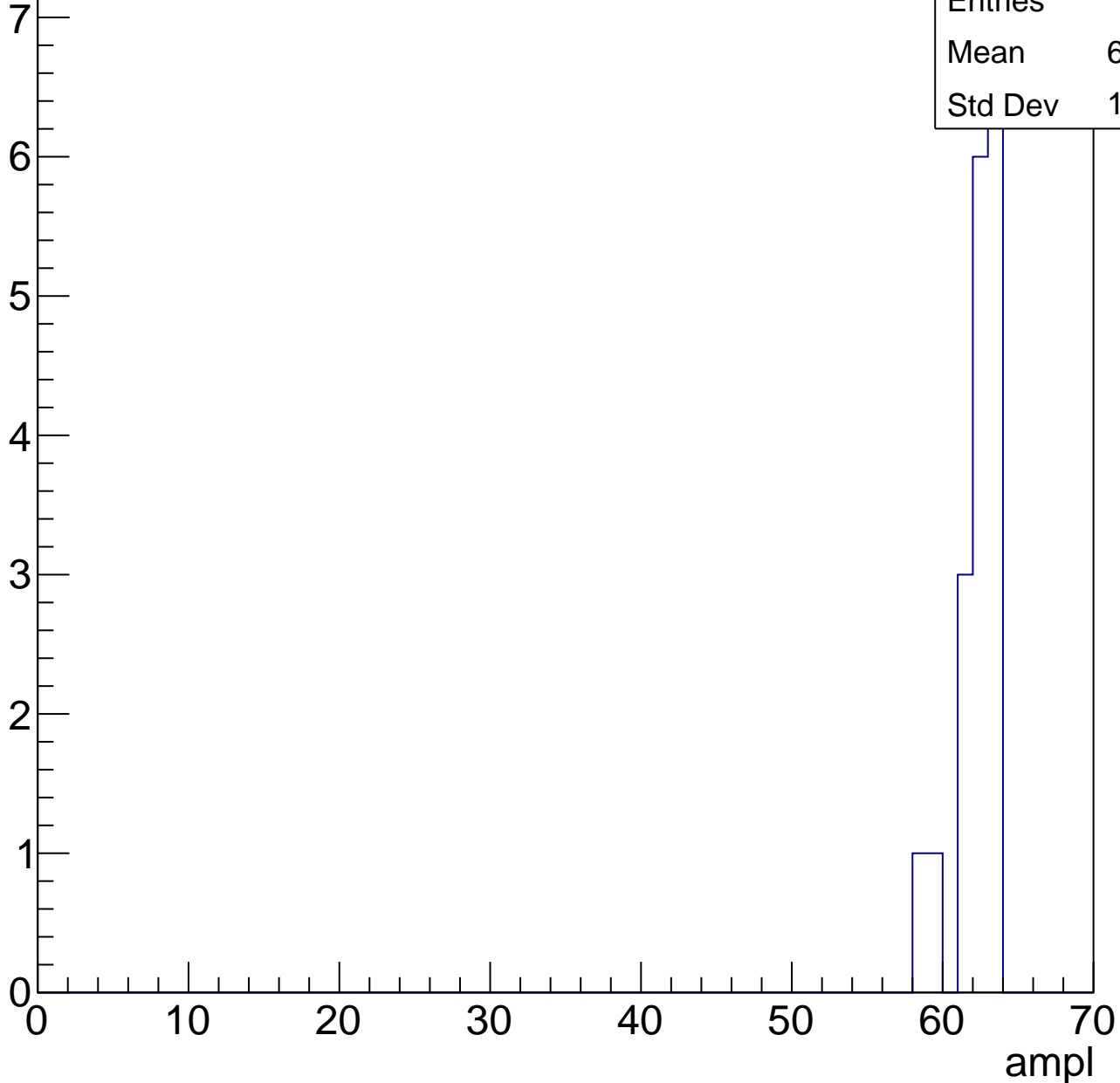


B1L103S, U6-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

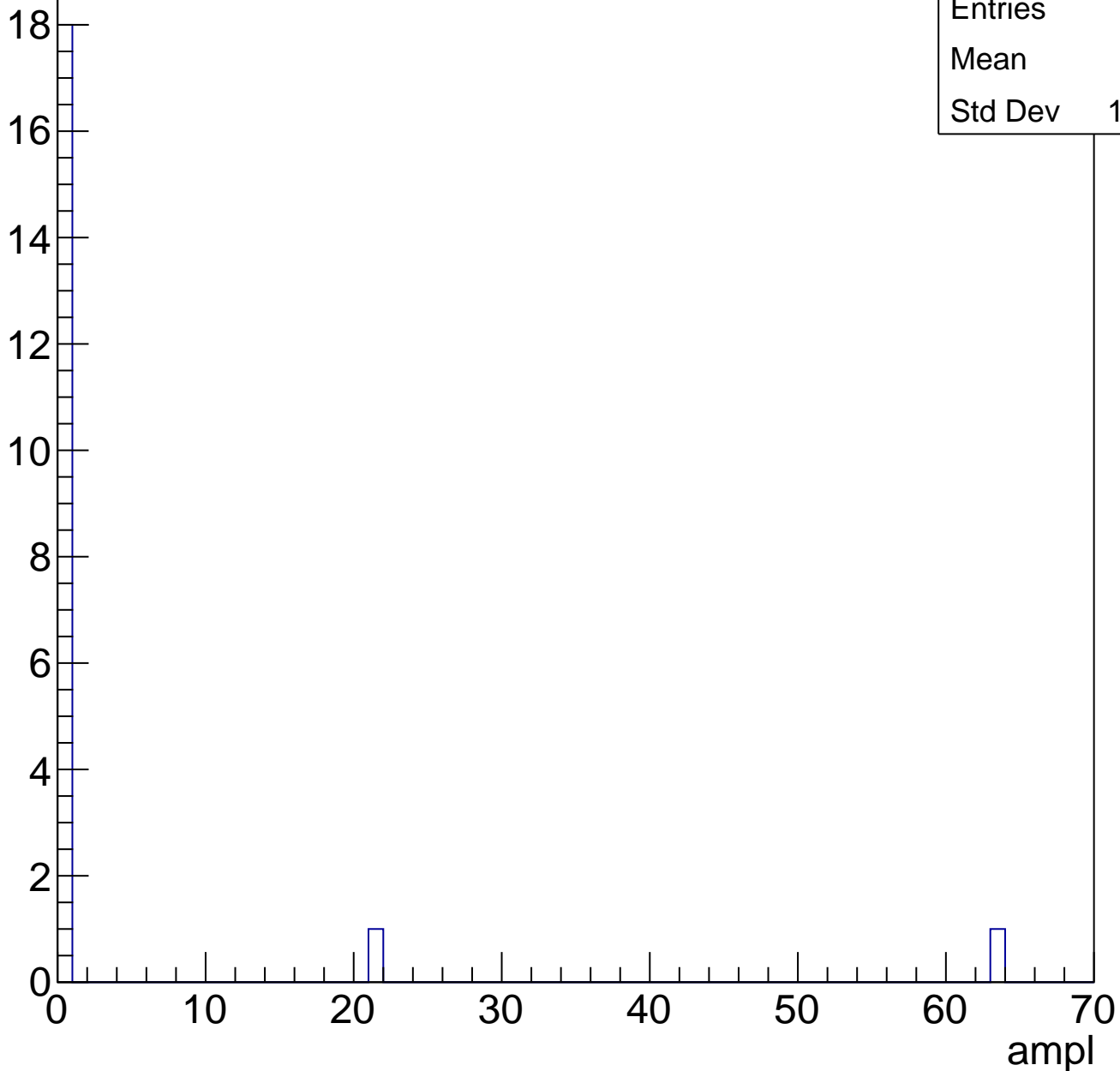
Entries	18
Mean	61.83
Std Dev	1.384



B1L103S, U6-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	4.2
Std Dev	14.24

B1L103S, U6-ch23, adc0

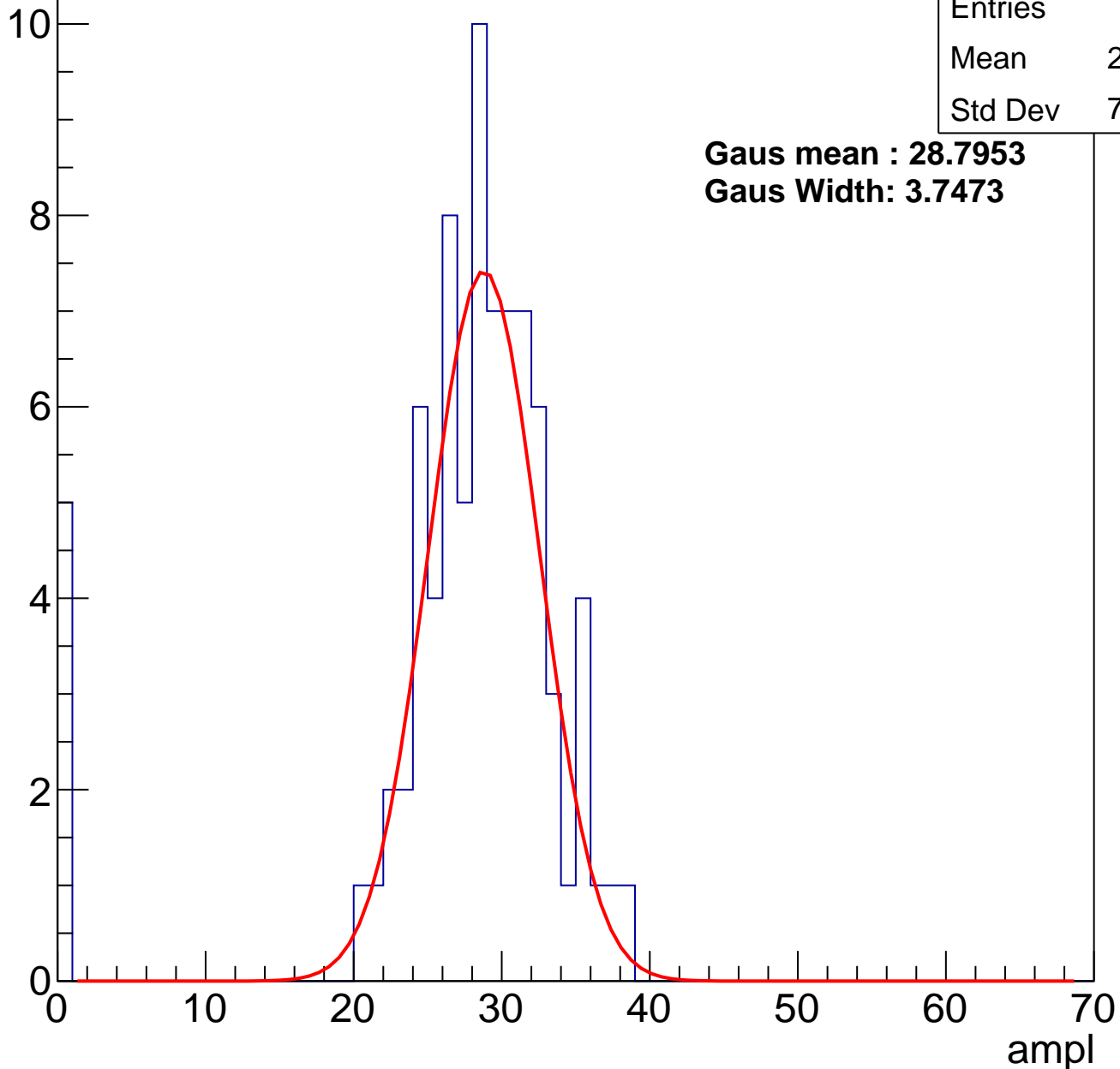
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	26.88
Std Dev	7.783

Gaus mean : 28.7953

Gaus Width: 3.7473

Entry



B1L103S, U6-ch23, adc1

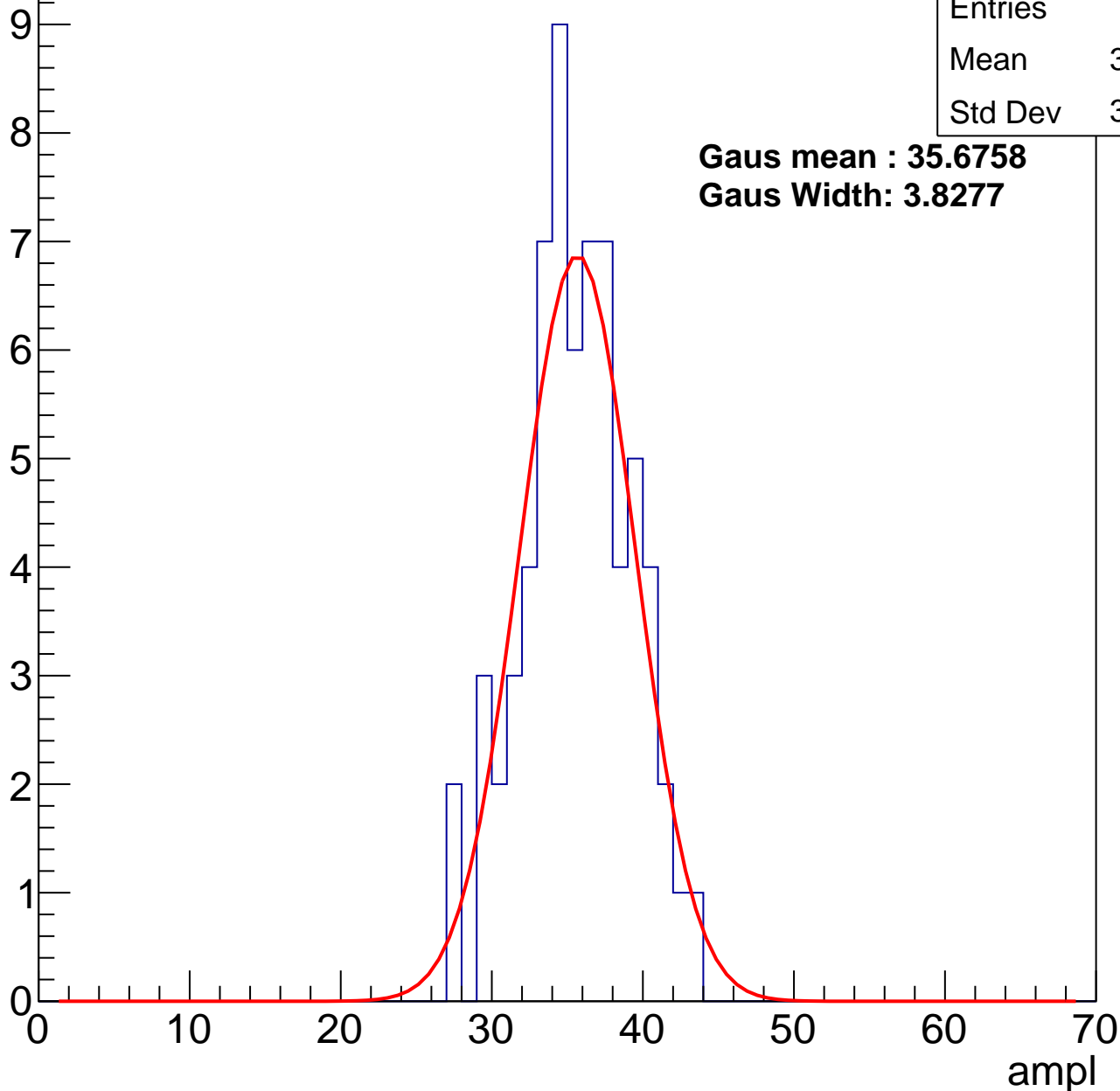
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.13
Std Dev	3.545

Gaus mean : 35.6758

Gaus Width: 3.8277



B1L103S, U6-ch23, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

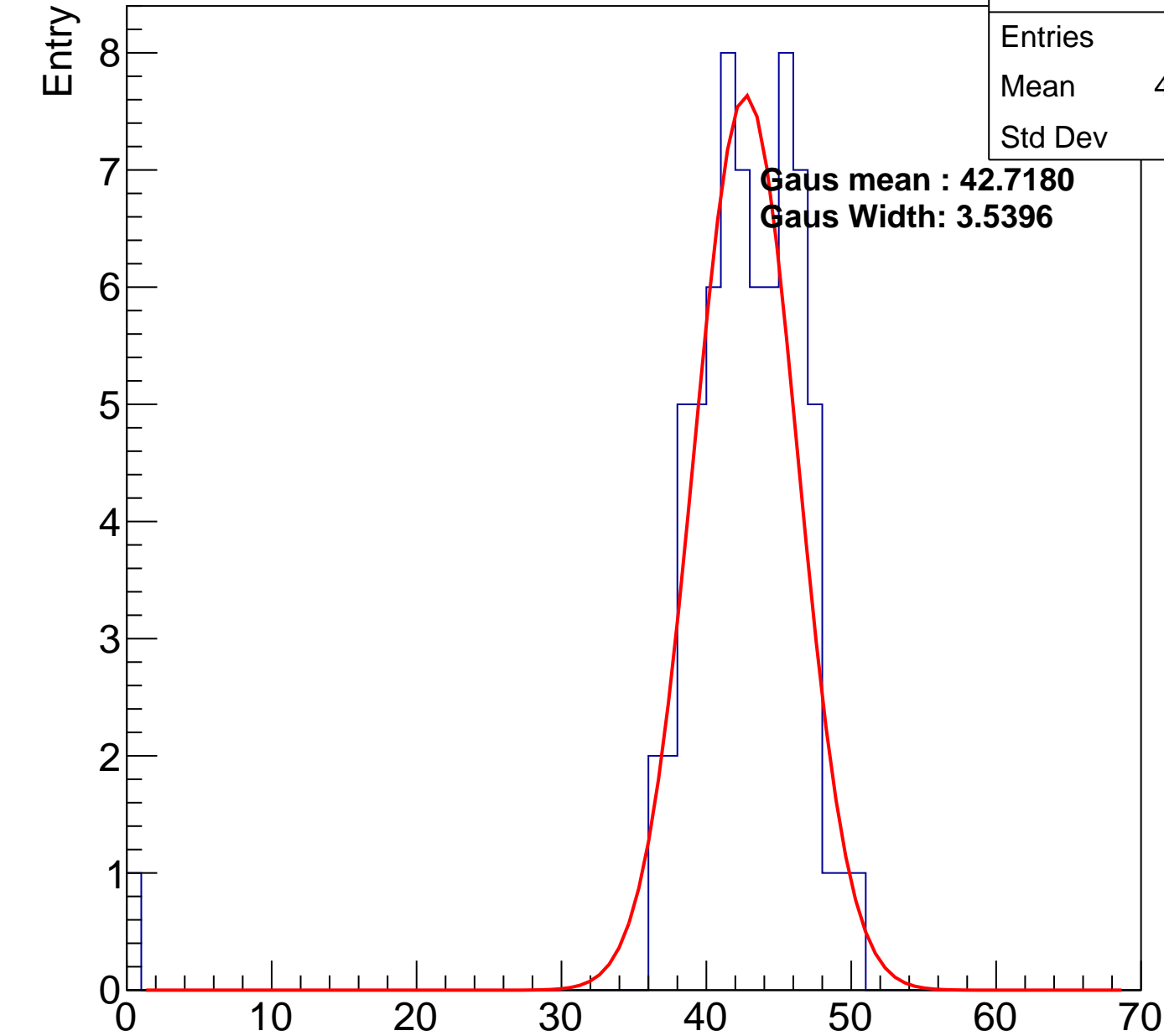
8
7
6
5
4
3
2
1
0

Entries	71
Mean	41.96
Std Dev	5.97

Gaus mean : 42.7180

Gaus Width: 3.5396

ampl

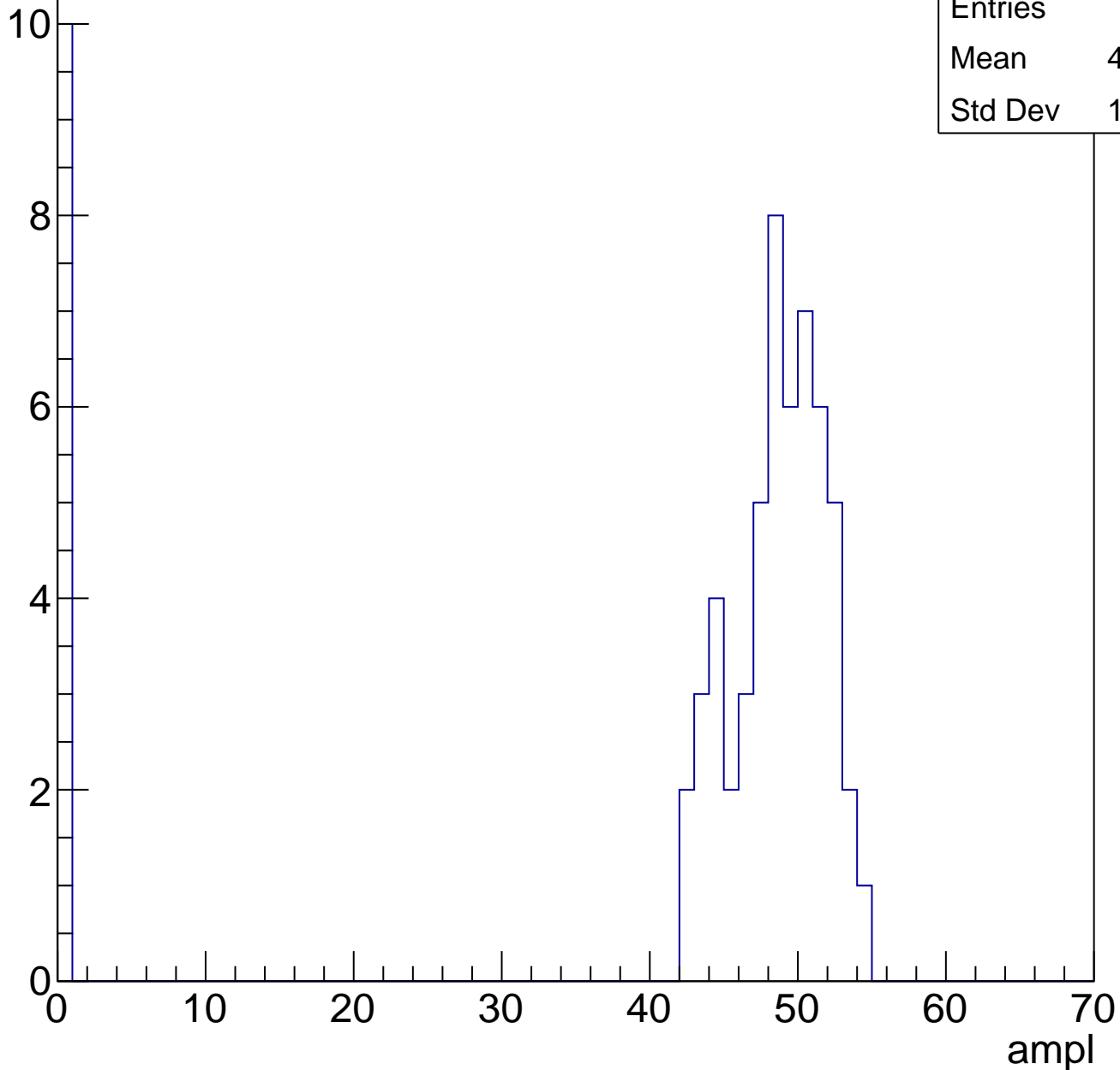


B1L103S, U6-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	40.72
Std Dev	17.74

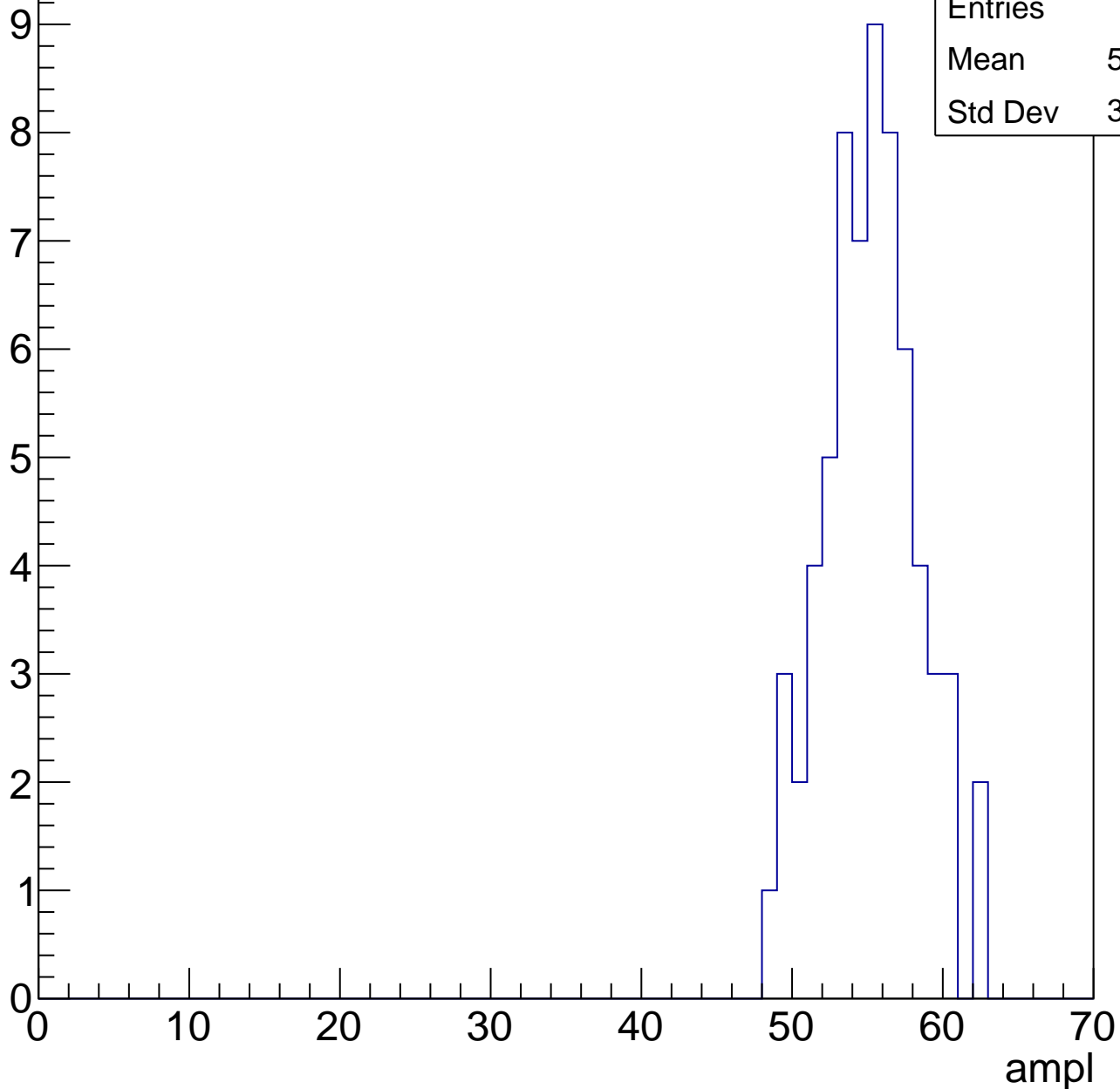
Entry



B1L103S, U6-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

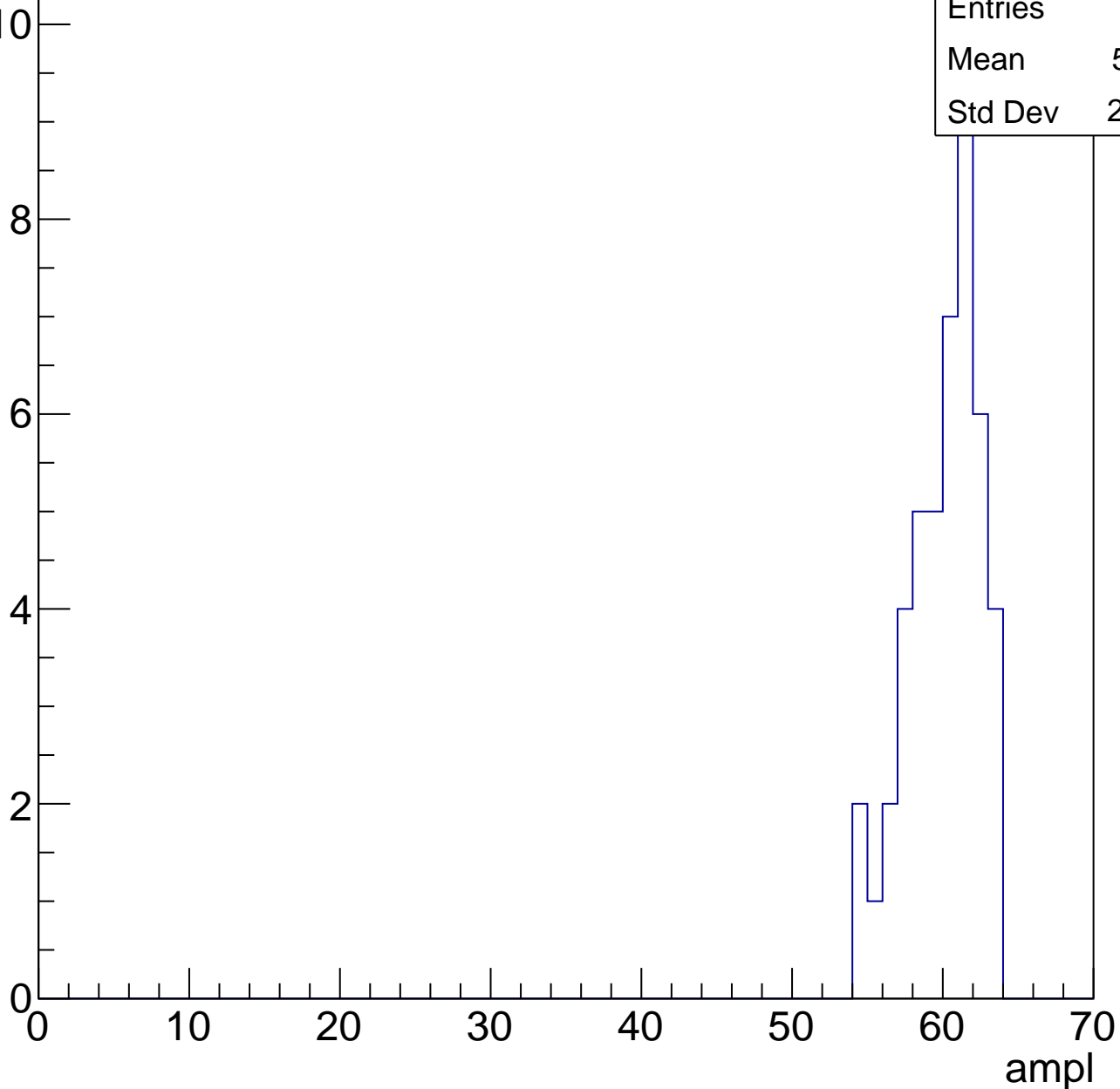


B1L103S, U6-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	59.61
Std Dev	2.345

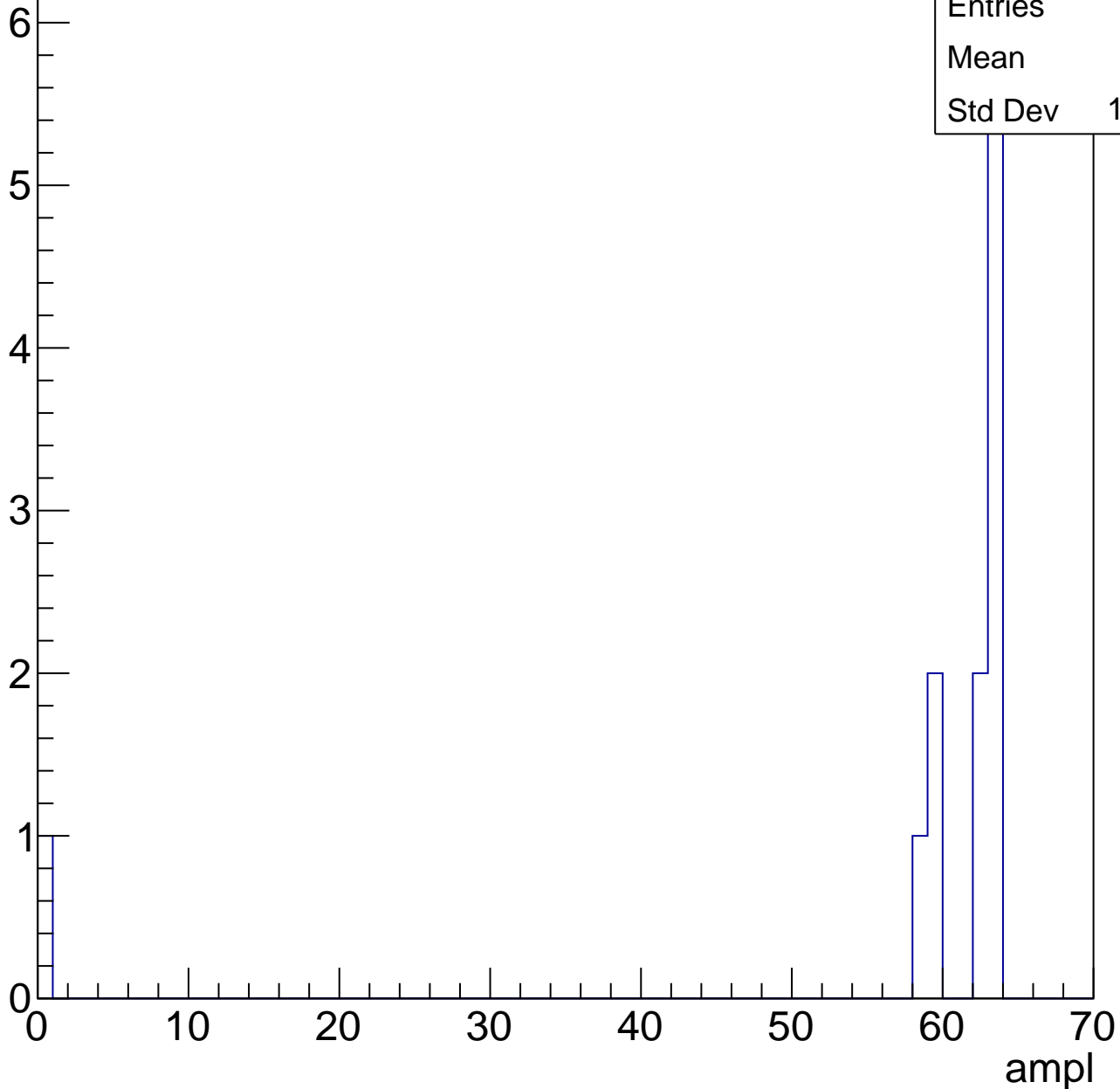


B1L103S, U6-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.5
Std Dev	17.13



B1L103S, U6-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch24, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	26.43
Std Dev	7.556

Gaus mean : 28.4435

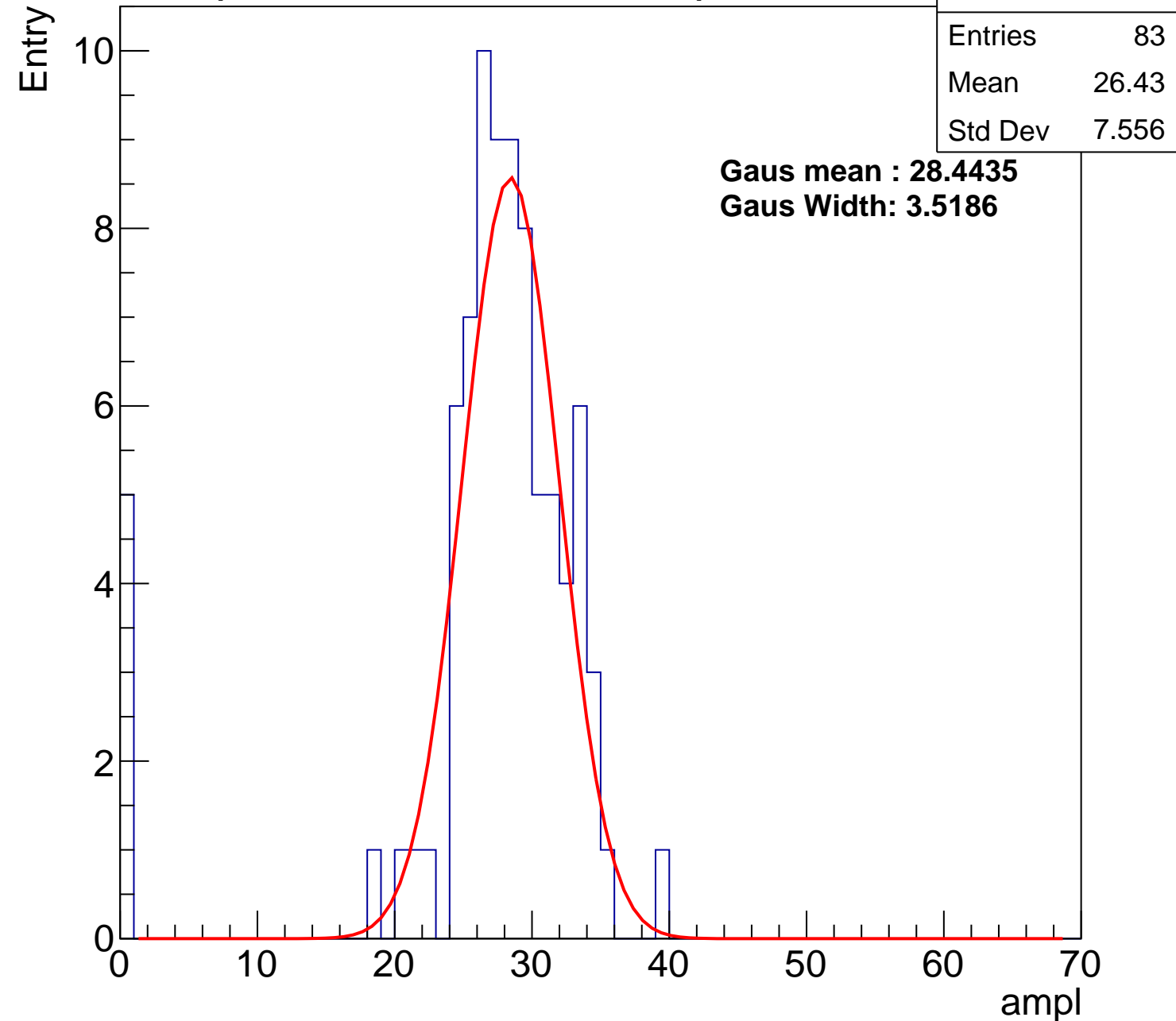
Gaus Width: 3.5186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch24, adc1

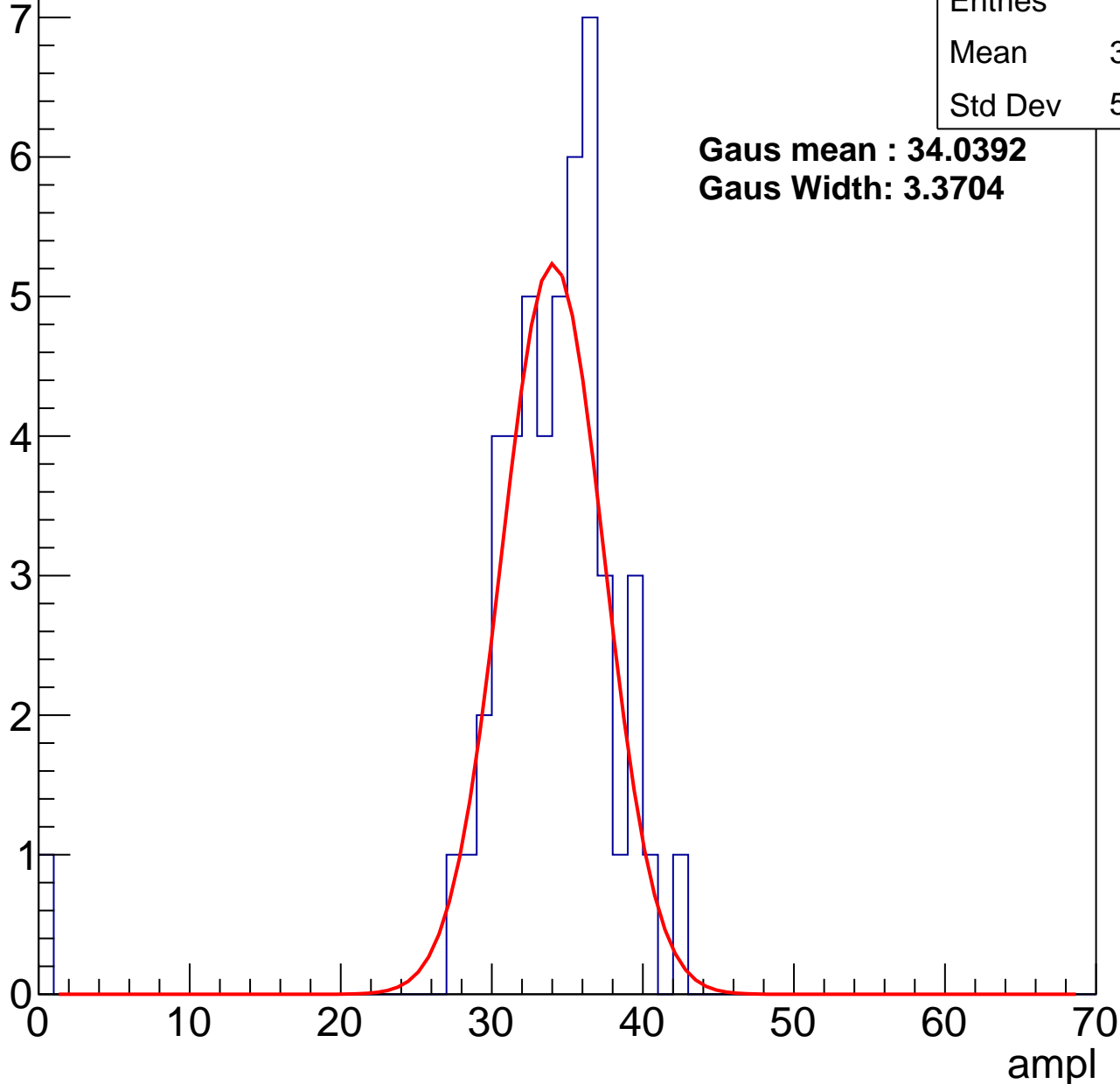
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	33.24
Std Dev	5.784

Gaus mean : 34.0392

Gaus Width: 3.3704



B1L103S, U6-ch24, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	36.2
Std Dev	13.04

Gaus mean : 41.0681

Gaus Width: 3.9437

Entry

10

8

6

4

2

0

0

10

20

30

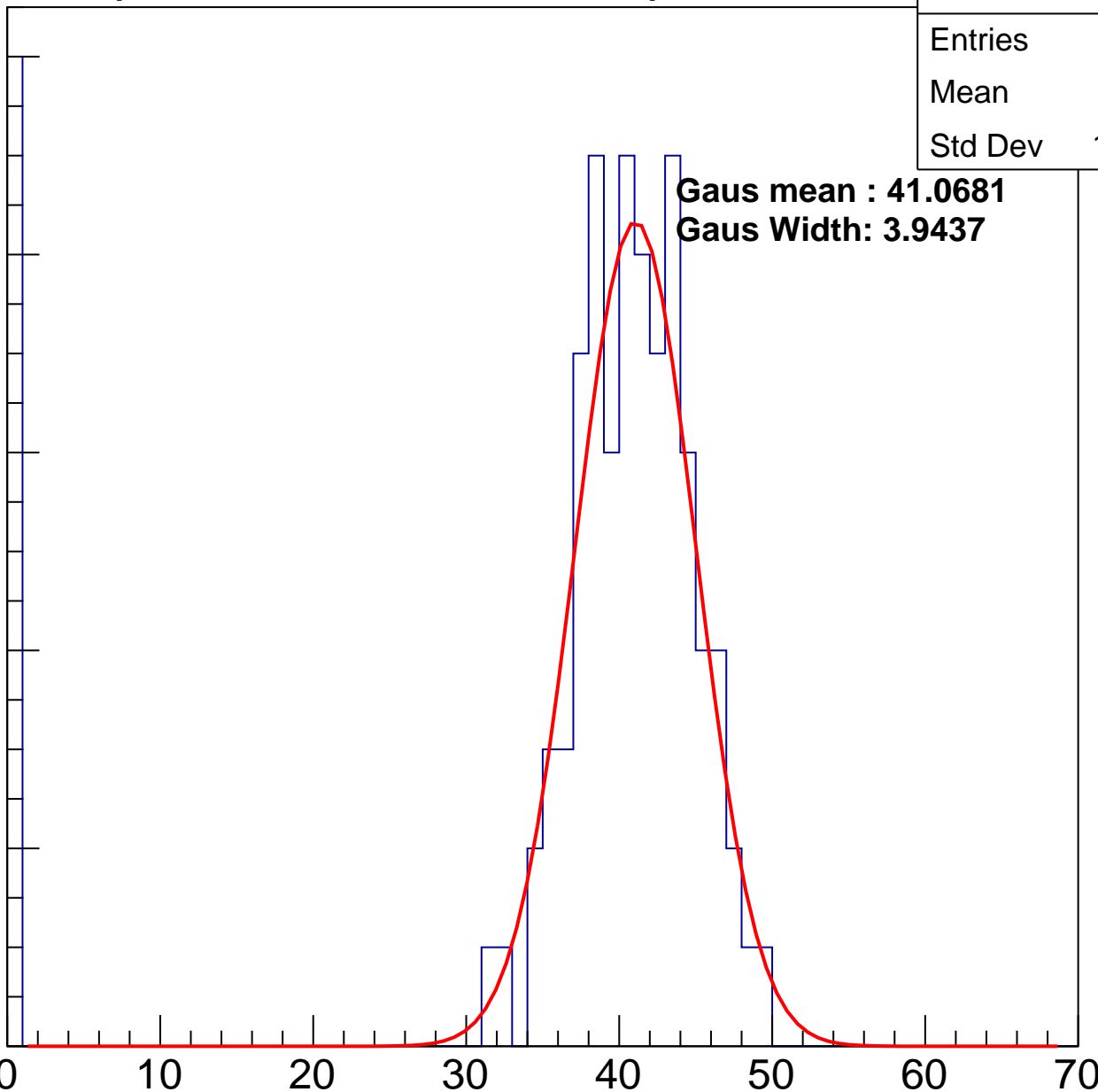
40

50

60

70

ampl

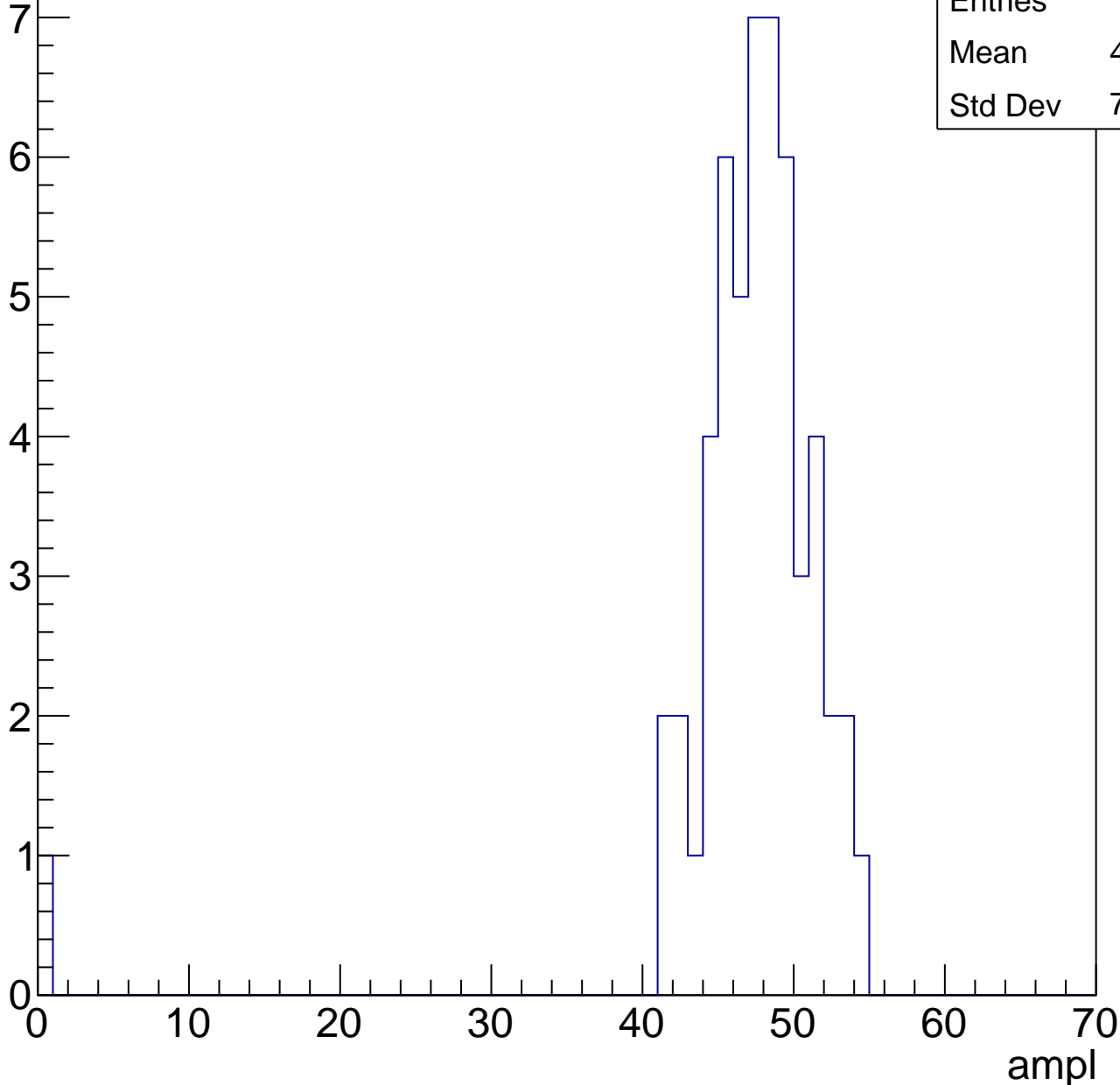


B1L103S, U6-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	46.45
Std Dev	7.123

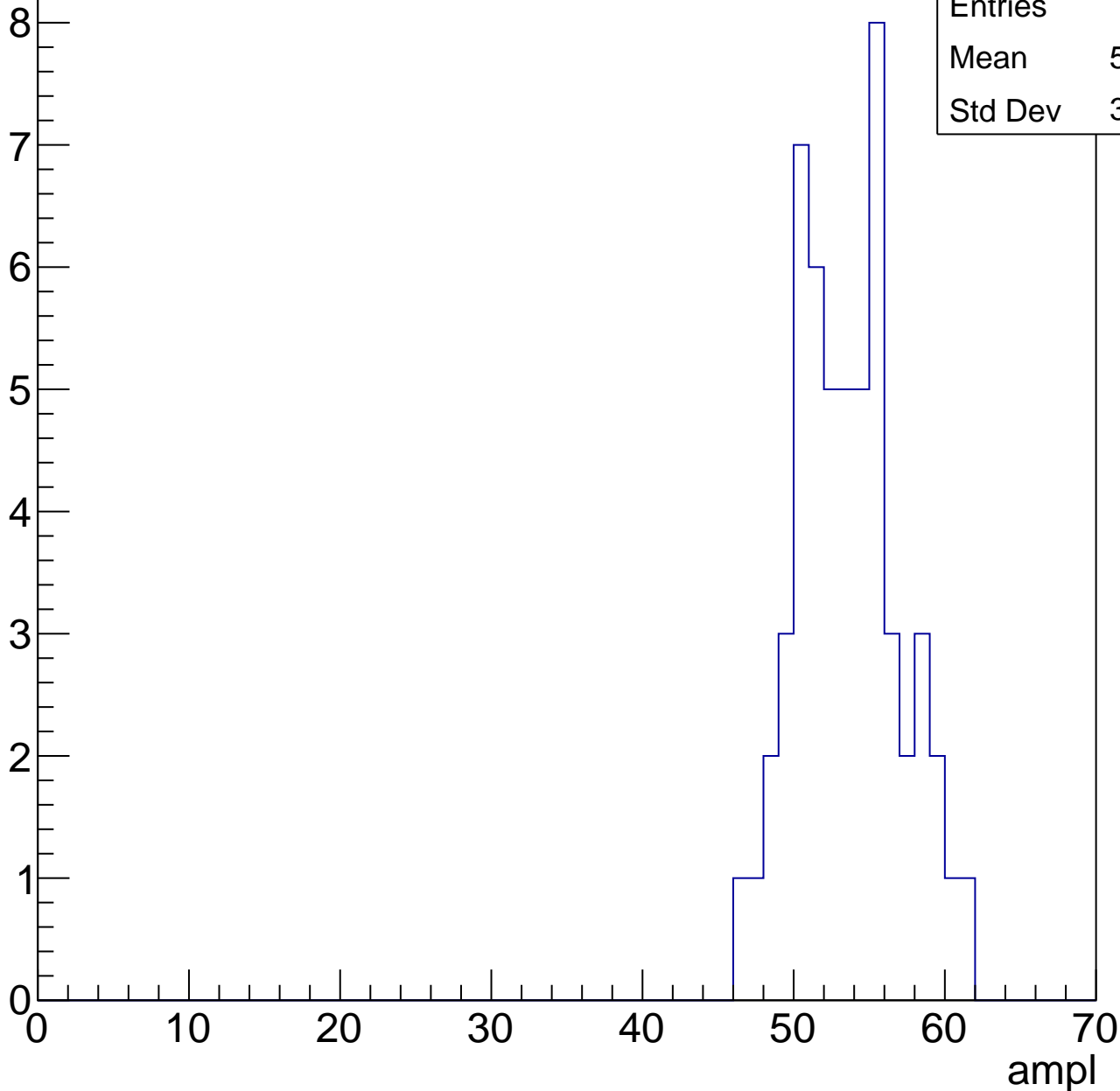


B1L103S, U6-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.13
Std Dev	3.385

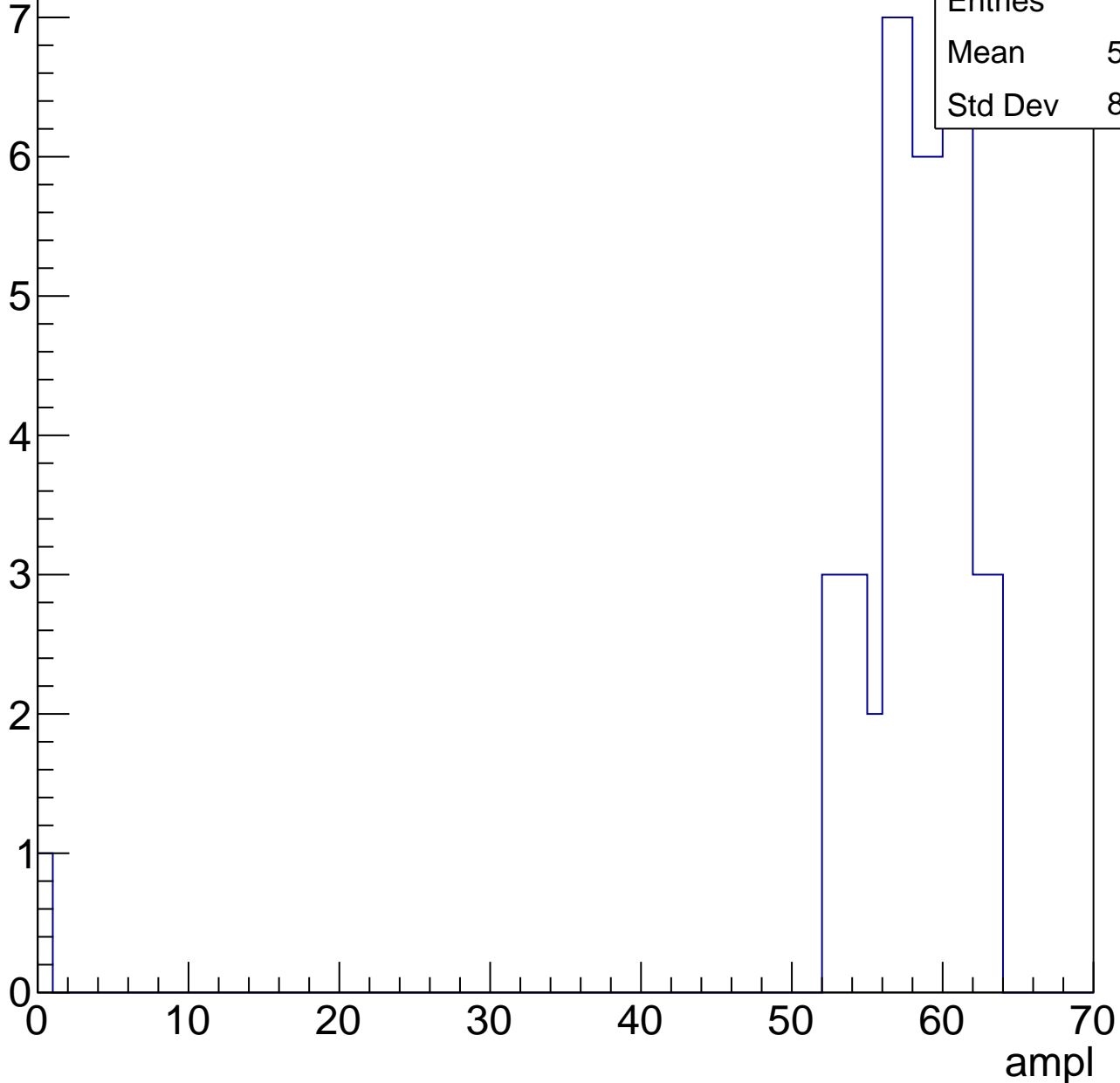


B1L103S, U6-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.93
Std Dev	8.096

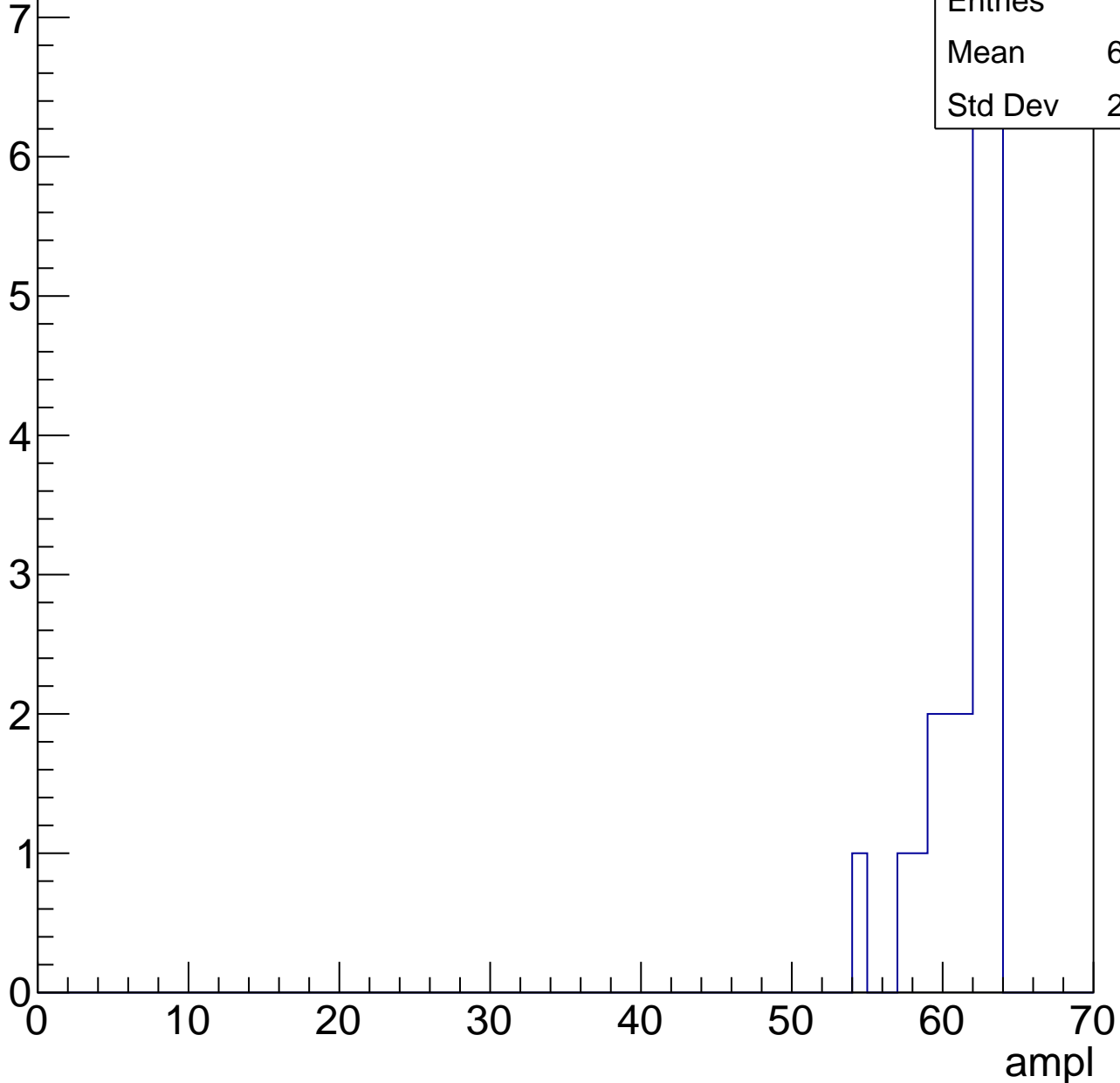


B1L103S, U6-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

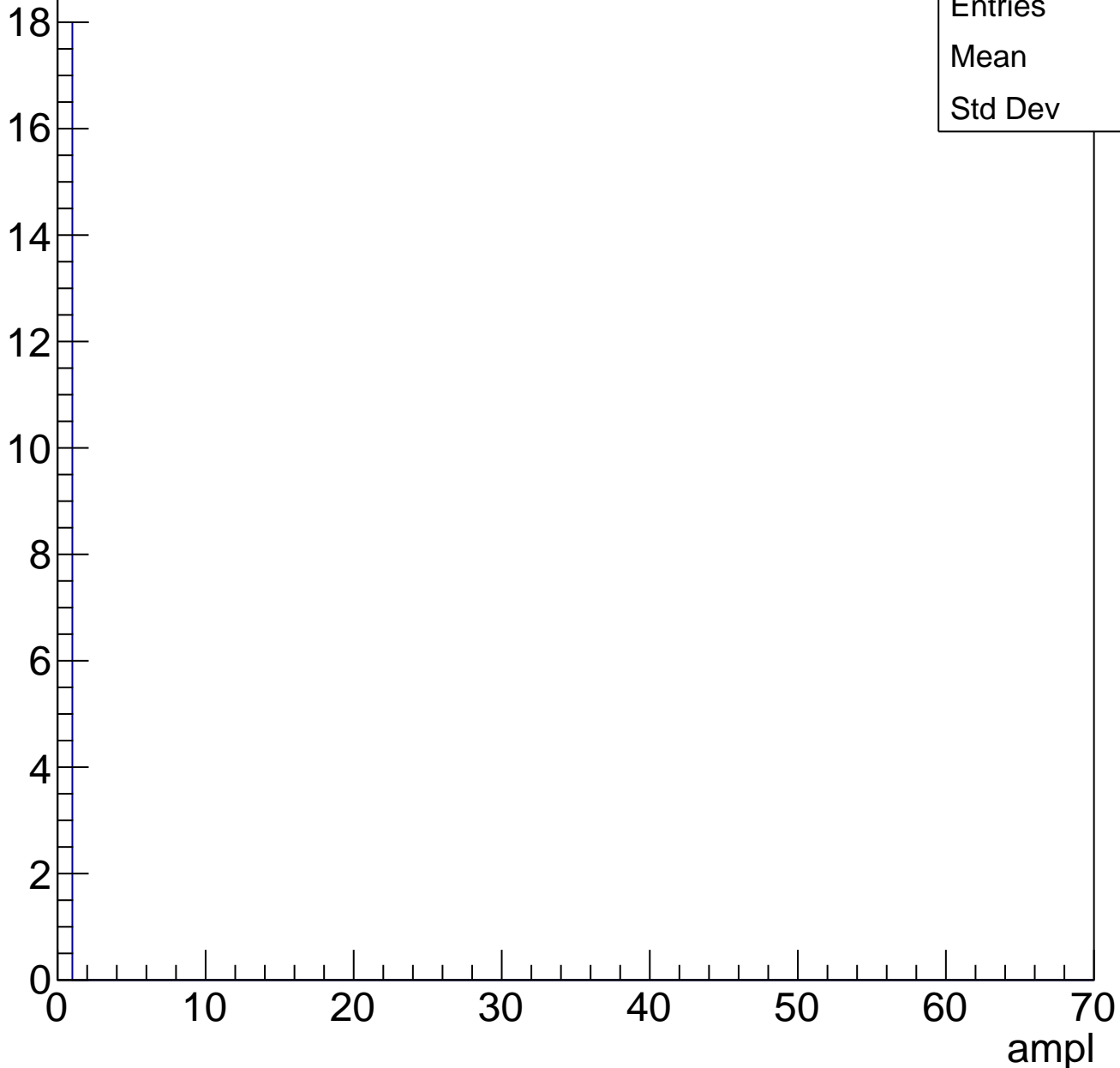
Entries	23
Mean	61.04
Std Dev	2.274



B1L103S, U6-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch25, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	26.26
Std Dev	7.508

Gaus mean : 28.4279

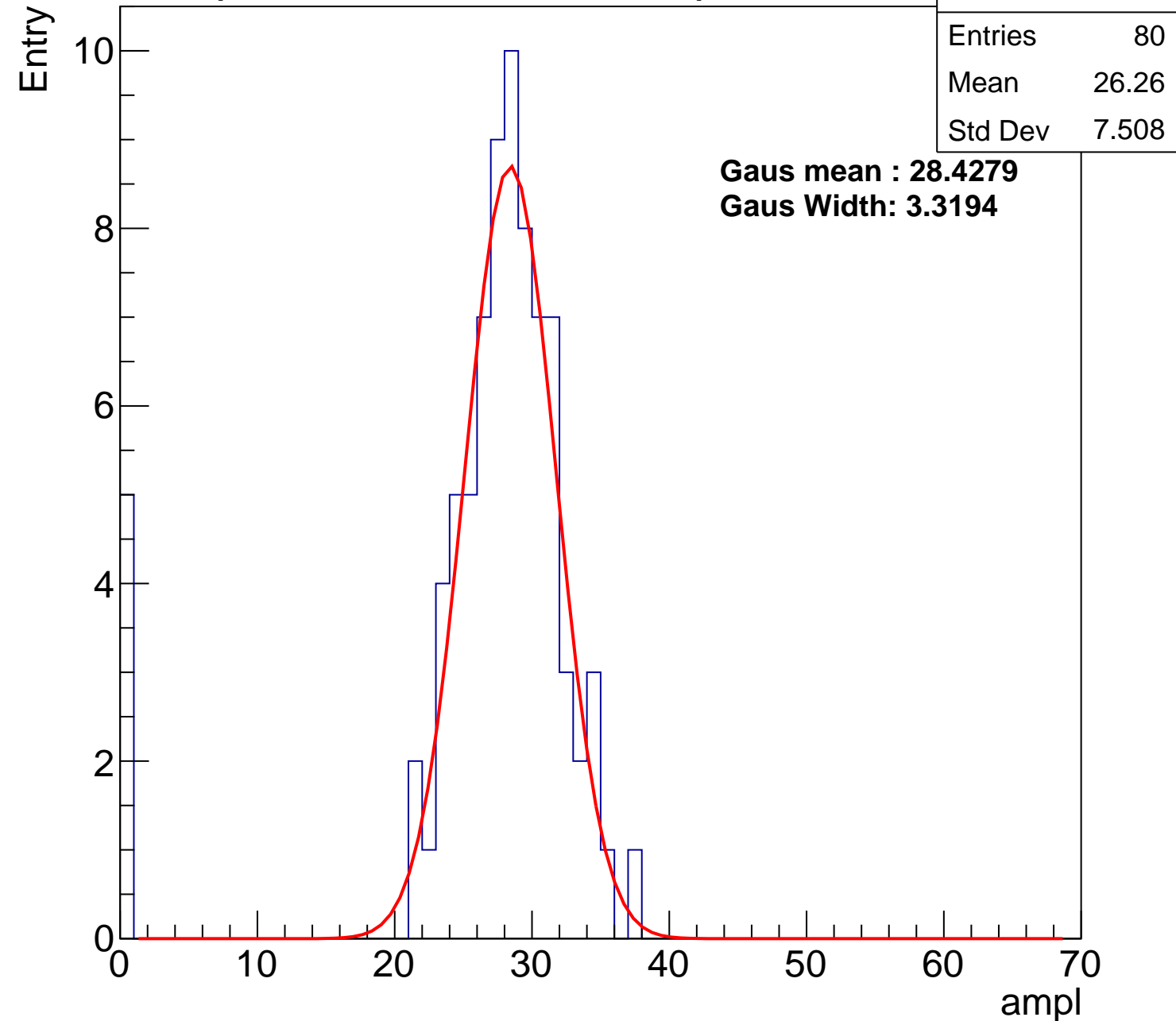
Gaus Width: 3.3194

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch25, adc1

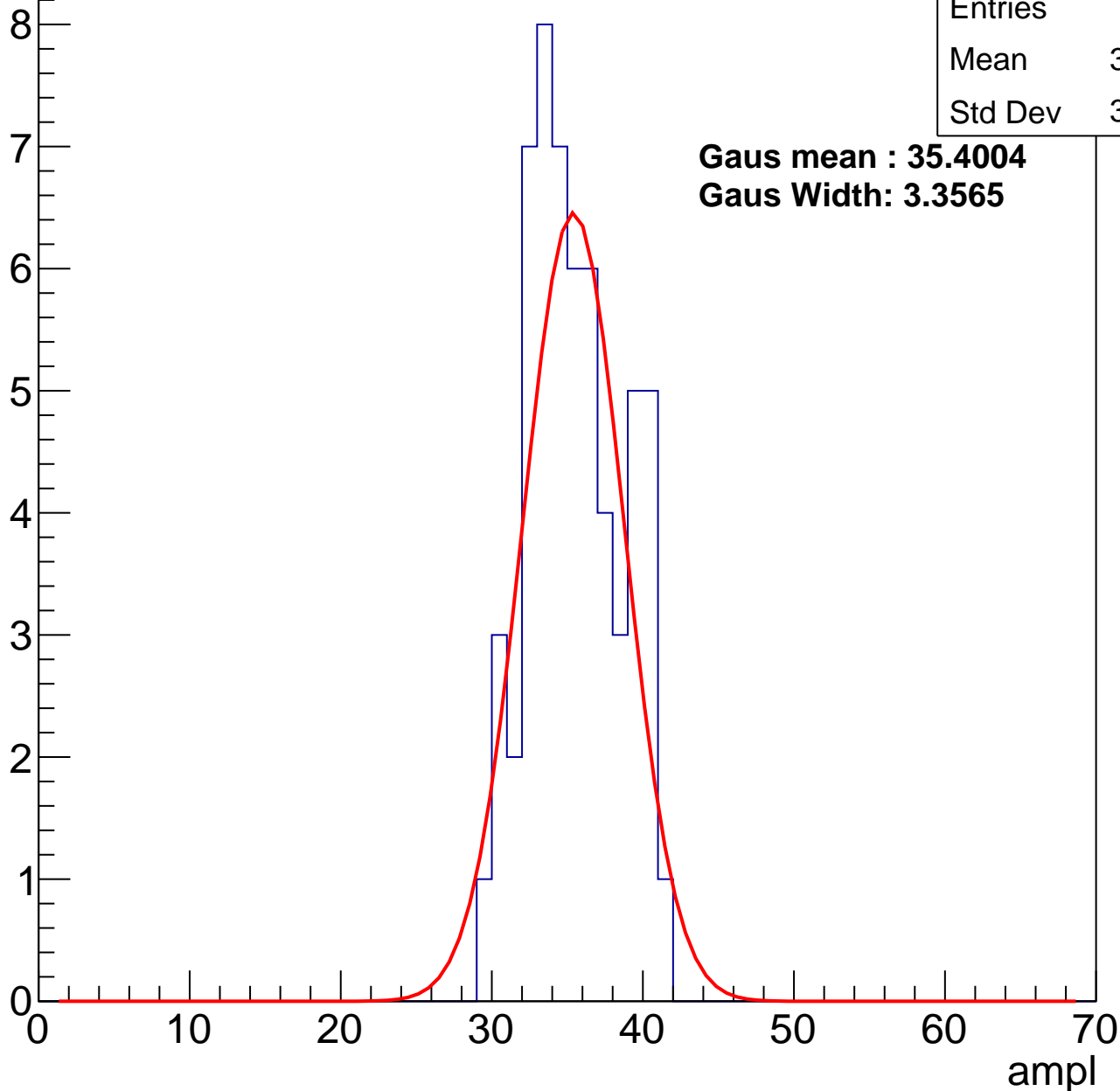
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	35.02
Std Dev	3.037

Gaus mean : 35.4004

Gaus Width: 3.3565



B1L103S, U6-ch25, adc2

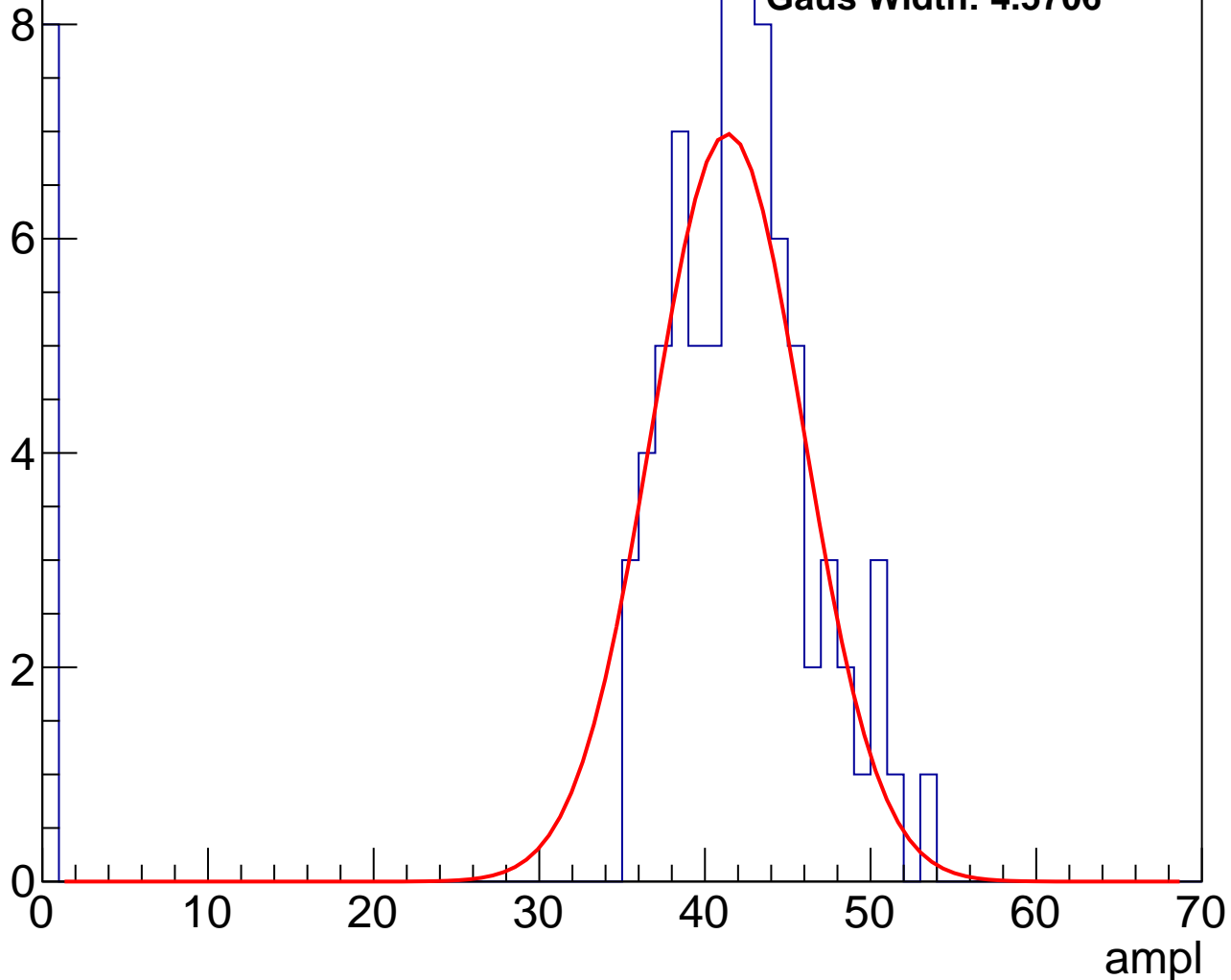
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	38.06
Std Dev	12.63

Gaus mean : 41.3695

Gaus Width: 4.5706

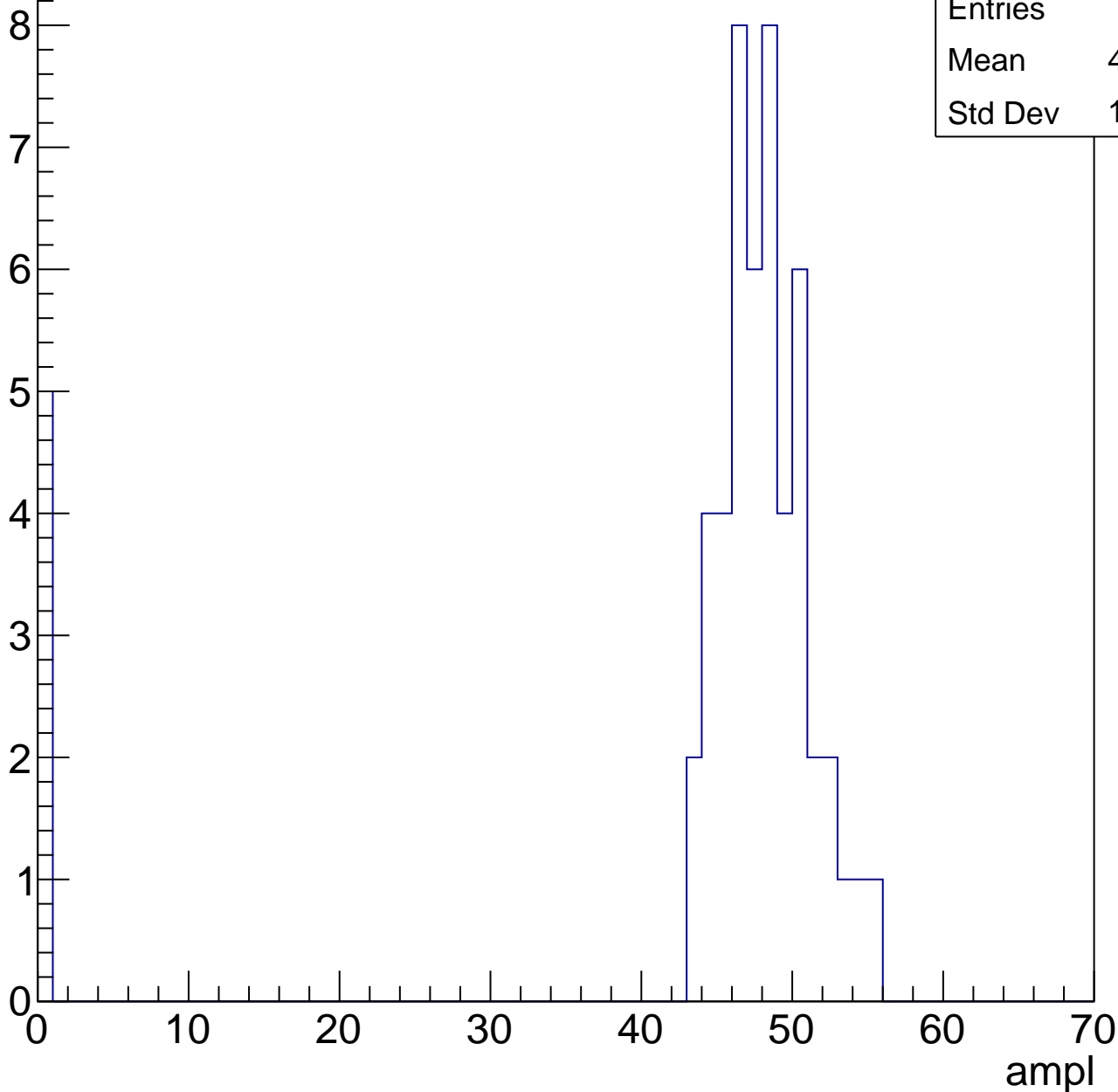


B1L103S, U6-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	43.33
Std Dev	14.09

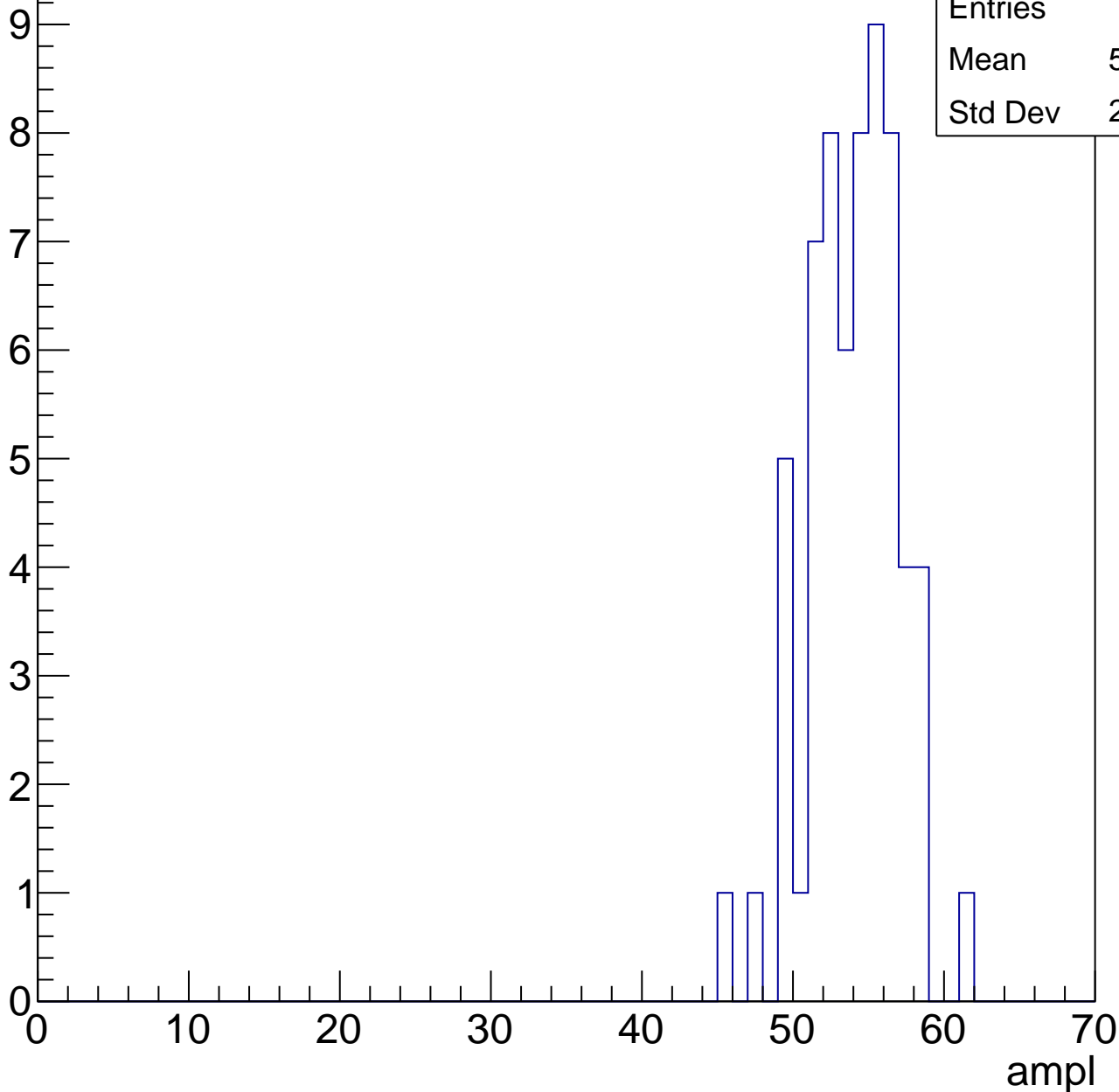


B1L103S, U6-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.56
Std Dev	2.953

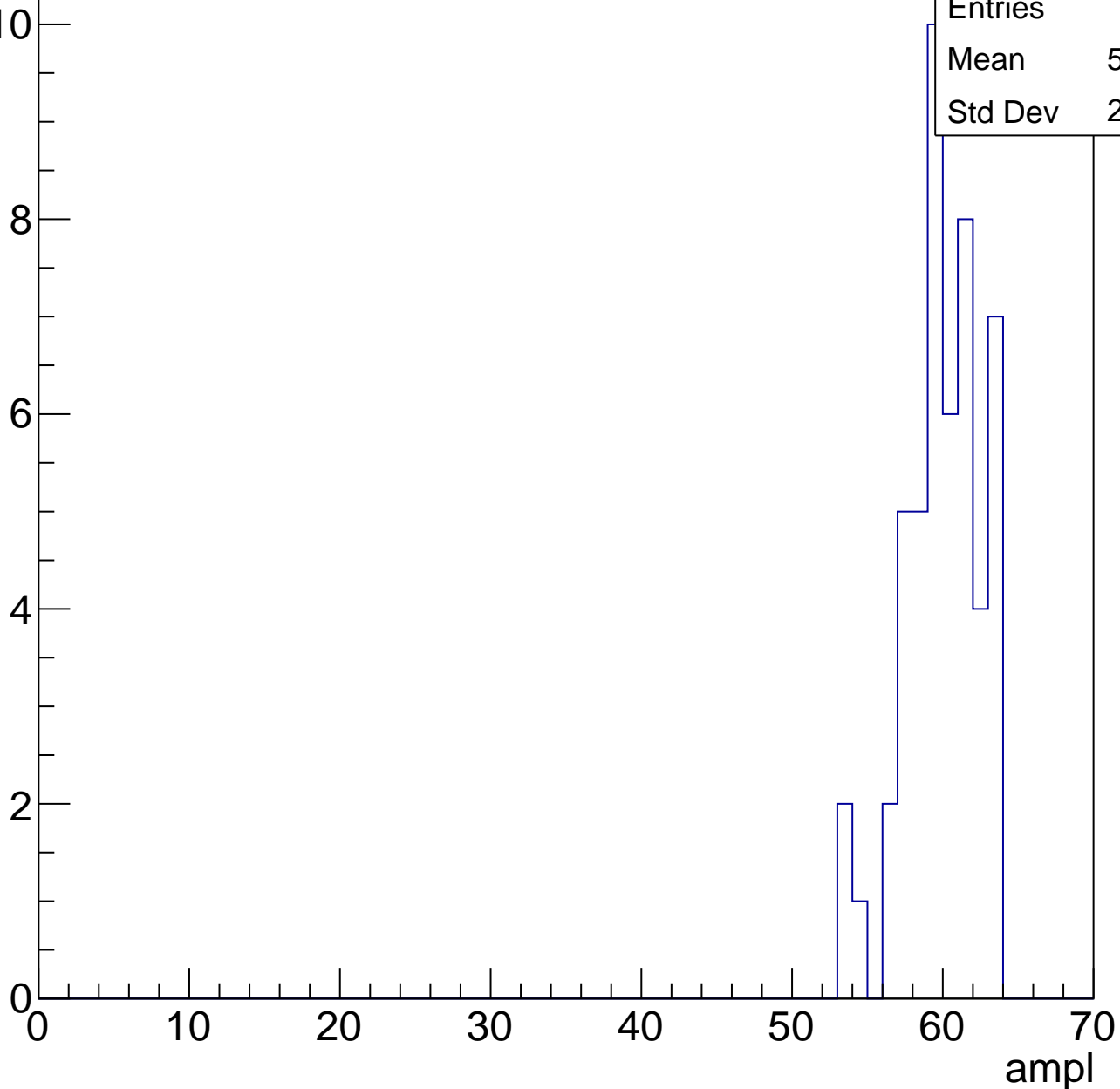


B1L103S, U6-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	59.48
Std Dev	2.508

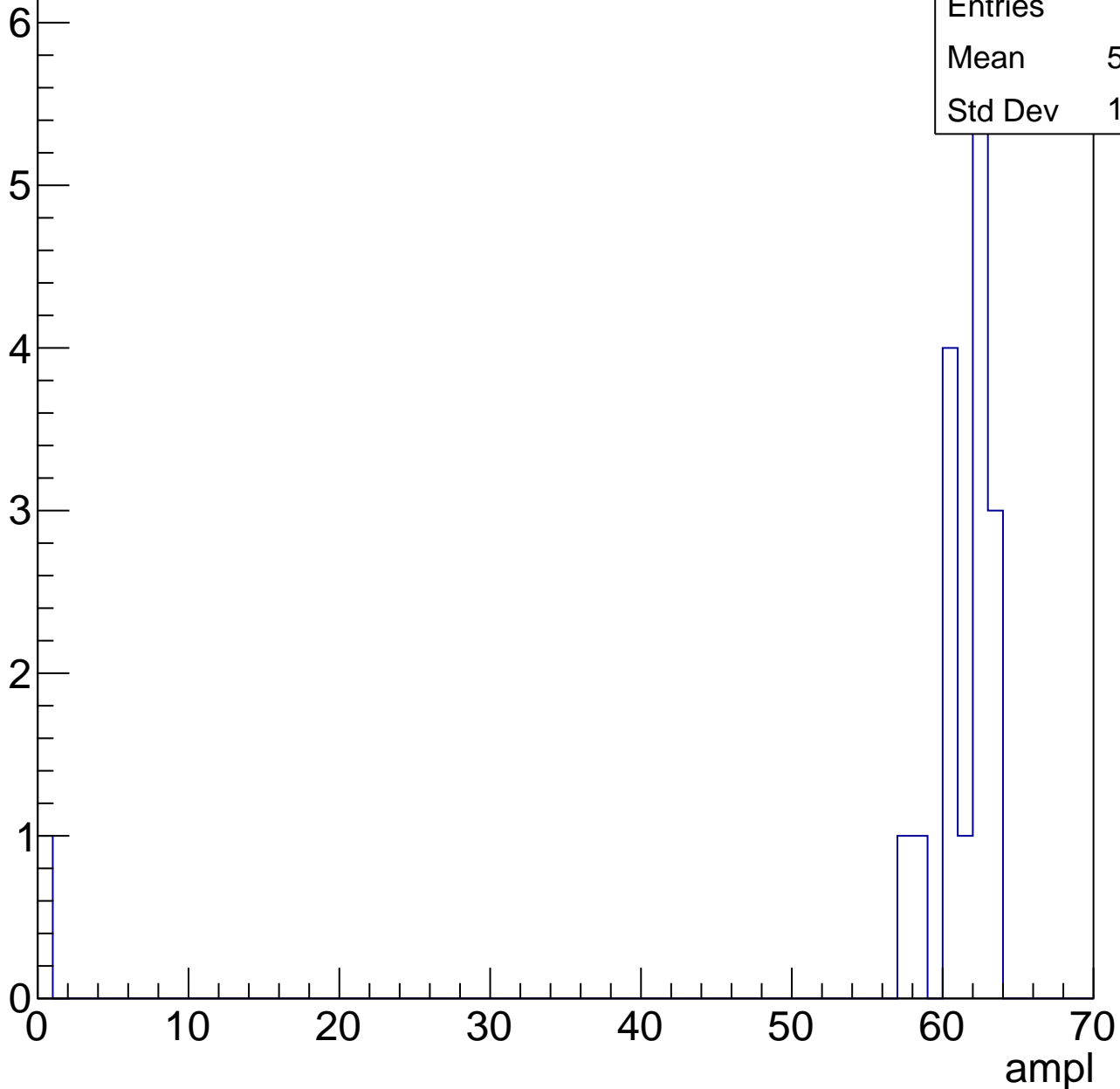


B1L103S, U6-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.47
Std Dev	14.46



B1L103S, U6-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



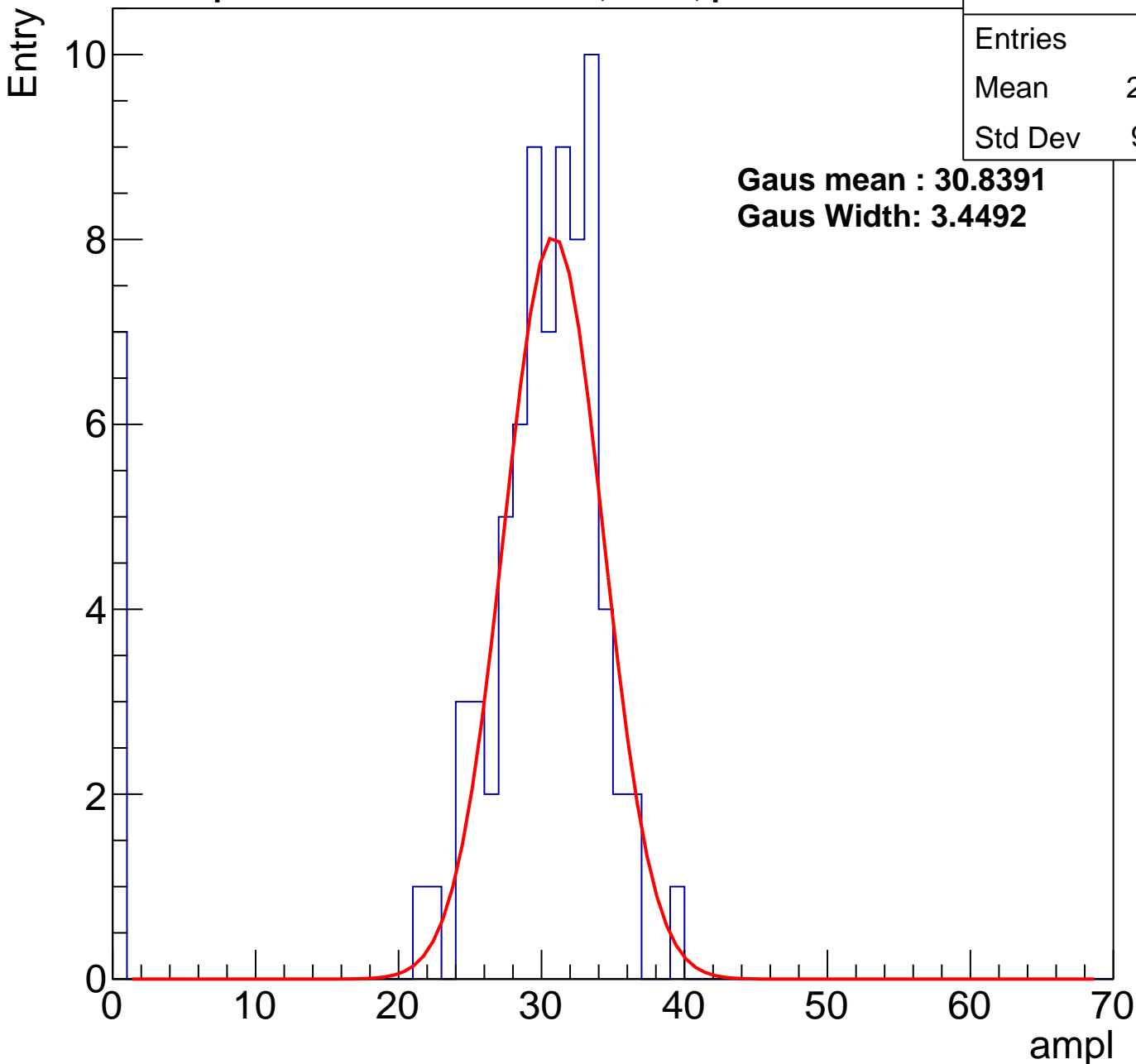
B1L103S, U6-ch26, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	27.48
Std Dev	9.101

Gaus mean : 30.8391

Gaus Width: 3.4492



B1L103S, U6-ch26, adc1

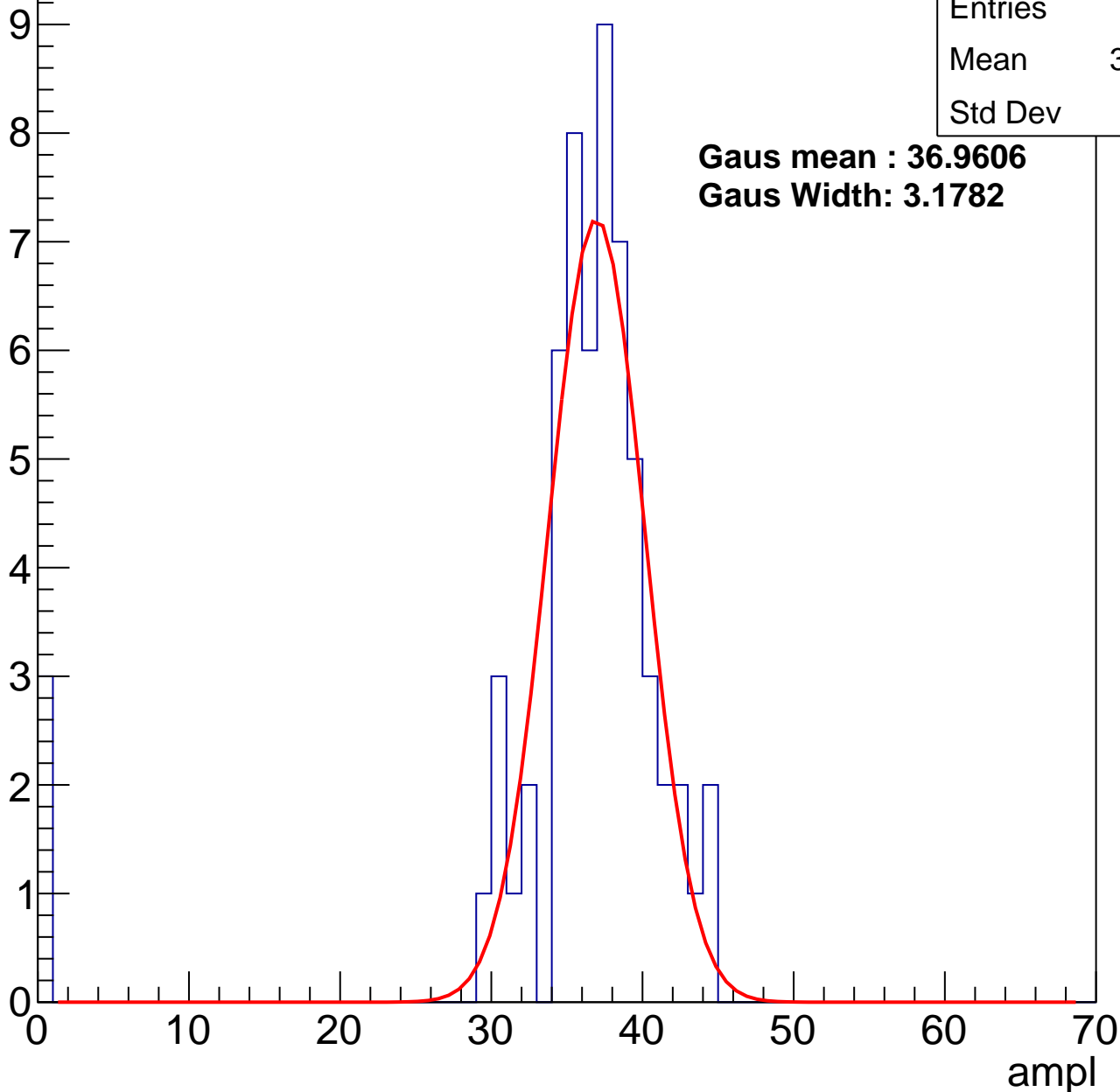
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.84
Std Dev	8.57

Gaus mean : 36.9606

Gaus Width: 3.1782



B1L103S, U6-ch26, adc2

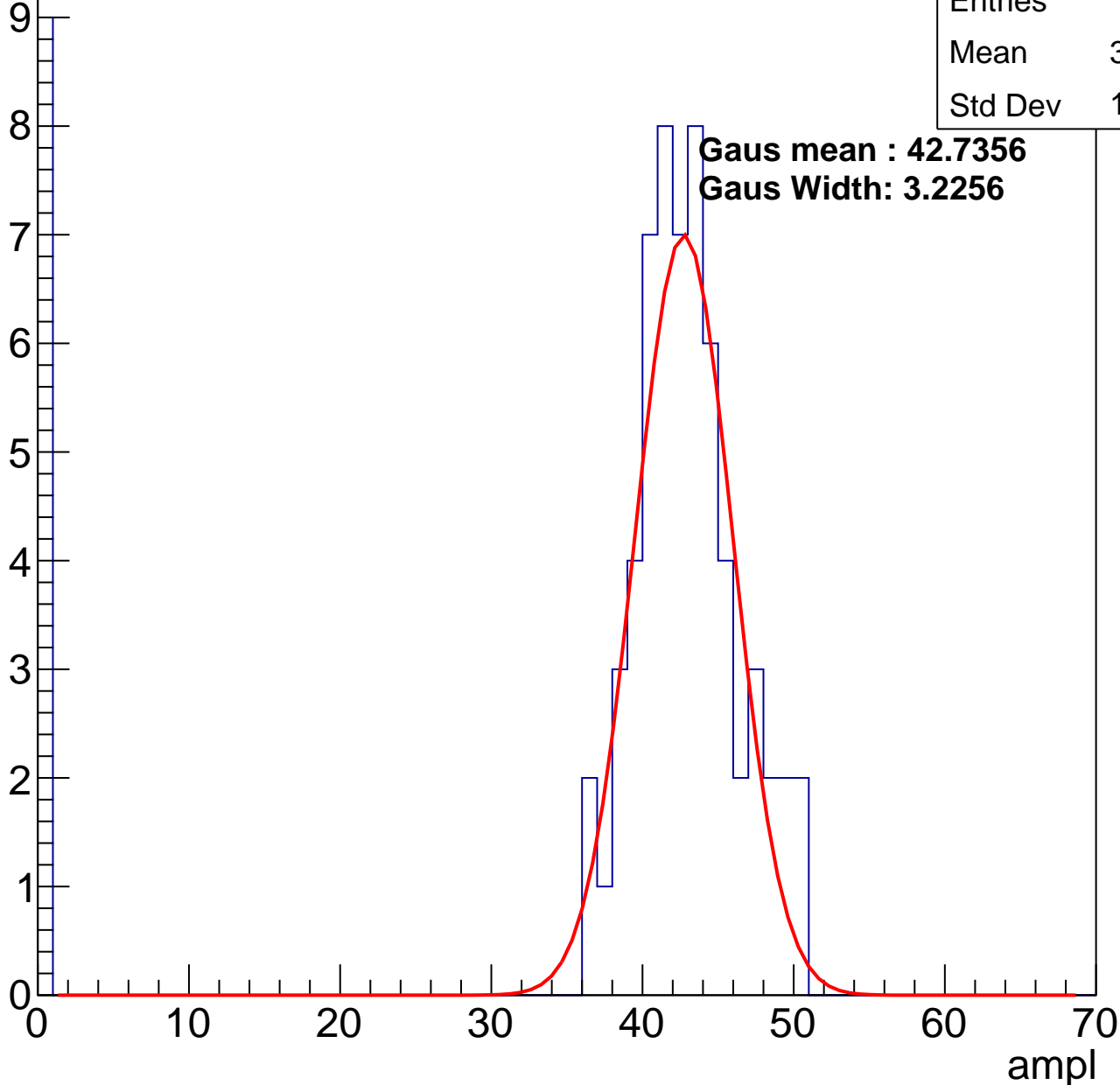
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.09
Std Dev	14.58

Gaus mean : 42.7356

Gaus Width: 3.2256

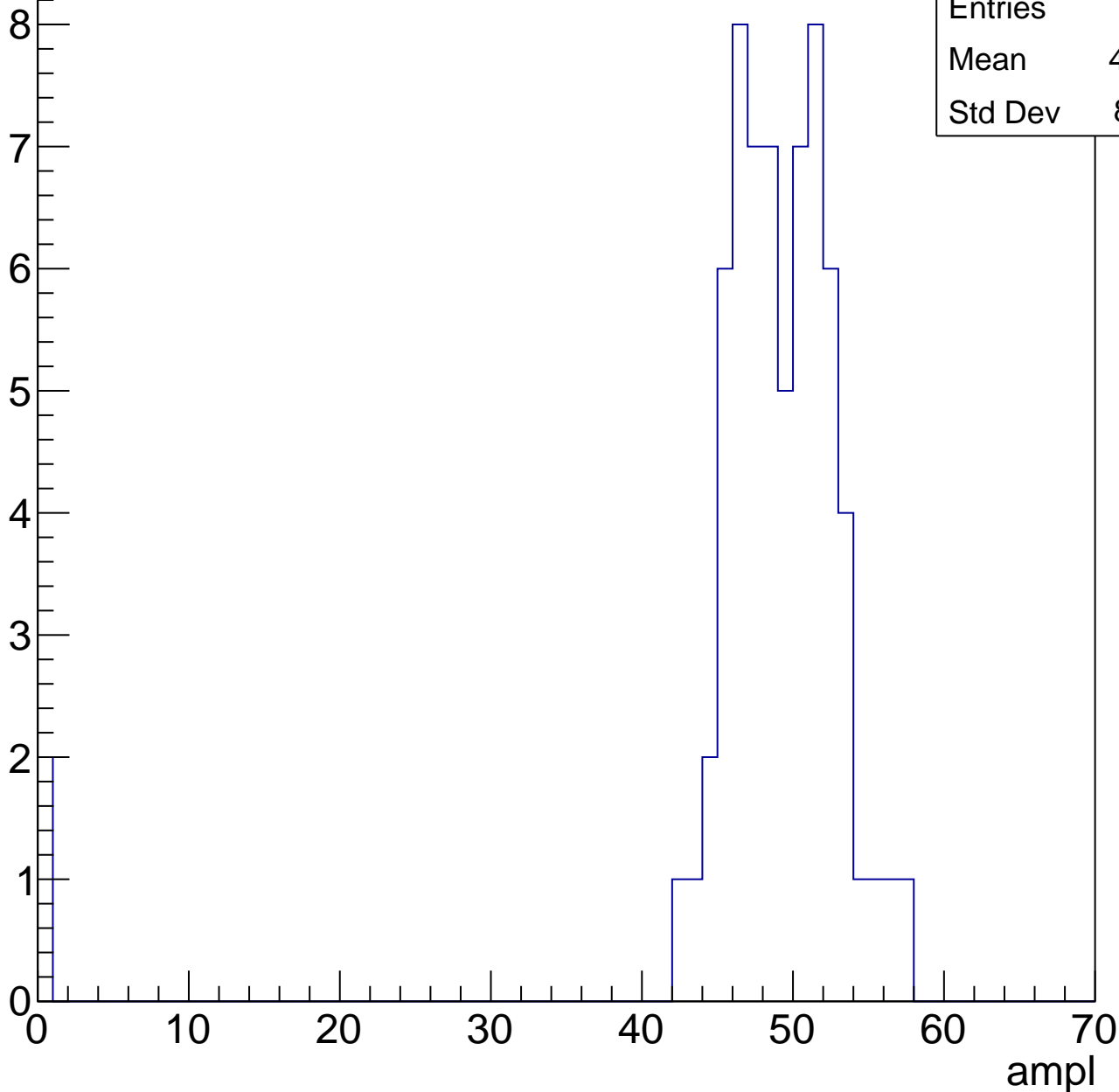


B1L103S, U6-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	47.43
Std Dev	8.831

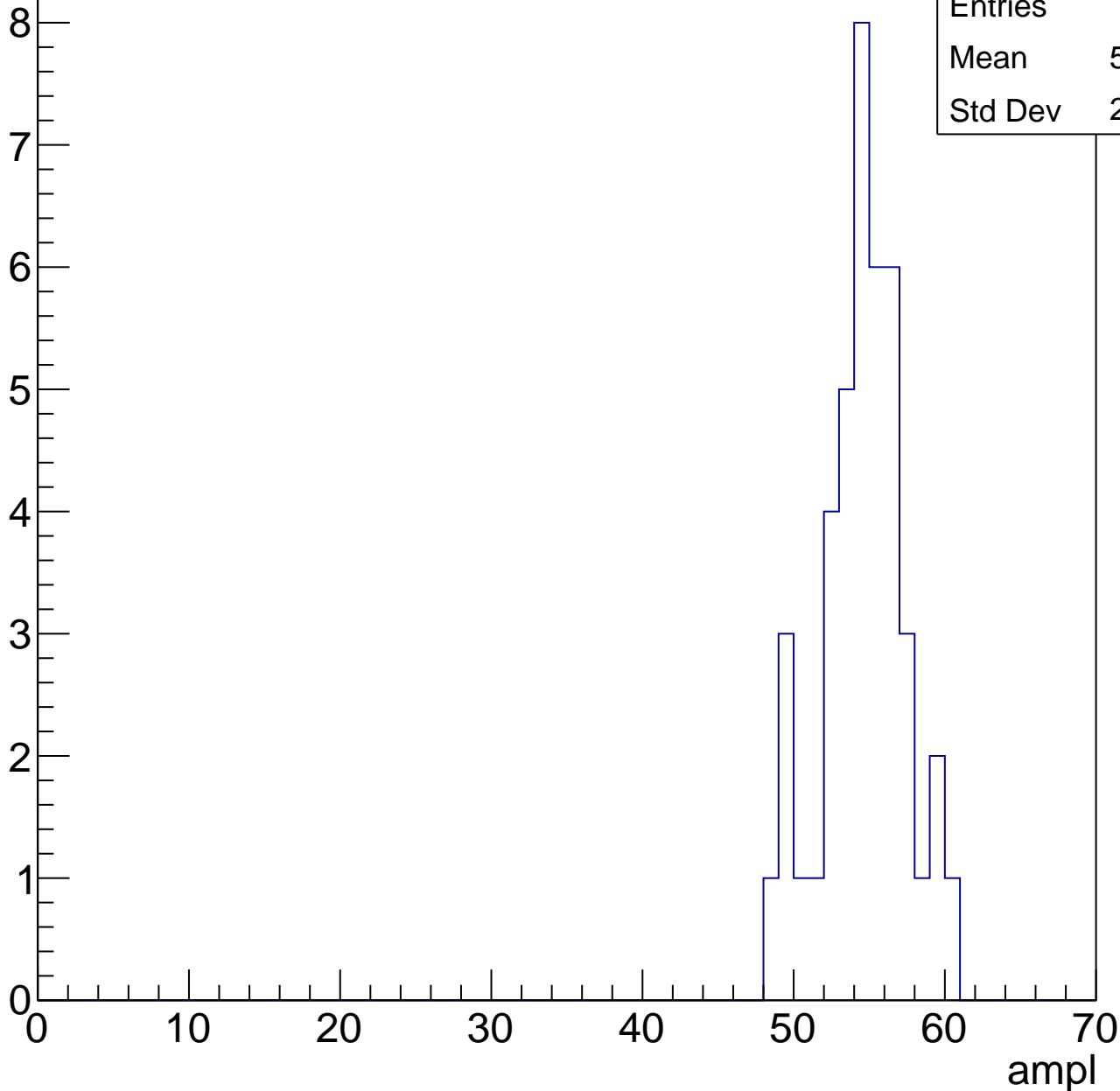


B1L103S, U6-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

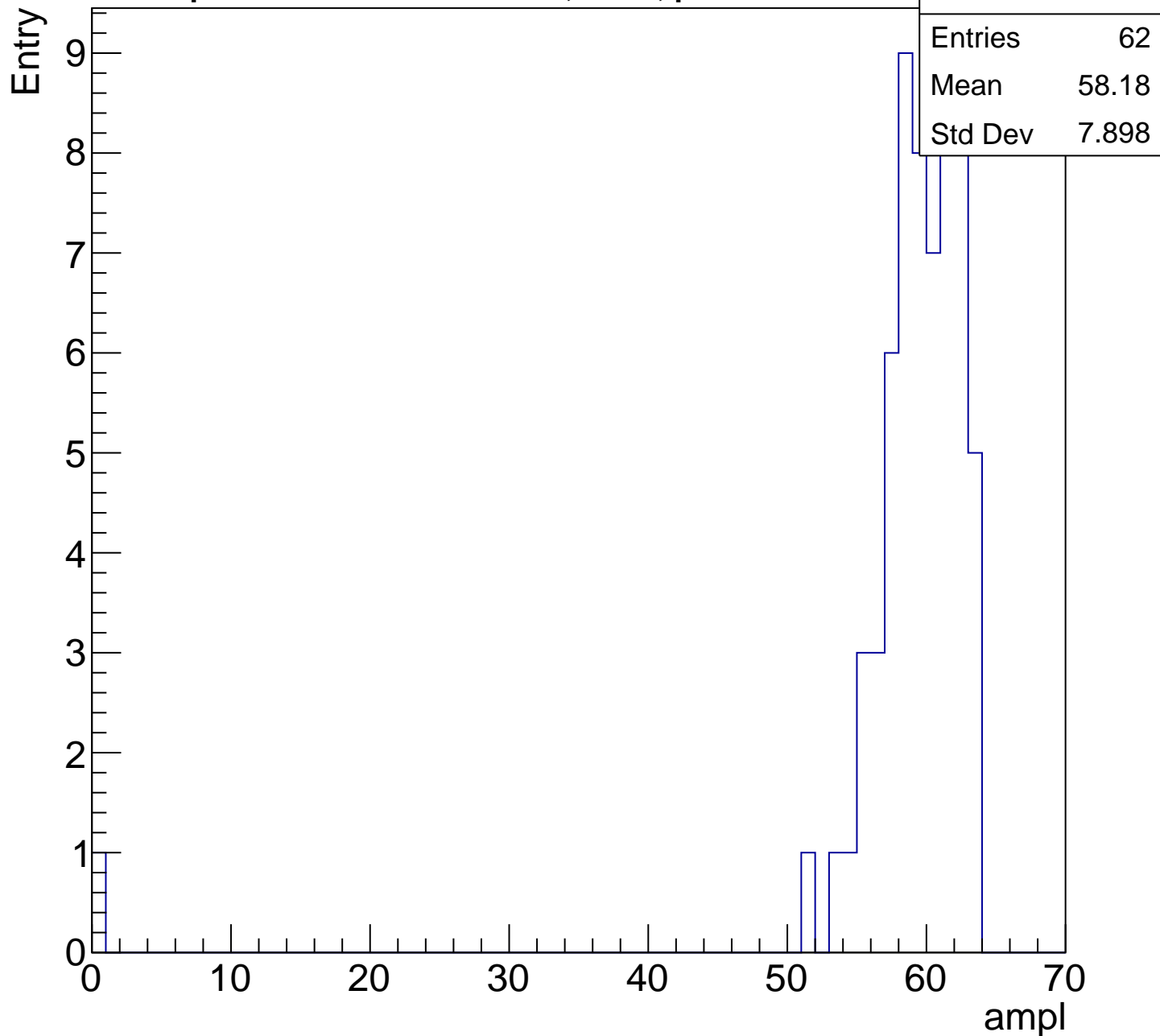
Entry

Entries	42
Mean	54.14
Std Dev	2.739



B1L103S, U6-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

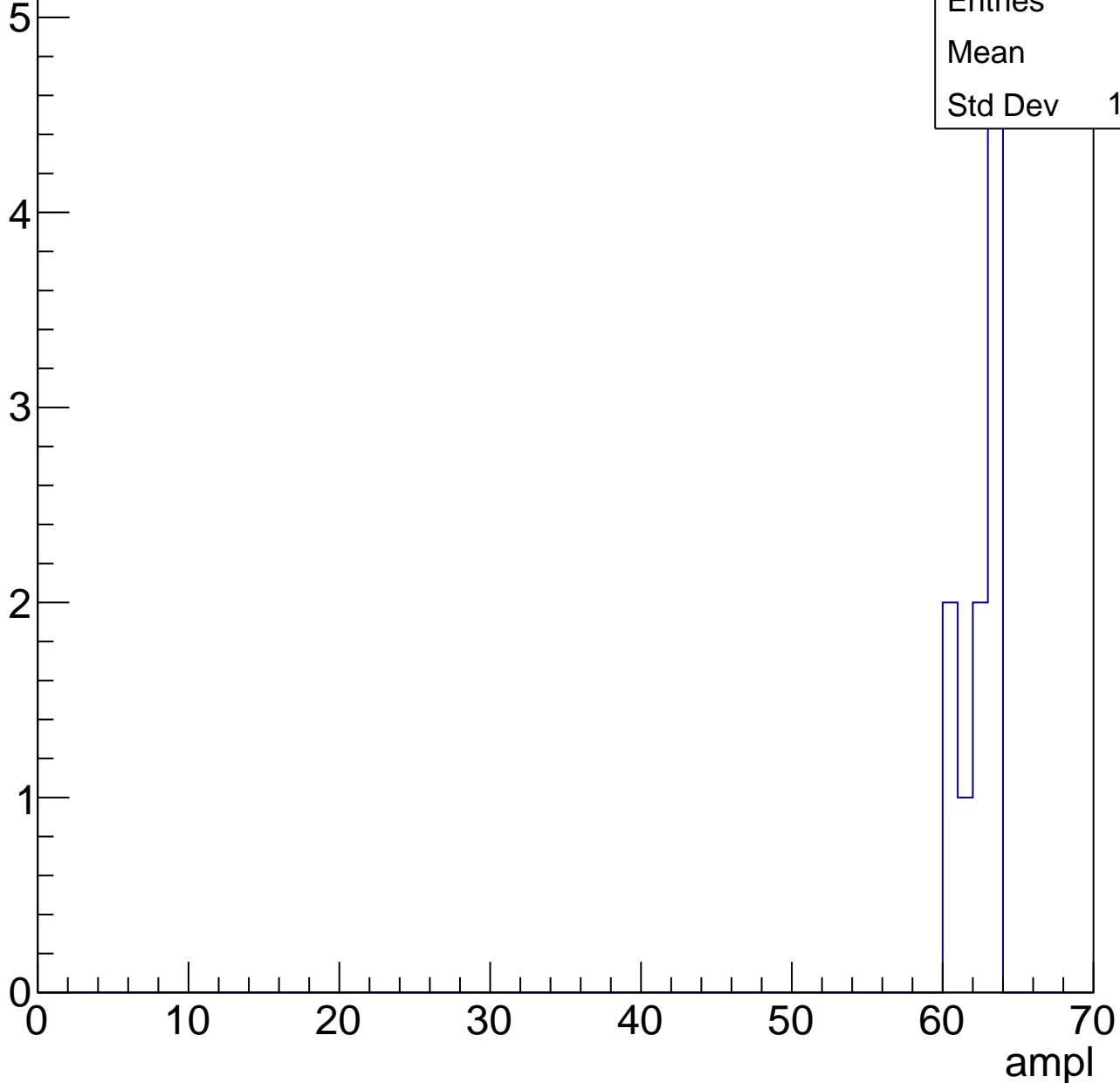


B1L103S, U6-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62
Std Dev	1.183



B1L103S, U6-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	25.47
Std Dev	10.65

Gaus mean : 29.9264

Gaus Width: 2.8729

Entry

10

8

6

4

2

0

0

10

20

30

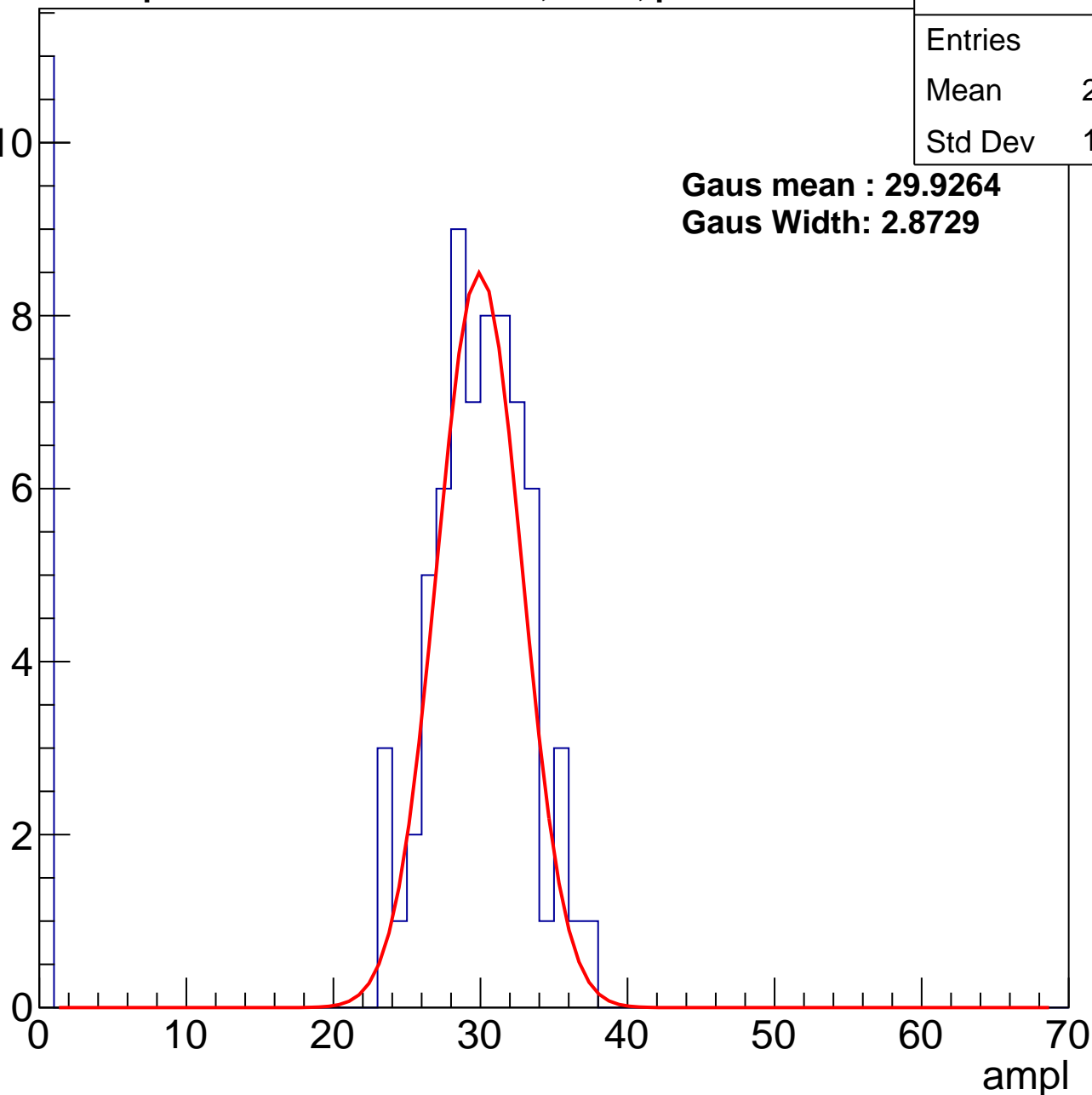
40

50

60

70

ampl



B1L103S, U6-ch27, adc1

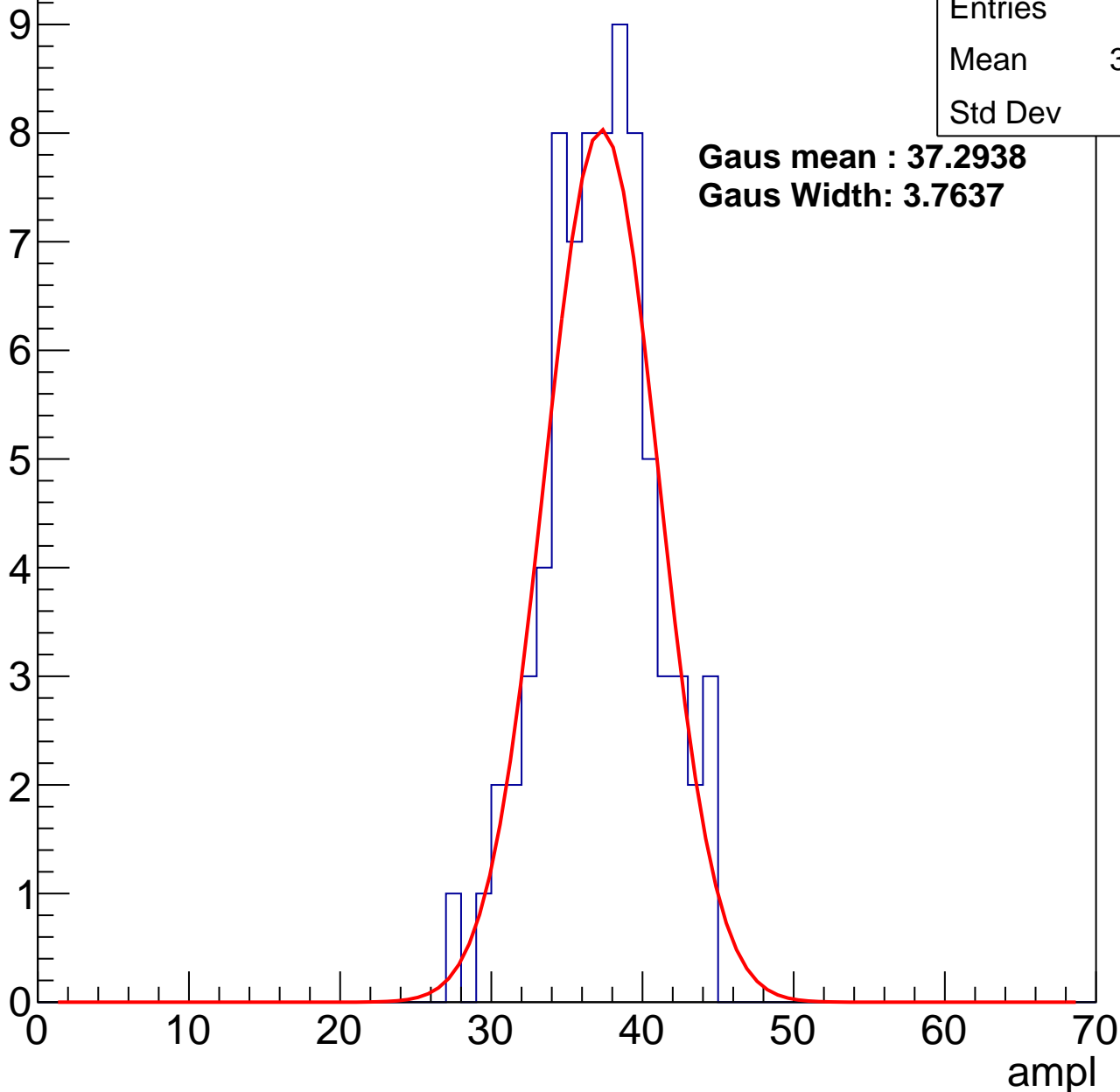
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	36.73
Std Dev	3.61

Gaus mean : 37.2938

Gaus Width: 3.7637



B1L103S, U6-ch27, adc2

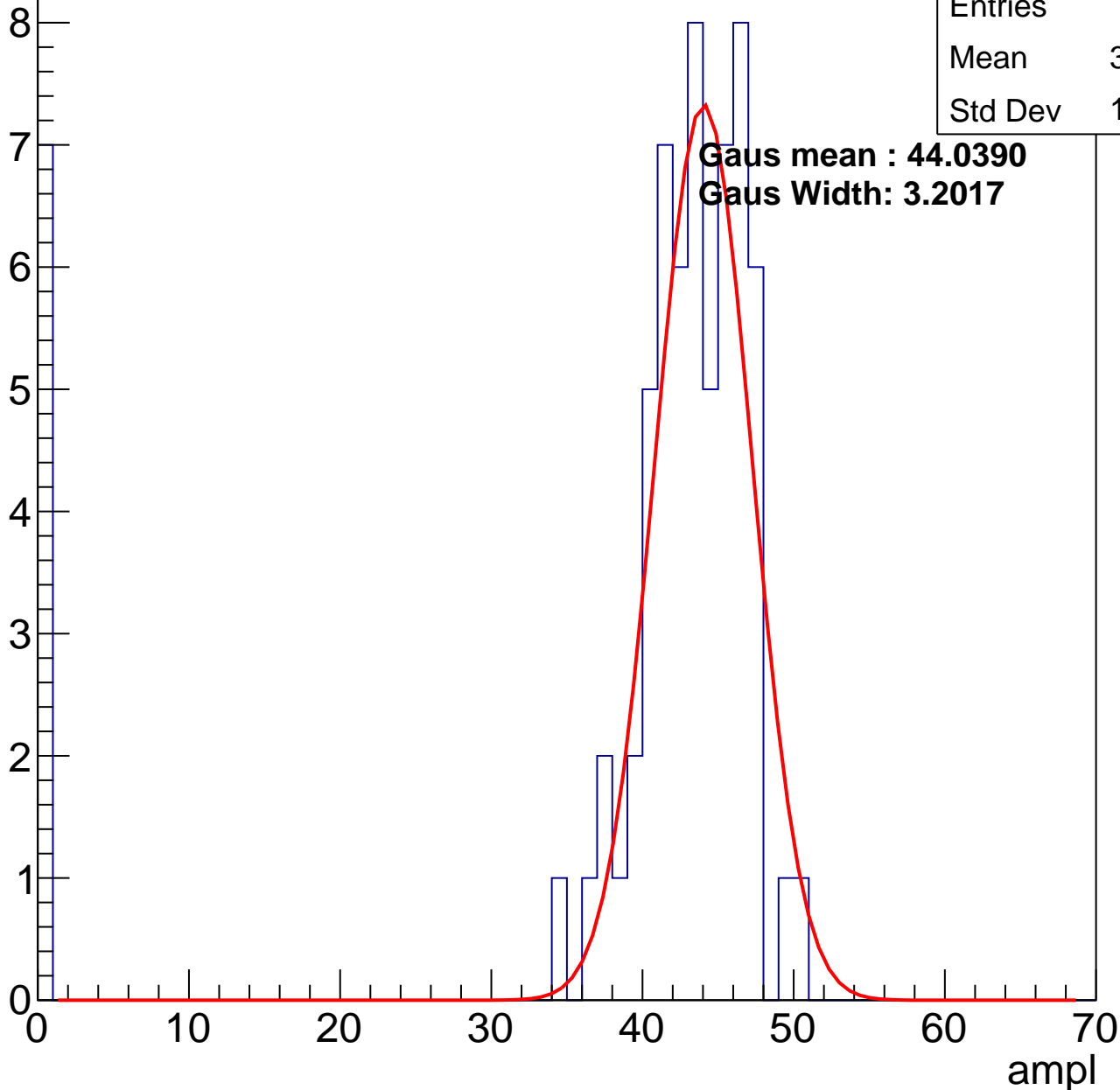
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.63
Std Dev	13.43

Gaus mean : 44.0390

Gaus Width: 3.2017

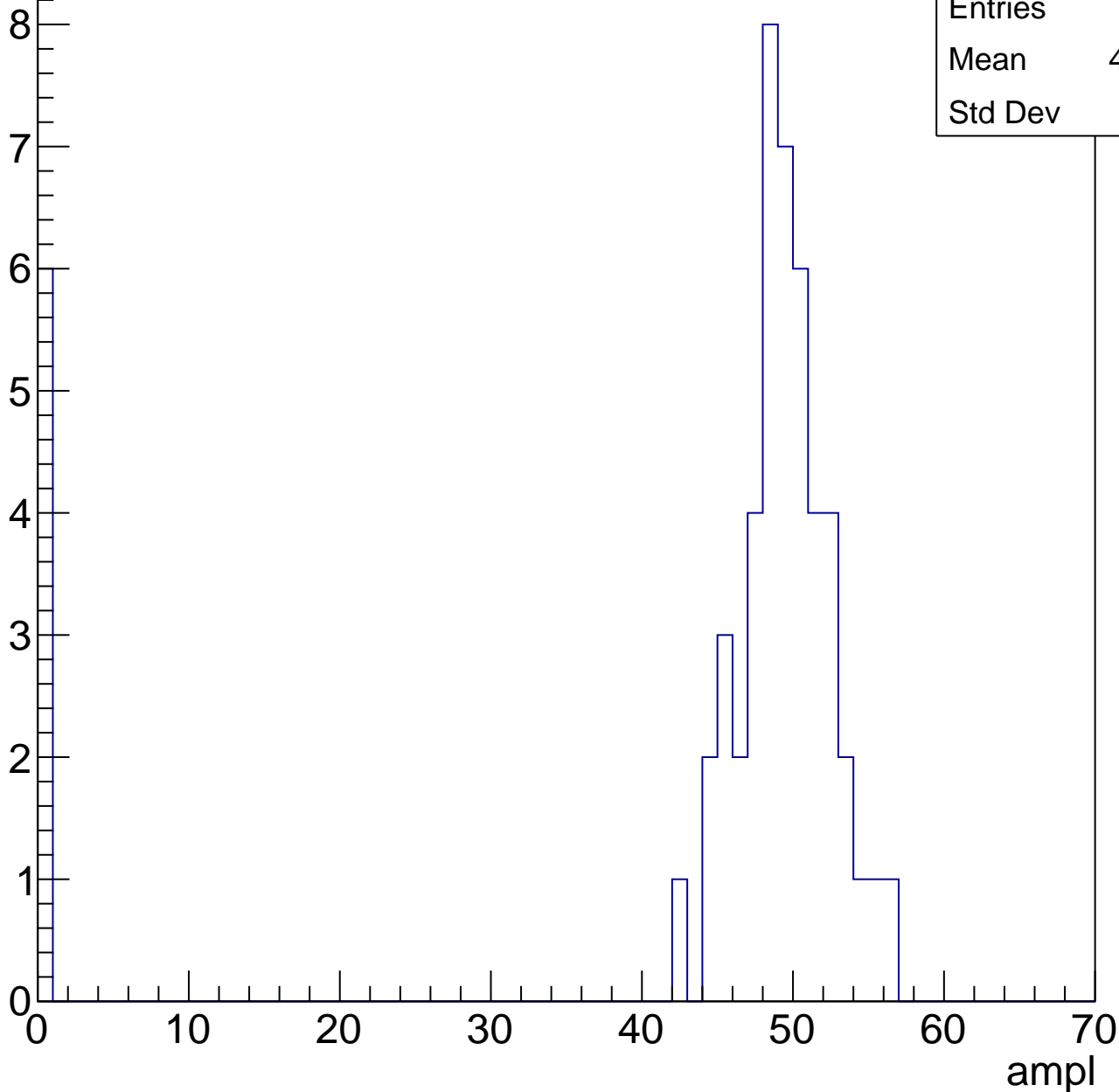


B1L103S, U6-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	43.37
Std Dev	15.9

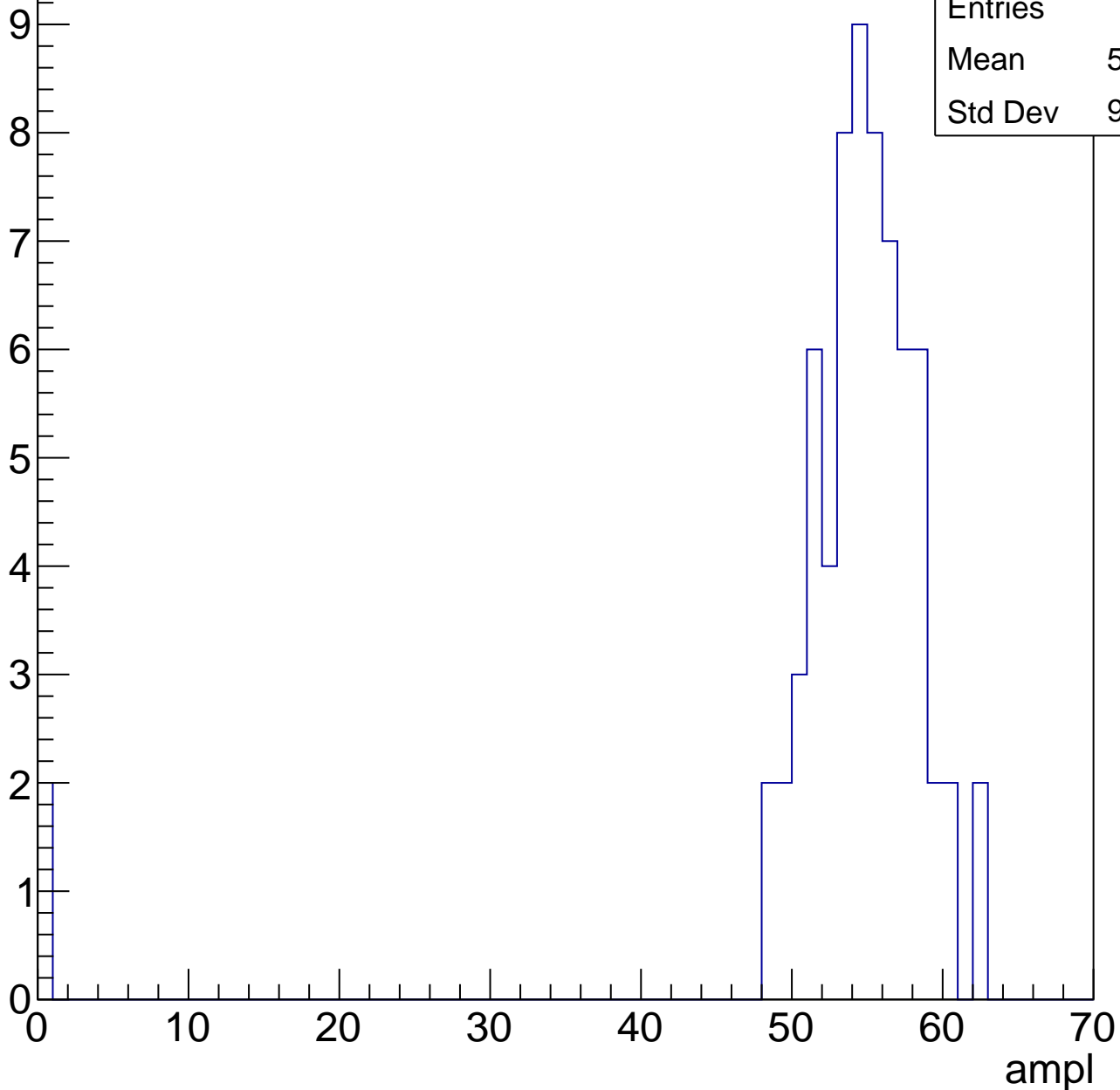


B1L103S, U6-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	52.93
Std Dev	9.658

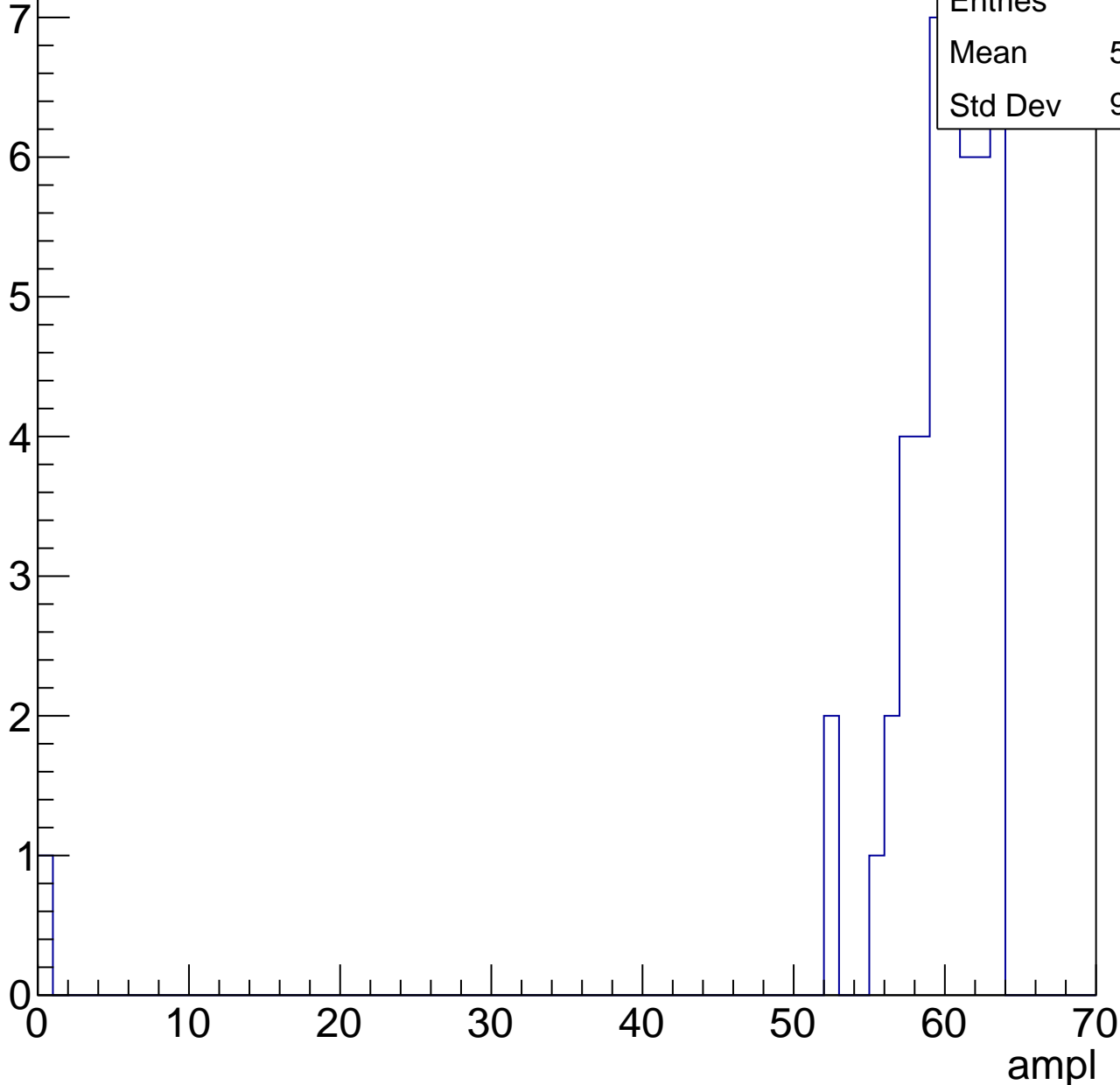


B1L103S, U6-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

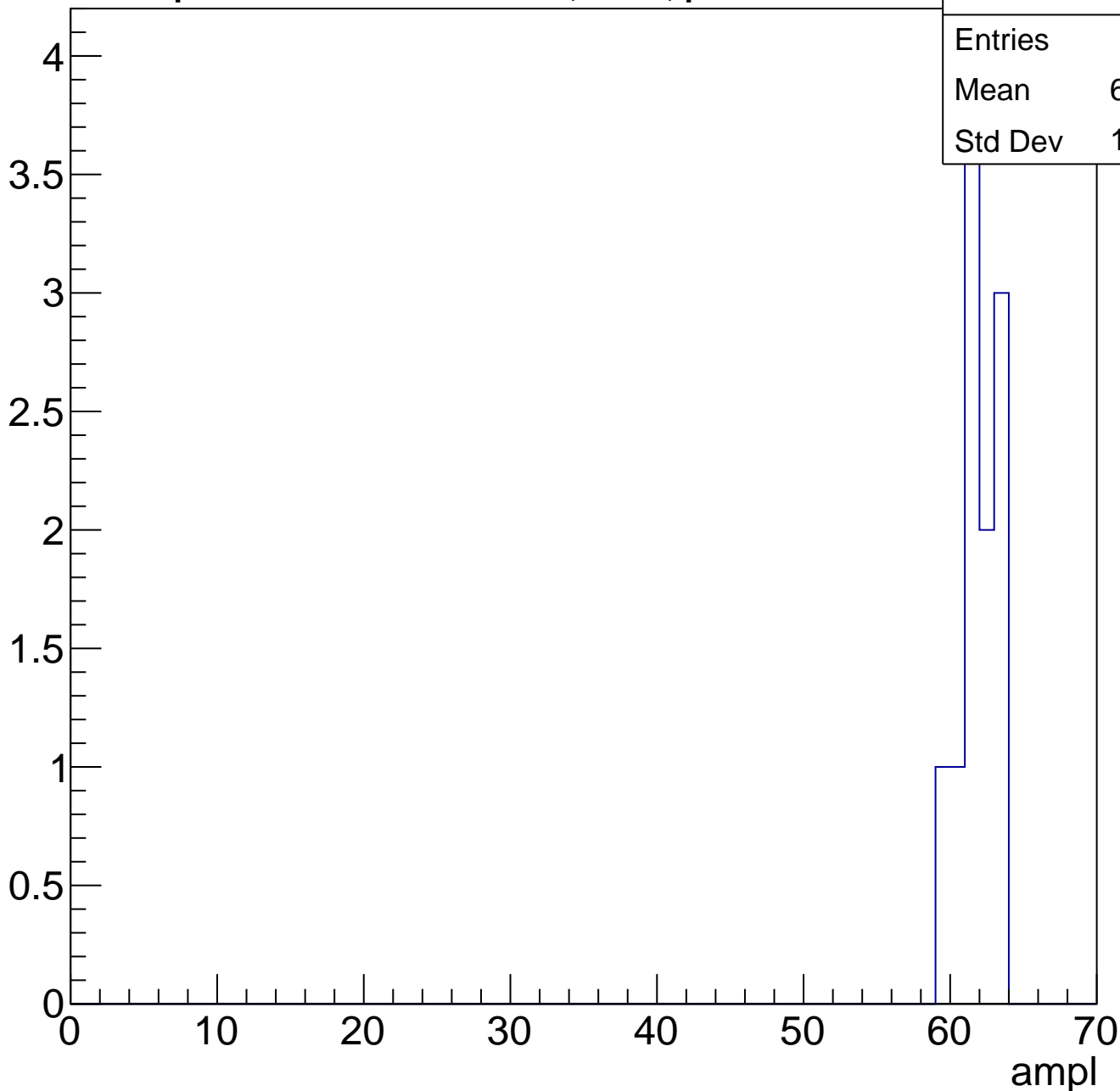
Entries	47
Mean	58.36
Std Dev	9.005



B1L103S, U6-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U6-ch28, adc0

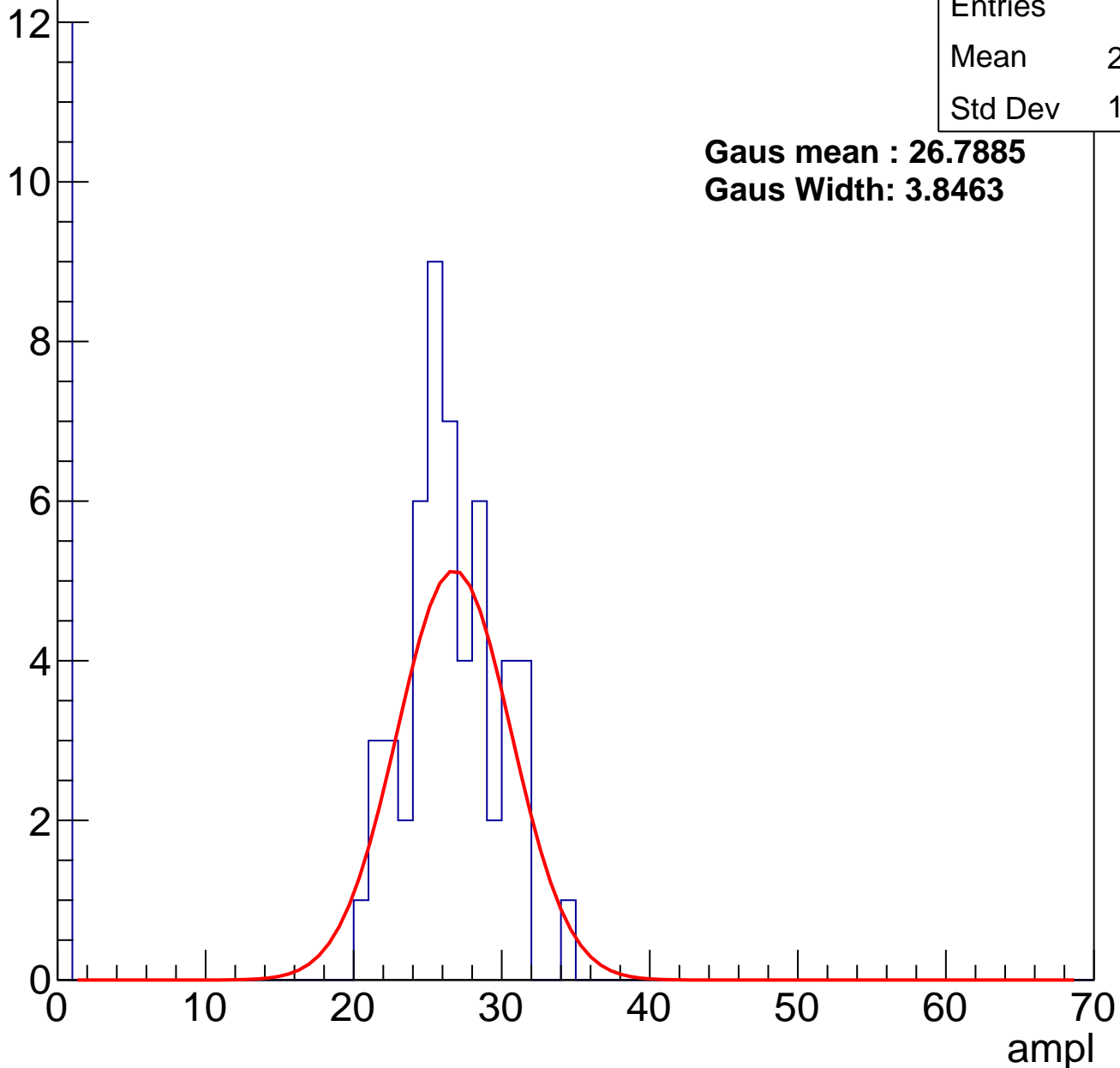
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	21.22
Std Dev	10.56

Gaus mean : 26.7885

Gaus Width: 3.8463

Entry



B1L103S, U6-ch28, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	32.02
Std Dev	4.987

Gaus mean : 32.9750

Gaus Width: 3.5673

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U6-ch28, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.05
Std Dev	14.27

Gaus mean : 40.5364

Gaus Width: 3.4039

10

8

6

4

2

0

0

10

20

30

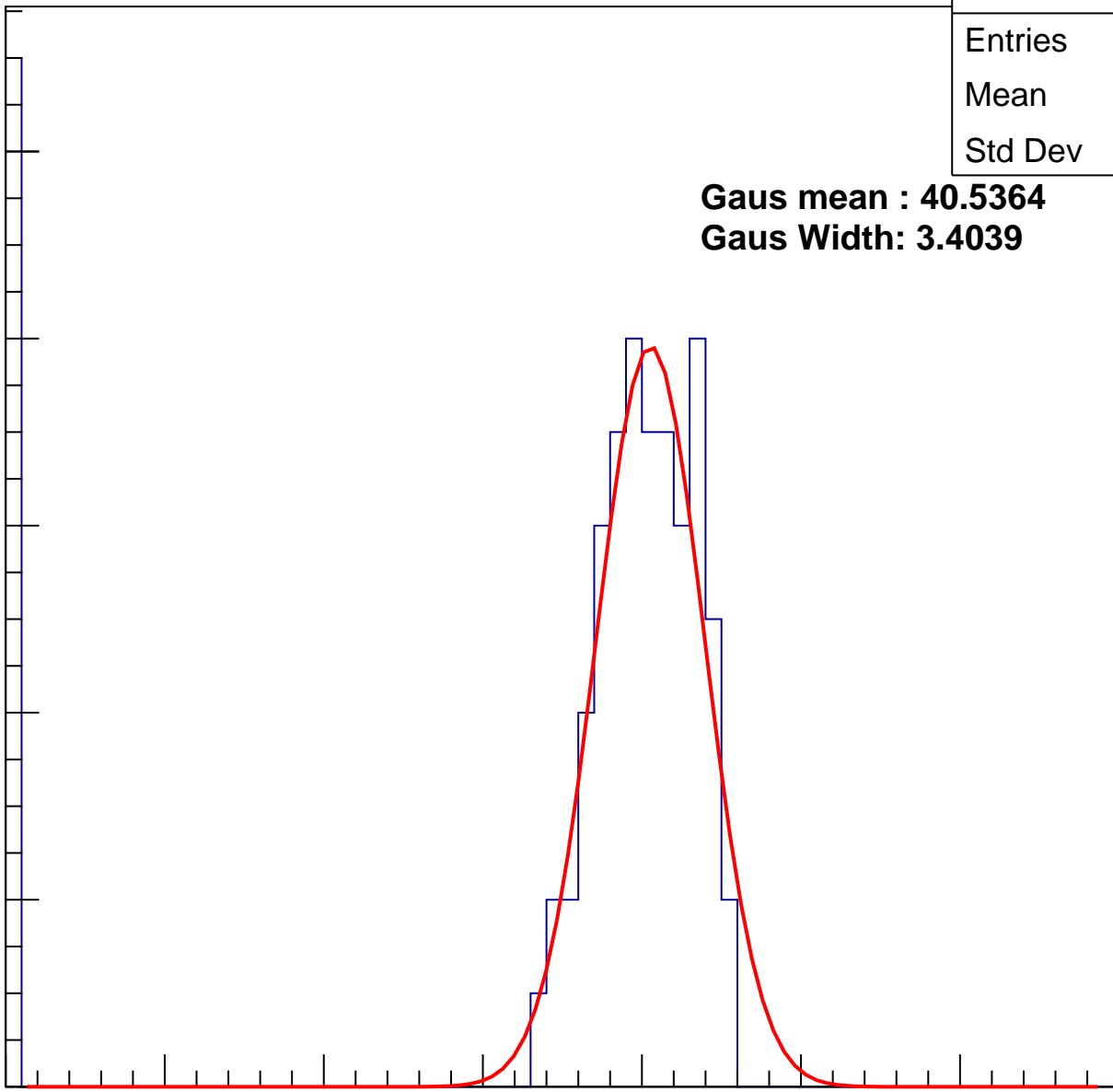
40

50

60

70

ampl

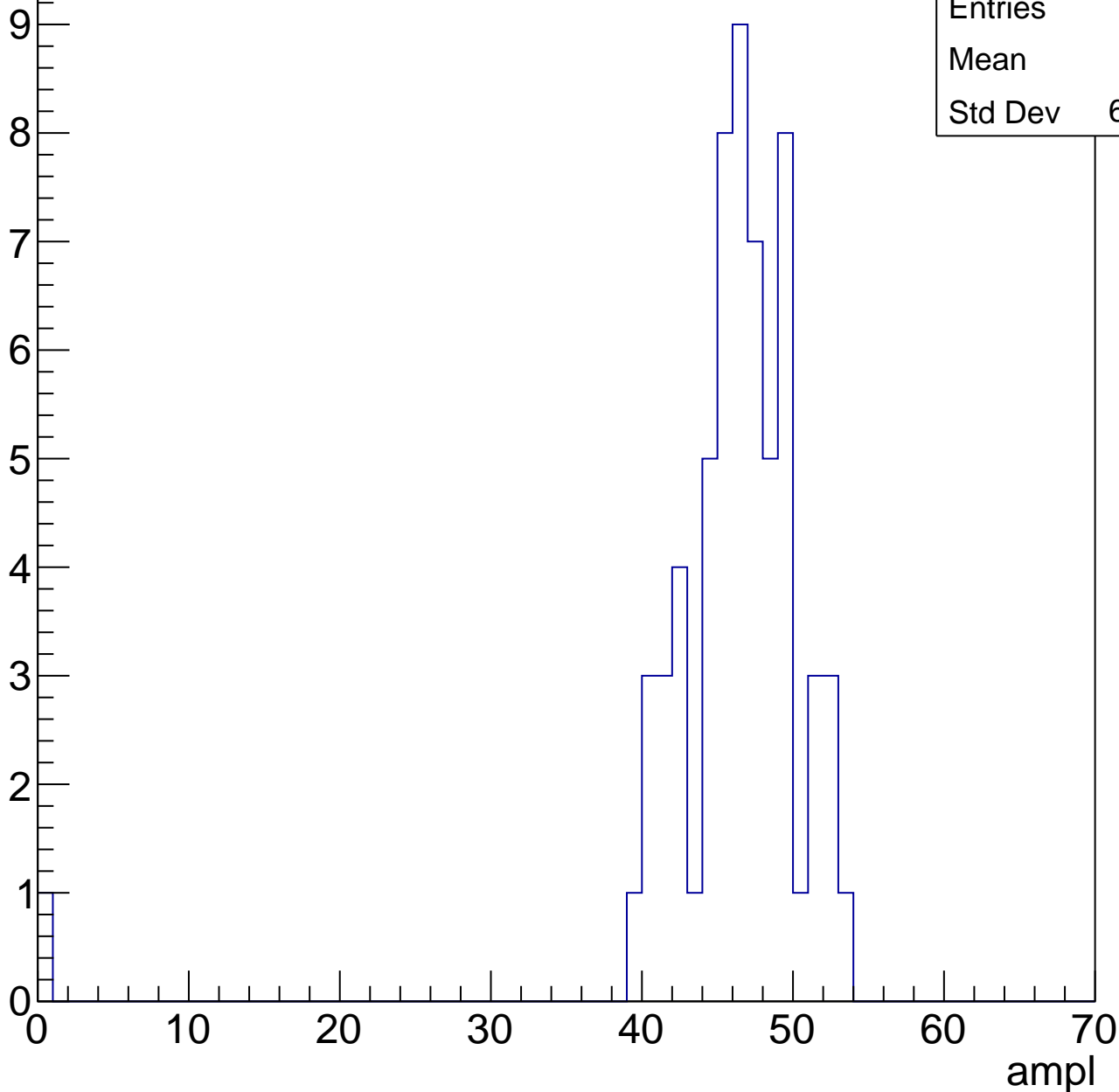


B1L103S, U6-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	45.4
Std Dev	6.635

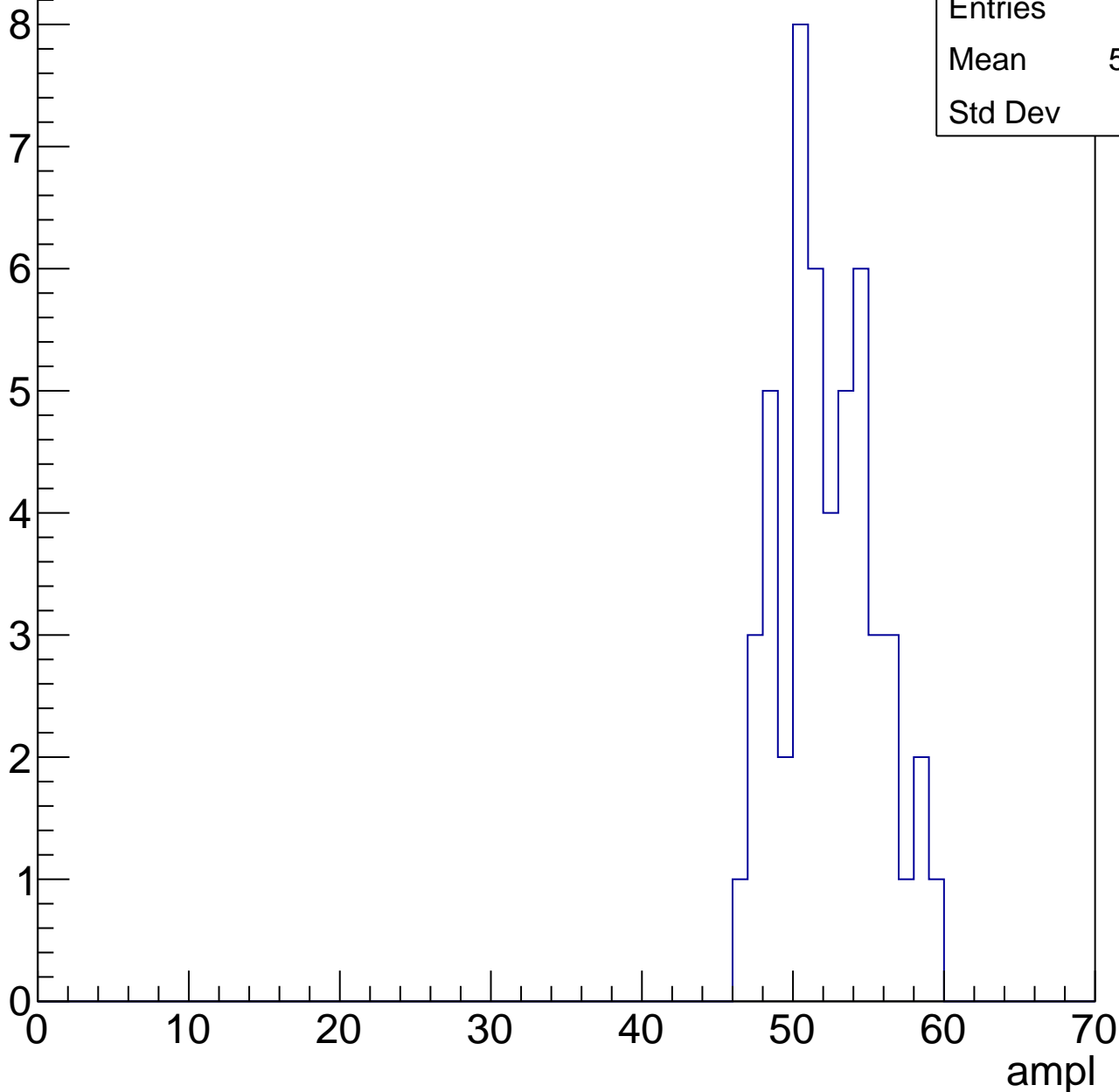


B1L103S, U6-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

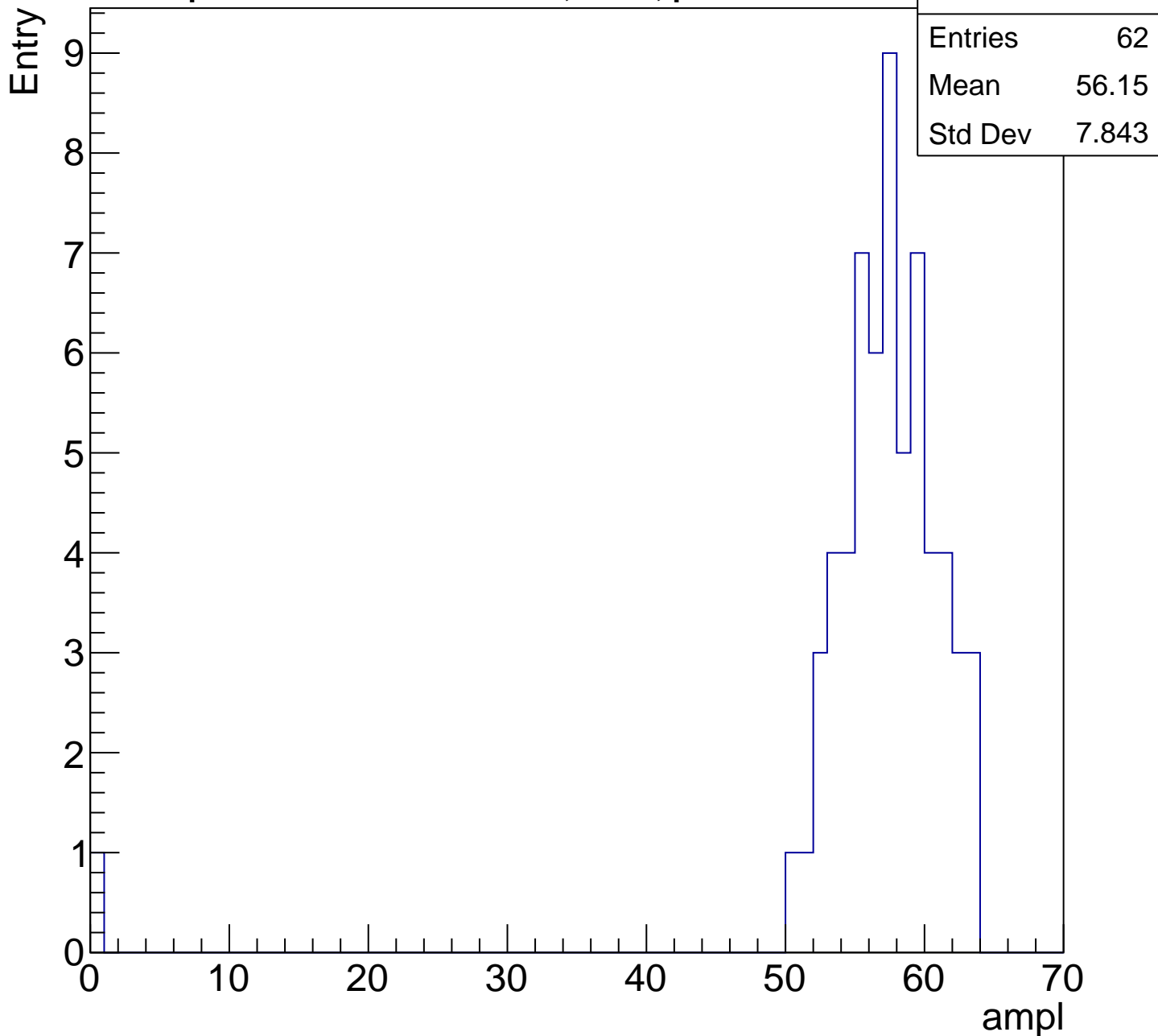
Entry

Entries	50
Mean	51.86
Std Dev	3.15



B1L103S, U6-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

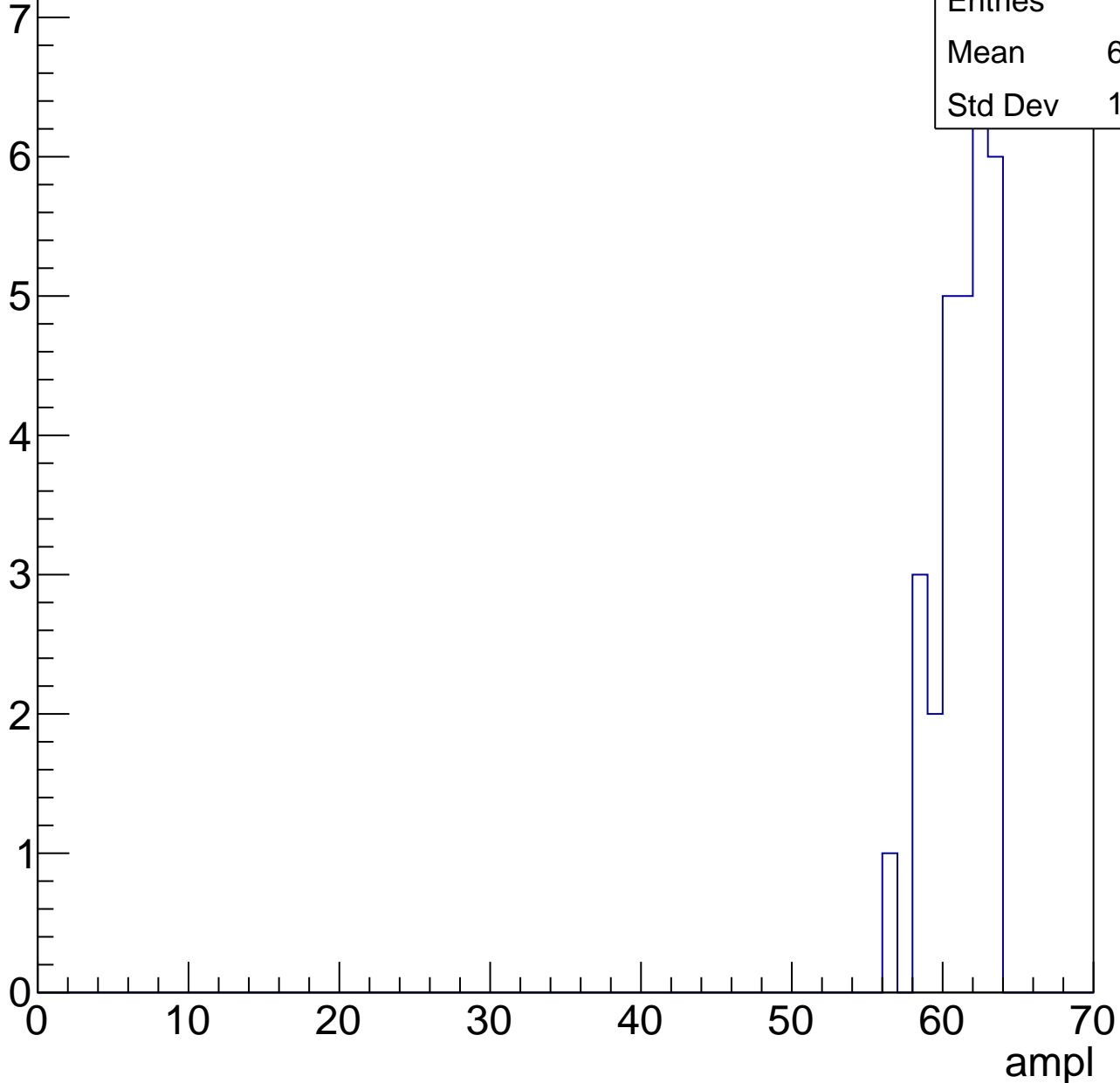


B1L103S, U6-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	60.86
Std Dev	1.814

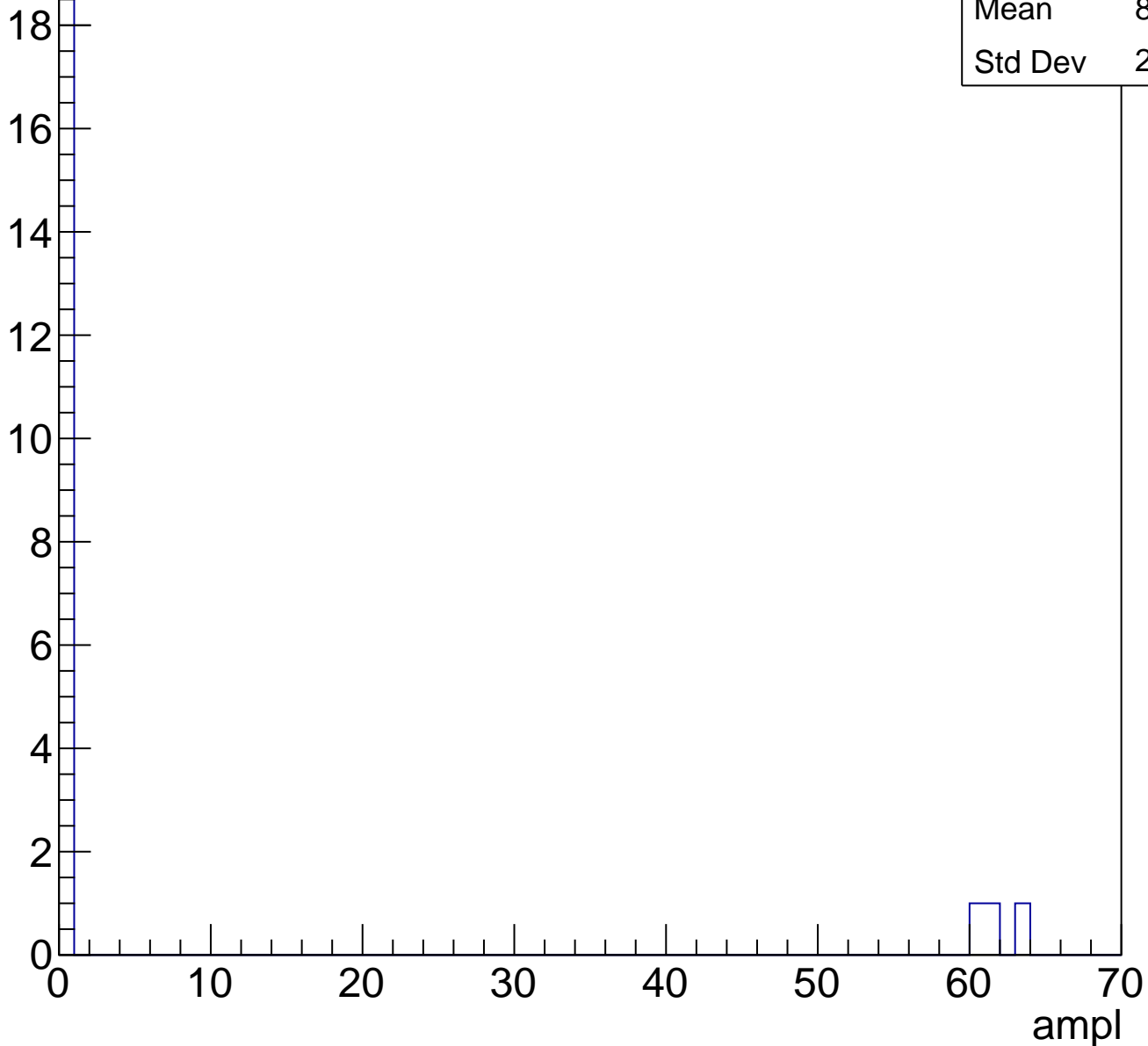


B1L103S, U6-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.364
Std Dev	21.05

Entry



B1L103S, U6-ch29, adc0

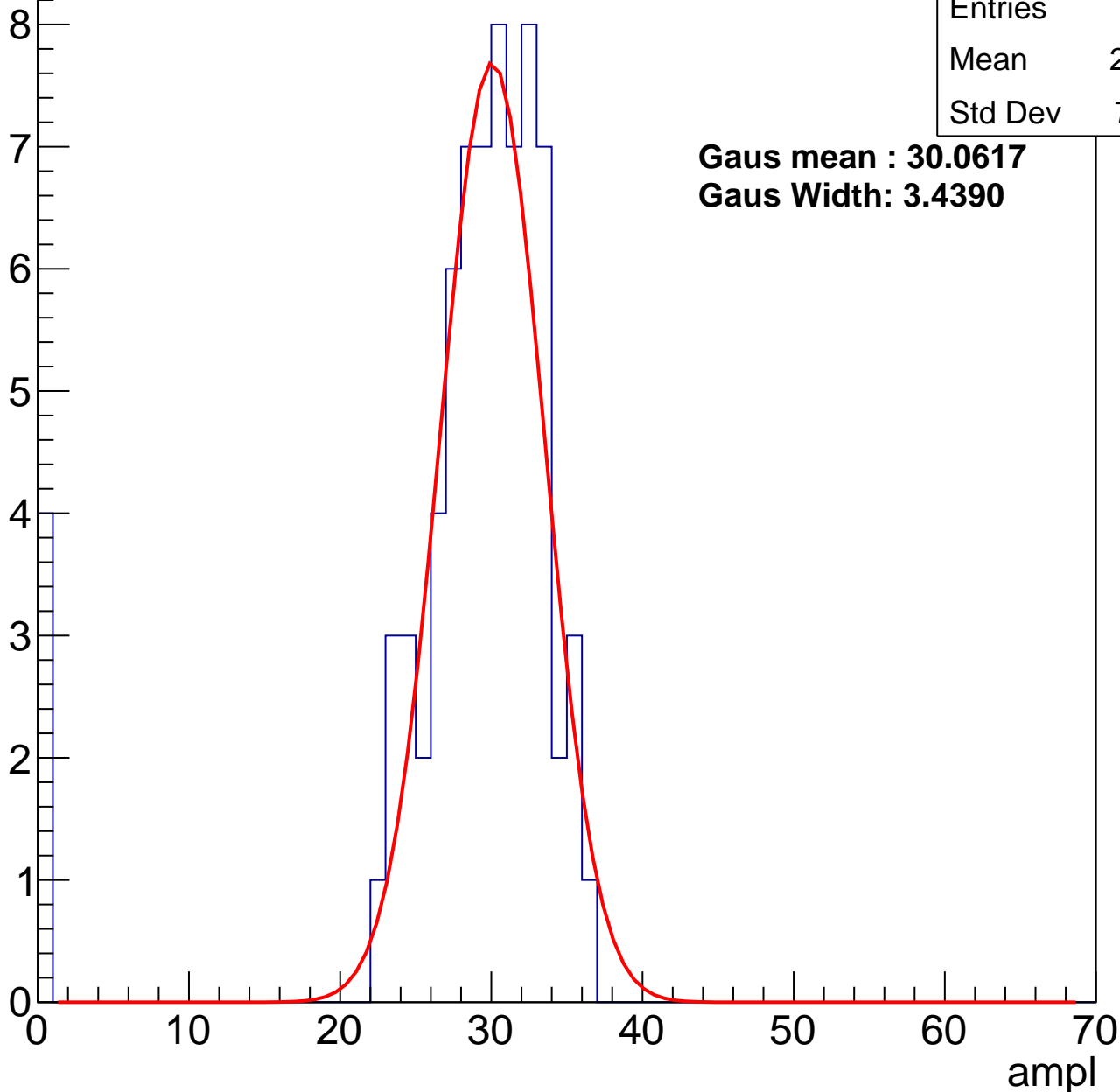
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	27.82
Std Dev	7.421

Gaus mean : 30.0617

Gaus Width: 3.4390



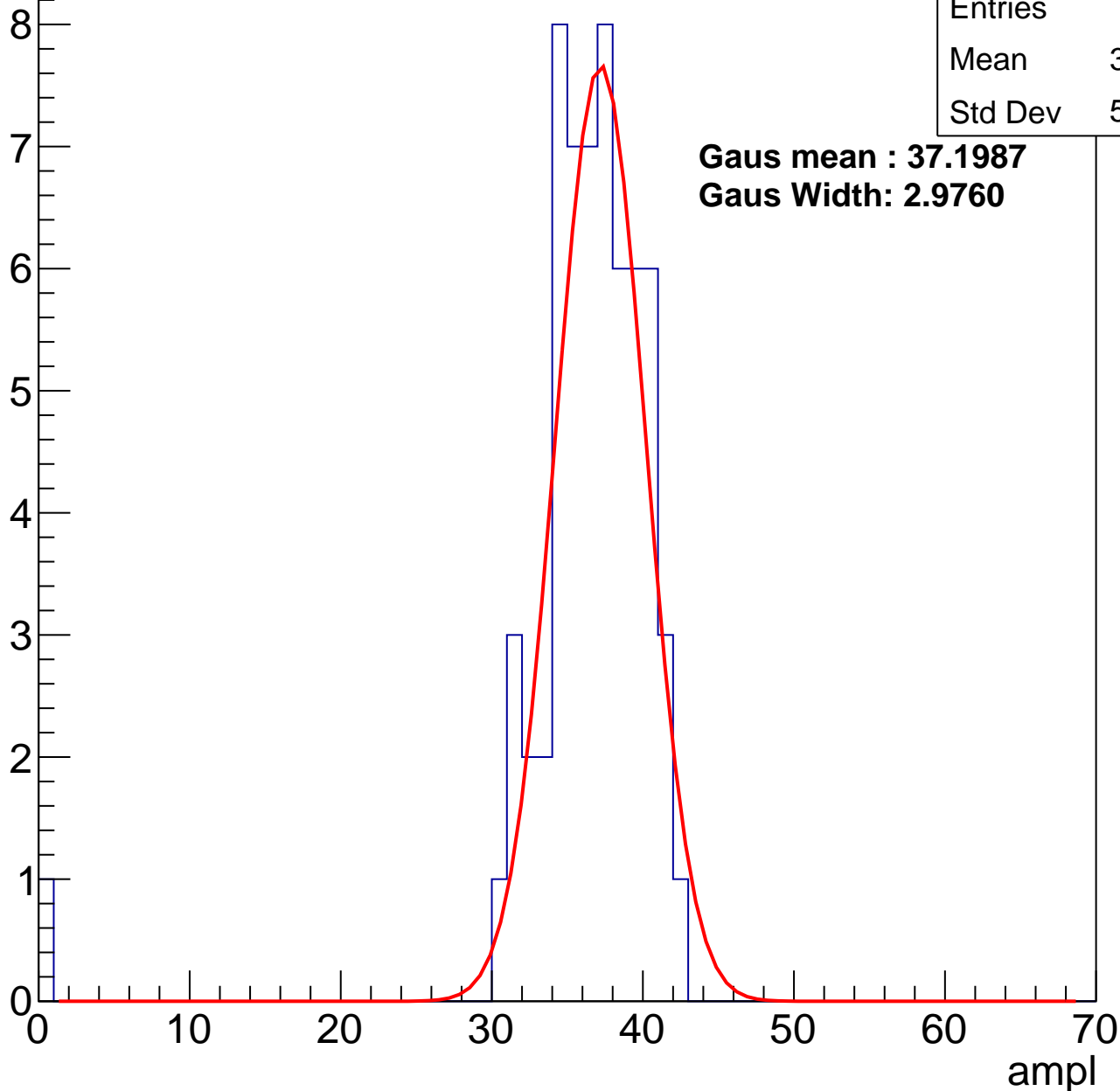
B1L103S, U6-ch29, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

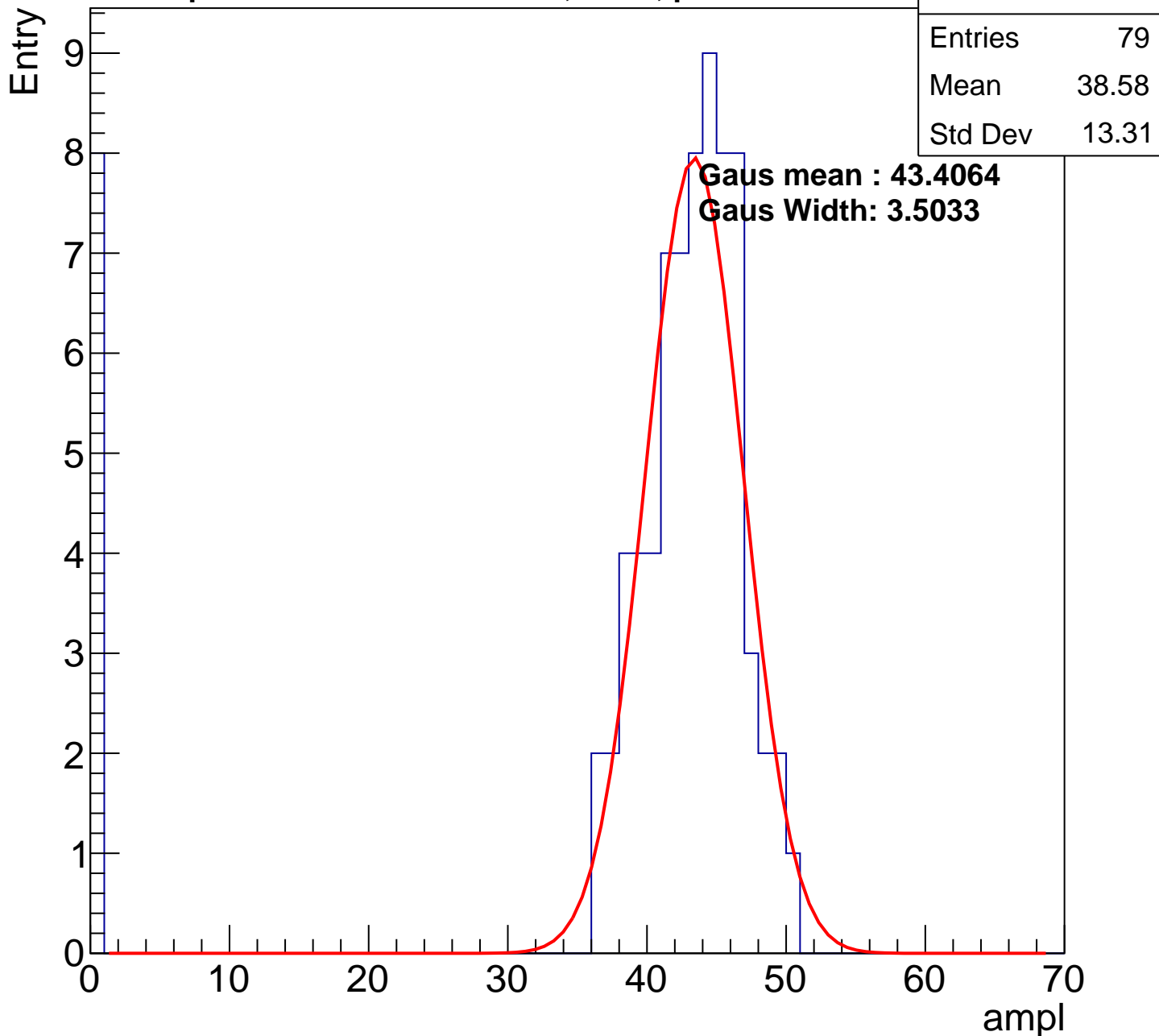
Entries	61
Mean	35.82
Std Dev	5.413

Gaus mean : 37.1987
Gaus Width: 2.9760



B1L103S, U6-ch29, adc2

calib_packv5_041523_1651.root, FC#0, port C2

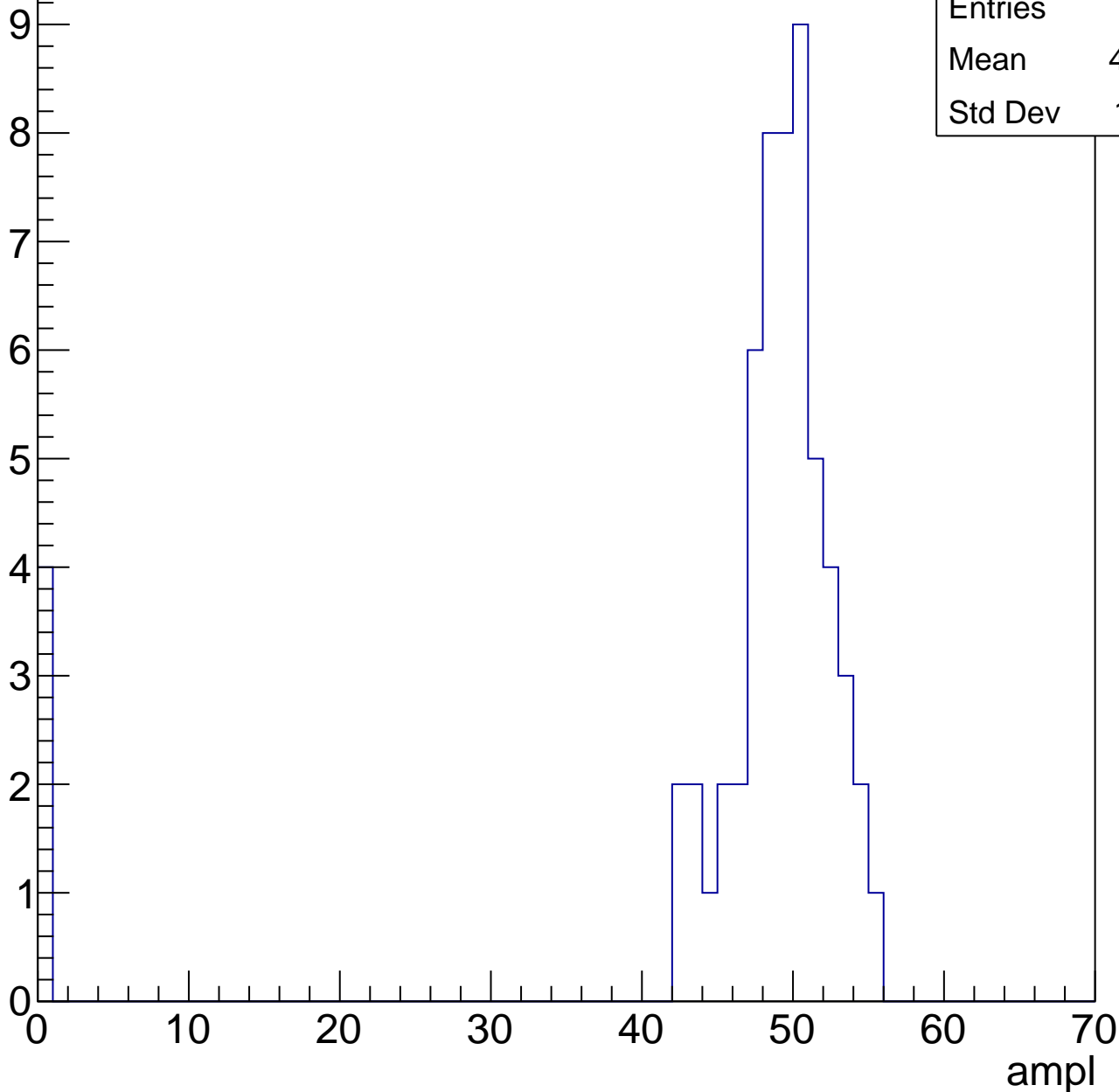


B1L103S, U6-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	45.58
Std Dev	12.61

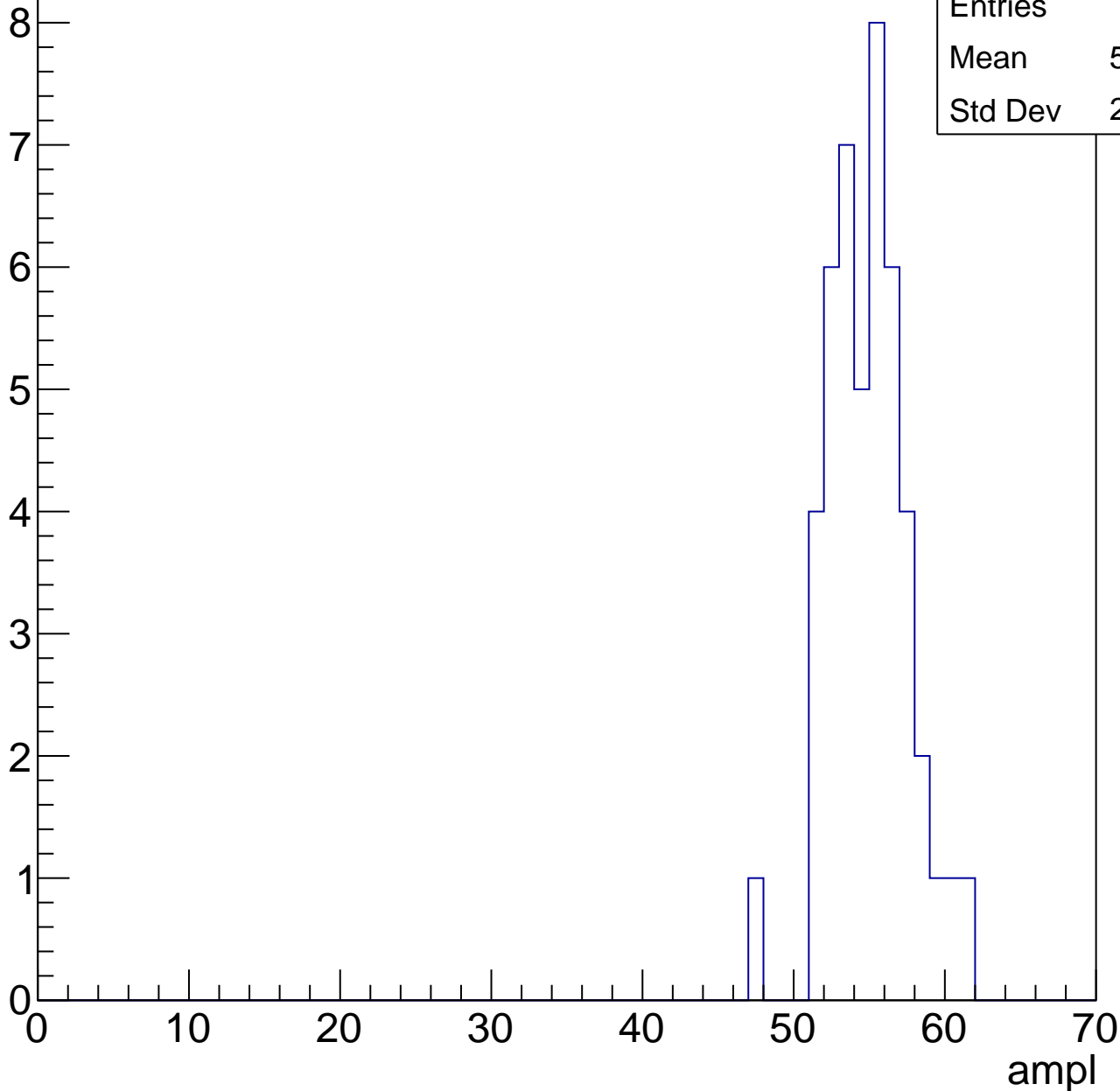


B1L103S, U6-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.43
Std Dev	2.626



B1L103S, U6-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

Entries 64

Mean 58.34

Std Dev 7.793

ampl

0

10

20

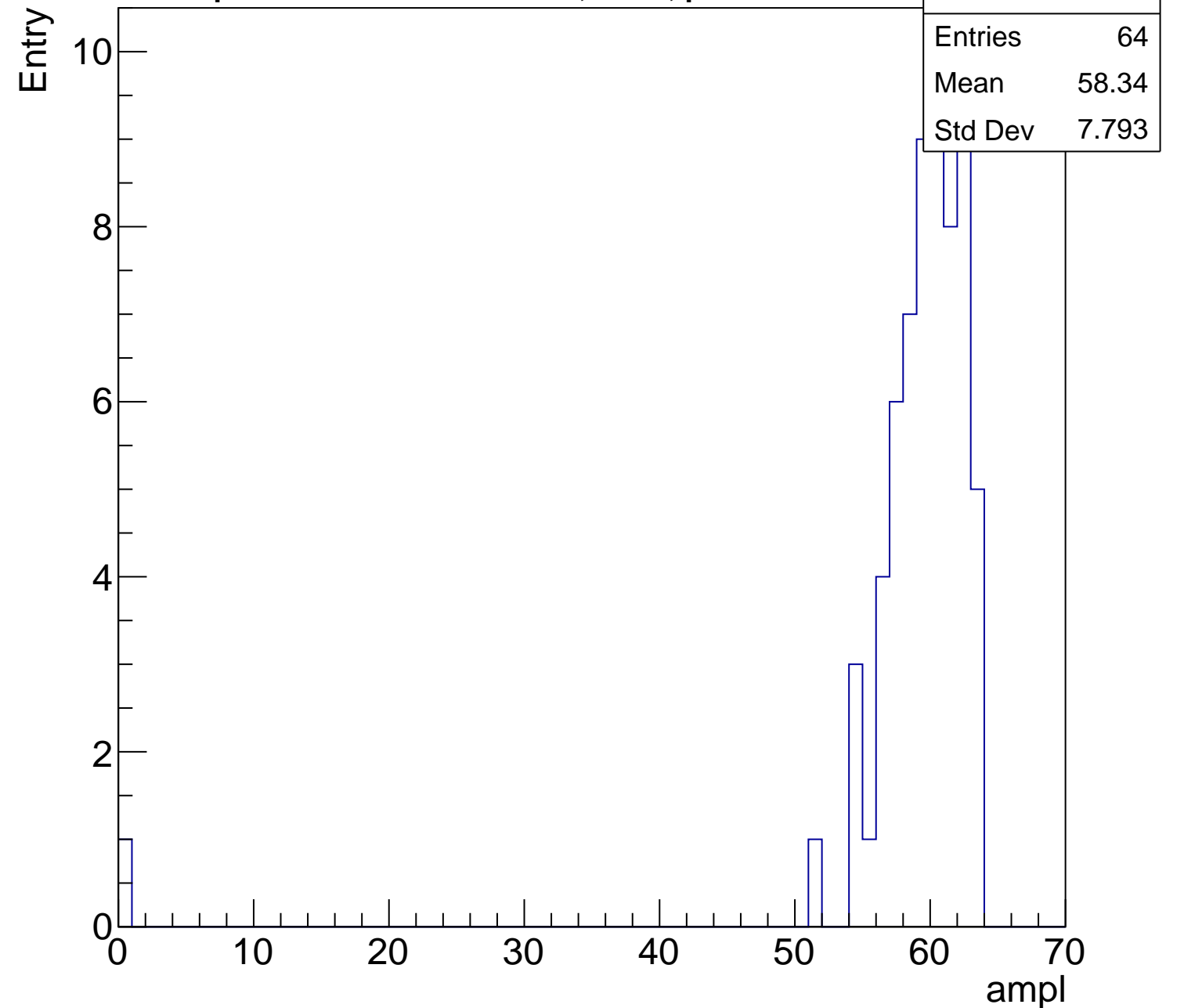
30

40

50

60

70

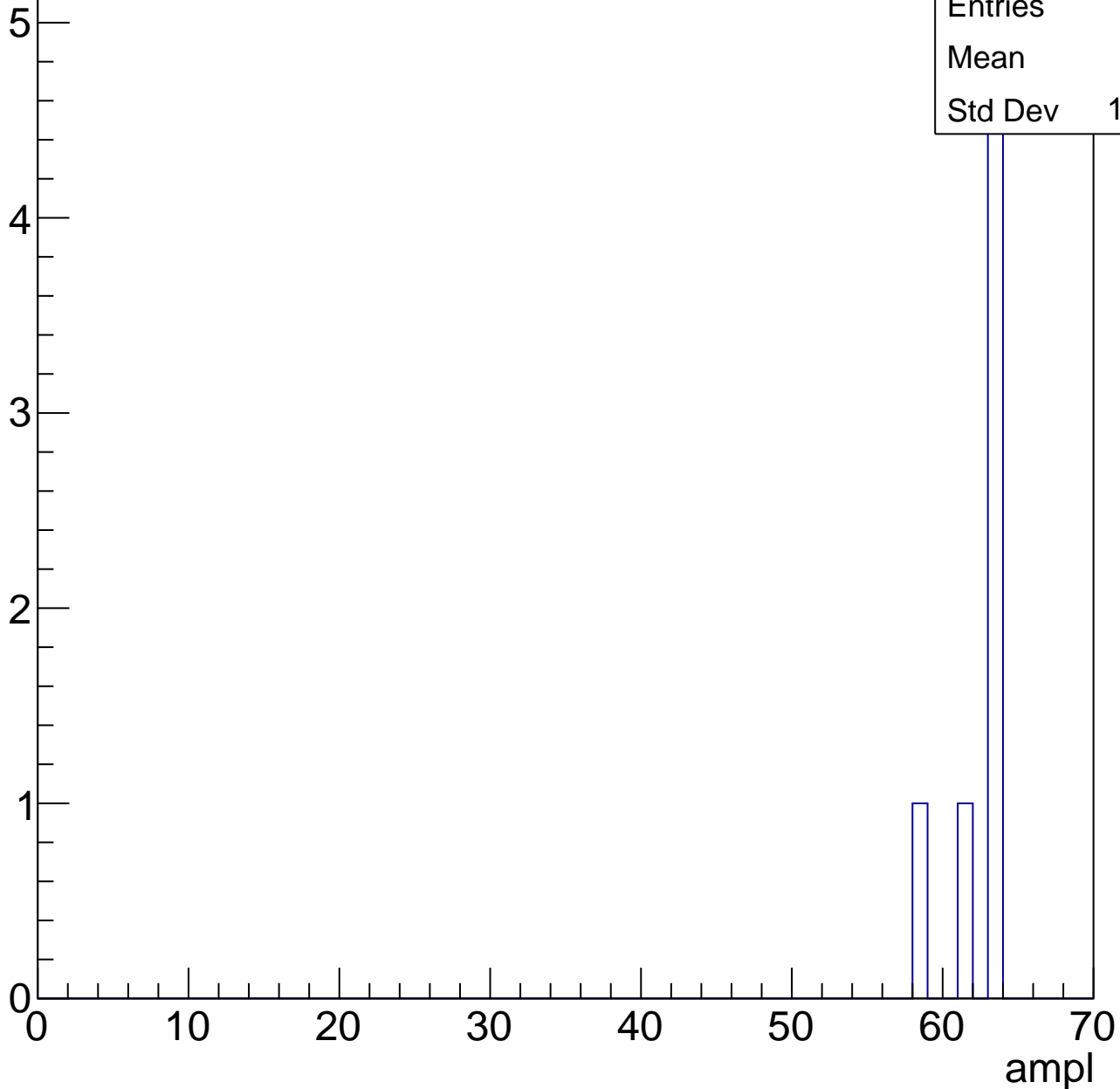


B1L103S, U6-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	7
Mean	62
Std Dev	1.773

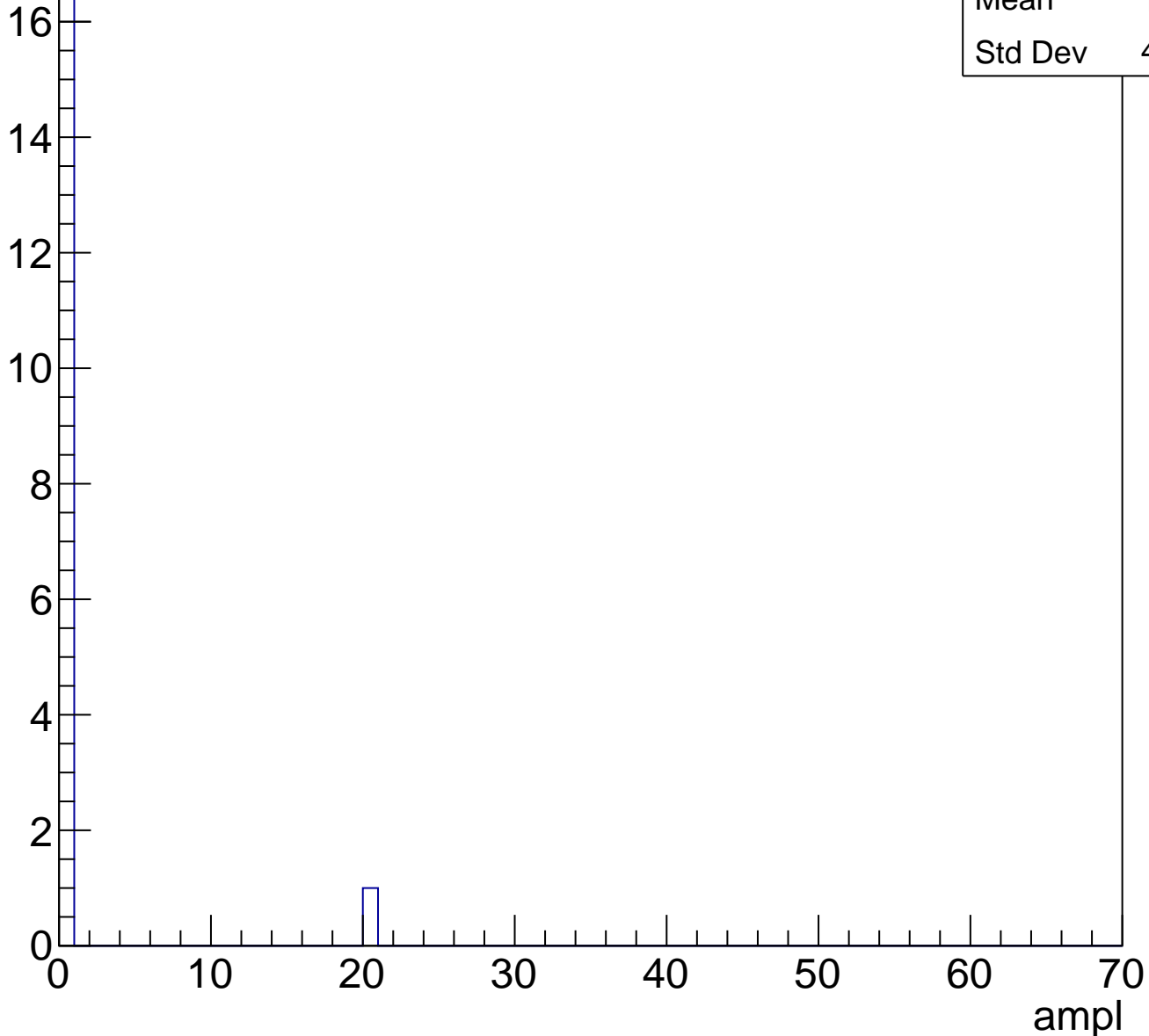


B1L103S, U6-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.111
Std Dev	4.581

Entry



B1L103S, U6-ch30, adc0

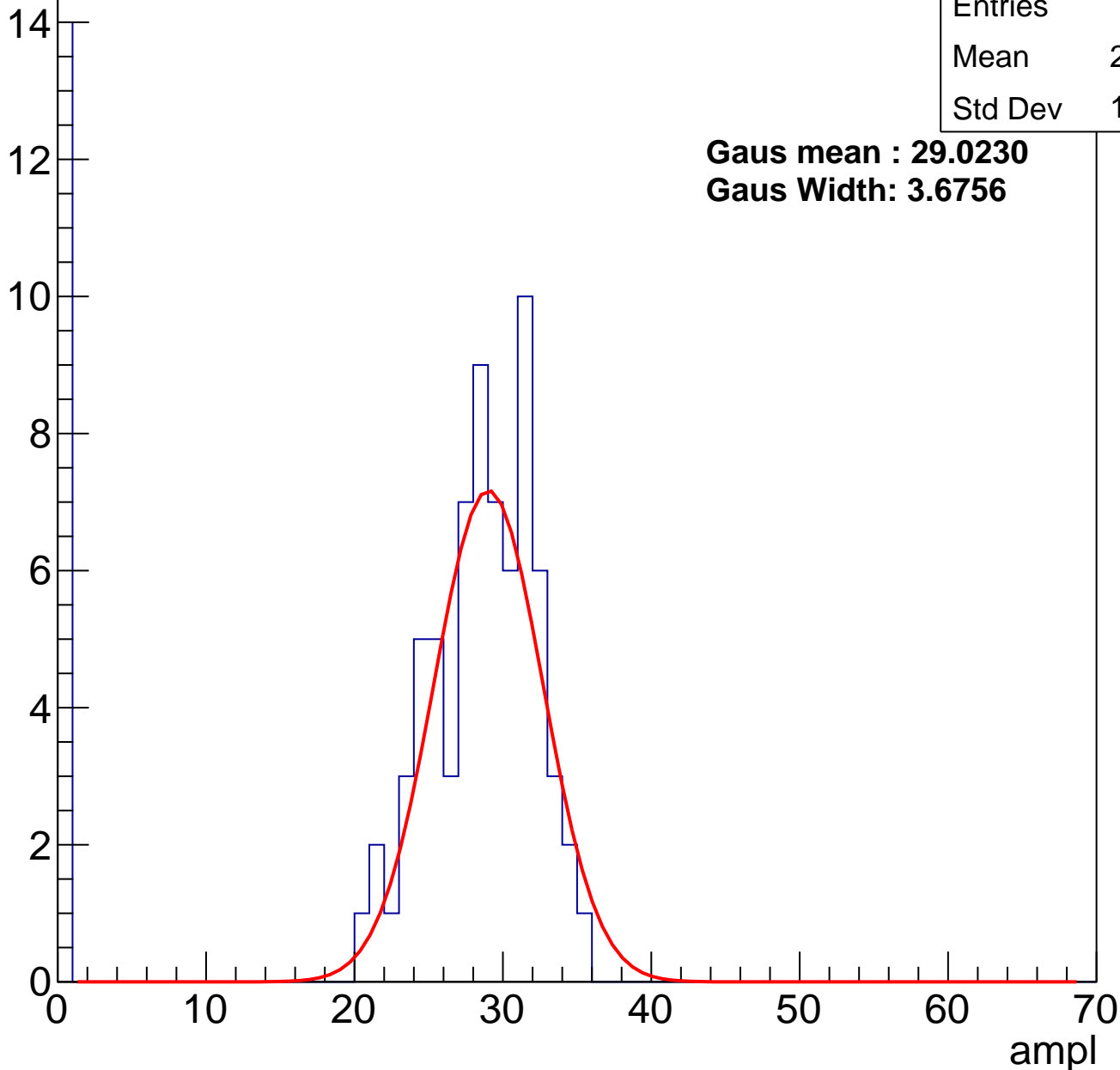
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.58
Std Dev	10.92

Gaus mean : 29.0230

Gaus Width: 3.6756

Entry



B1L103S, U6-ch30, adc1

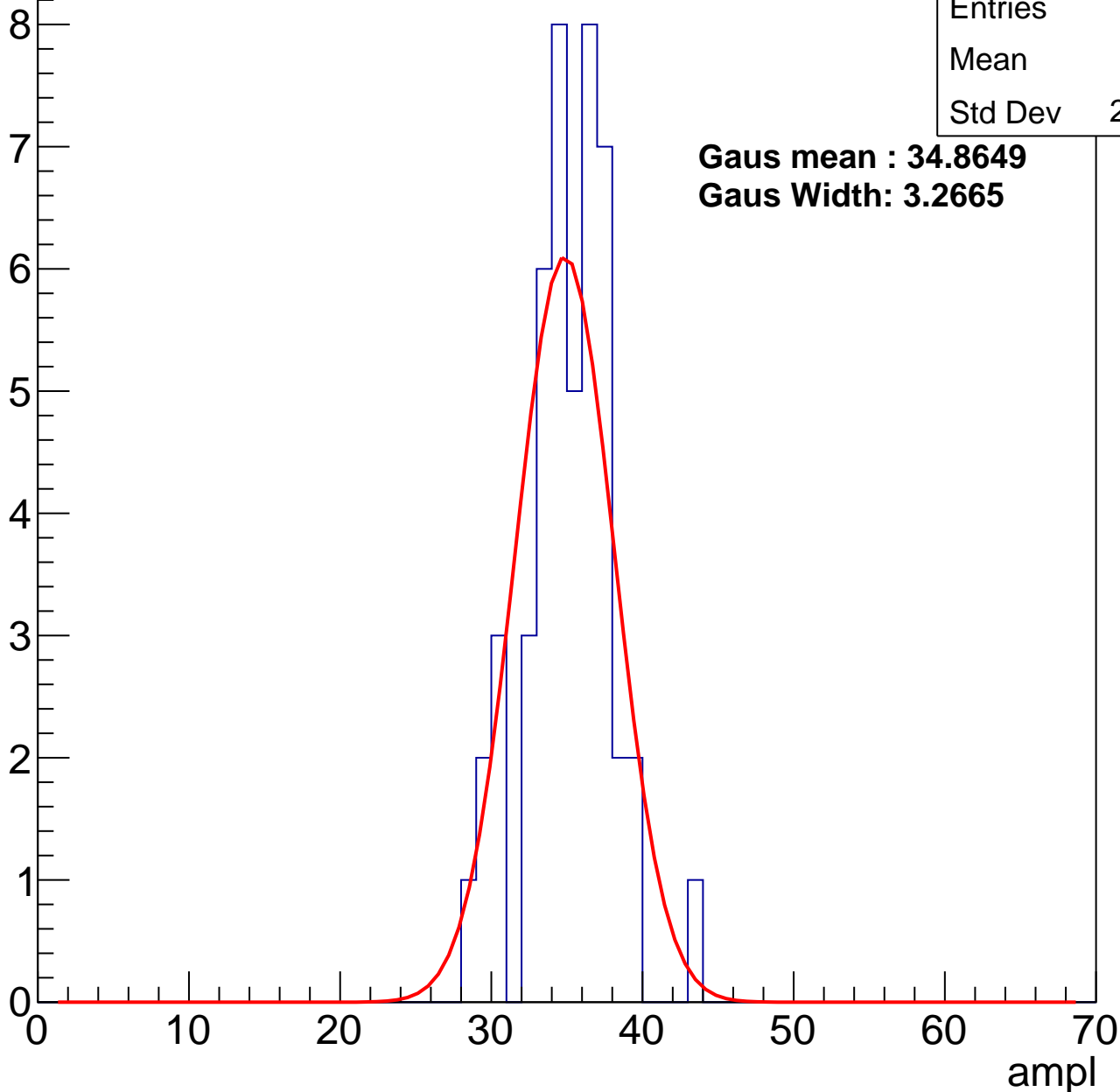
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	34.6
Std Dev	2.878

Gaus mean : 34.8649

Gaus Width: 3.2665



B1L103S, U6-ch30, adc2

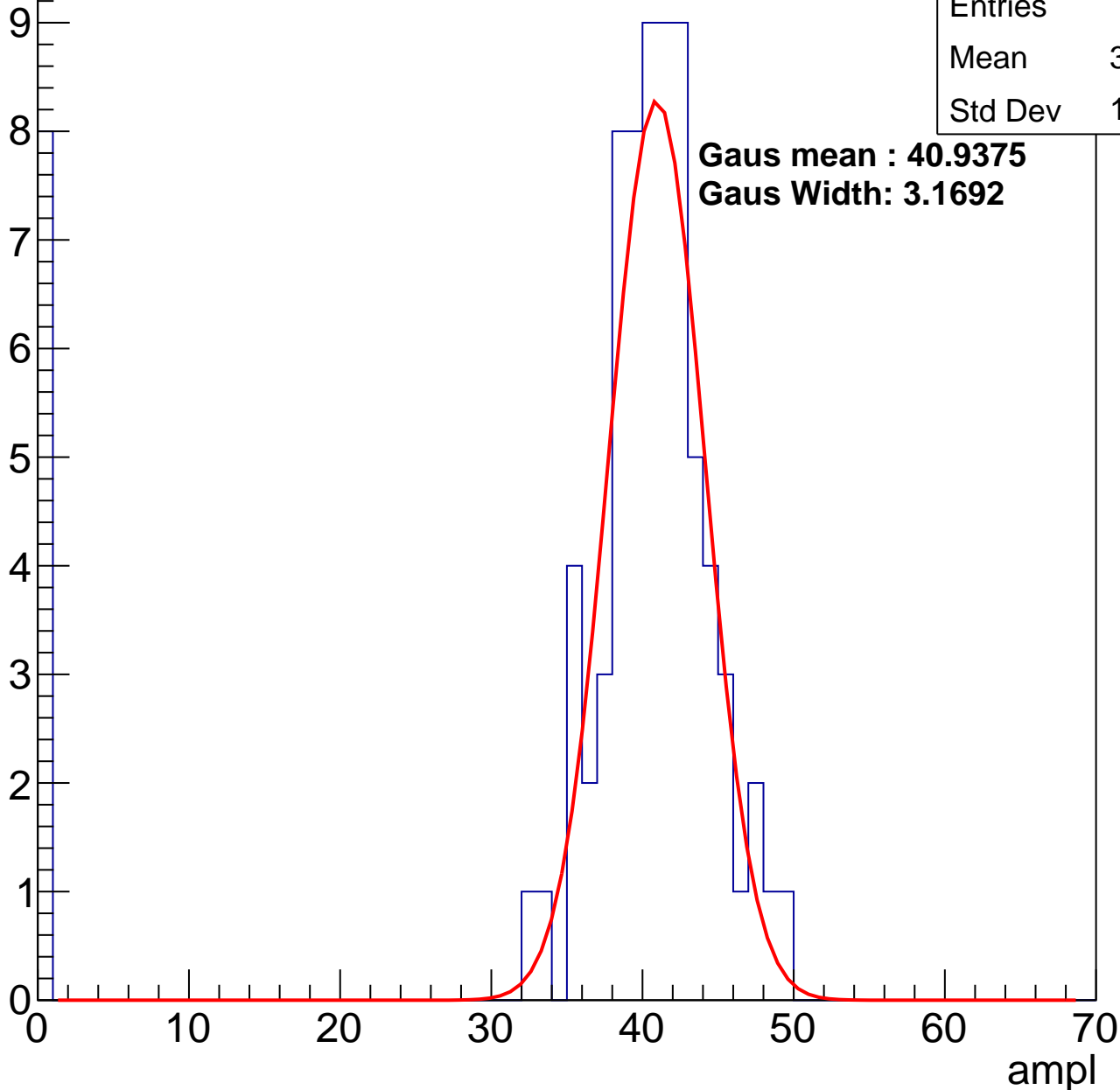
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	36.38
Std Dev	12.62

Gaus mean : 40.9375

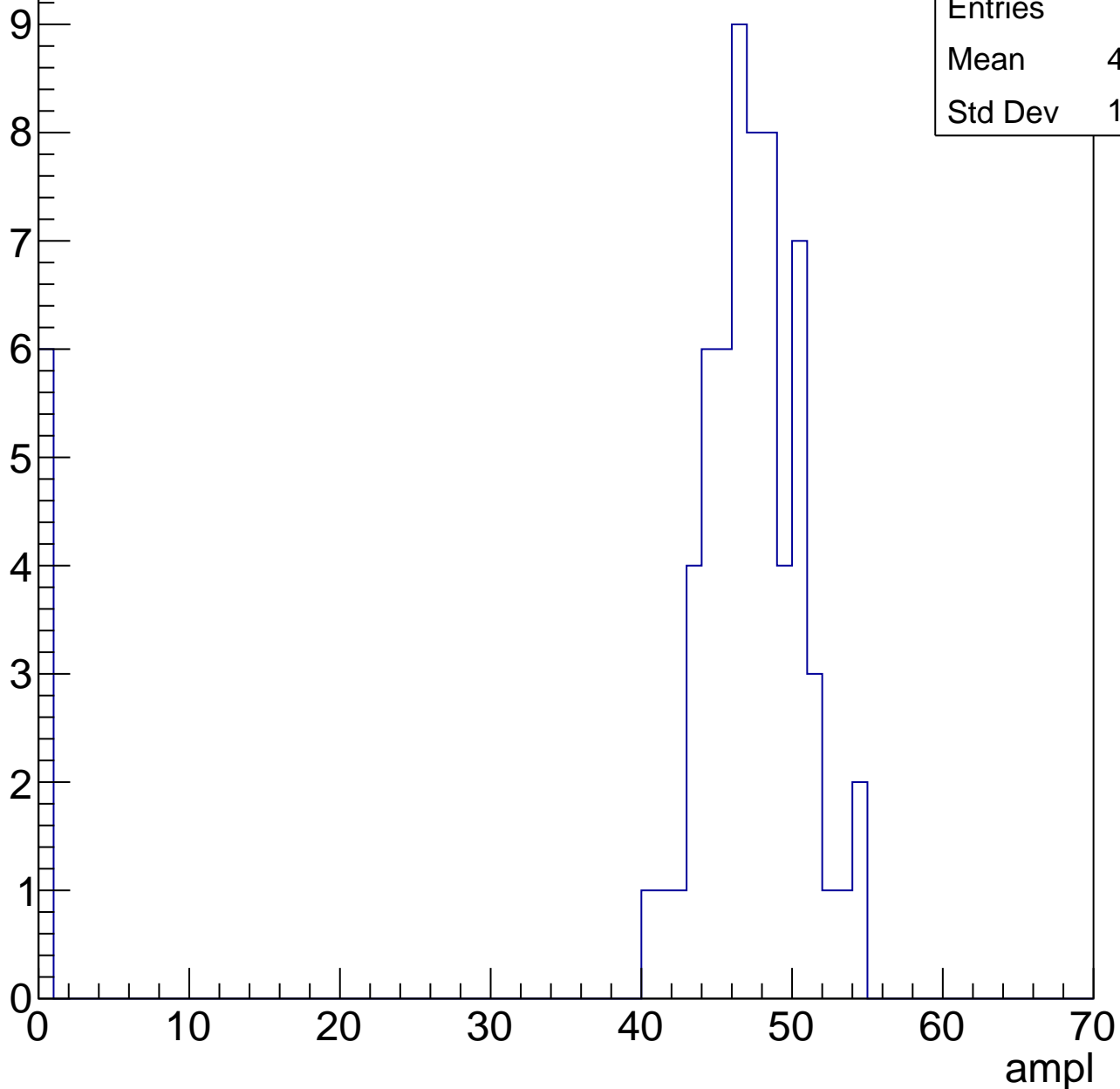
Gaus Width: 3.1692



B1L103S, U6-ch30, adc3

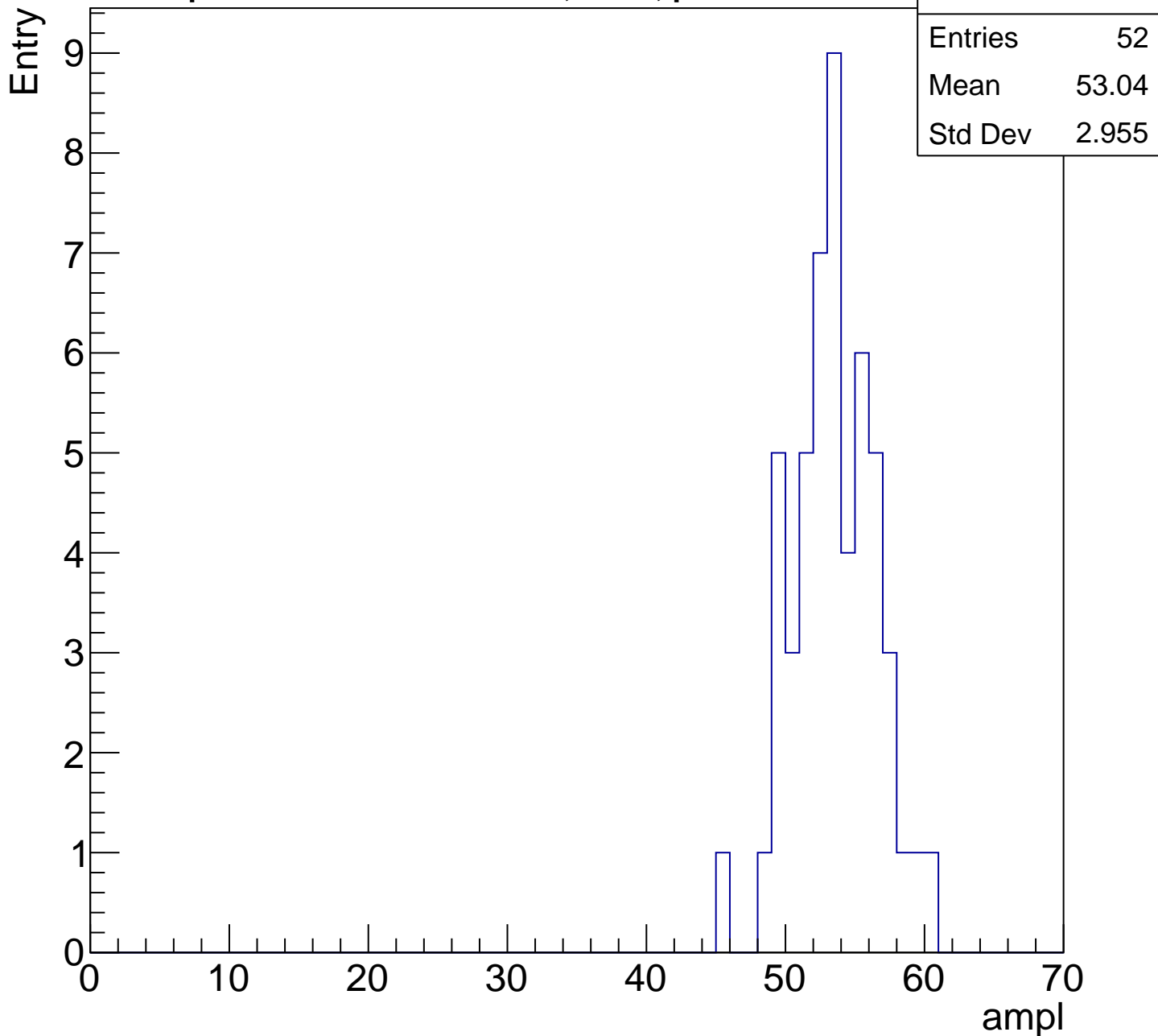
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

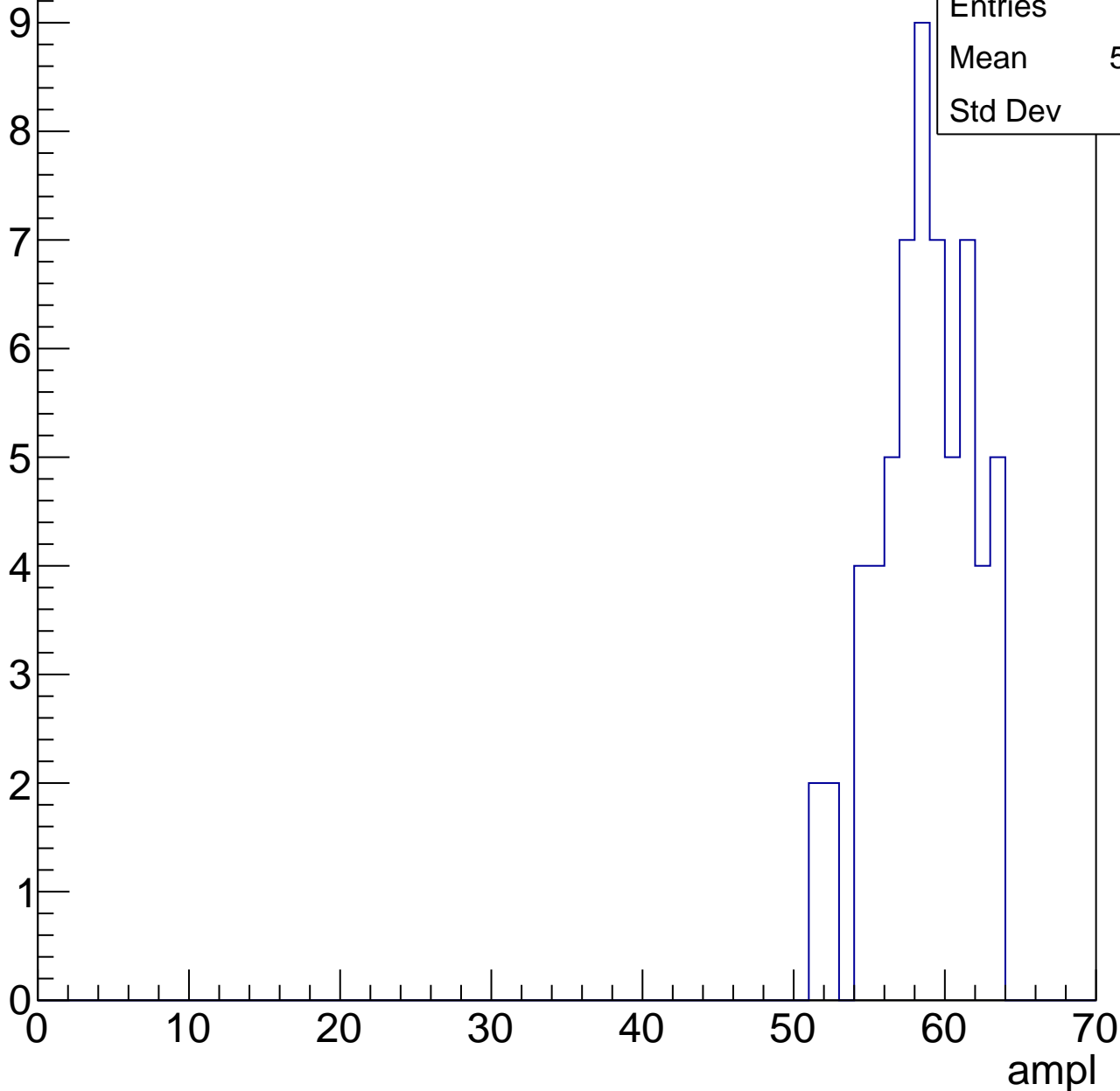


B1L103S, U6-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.13
Std Dev	3.07

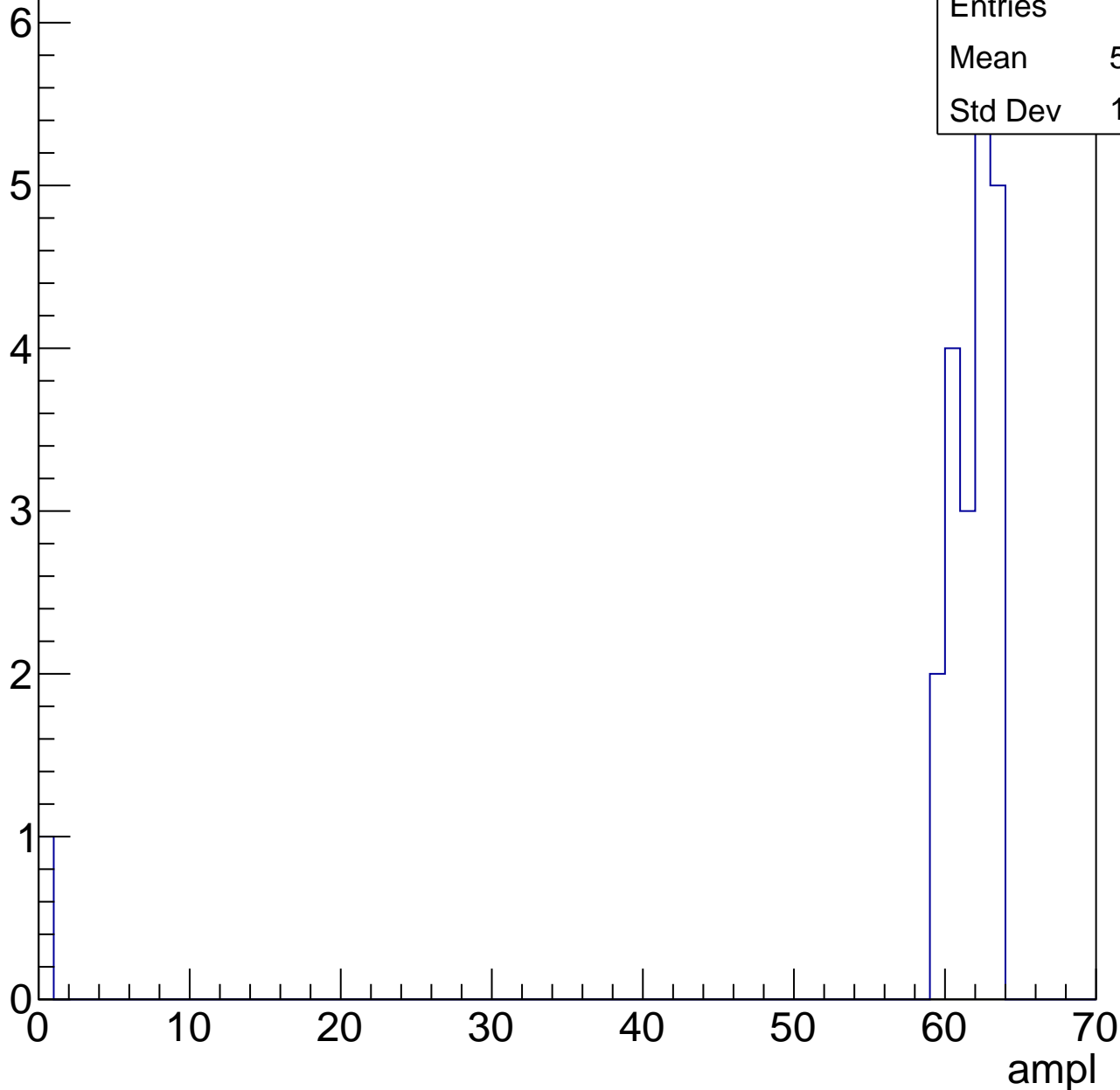


B1L103S, U6-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.48
Std Dev	13.14



B1L103S, U6-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch31, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	25.04
Std Dev	12.74

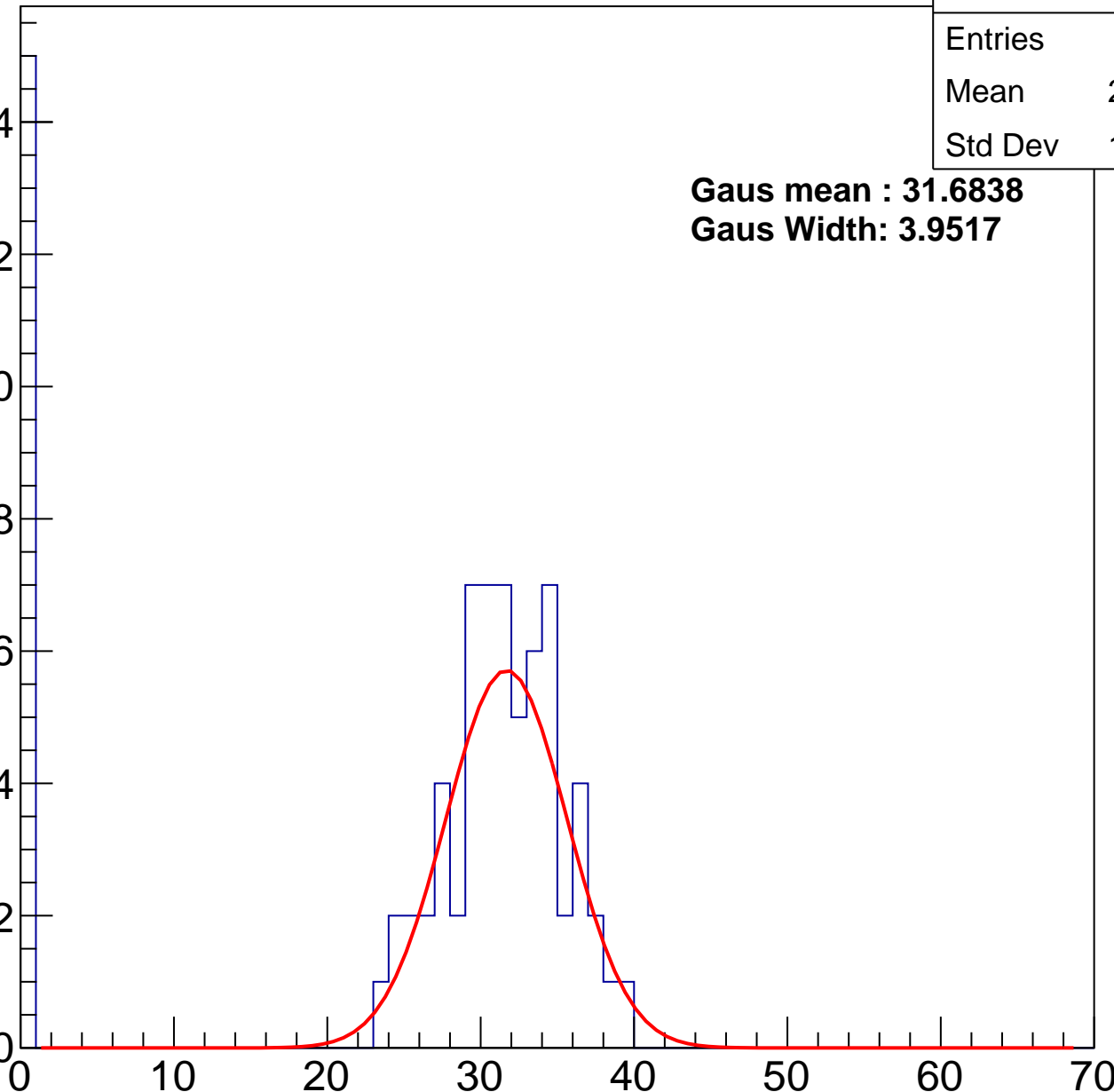
Gaus mean : 31.6838

Gaus Width: 3.9517

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch31, adc1

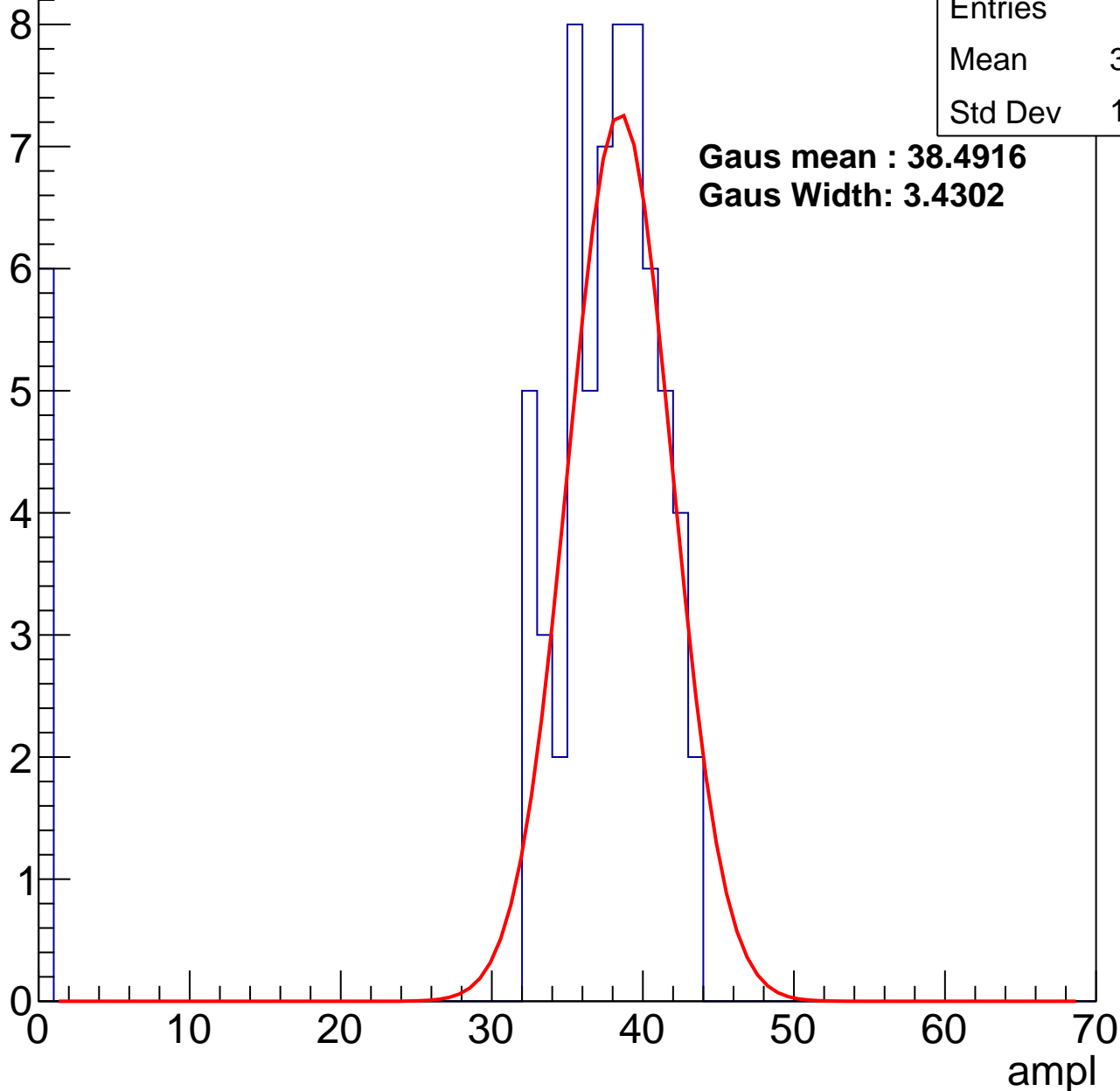
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	34.22
Std Dev	10.94

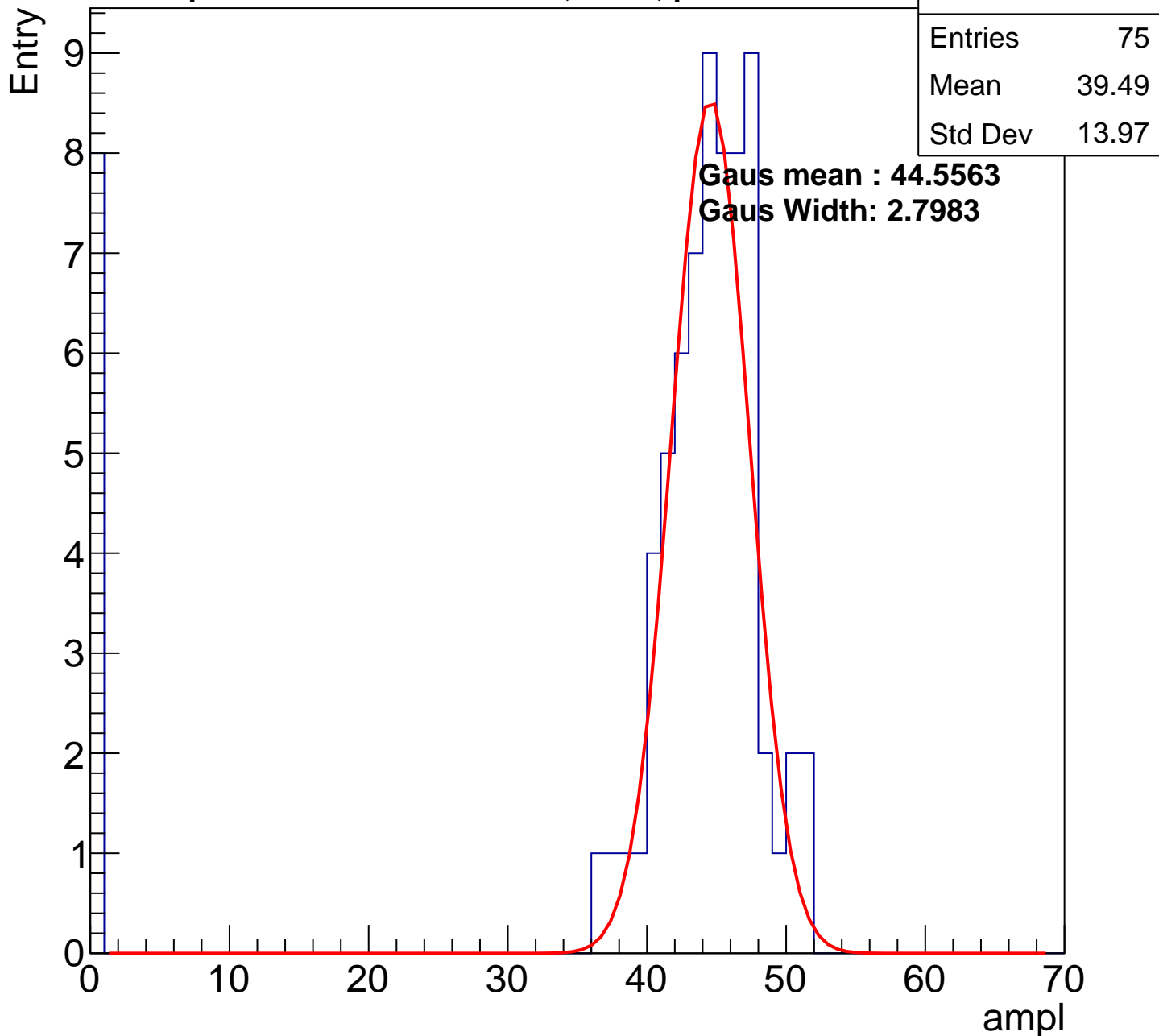
Gaus mean : 38.4916

Gaus Width: 3.4302



B1L103S, U6-ch31, adc2

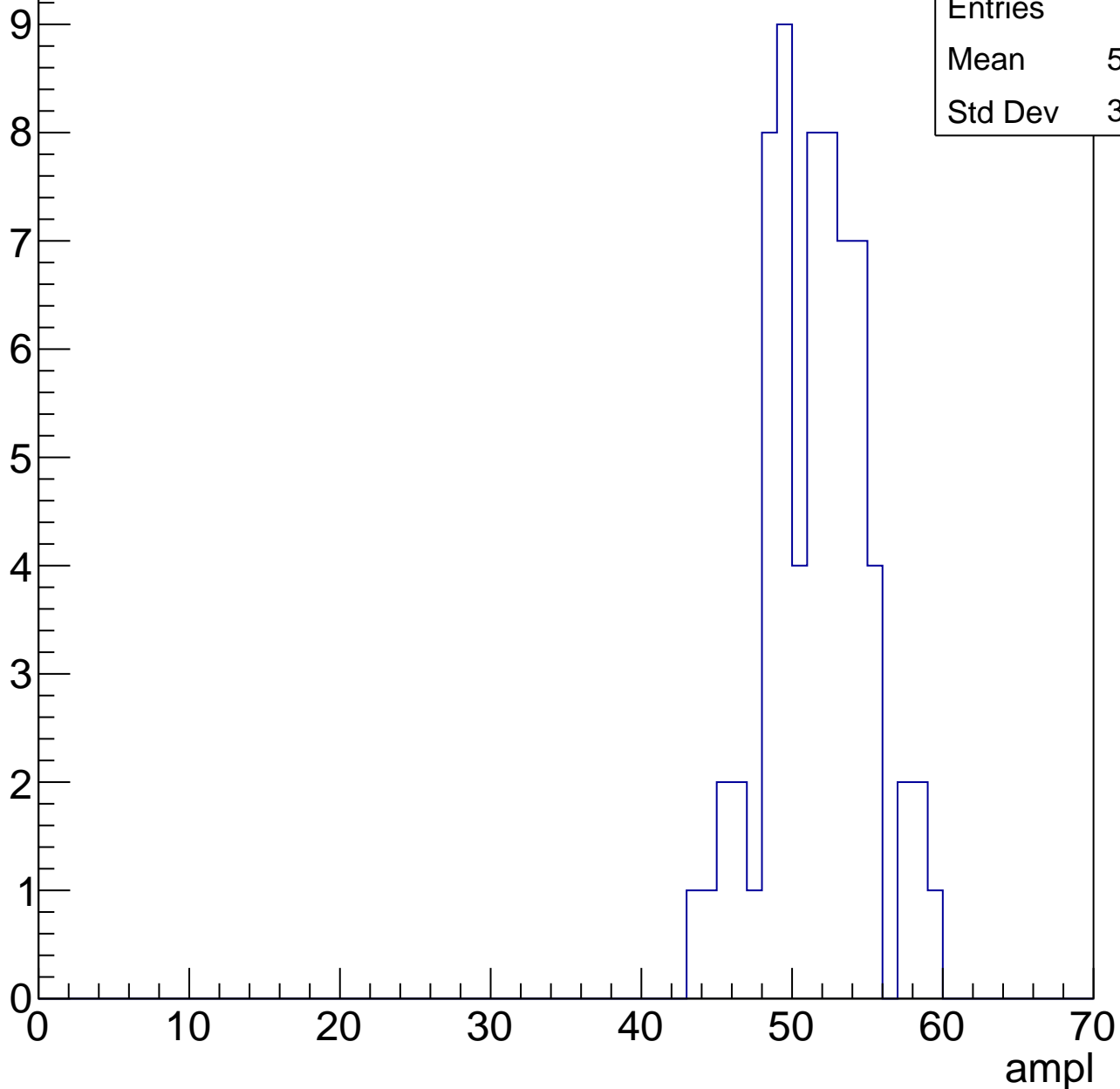
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

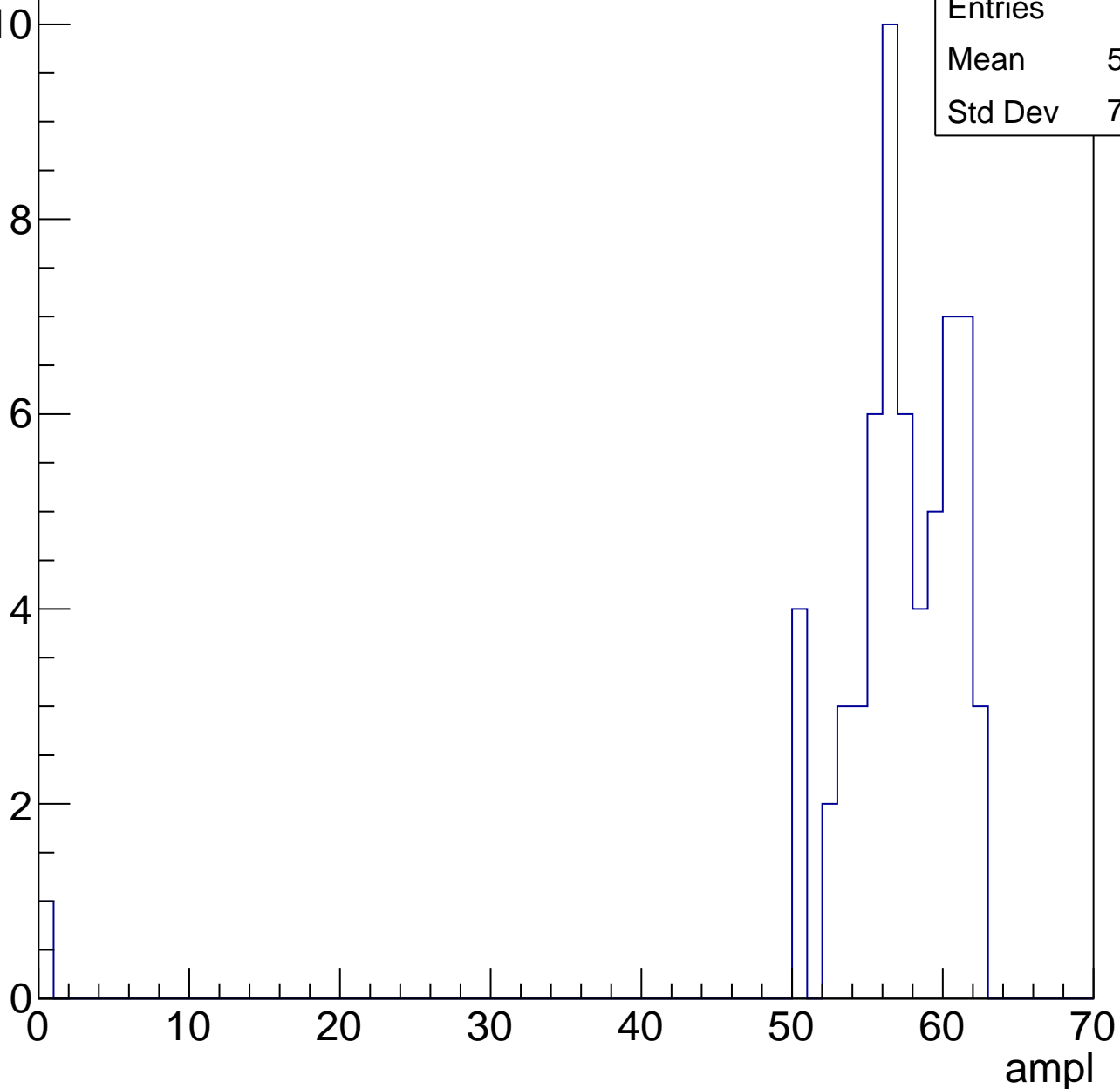


B1L103S, U6-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	56.02
Std Dev	7.912

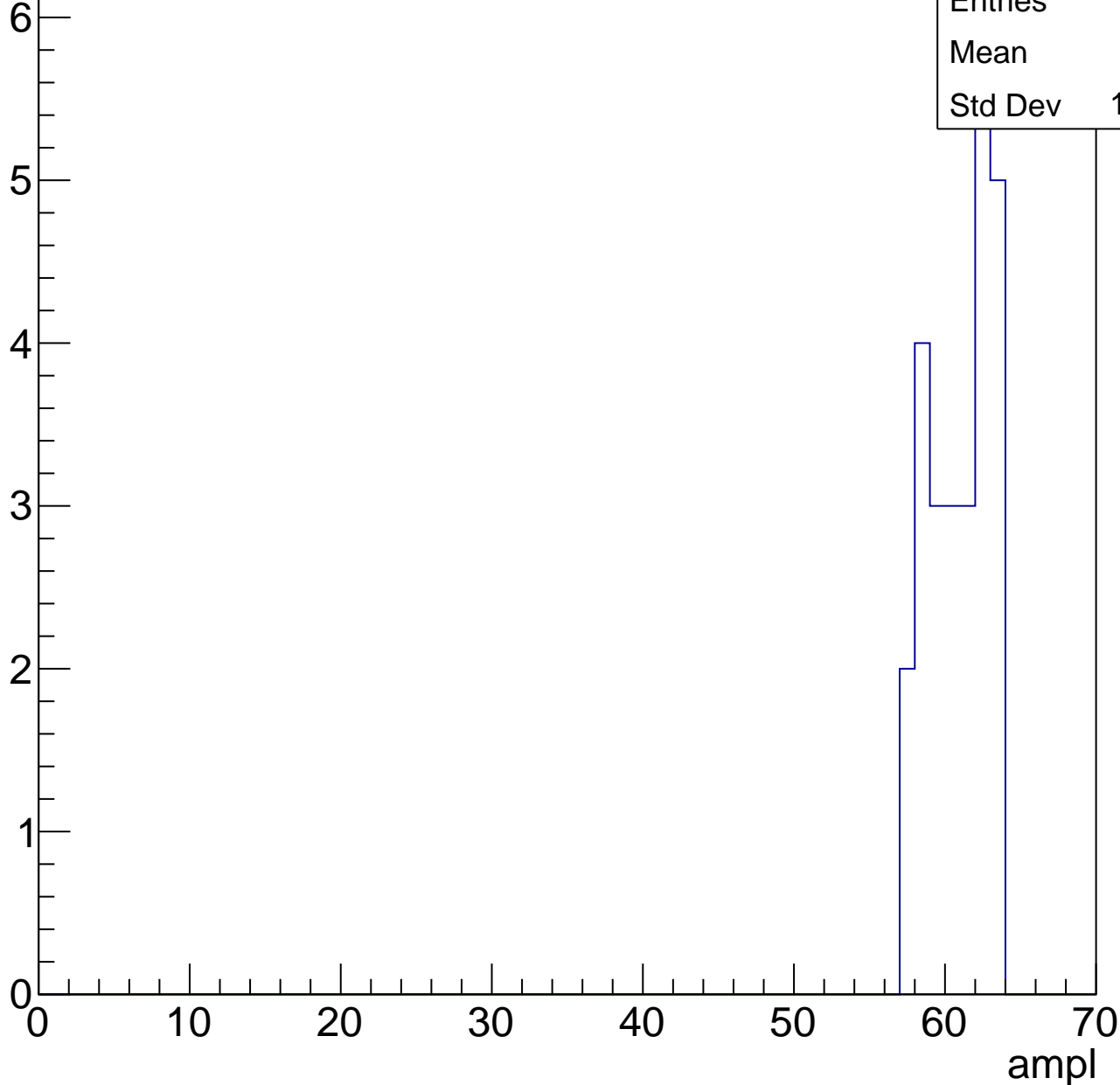


B1L103S, U6-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	60.5
Std Dev	1.986

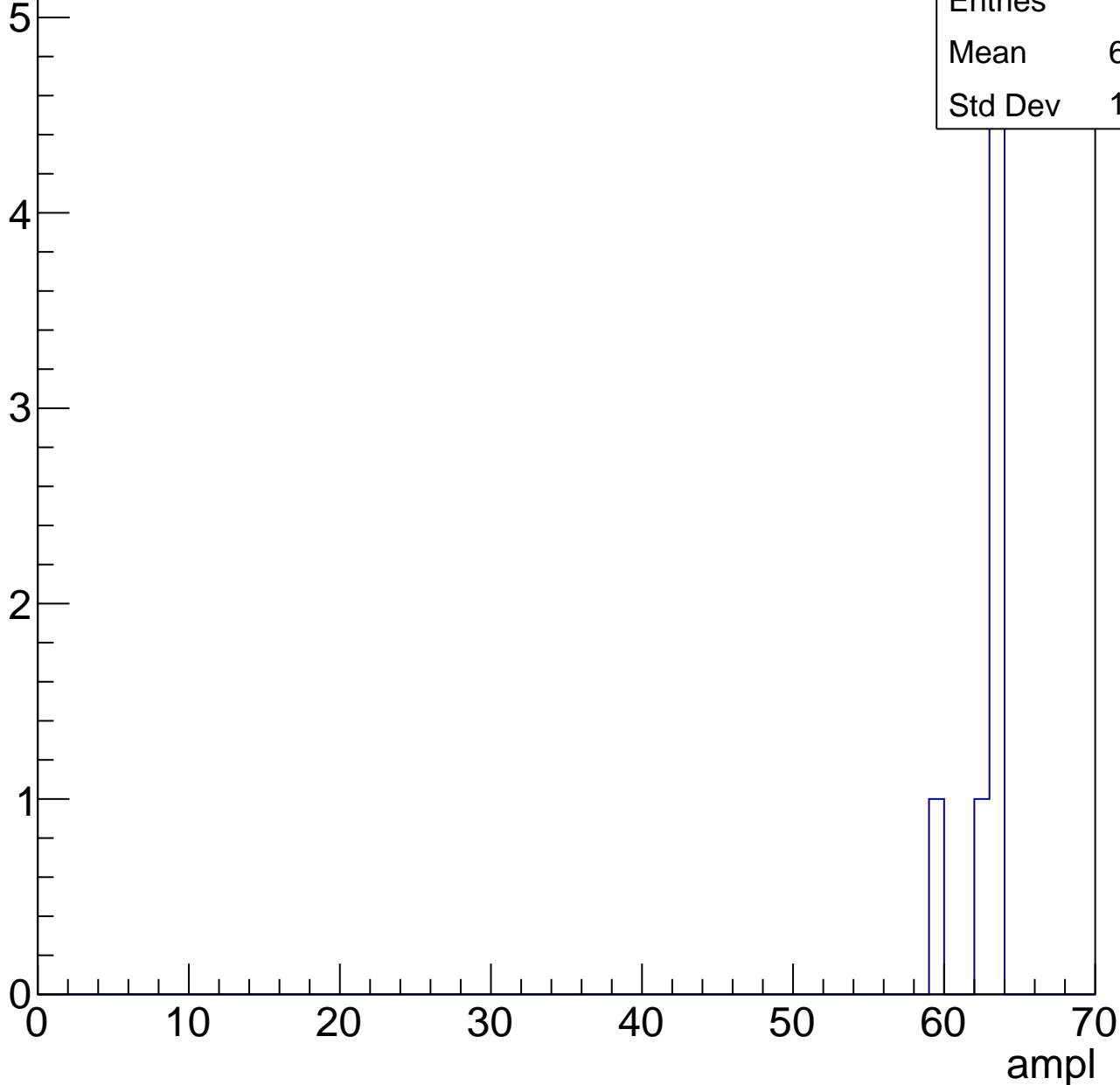


B1L103S, U6-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	7
Mean	62.29
Std Dev	1.385



B1L103S, U6-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch32, adc0

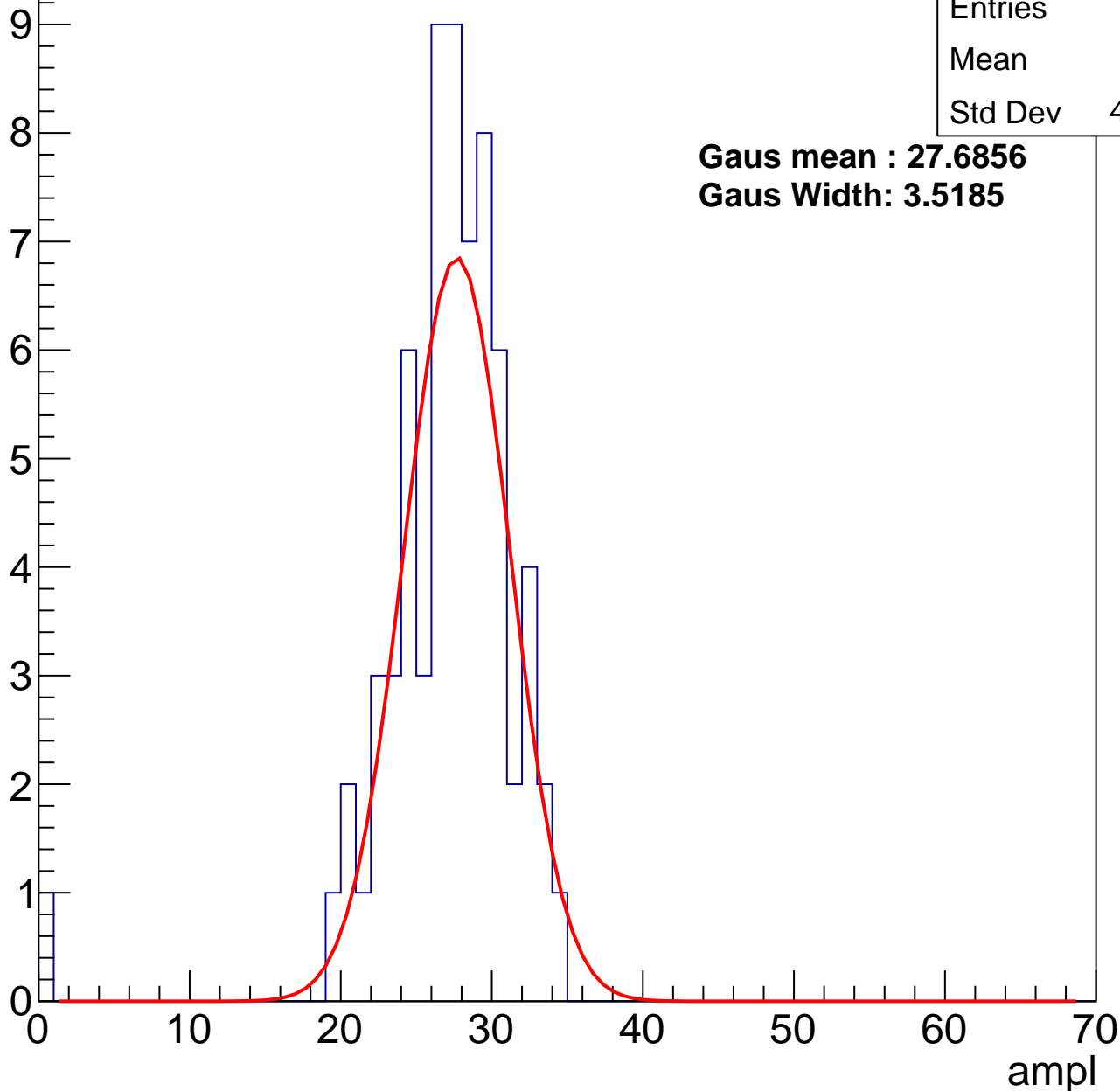
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	26.6
Std Dev	4.634

Gaus mean : 27.6856

Gaus Width: 3.5185



B1L103S, U6-ch32, adc1

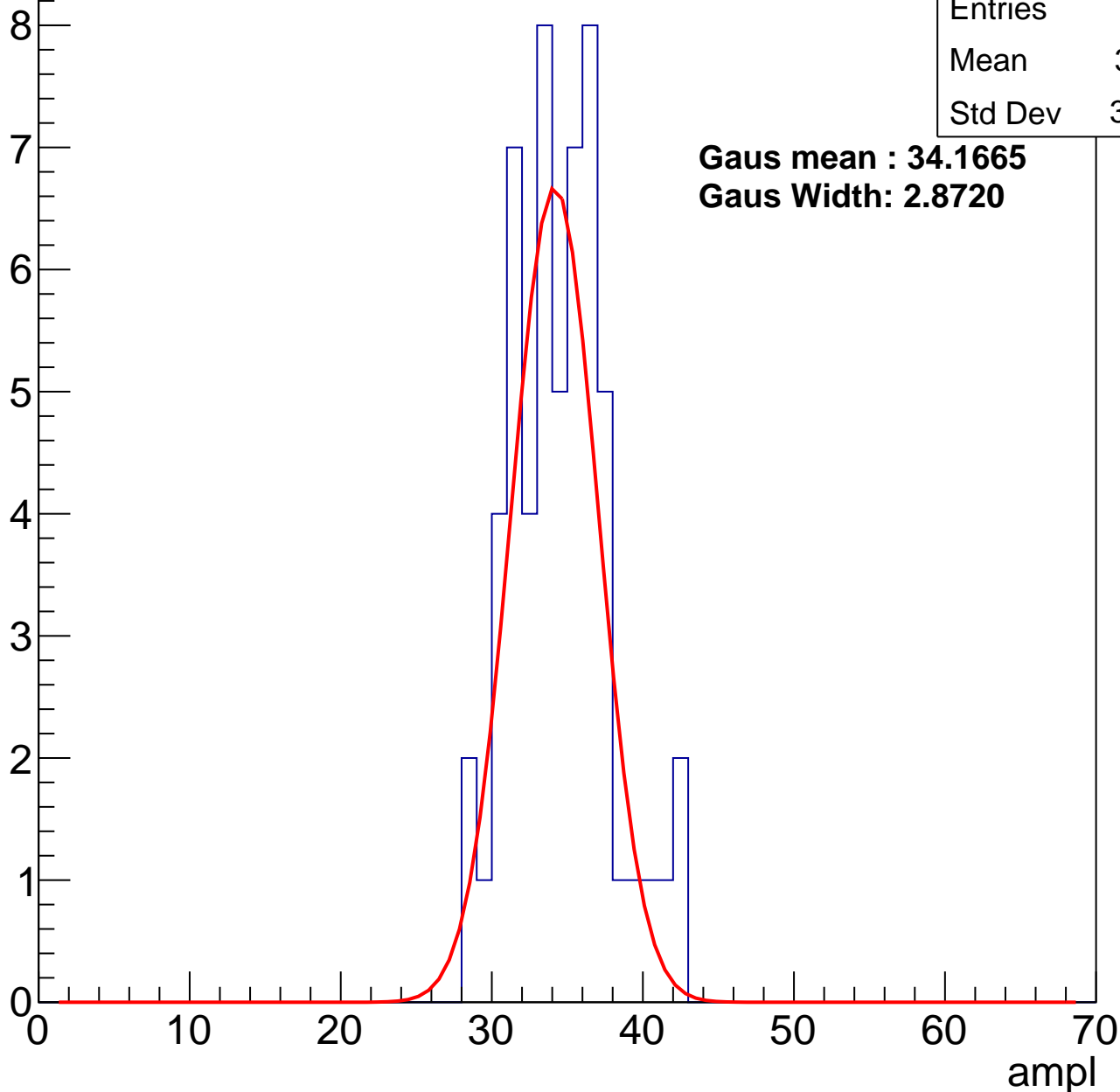
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	34.11
Std Dev	3.205

Gaus mean : 34.1665

Gaus Width: 2.8720



B1L103S, U6-ch32, adc2

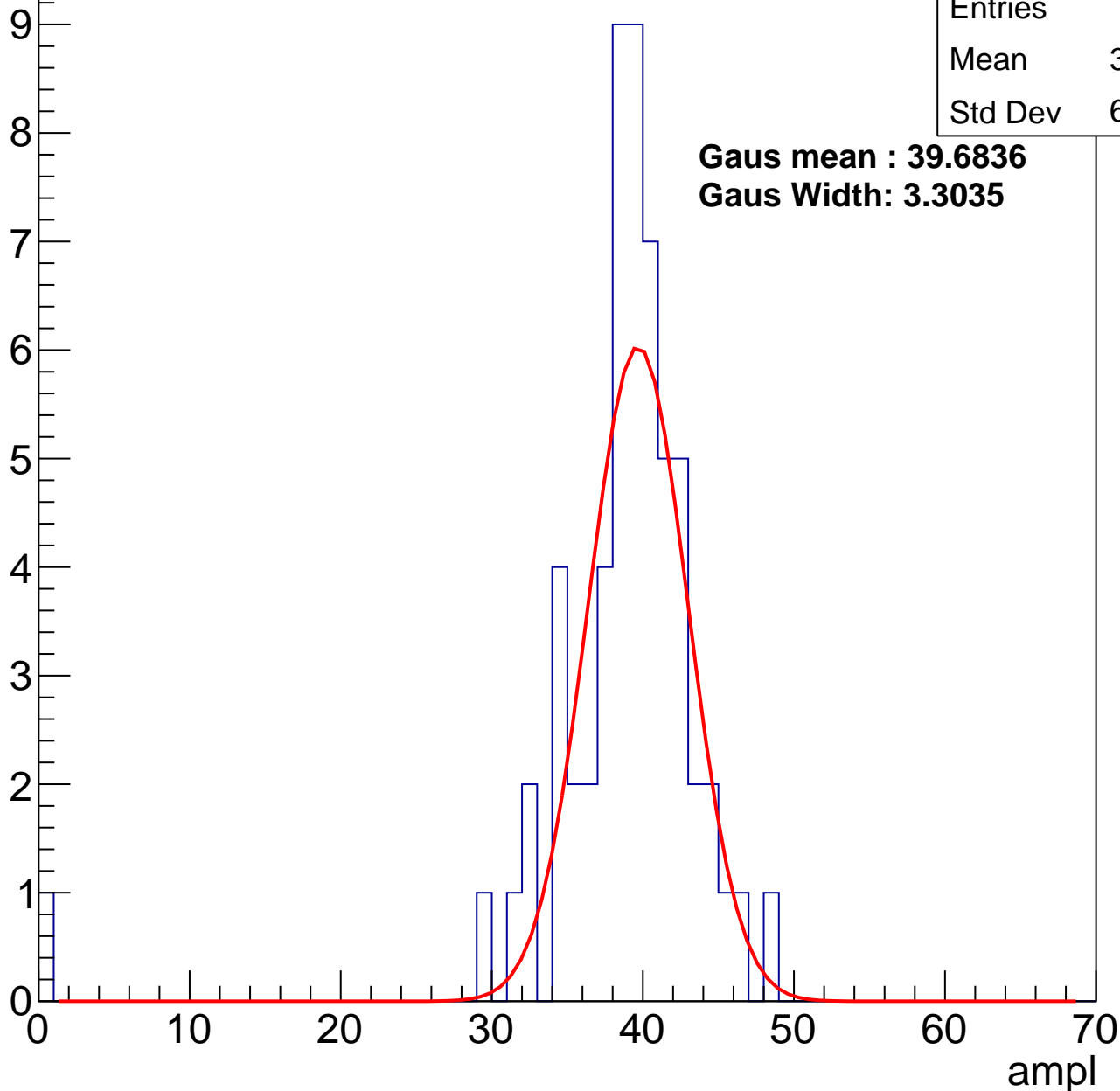
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	38.15
Std Dev	6.156

Gaus mean : 39.6836

Gaus Width: 3.3035



B1L103S, U6-ch32, adc3

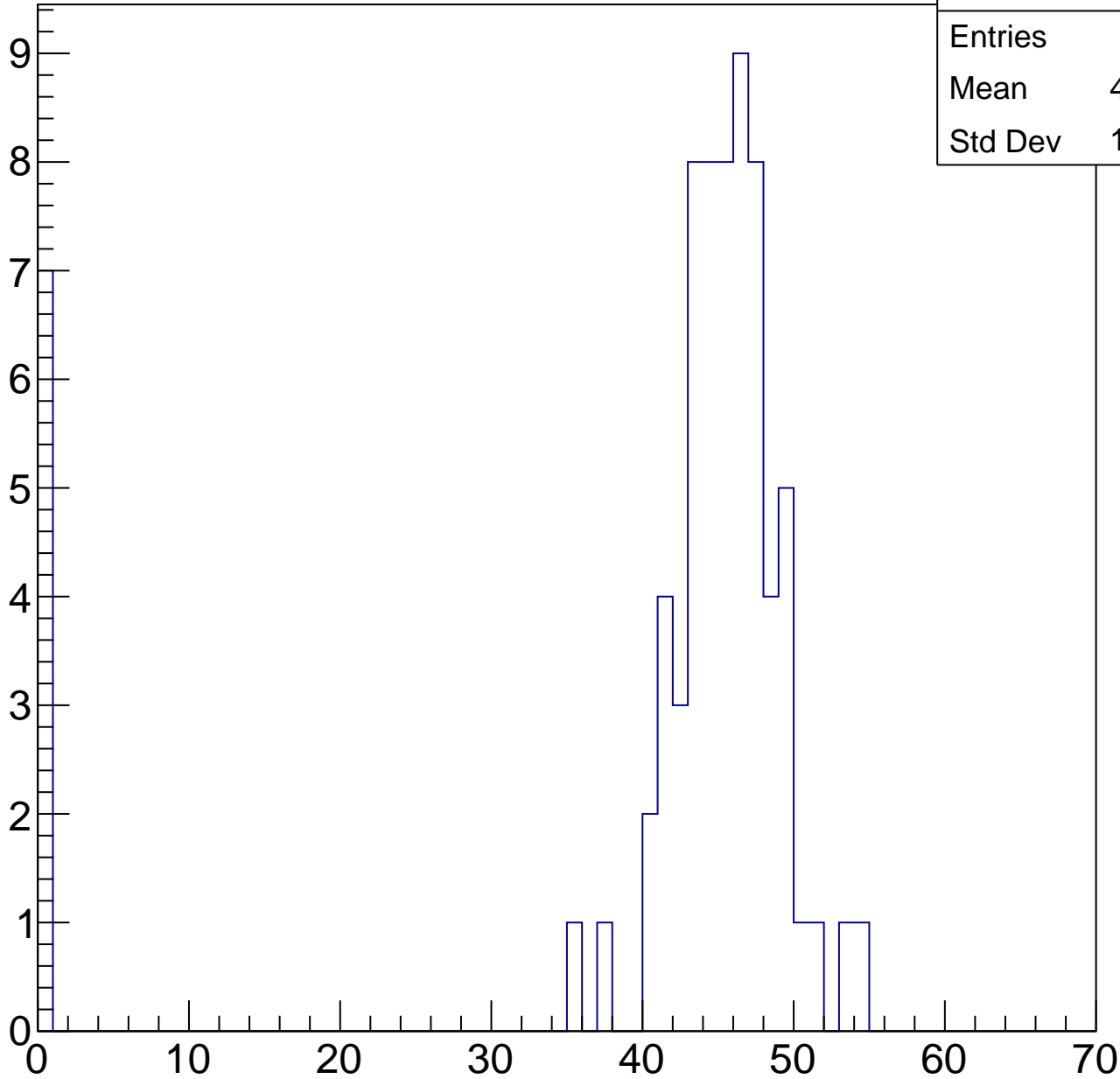
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	72
Mean	40.74
Std Dev	13.73

ampl

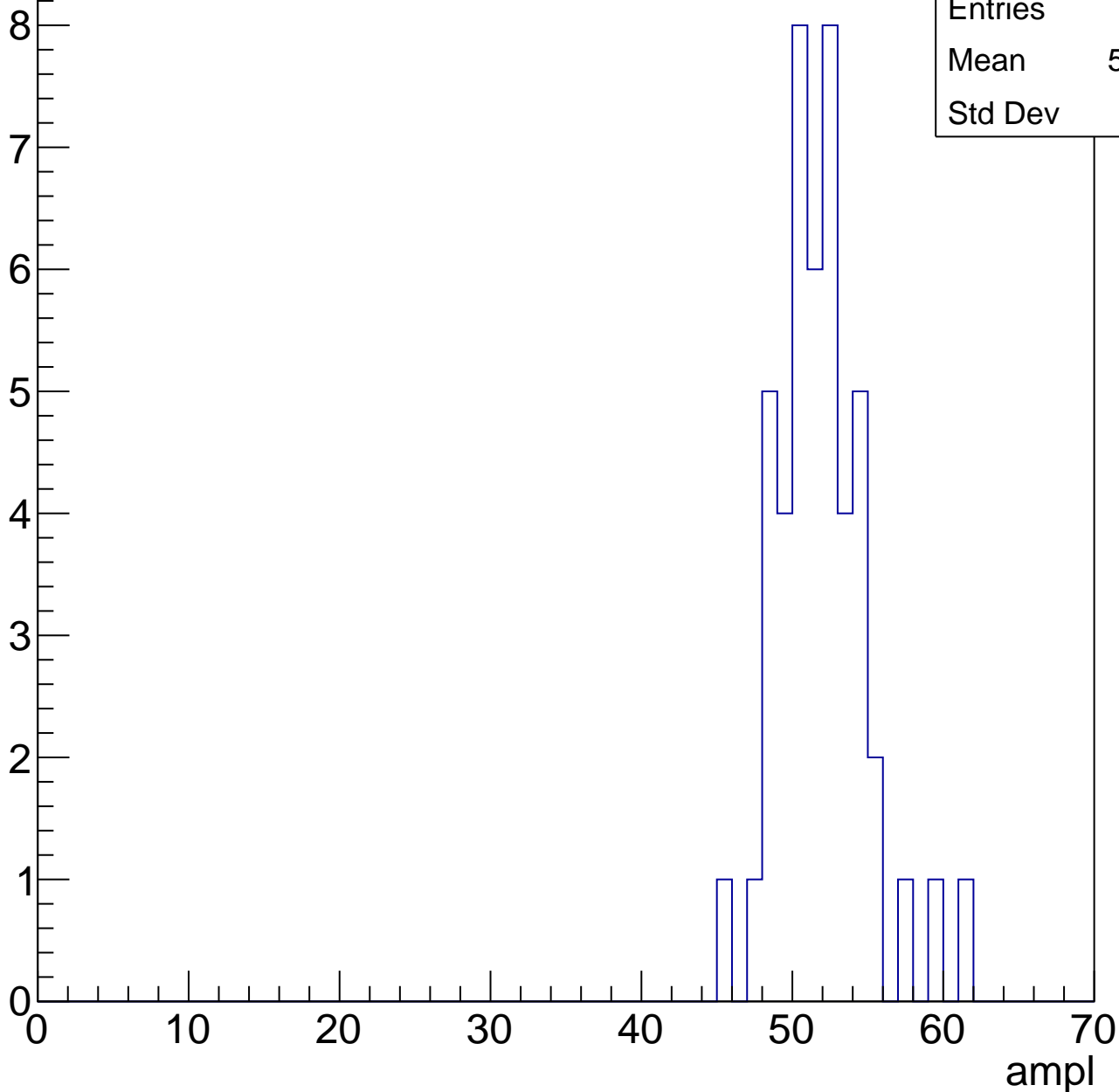


B1L103S, U6-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	51.47
Std Dev	2.96



B1L103S, U6-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

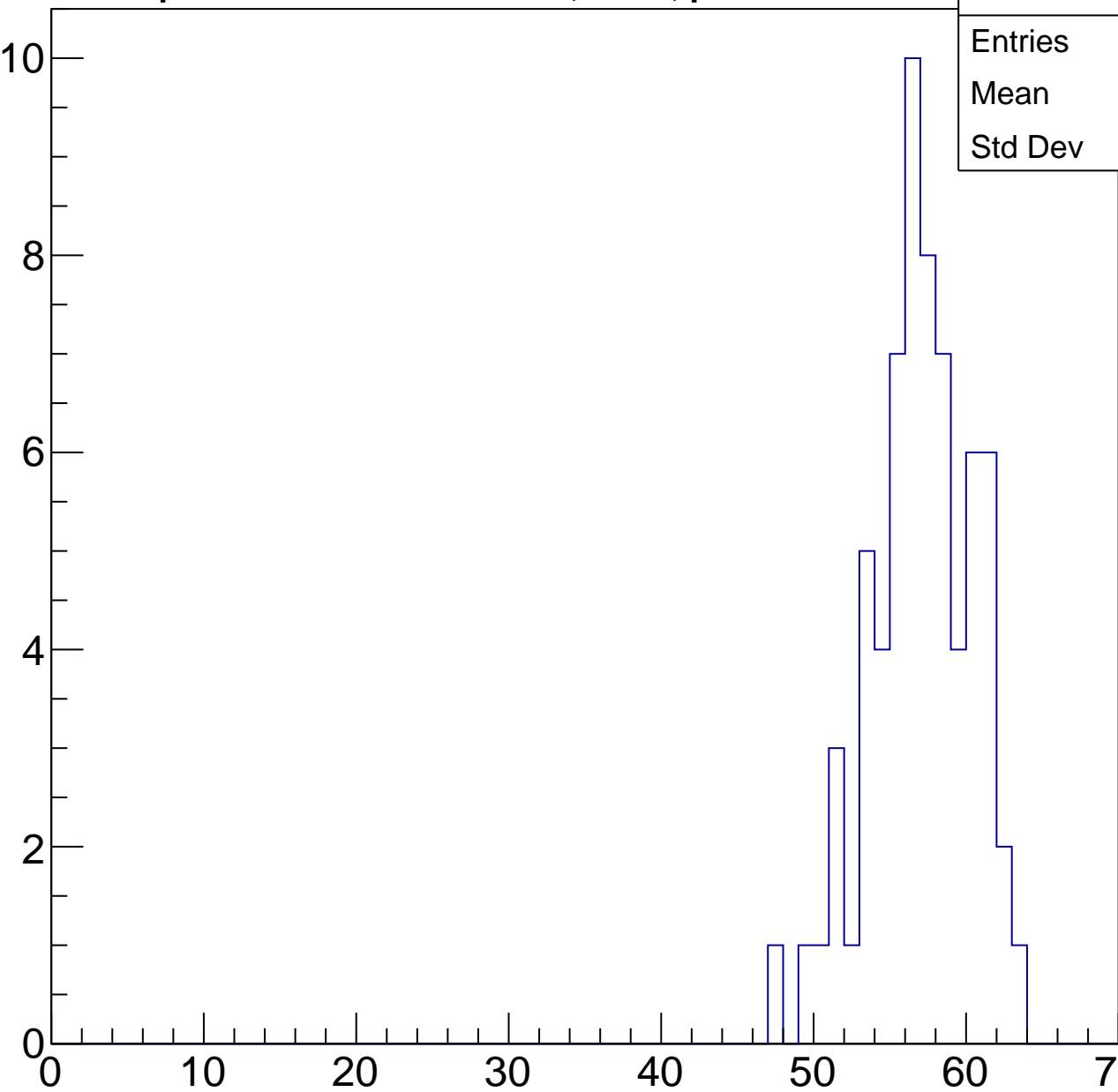
Entries	67
Mean	56.54
Std Dev	3.334

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

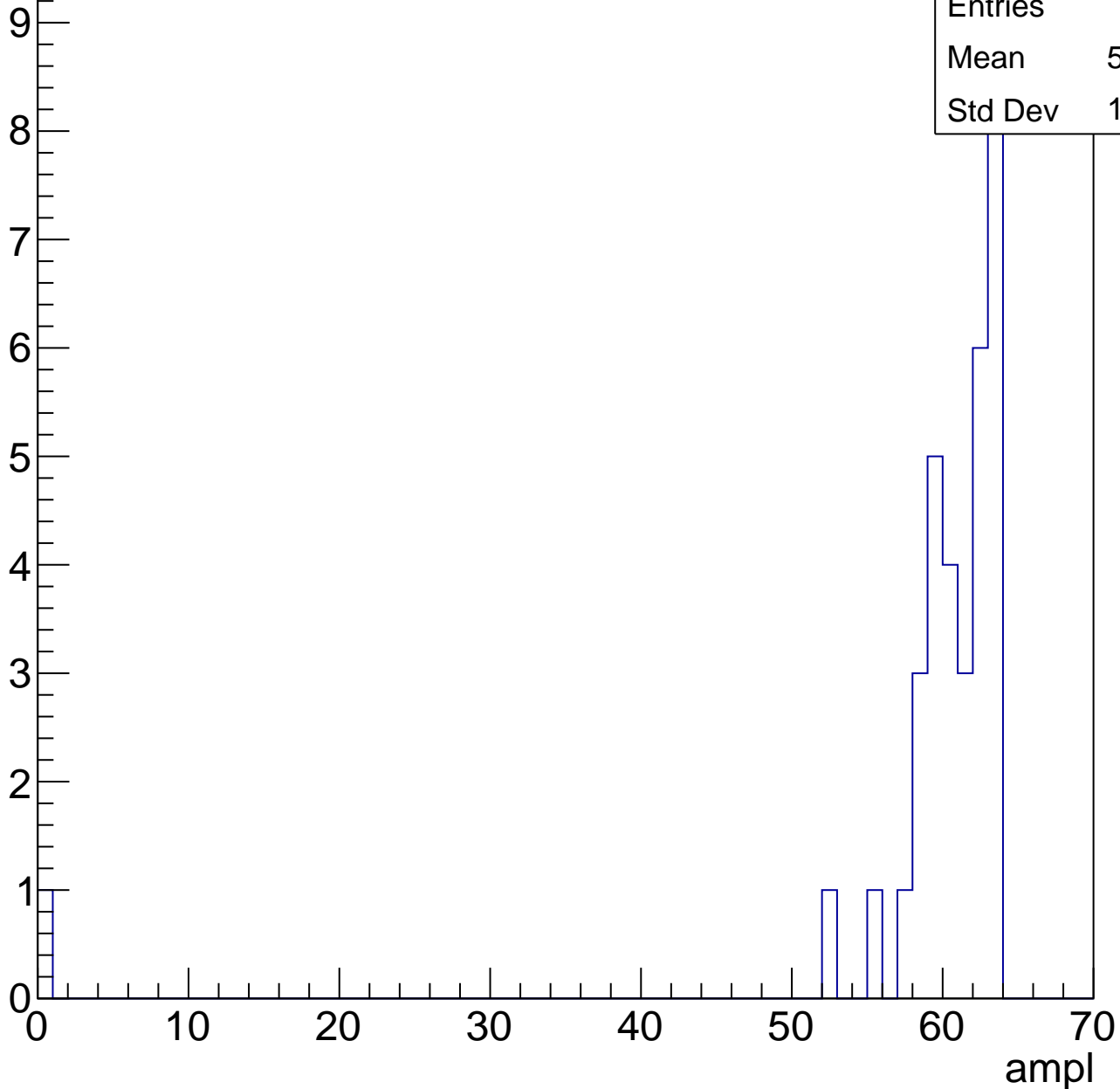


B1L103S, U6-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

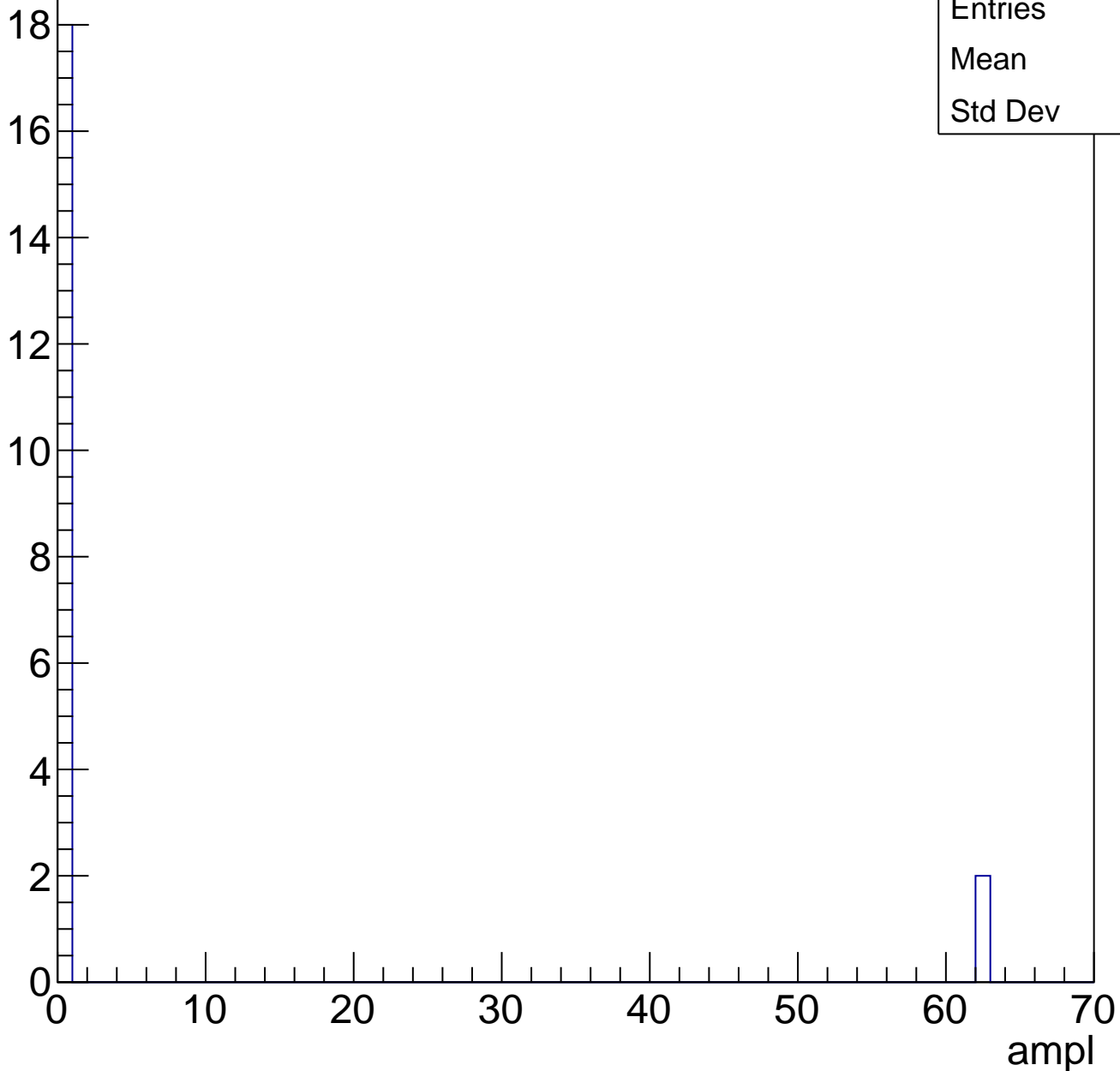
Entries	34
Mean	58.68
Std Dev	10.52



B1L103S, U6-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	6.2
Std Dev	18.6

B1L103S, U6-ch33, adc0

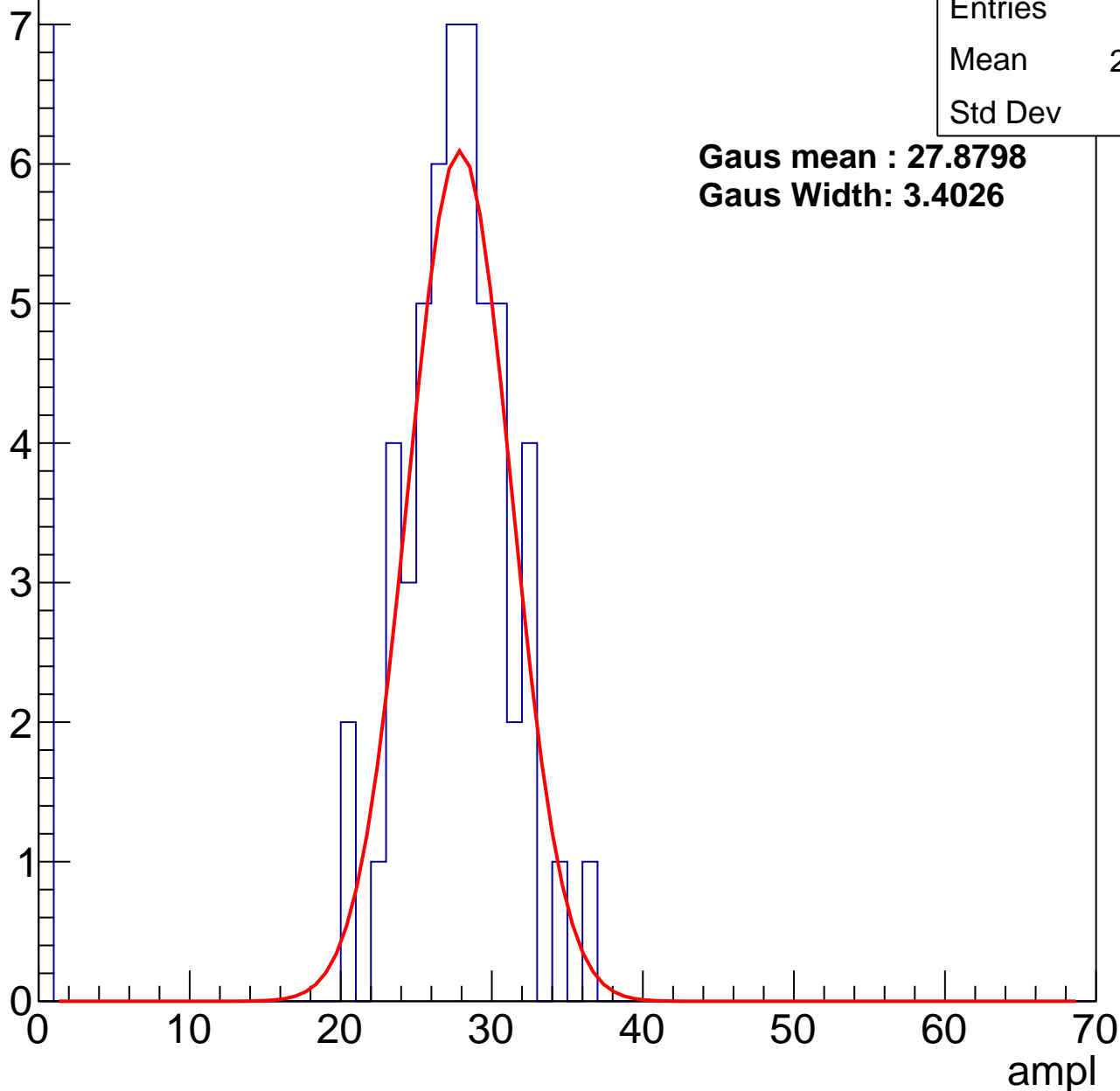
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	24.12
Std Dev	9.29

Gaus mean : 27.8798

Gaus Width: 3.4026



B1L103S, U6-ch33, adc1

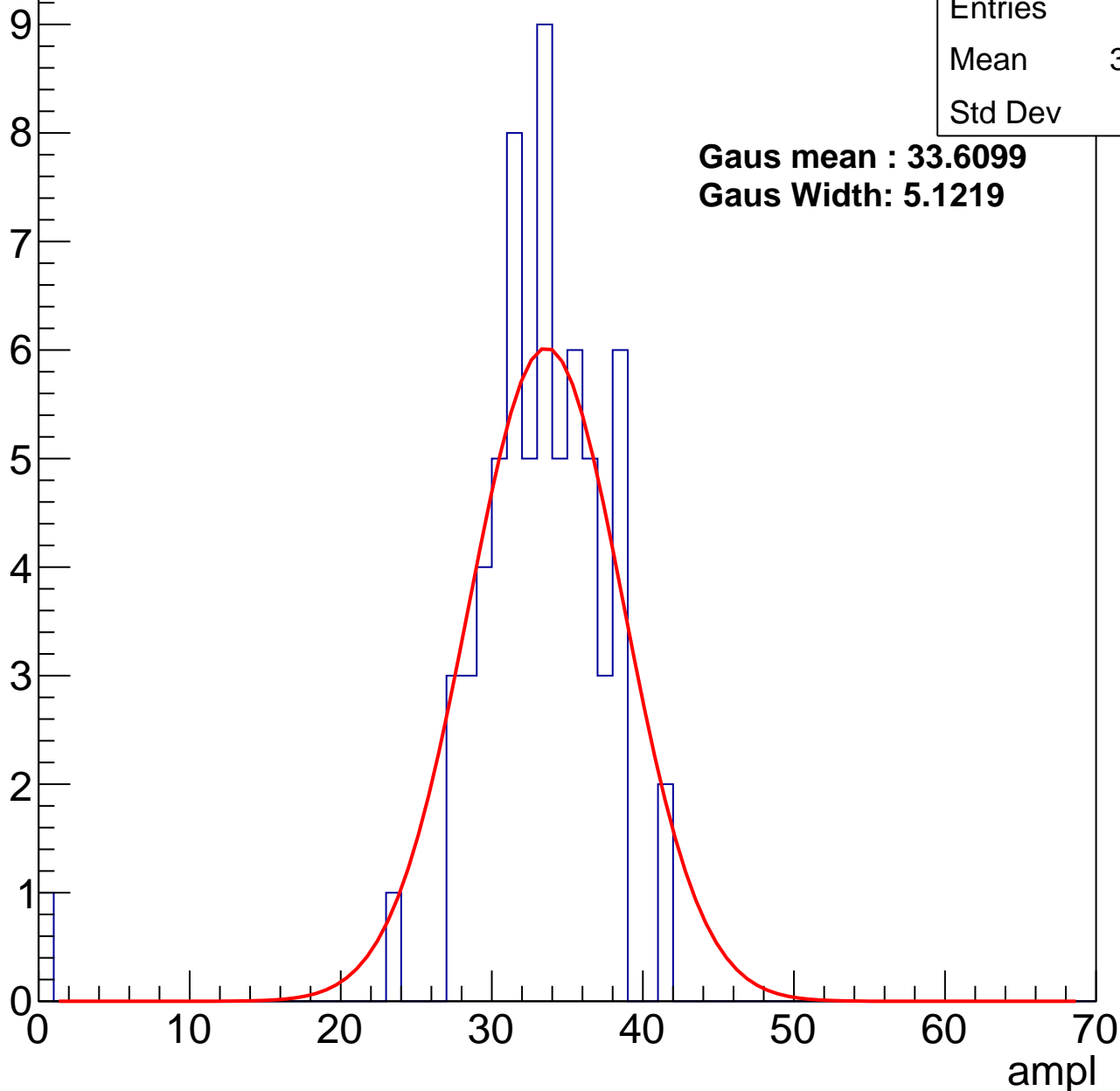
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.42
Std Dev	5.36

Gaus mean : 33.6099

Gaus Width: 5.1219



B1L103S, U6-ch33, adc2

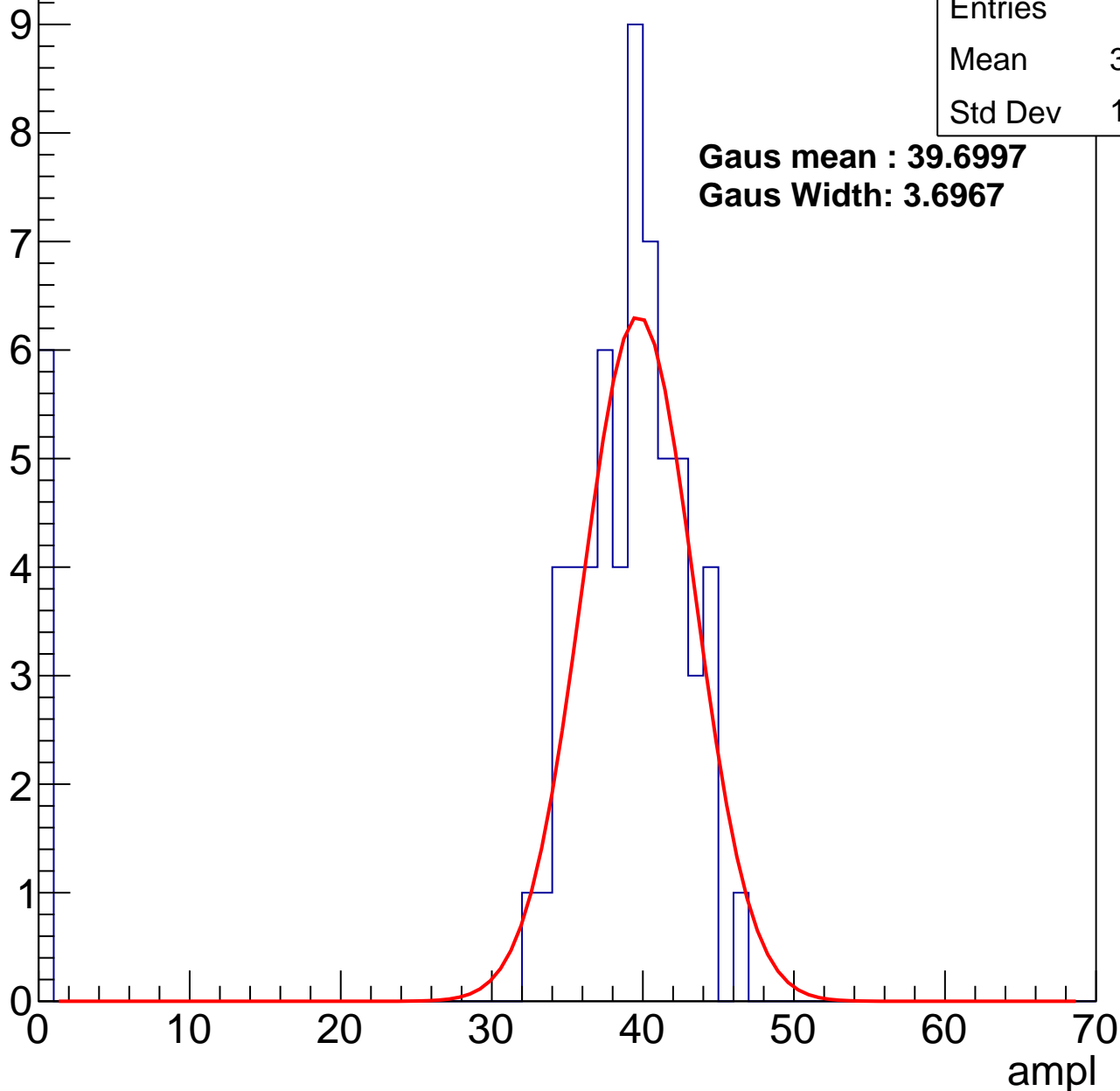
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.25
Std Dev	11.73

Gaus mean : 39.6997

Gaus Width: 3.6967



B1L103S, U6-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

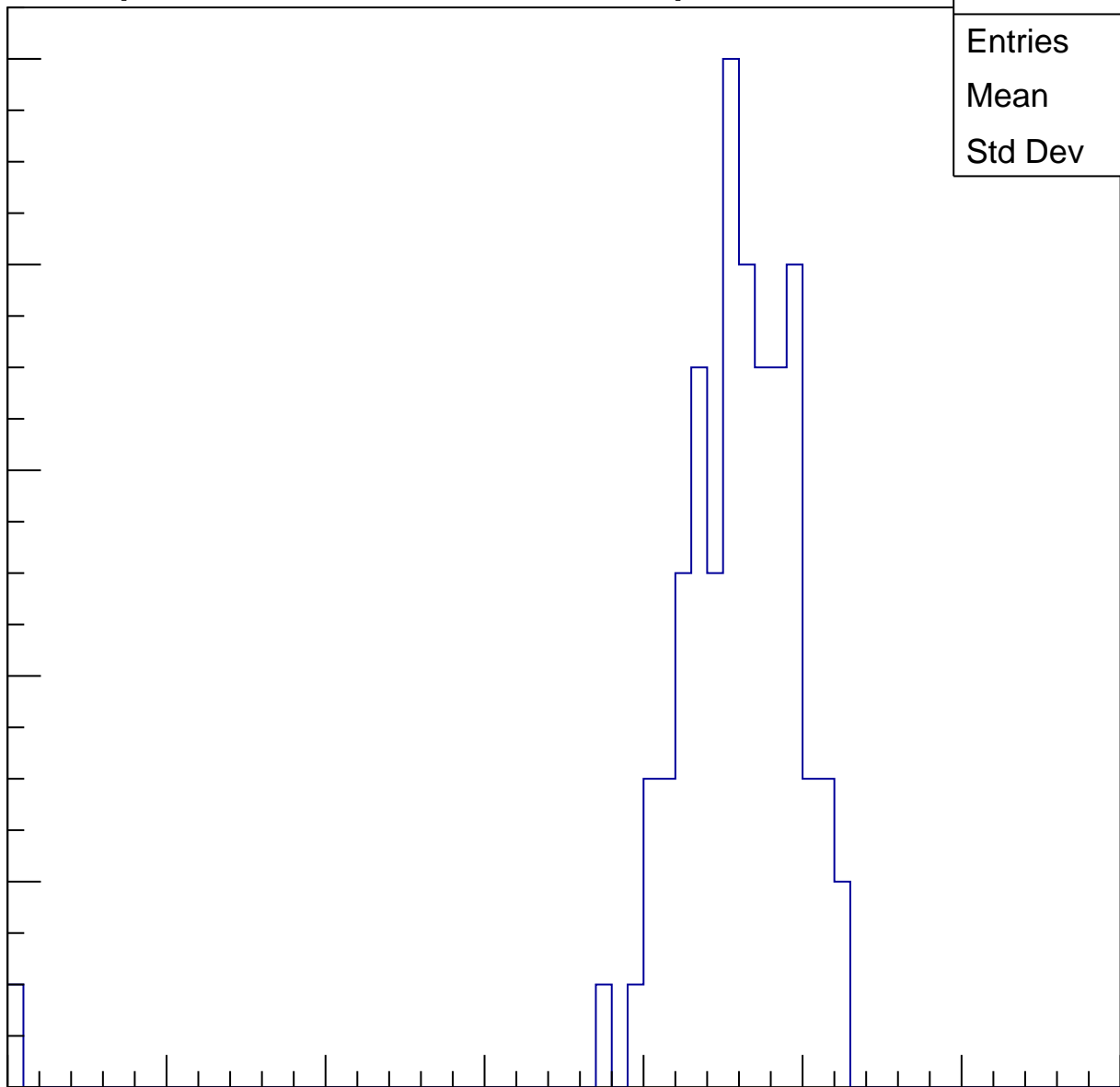
Entries	74
Mean	45.03
Std Dev	6.188

Entry

10
8
6
4
2
0

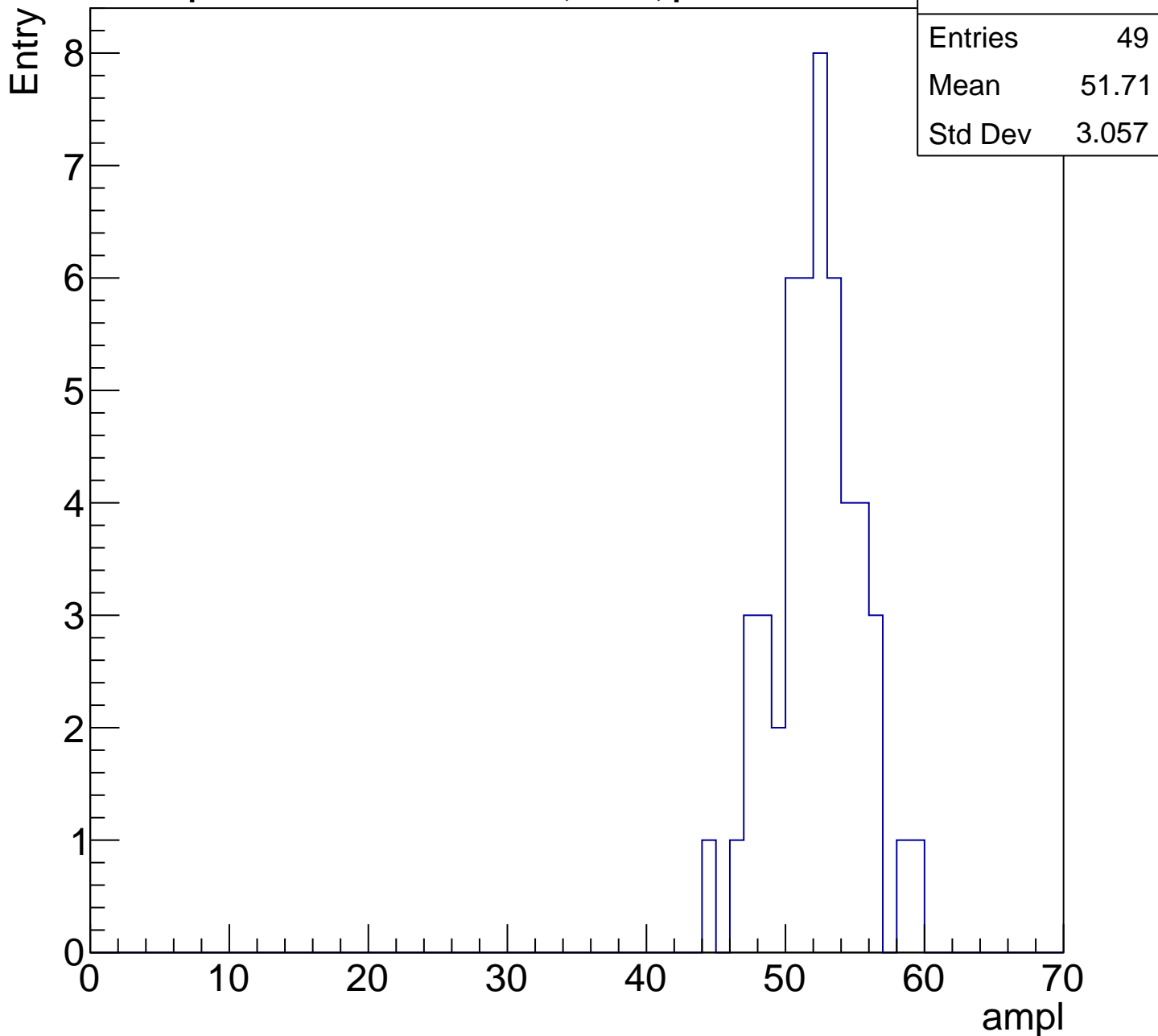
0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch33, adc5

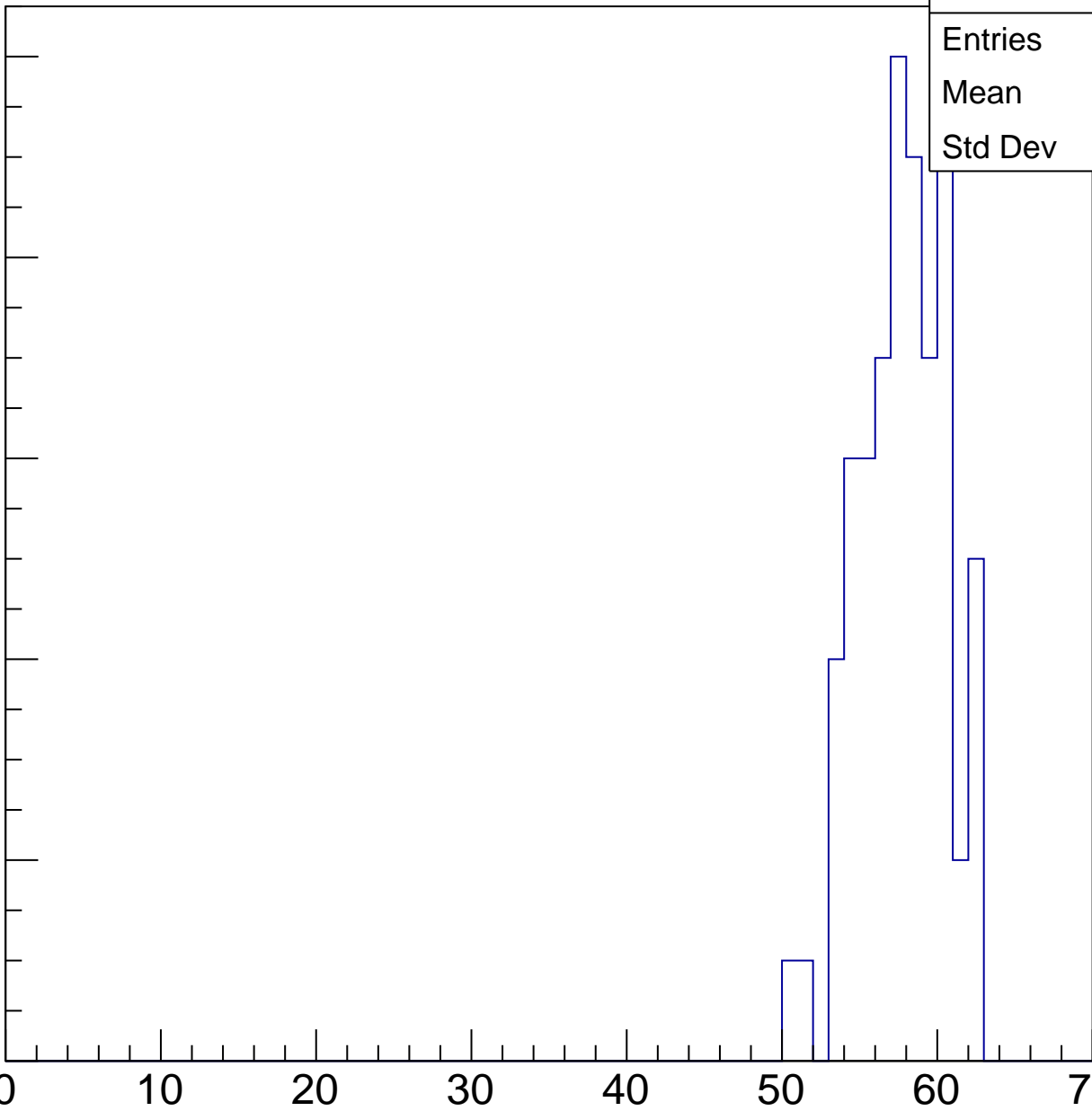
calib_packv5_041523_1651.root, FC#0, port C2

Entry

10
8
6
4
2
0

Entries	67
Mean	57.25
Std Dev	2.745

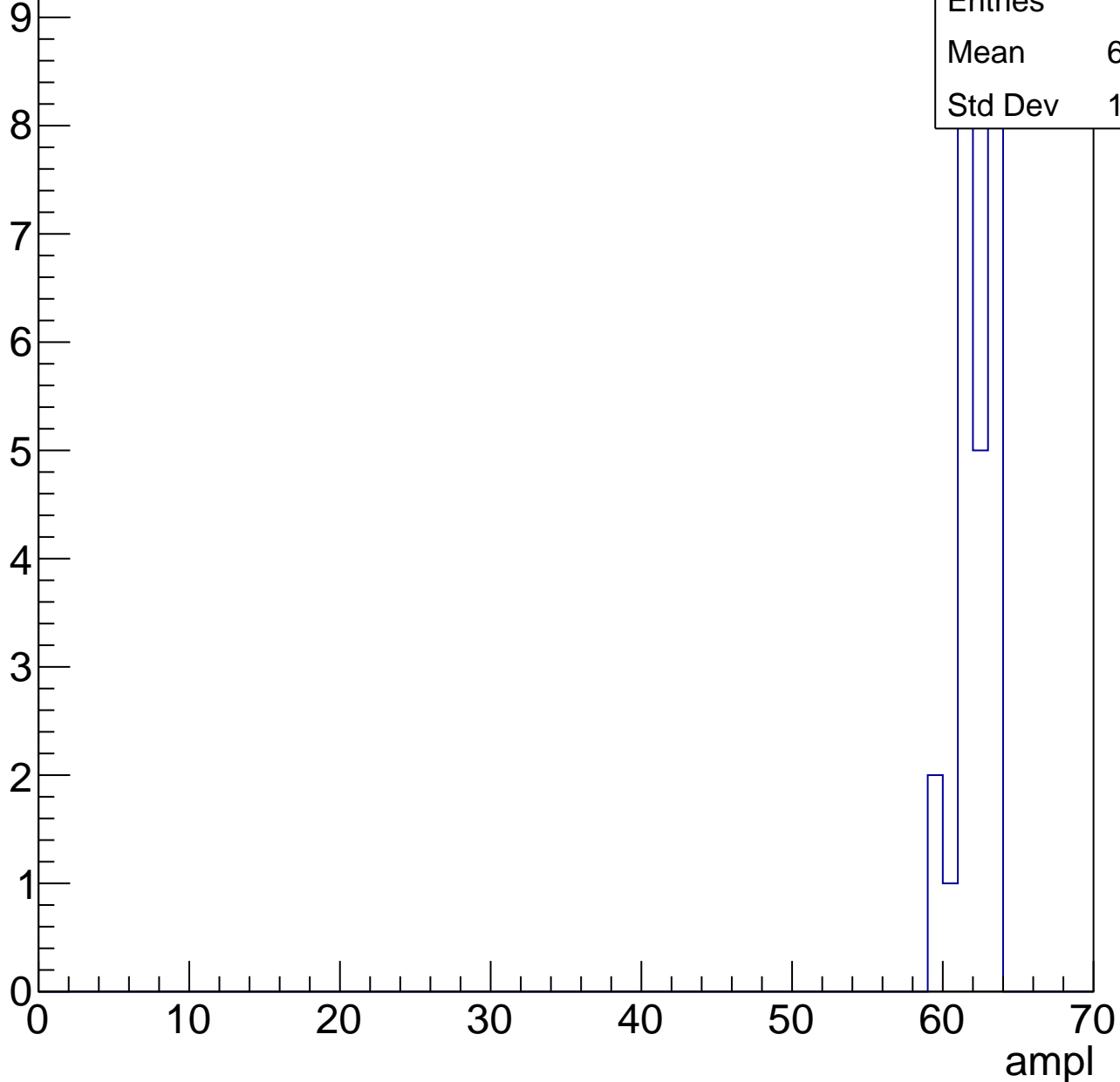
ampl



B1L103S, U6-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

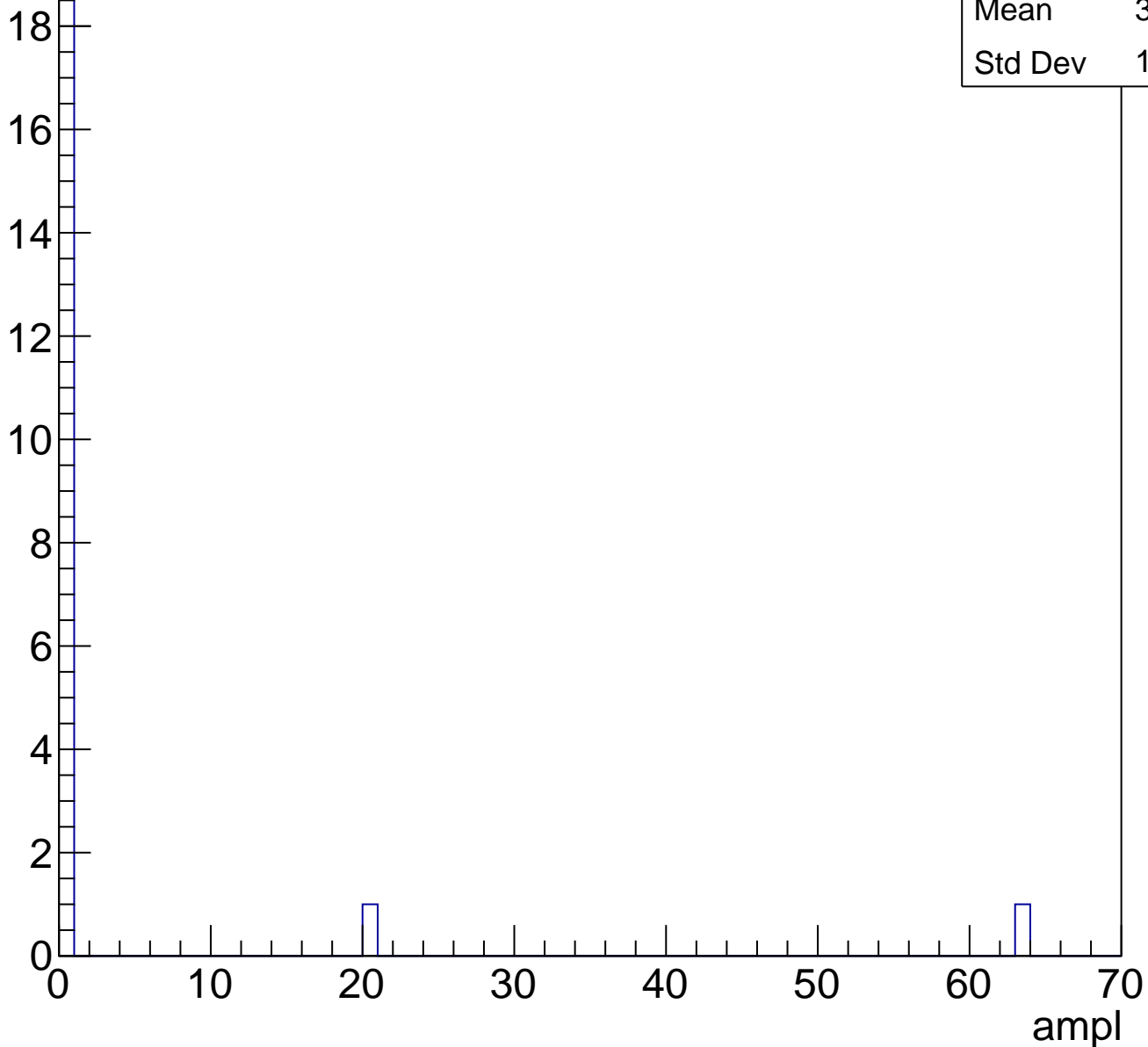
Entry



B1L103S, U6-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch34, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	22.64
Std Dev	11.13

Gaus mean : 28.1009

Gaus Width: 2.5793

Entry

12

10

8

6

4

2

0

0

10

20

30

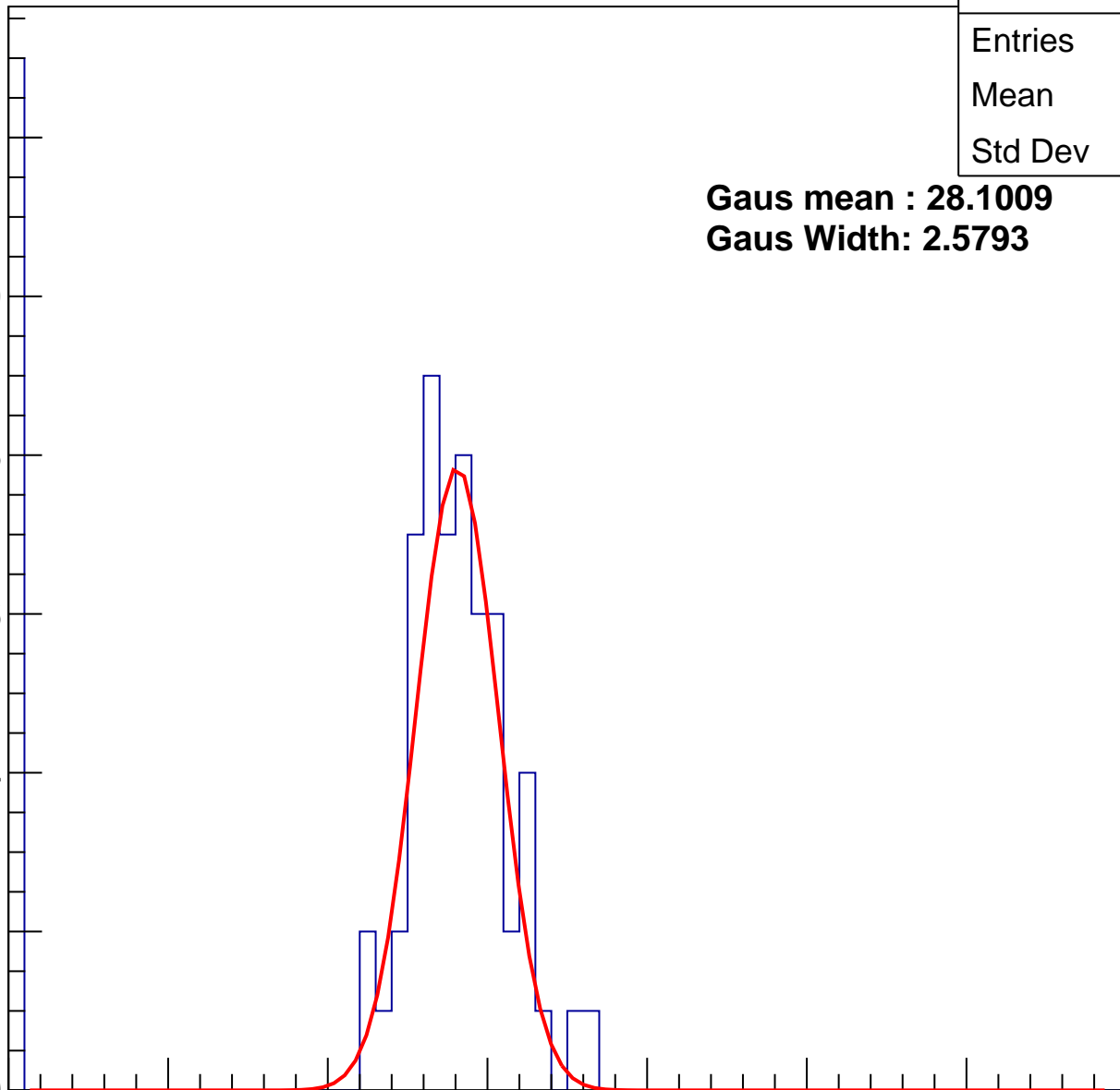
40

50

60

70

ampl



B1L103S, U6-ch34, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.45
Std Dev	8.842

Gaus mean : 34.5724

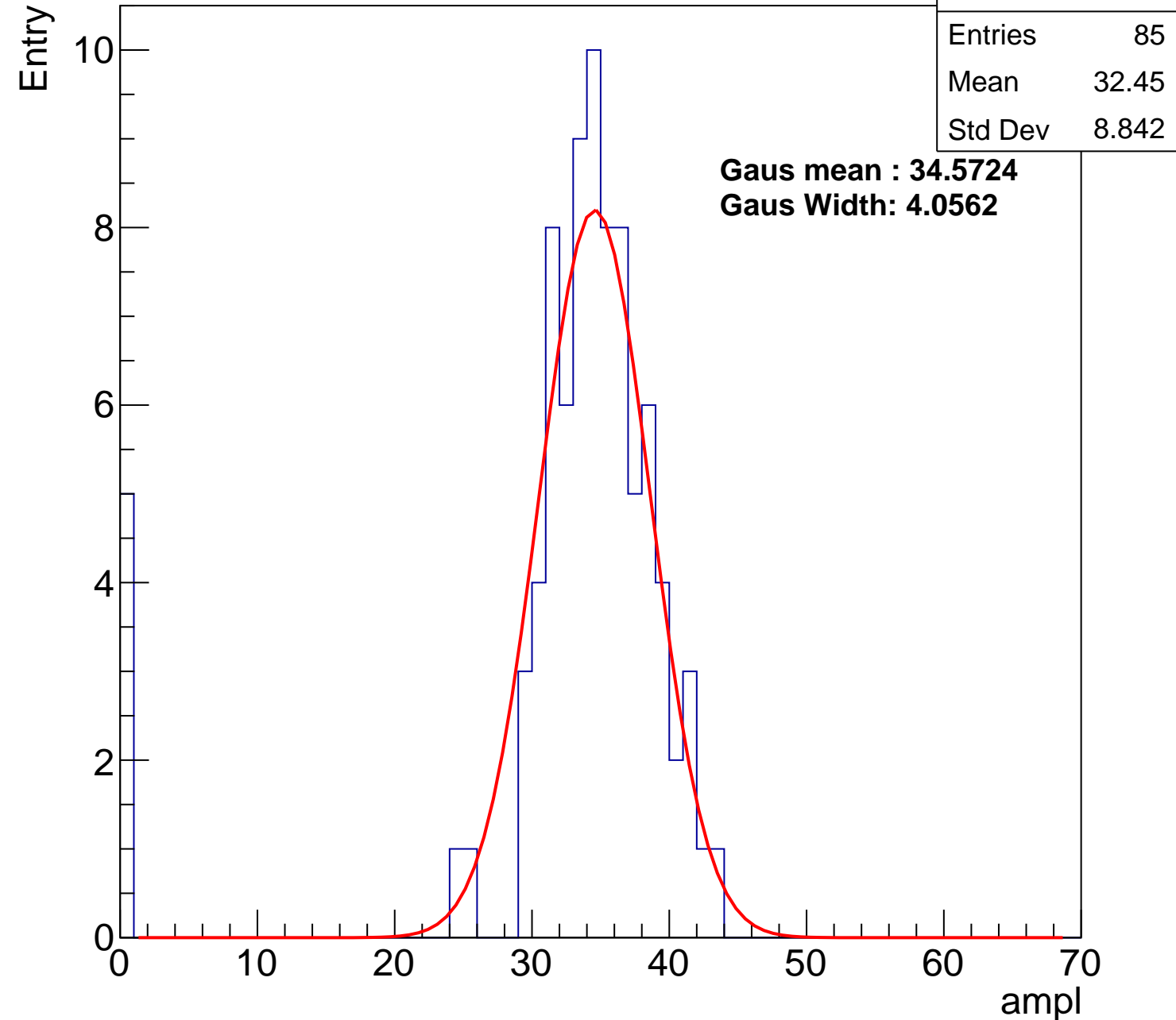
Gaus Width: 4.0562

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch34, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	33.94
Std Dev	15.86

Gaus mean : 41.0951
Gaus Width: 2.8725

Entry

10

8

6

4

2

0

0

10

20

30

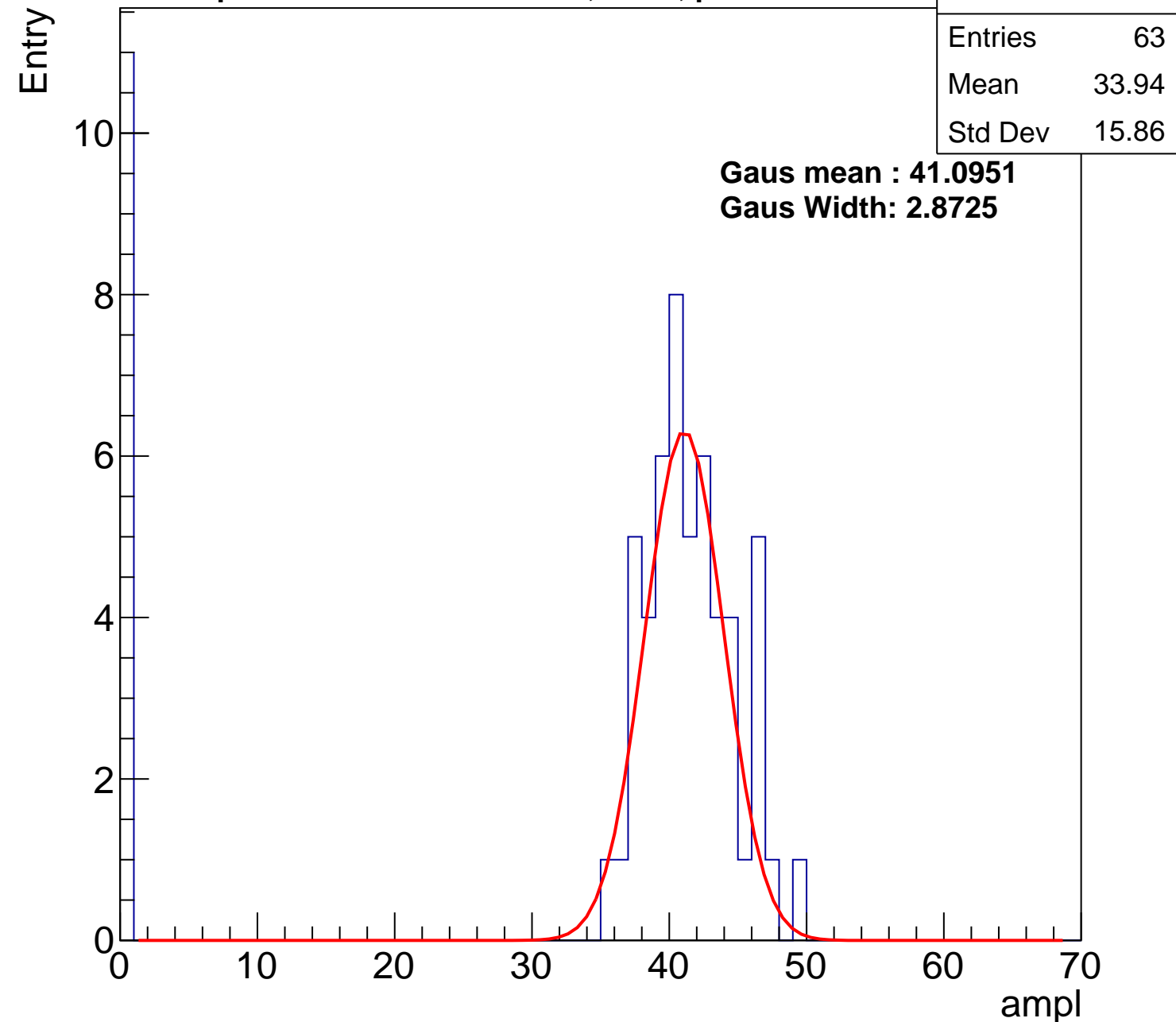
40

50

60

70

ampl

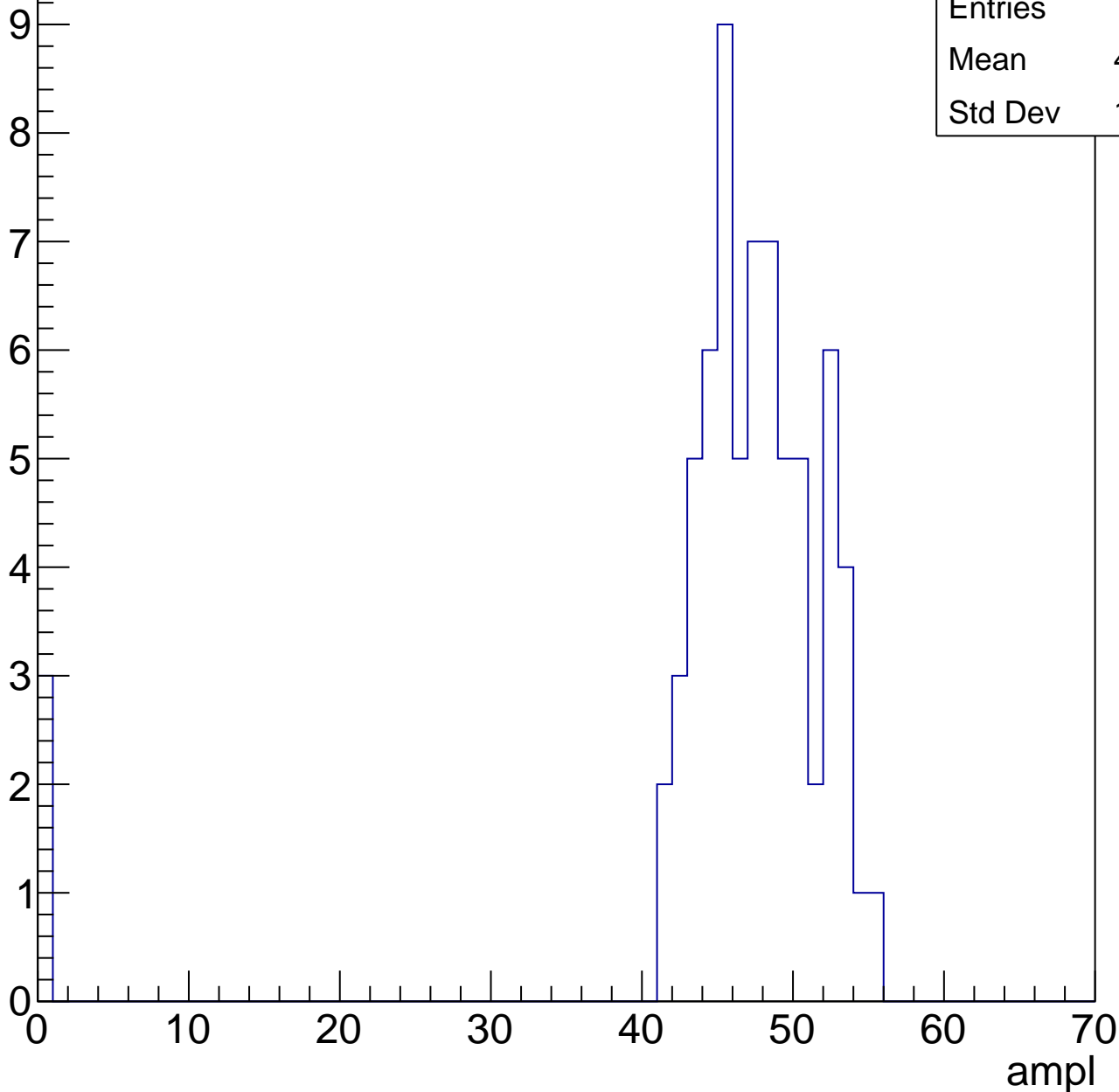


B1L103S, U6-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	45.31
Std Dev	10.11

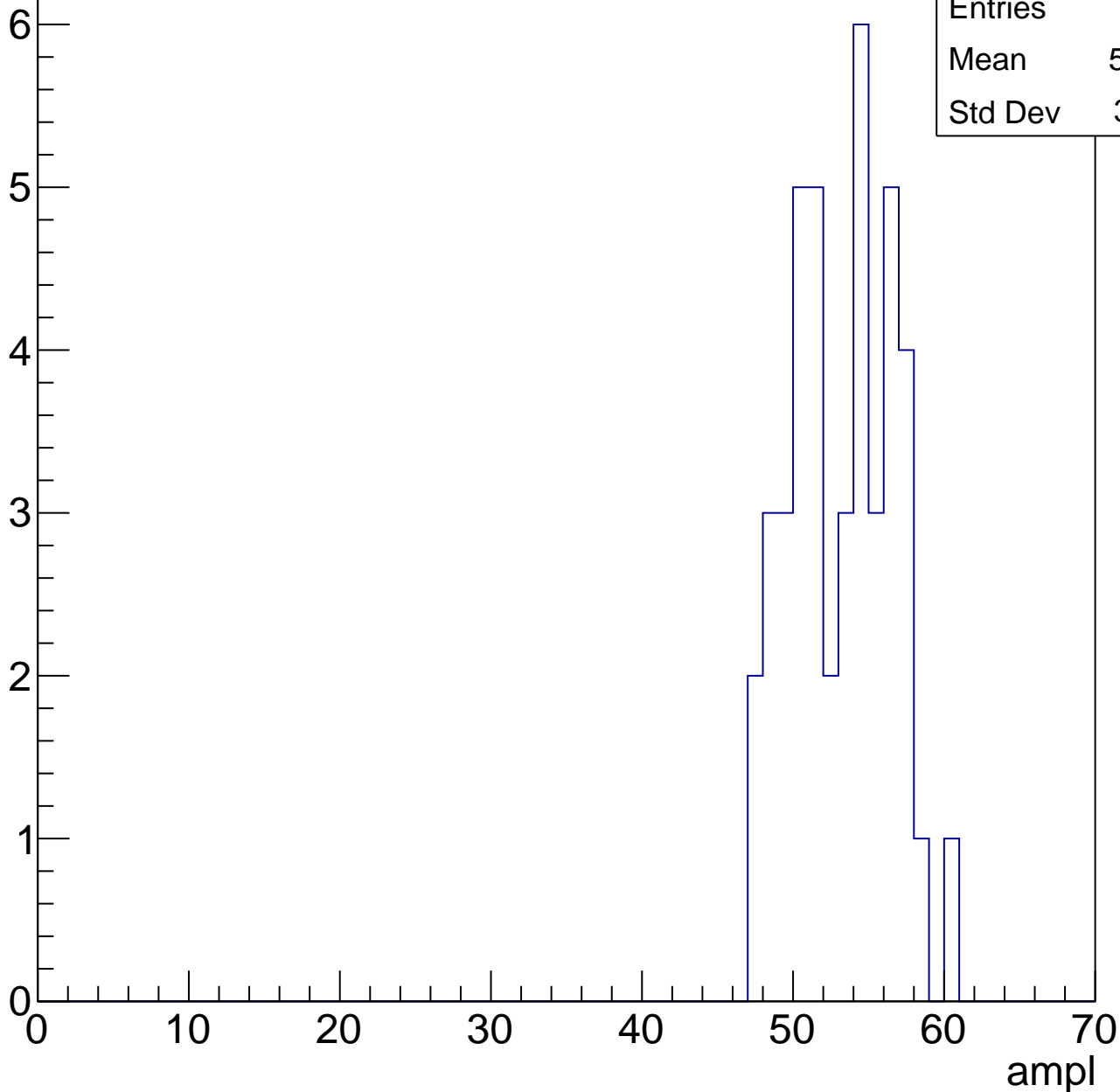


B1L103S, U6-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

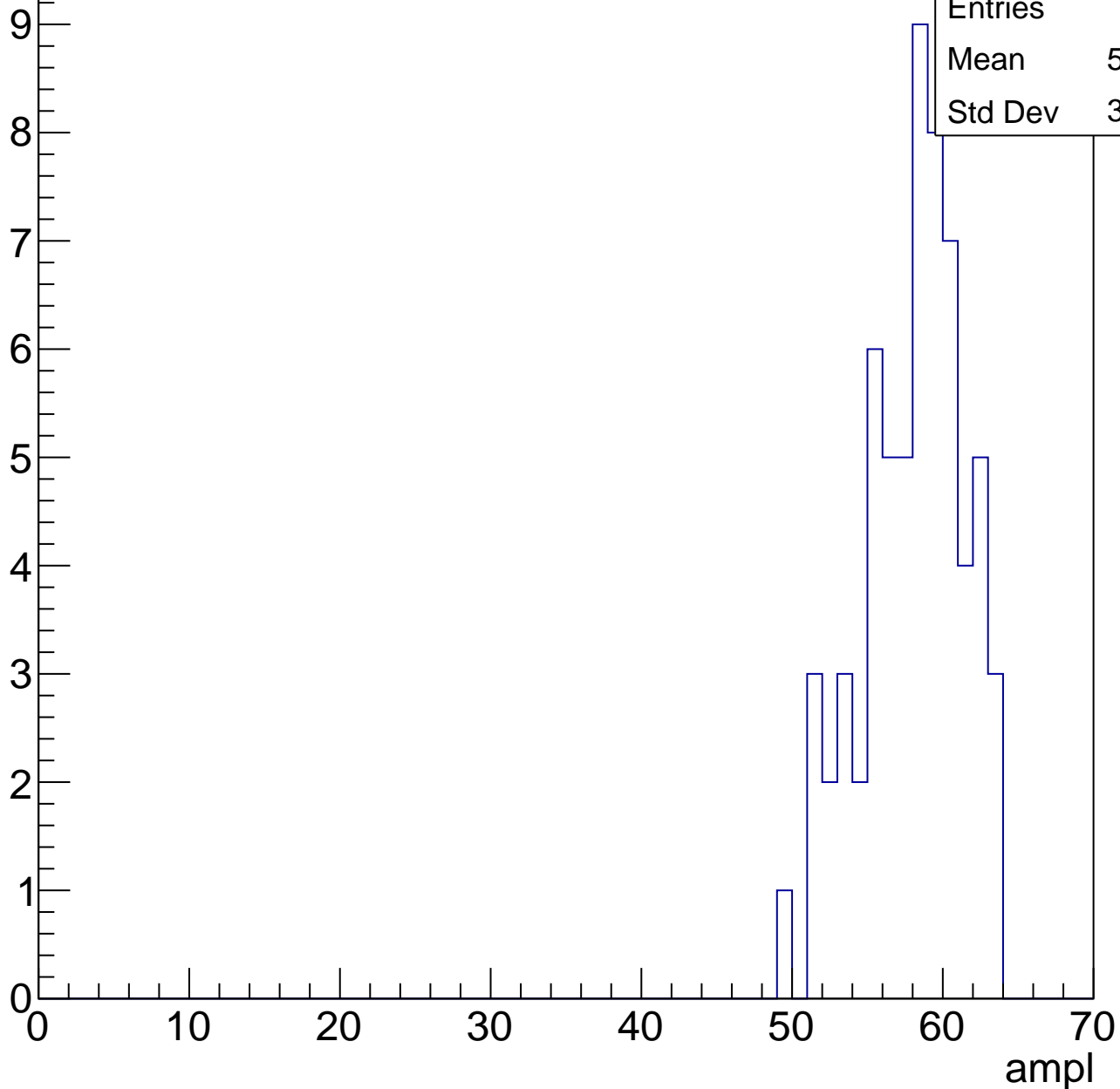
Entries	43
Mean	52.74
Std Dev	3.271



B1L103S, U6-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



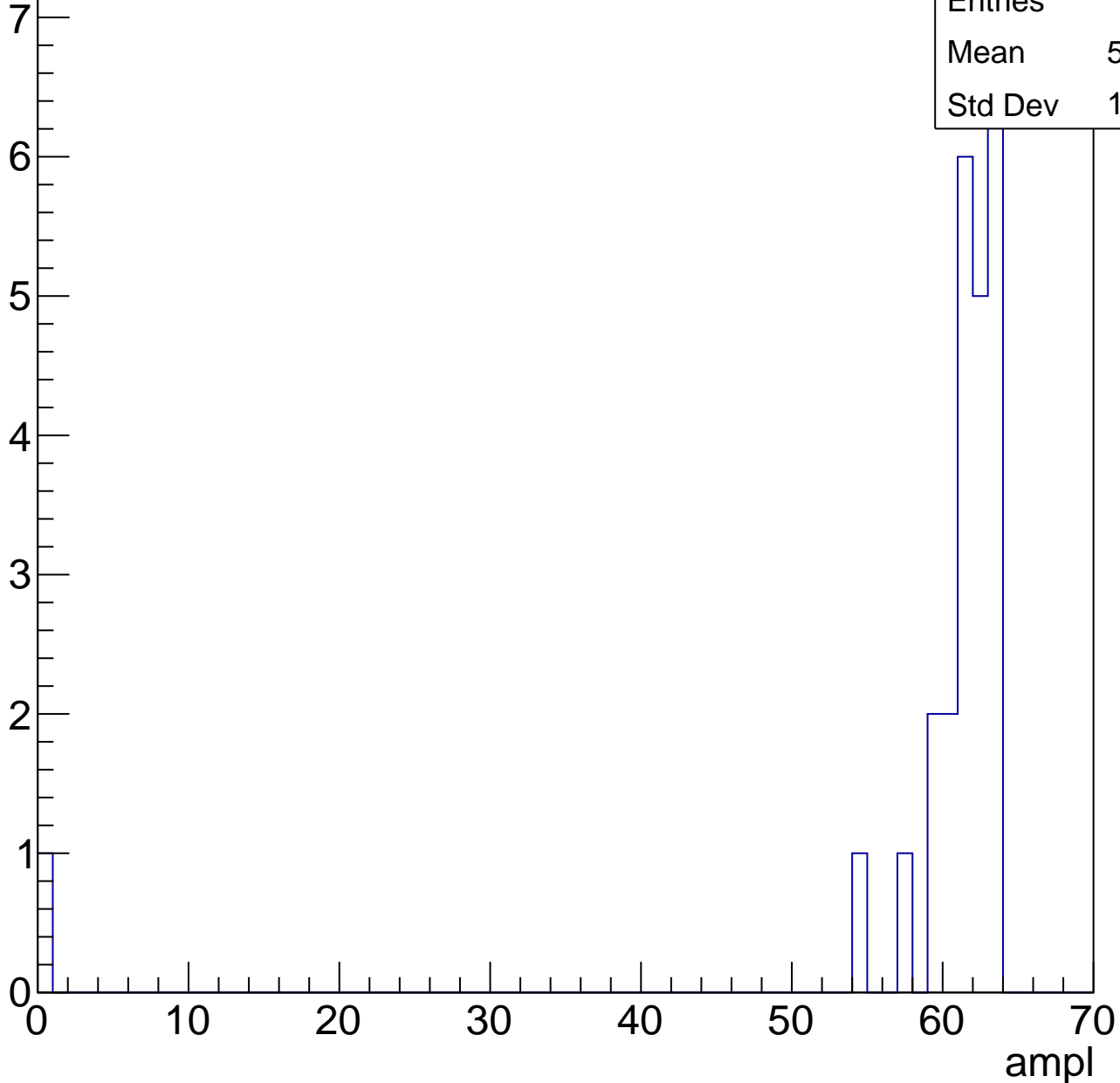
Entries	63
Mean	57.54
Std Dev	3.333

B1L103S, U6-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

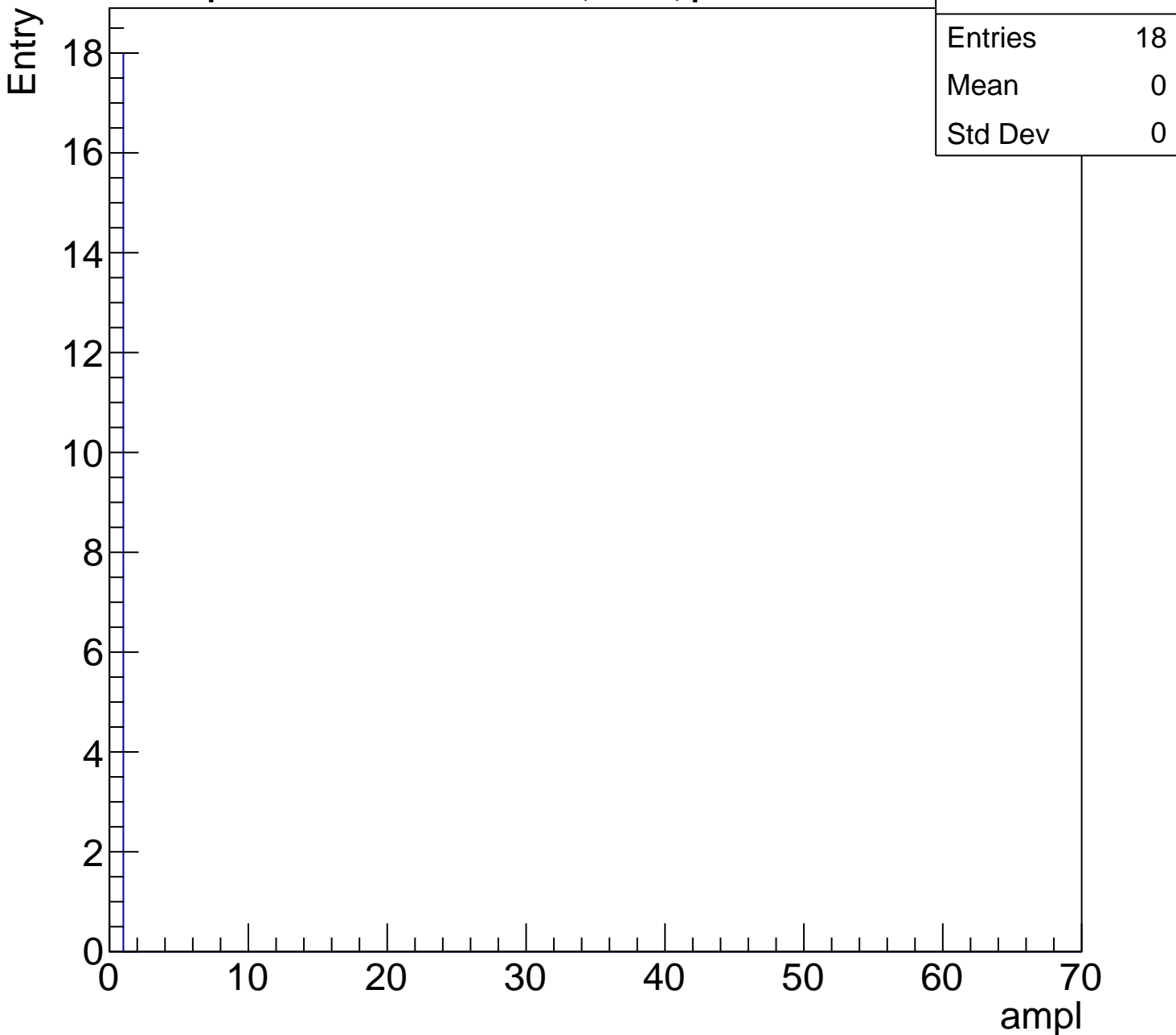
Entry

Entries	25
Mean	58.64
Std Dev	12.15



B1L103S, U6-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch35, adc0

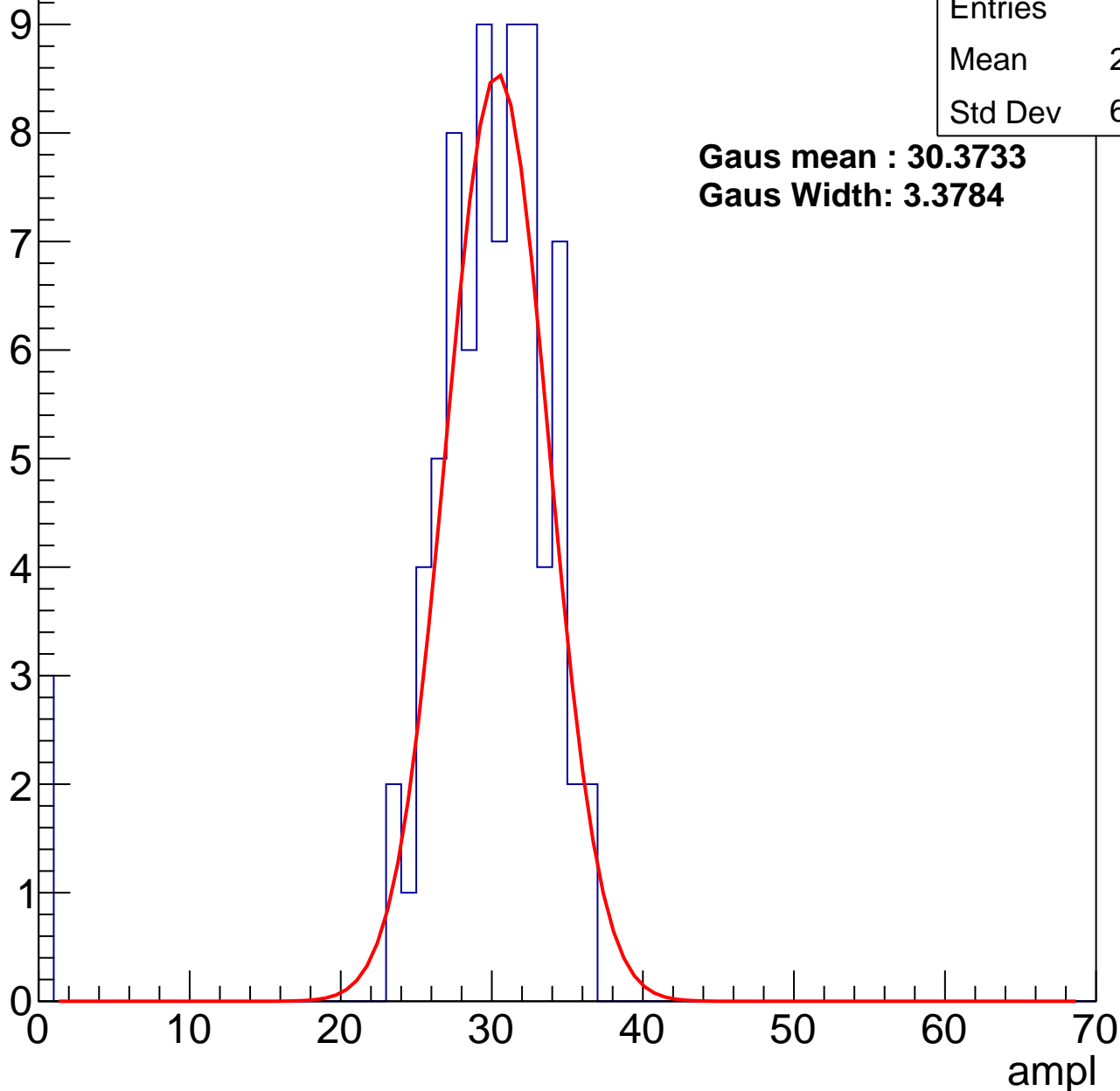
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	28.64
Std Dev	6.495

Gaus mean : 30.3733

Gaus Width: 3.3784



B1L103S, U6-ch35, adc1

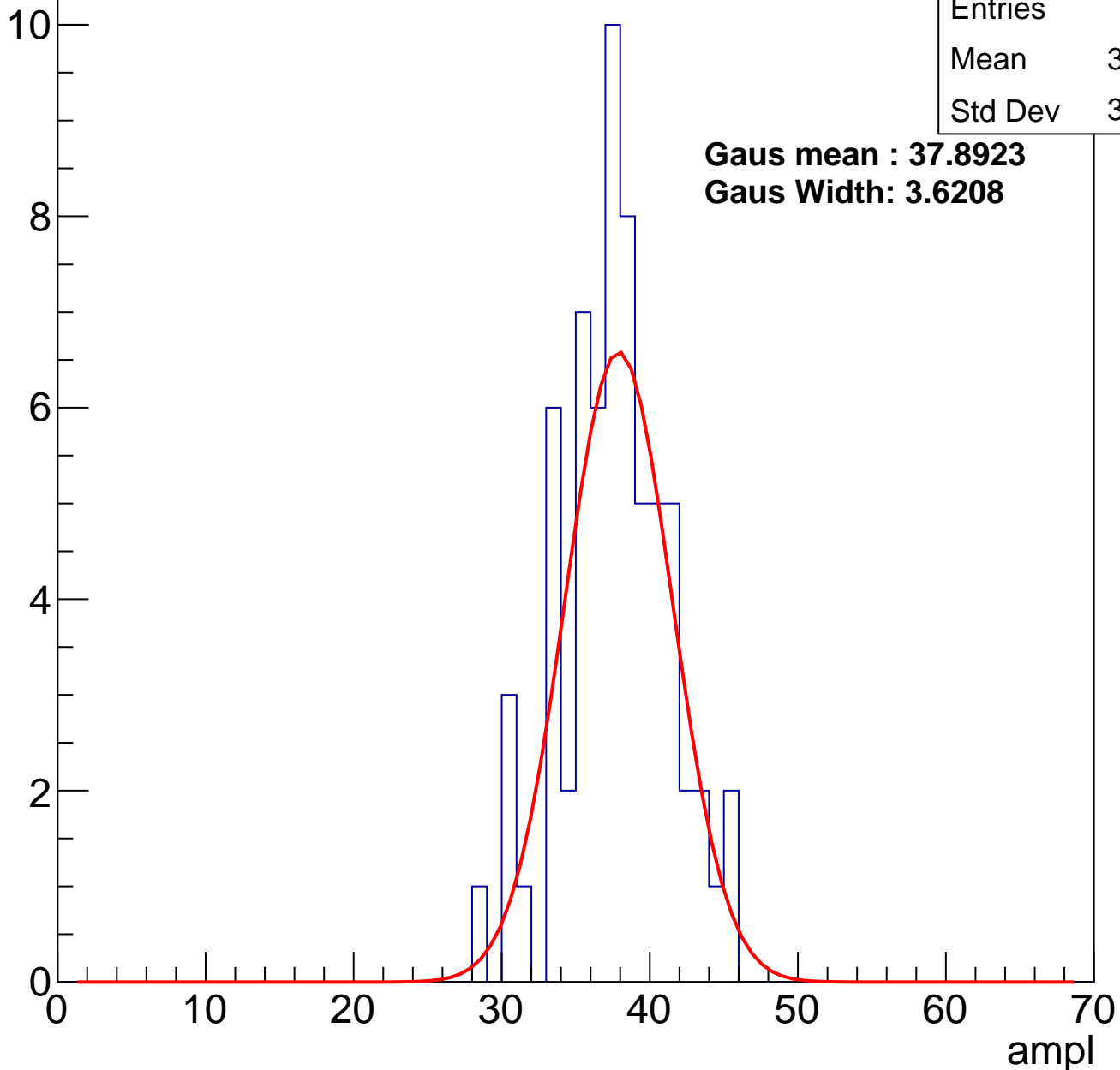
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	37.18
Std Dev	3.614

Gaus mean : 37.8923

Gaus Width: 3.6208

Entry



B1L103S, U6-ch35, adc2

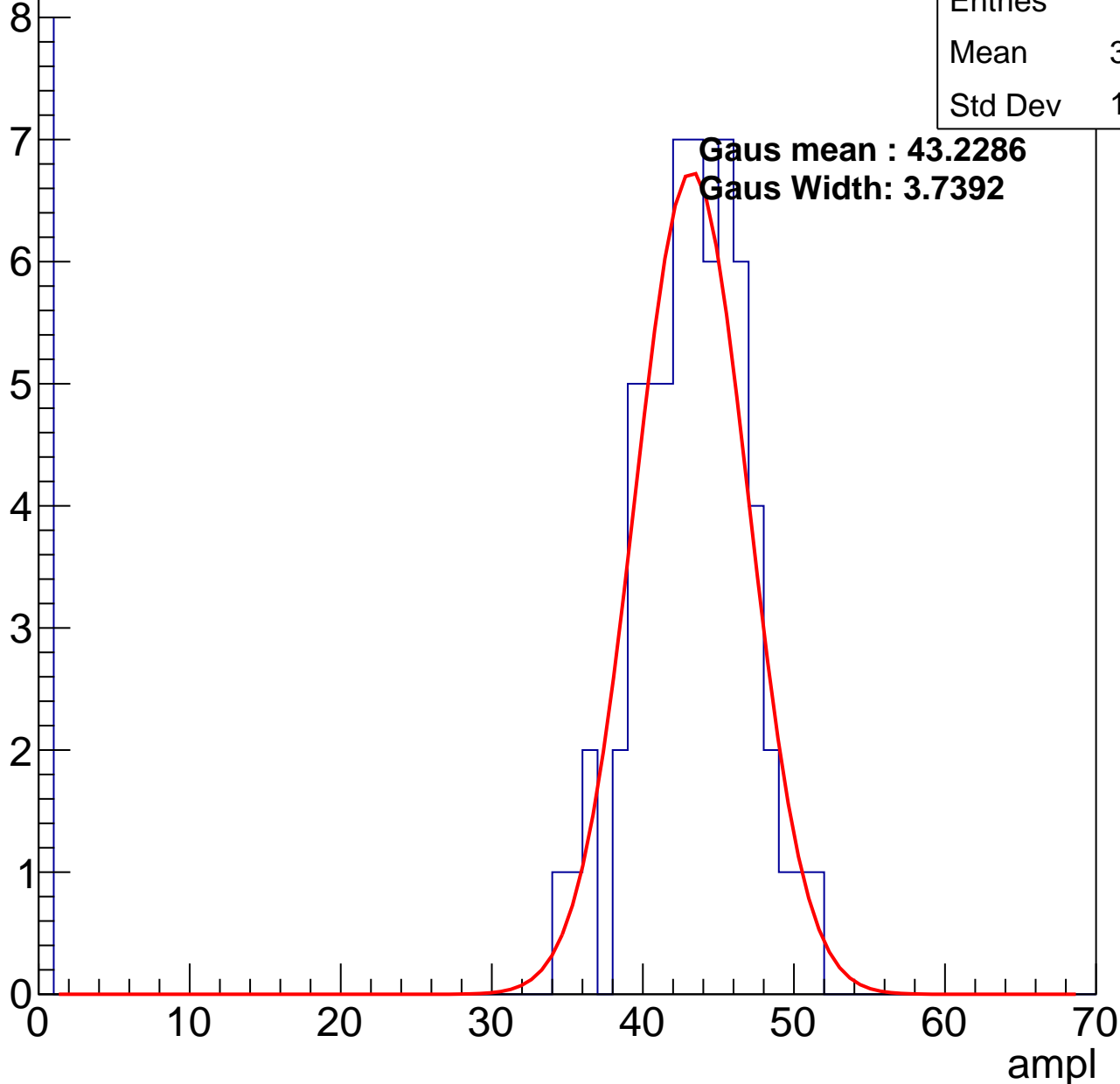
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38.04
Std Dev	13.96

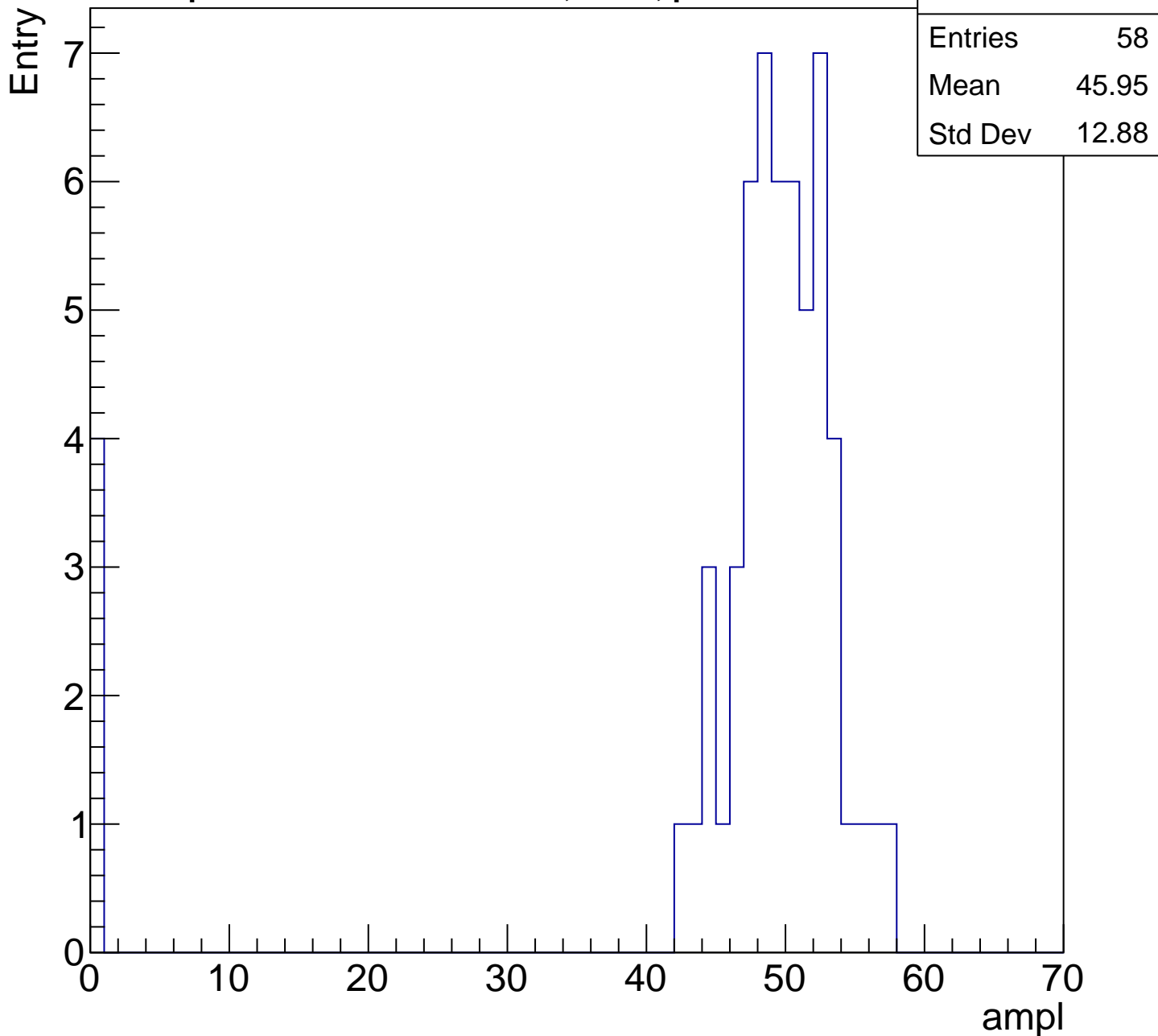
Gaus mean : 43.2286

Gaus Width: 3.7392



B1L103S, U6-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

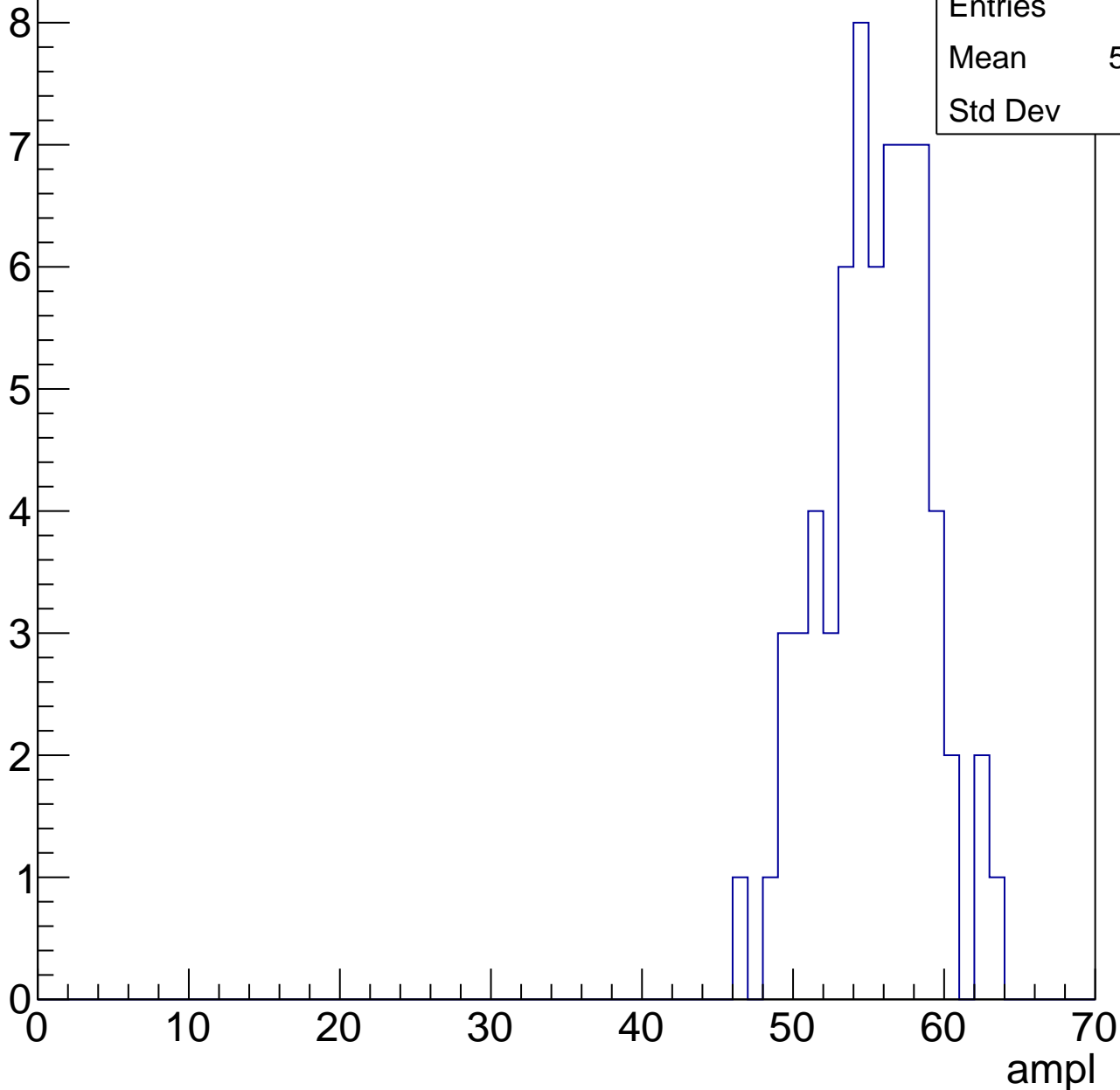


B1L103S, U6-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	54.94
Std Dev	3.53

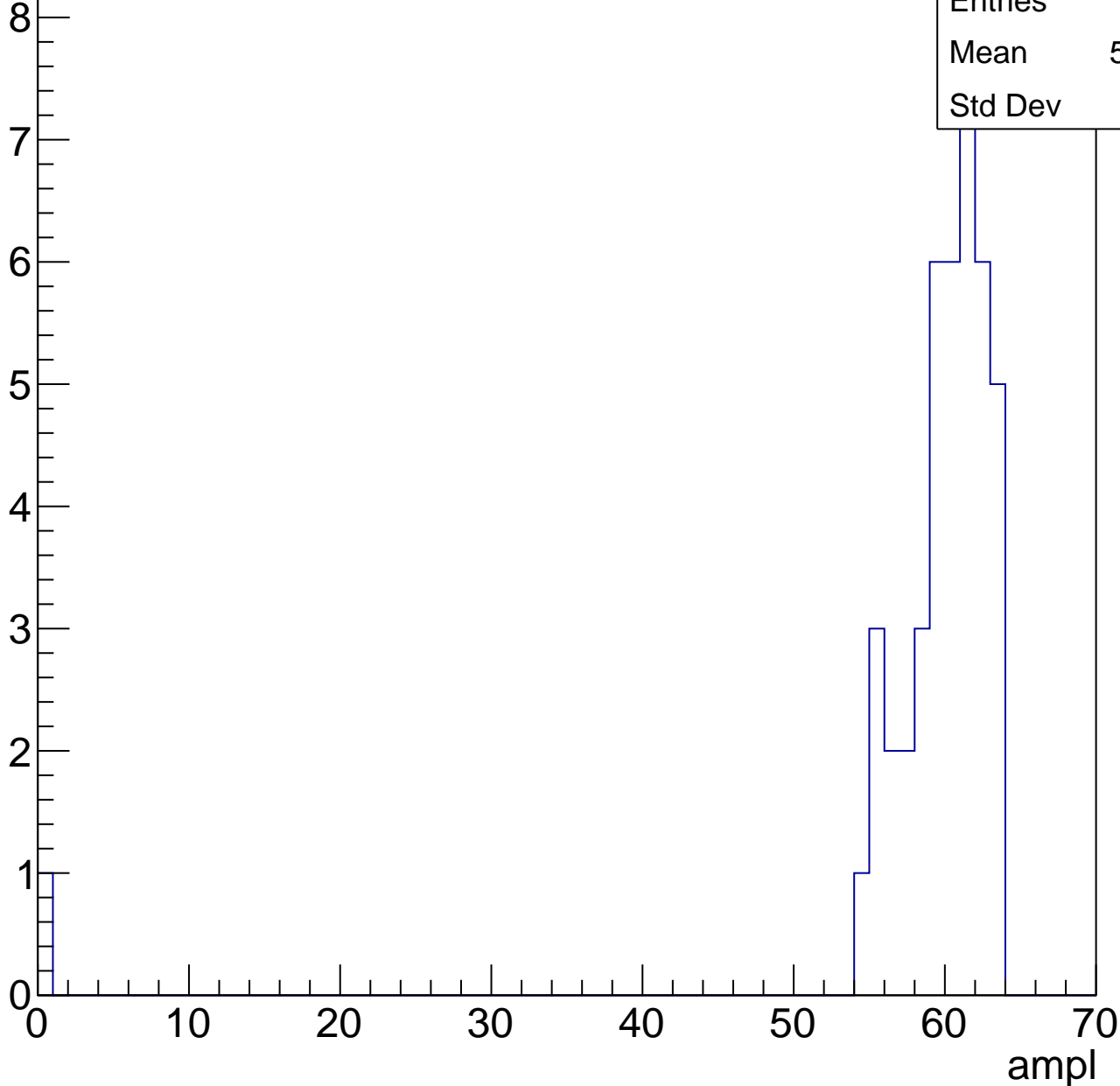


B1L103S, U6-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

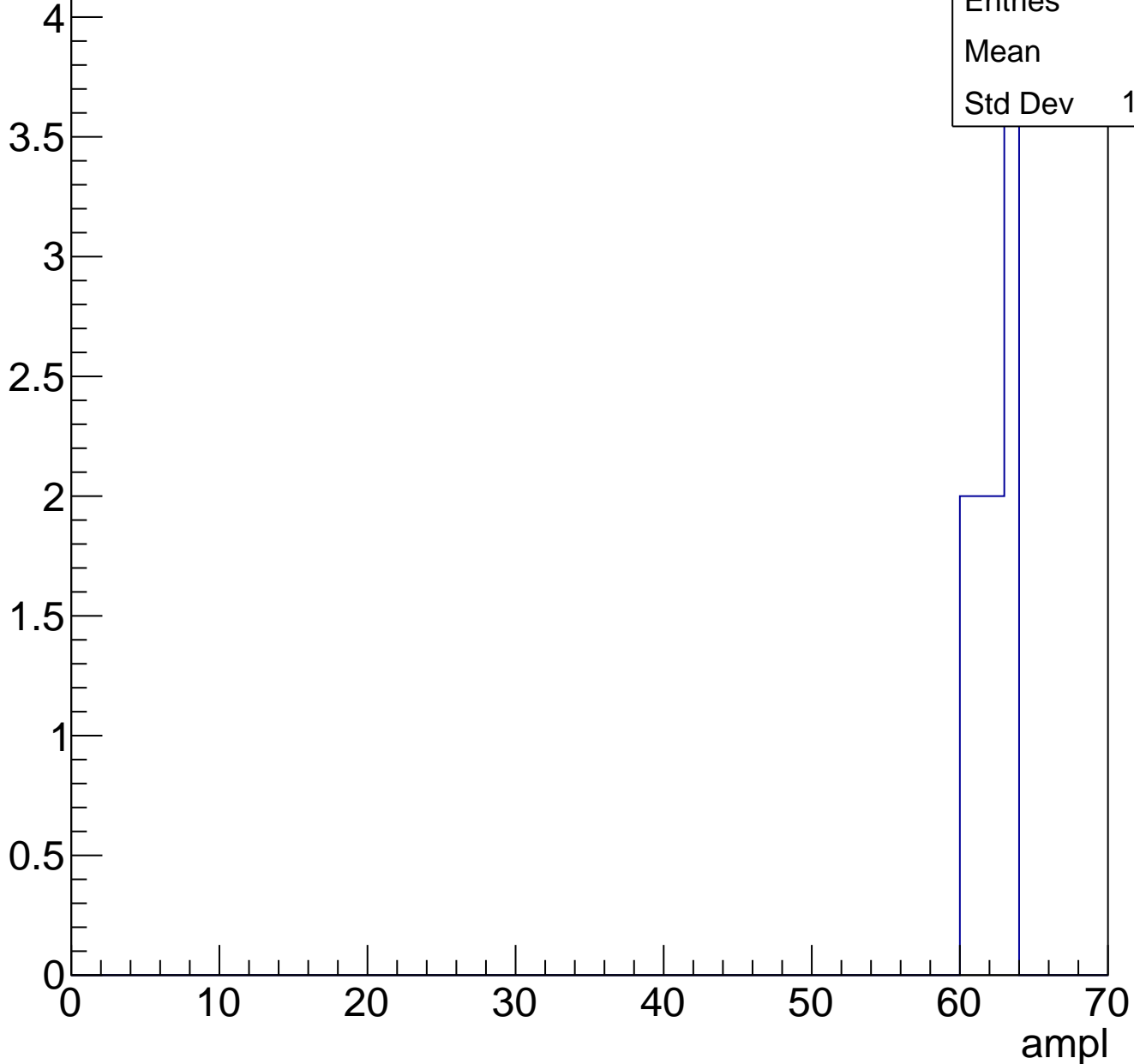
Entries	43
Mean	58.33
Std Dev	9.32



B1L103S, U6-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

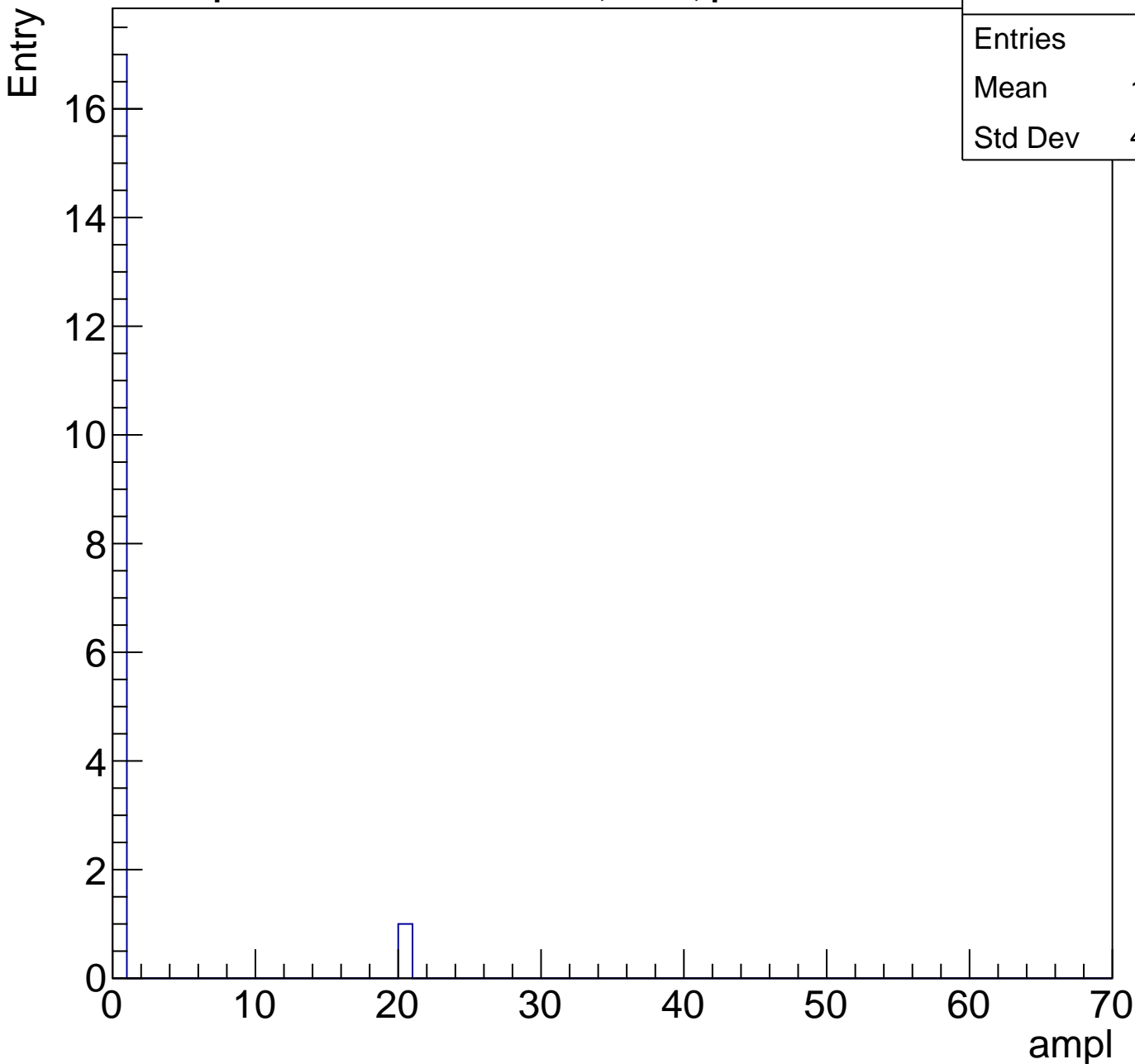


Entries	10
Mean	61.8
Std Dev	1.166

B1L103S, U6-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.111
Std Dev	4.581



B1L103S, U6-ch36, adc0

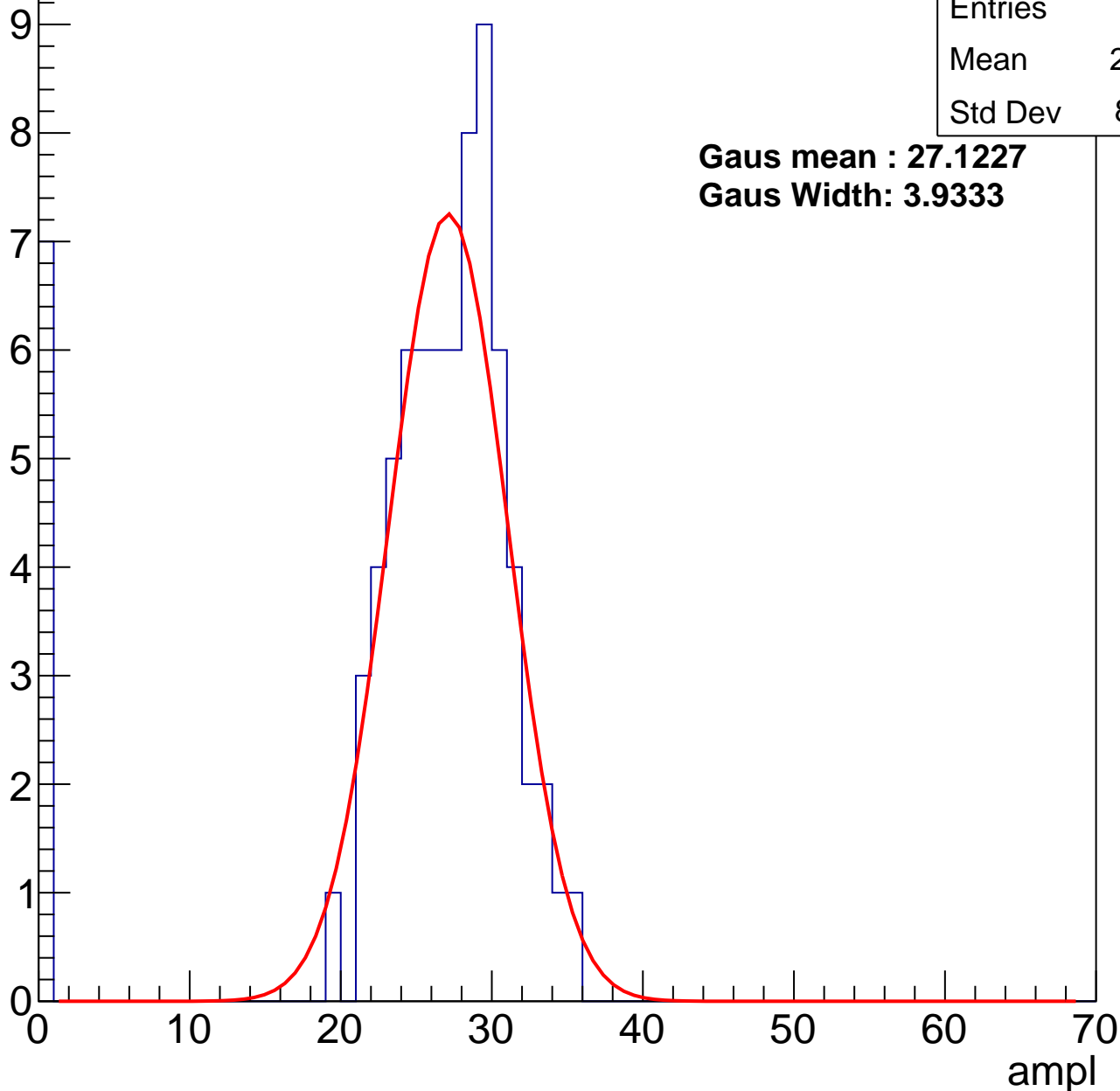
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	24.48
Std Dev	8.411

Gaus mean : 27.1227

Gaus Width: 3.9333



B1L103S, U6-ch36, adc1

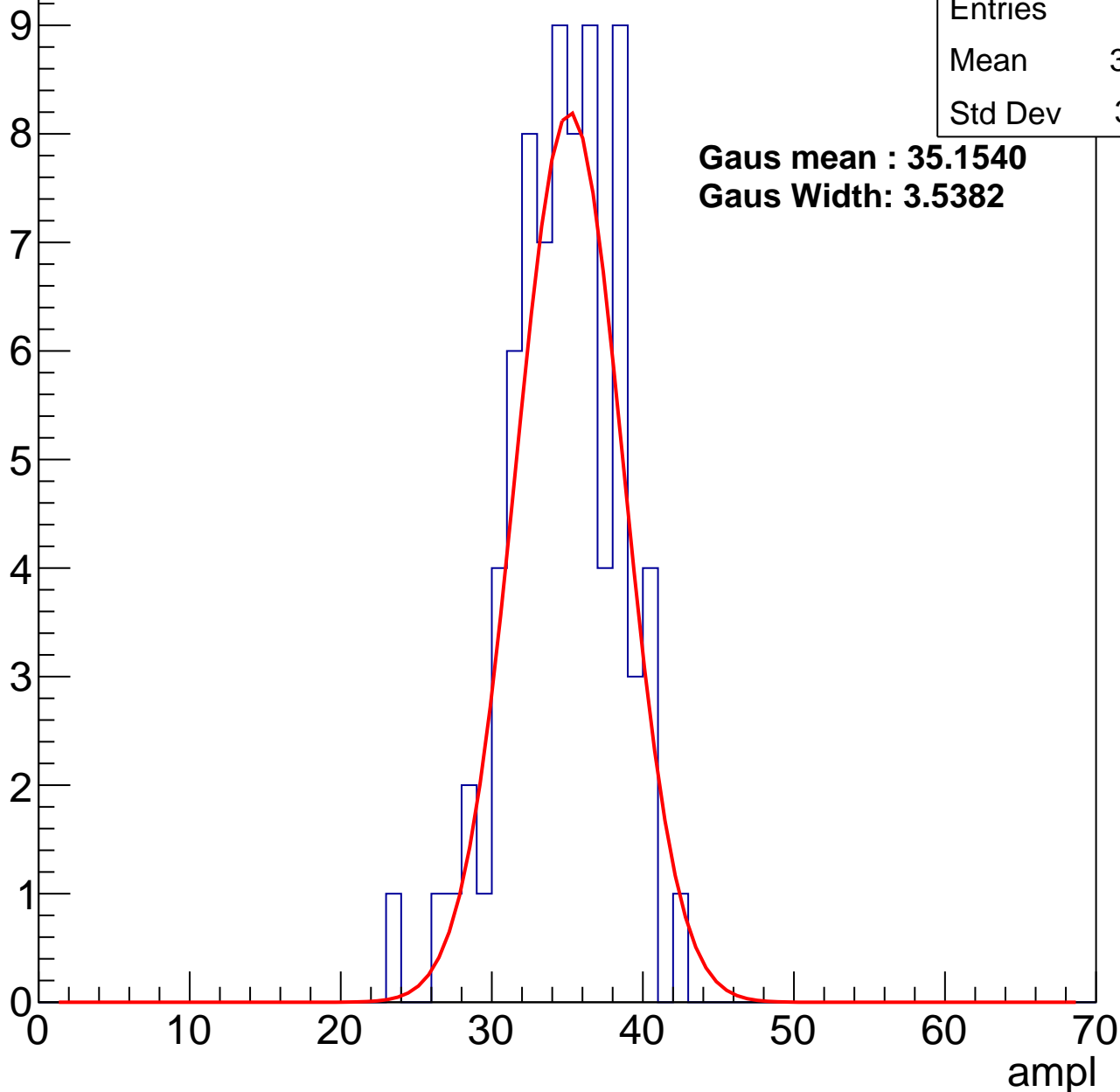
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.27
Std Dev	3.561

Gaus mean : 35.1540

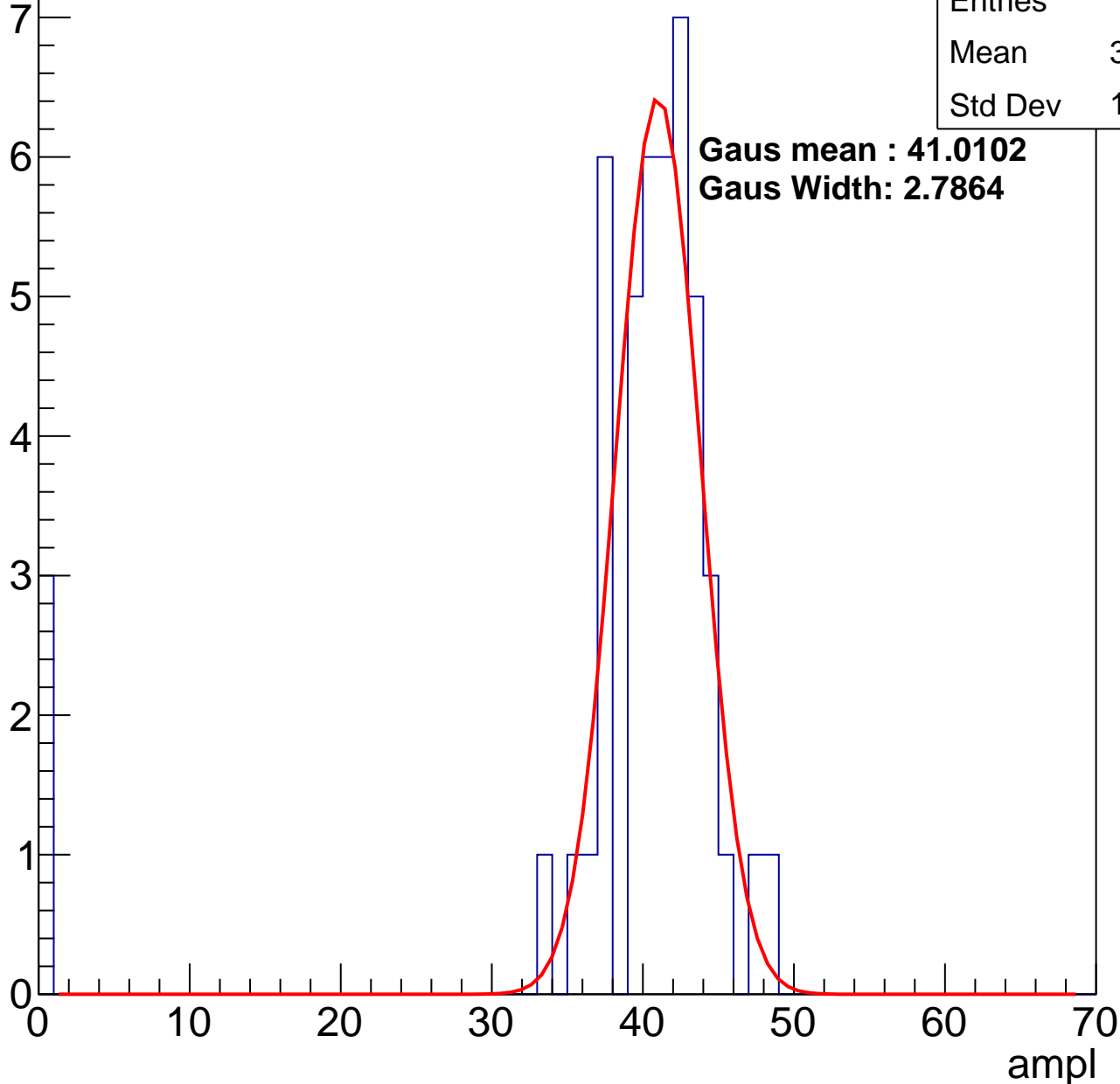
Gaus Width: 3.5382



B1L103S, U6-ch36, adc2

calib_packv5_041523_1651.root, FC#0, port C2

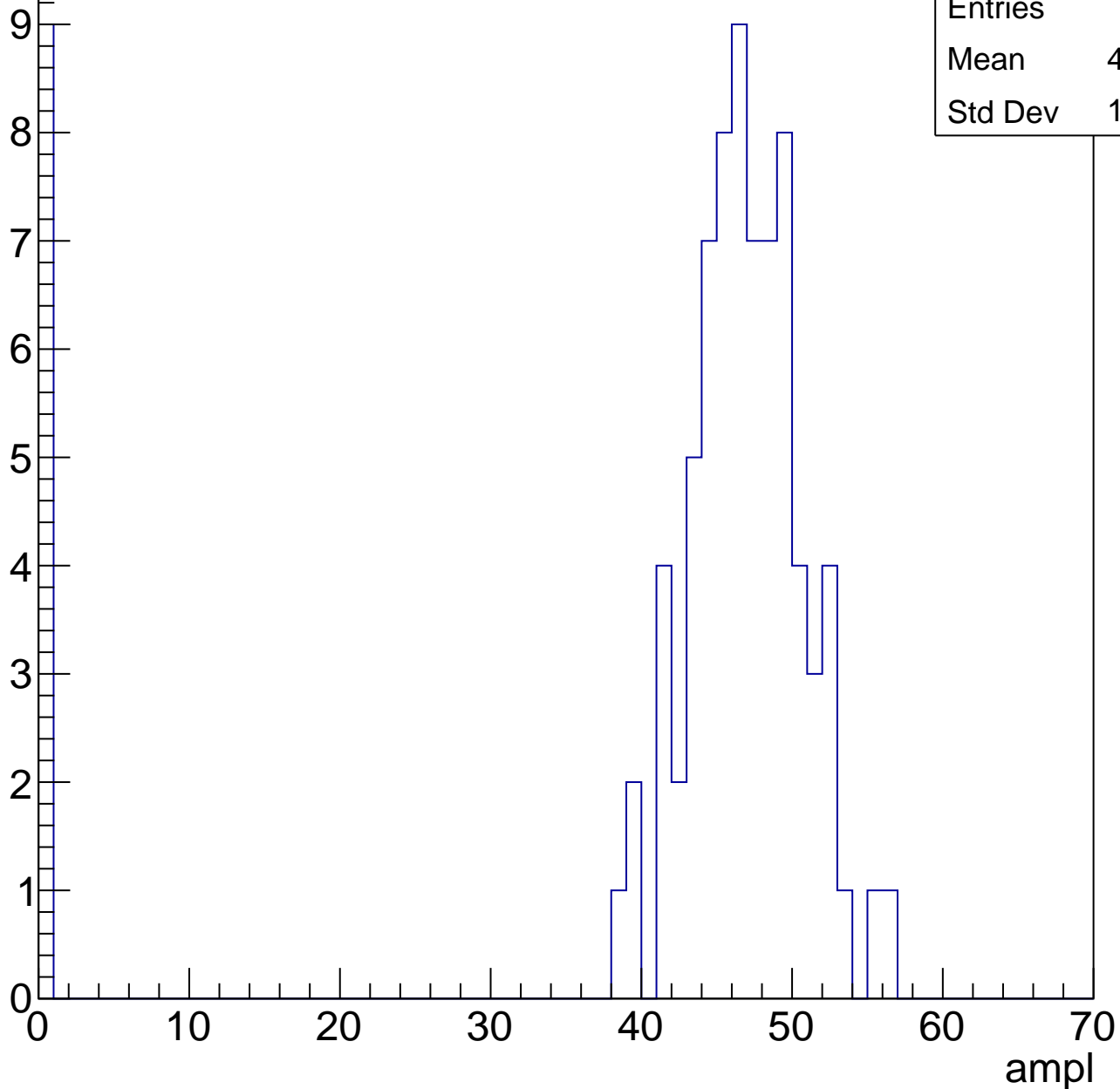
Entry



B1L103S, U6-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



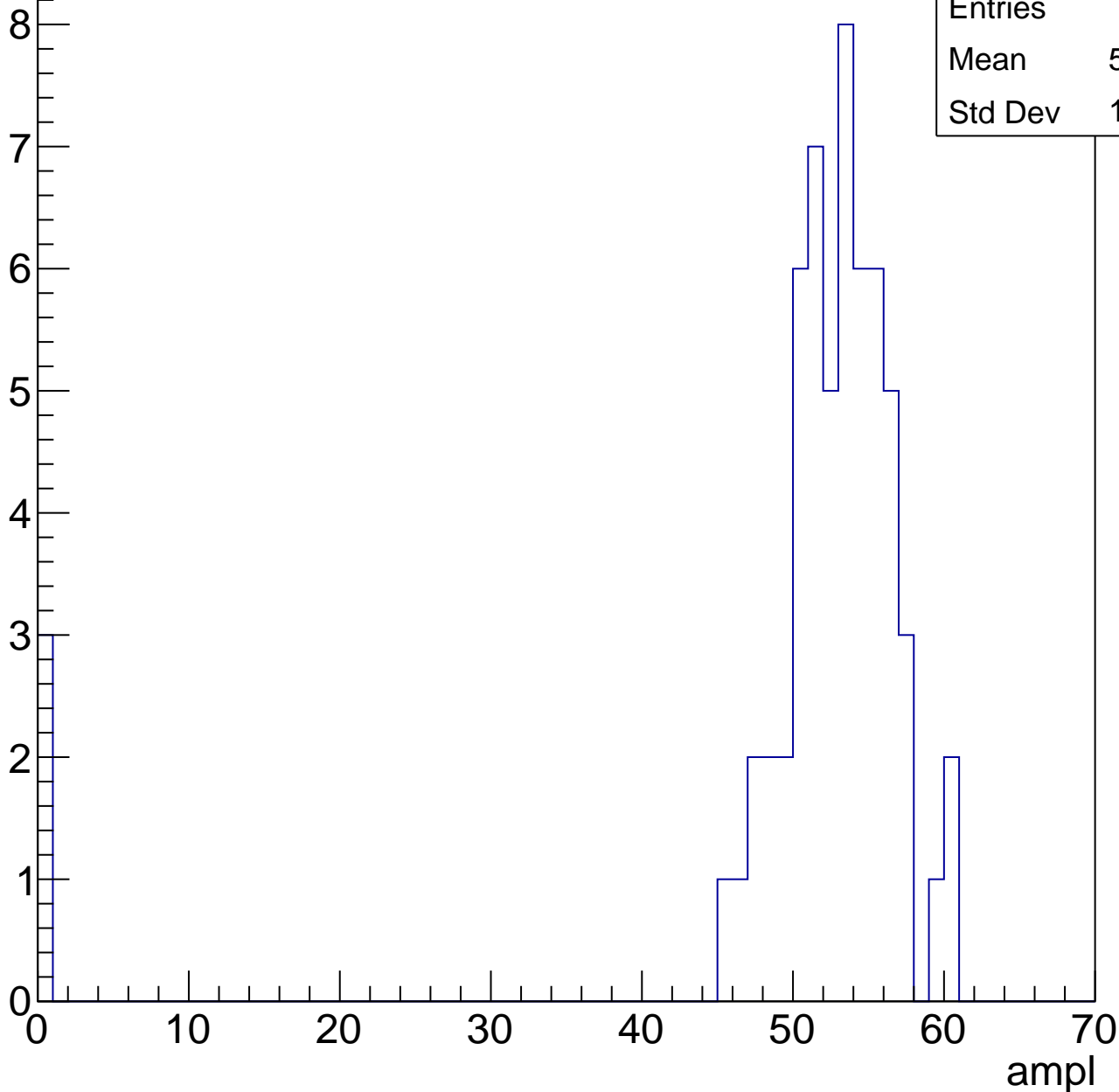
Entries	83
Mean	41.48
Std Dev	14.87

B1L103S, U6-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	50.07
Std Dev	11.92



B1L103S, U6-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 70

Mean 57.23

Std Dev 10.18

8

6

4

2

0

ampl

0

10

20

30

40

50

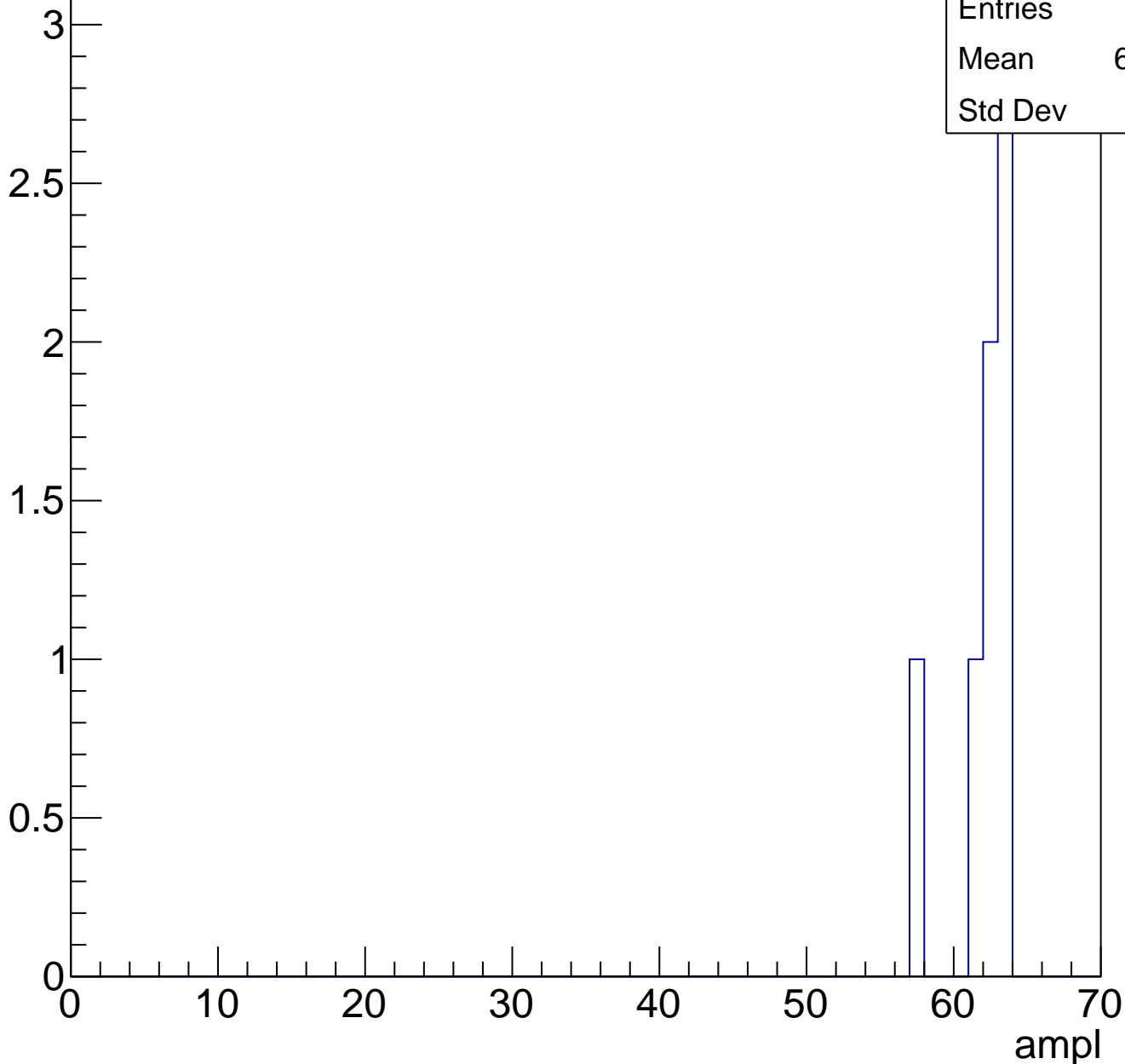
60

70

B1L103S, U6-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

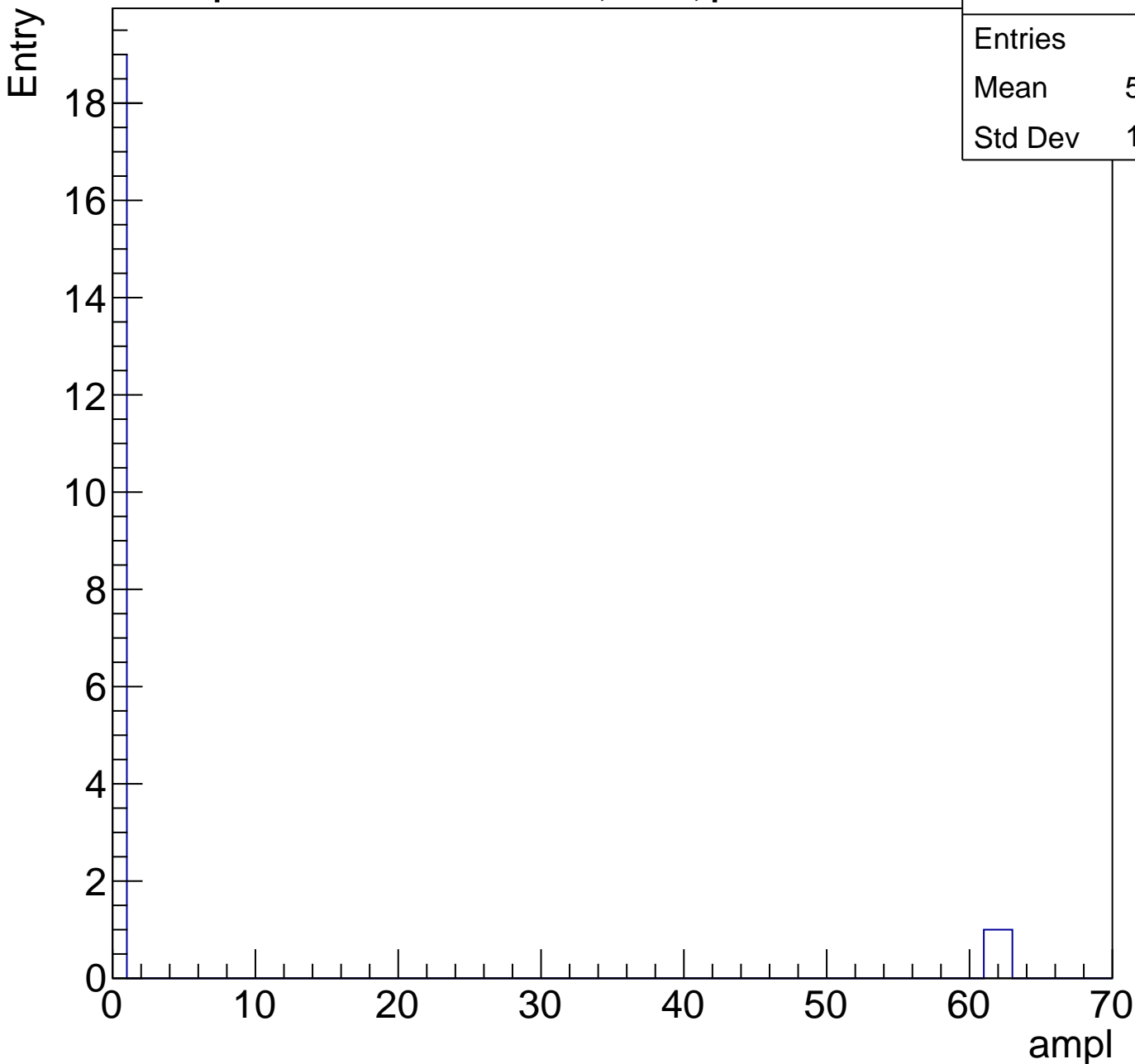
Entry



B1L103S, U6-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.857
Std Dev	18.05



B1L103S, U6-ch37, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	23.36
Std Dev	12.1

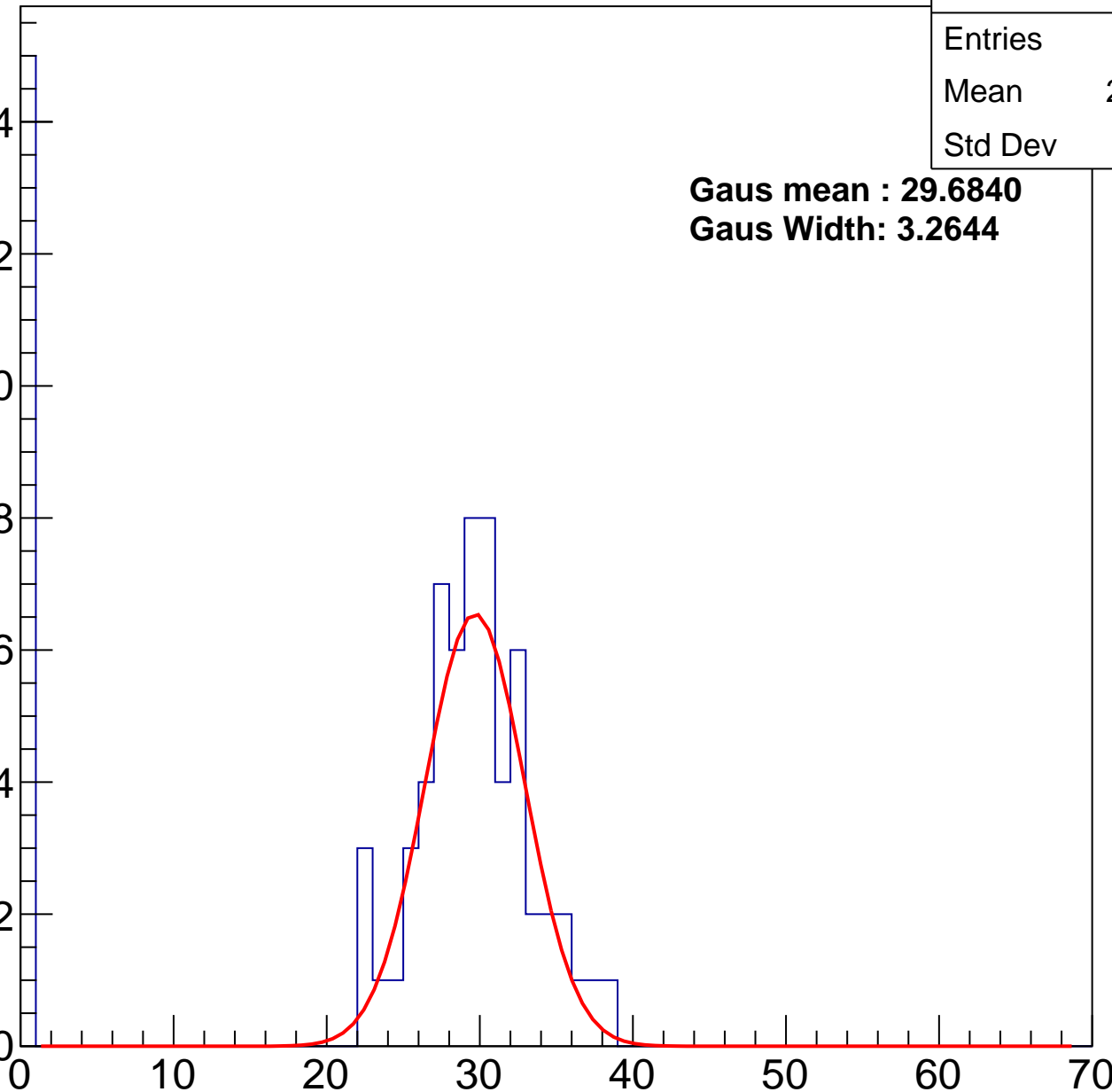
Gaus mean : 29.6840

Gaus Width: 3.2644

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch37, adc1

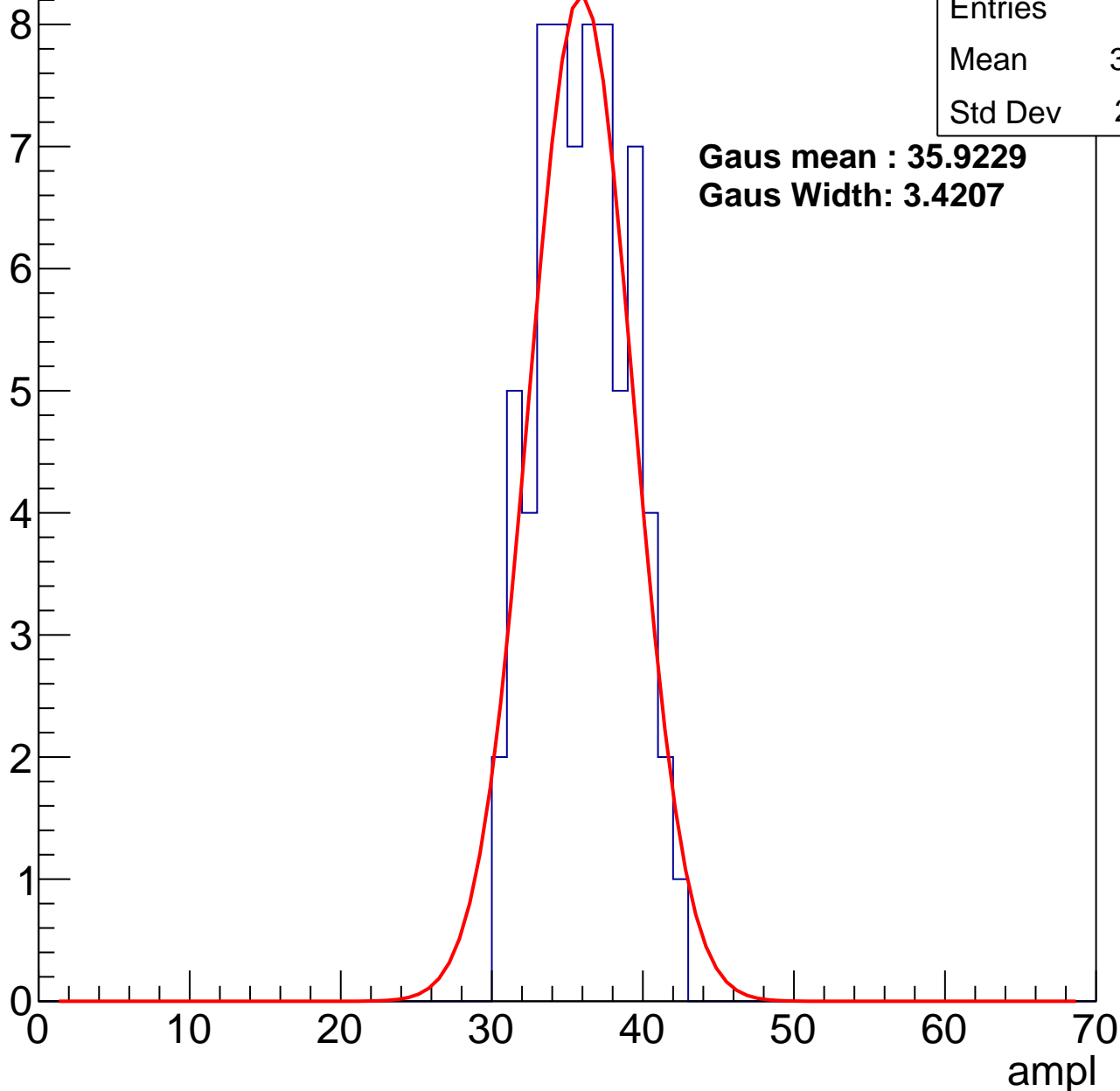
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.58
Std Dev	2.951

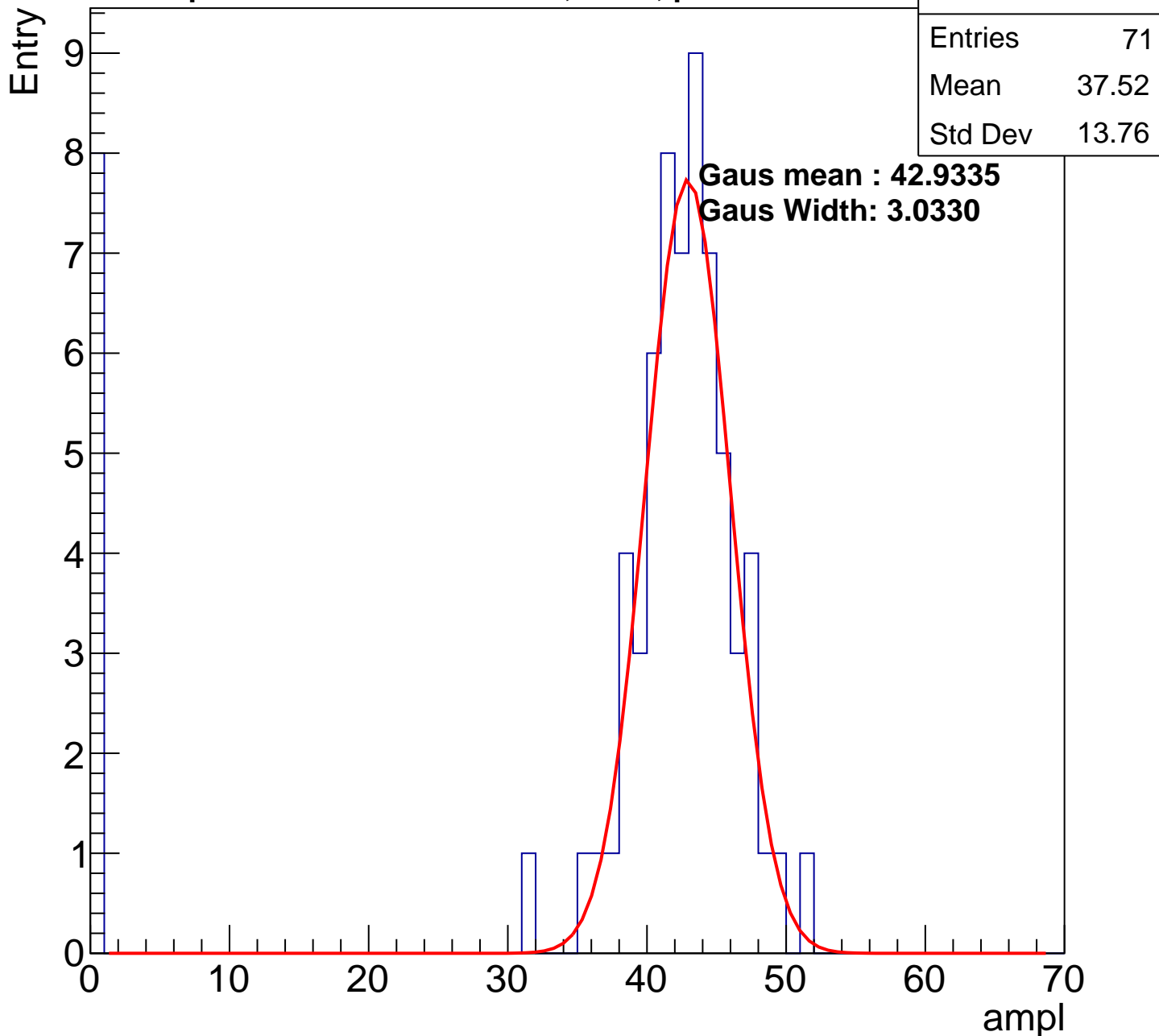
Gaus mean : 35.9229

Gaus Width: 3.4207



B1L103S, U6-ch37, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	47.54
Std Dev	9.852

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

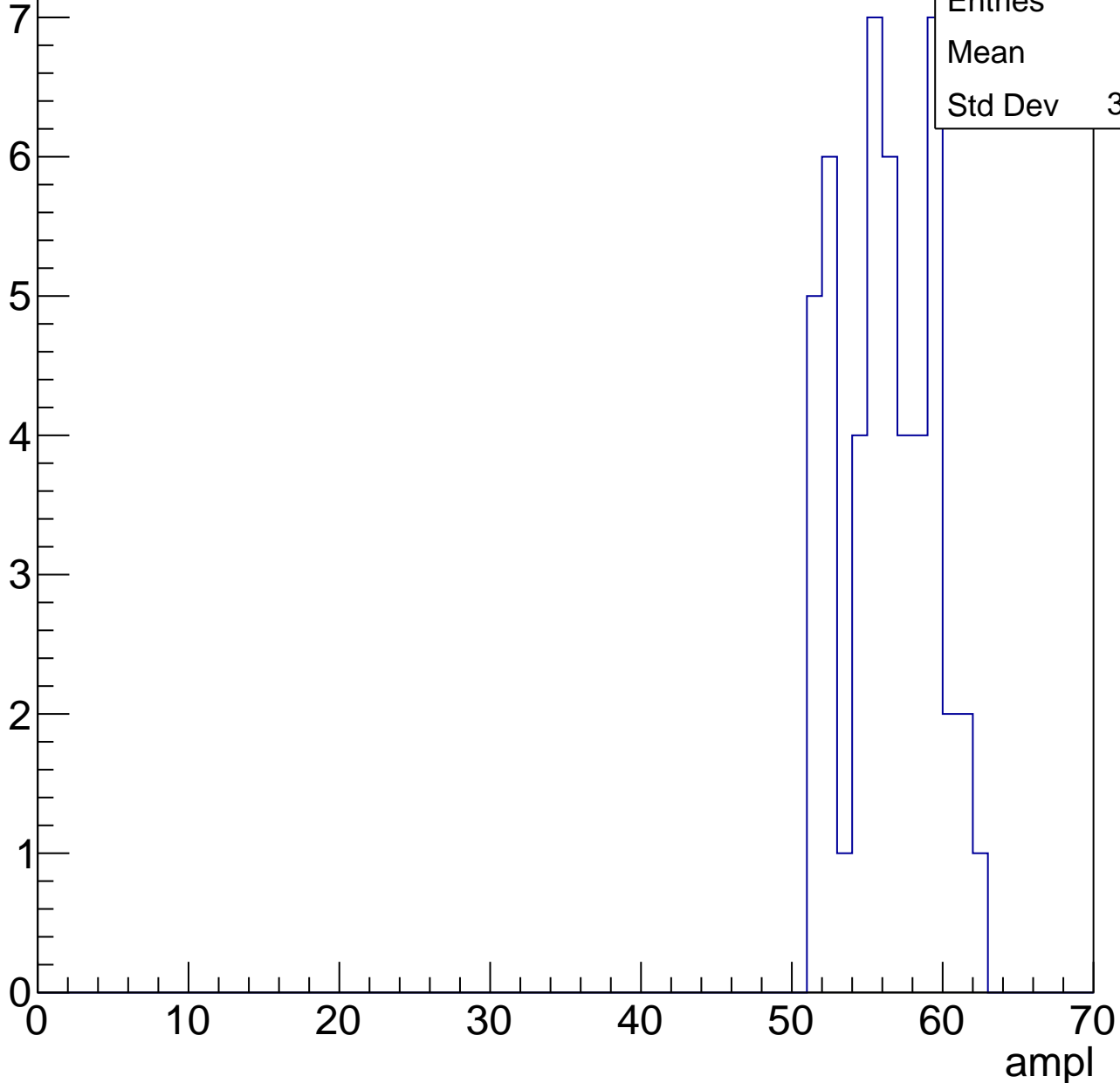
70

B1L103S, U6-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.8
Std Dev	3.037

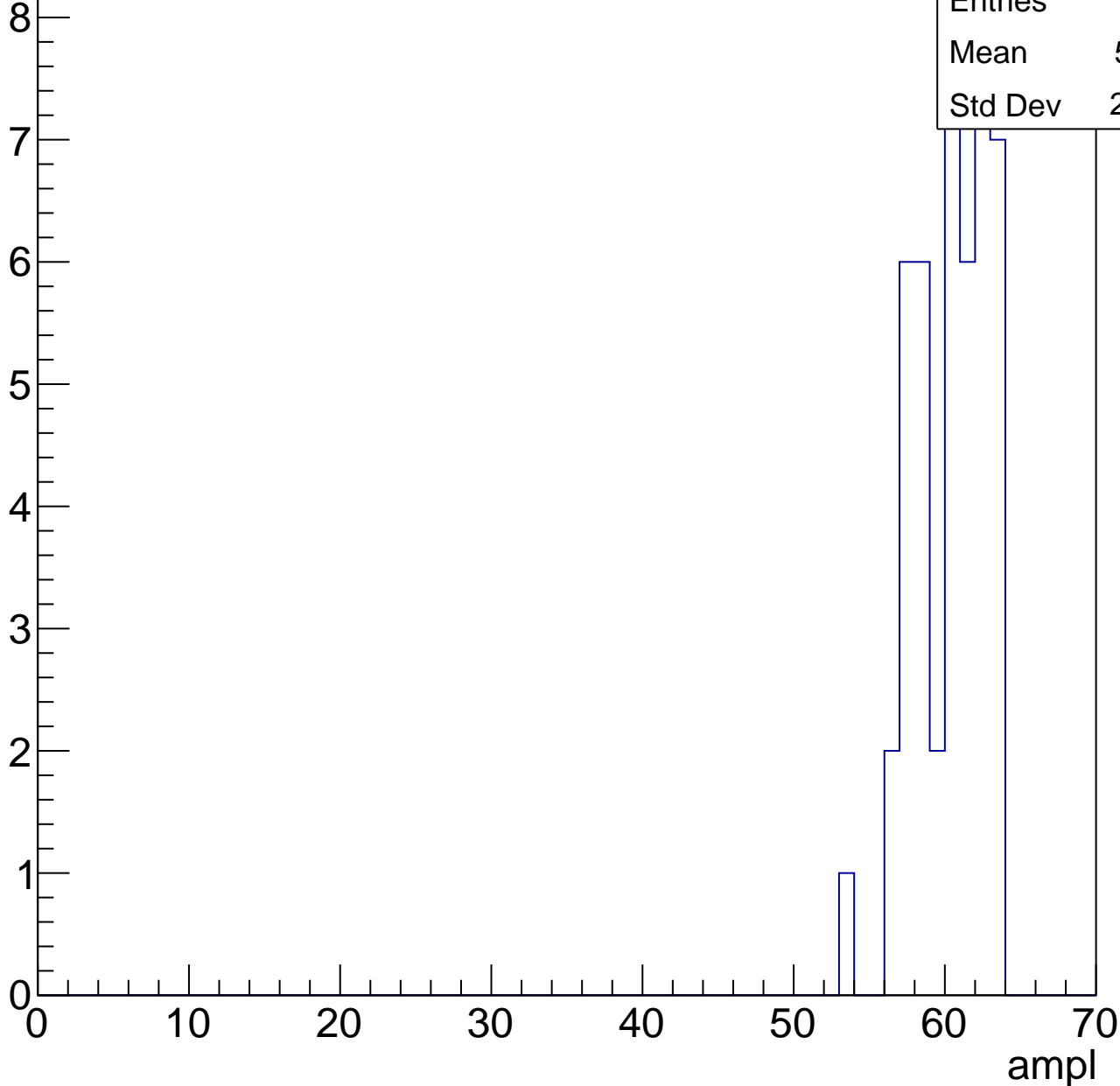


B1L103S, U6-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

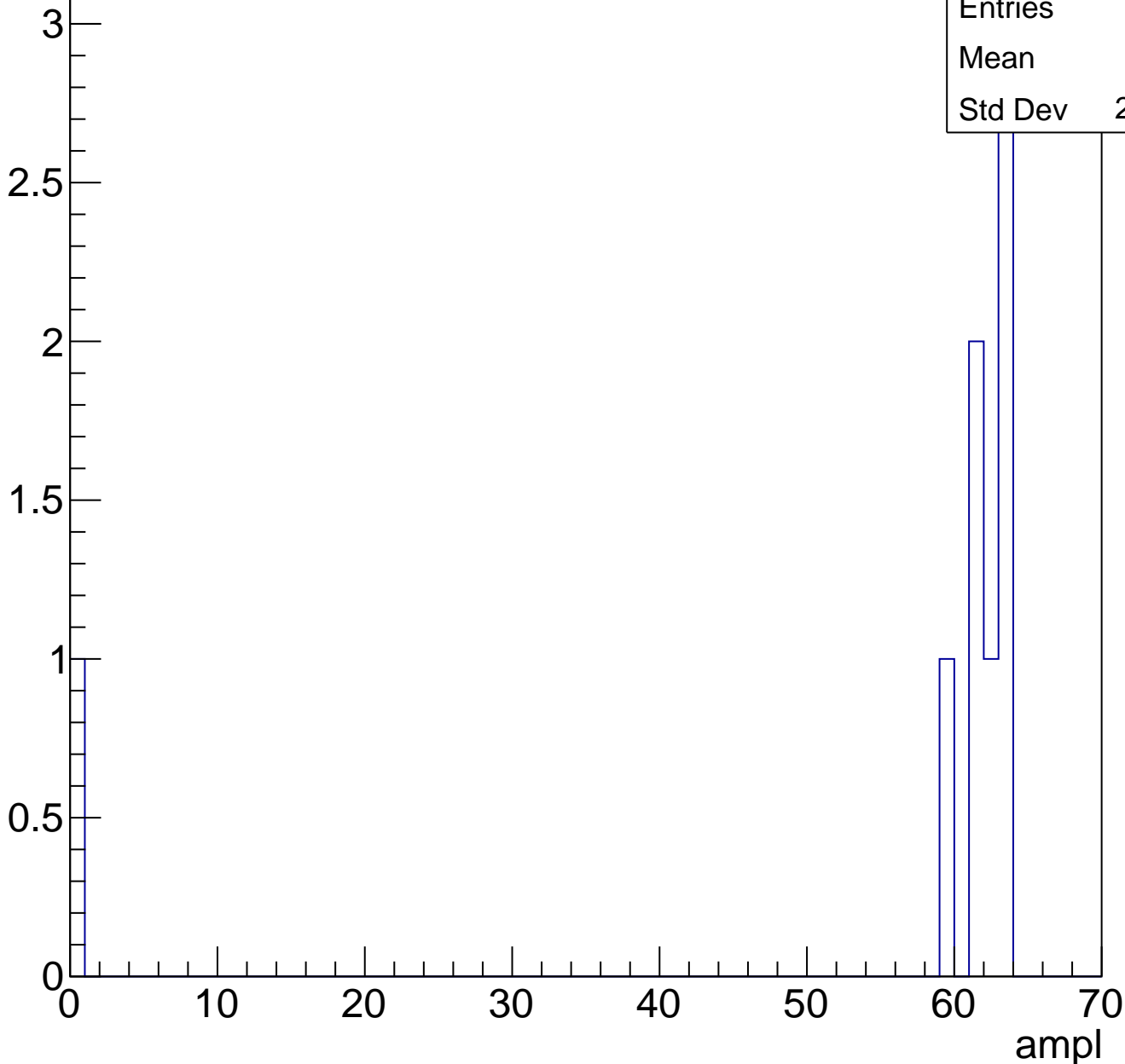
Entries	46
Mean	59.91
Std Dev	2.385



B1L103S, U6-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

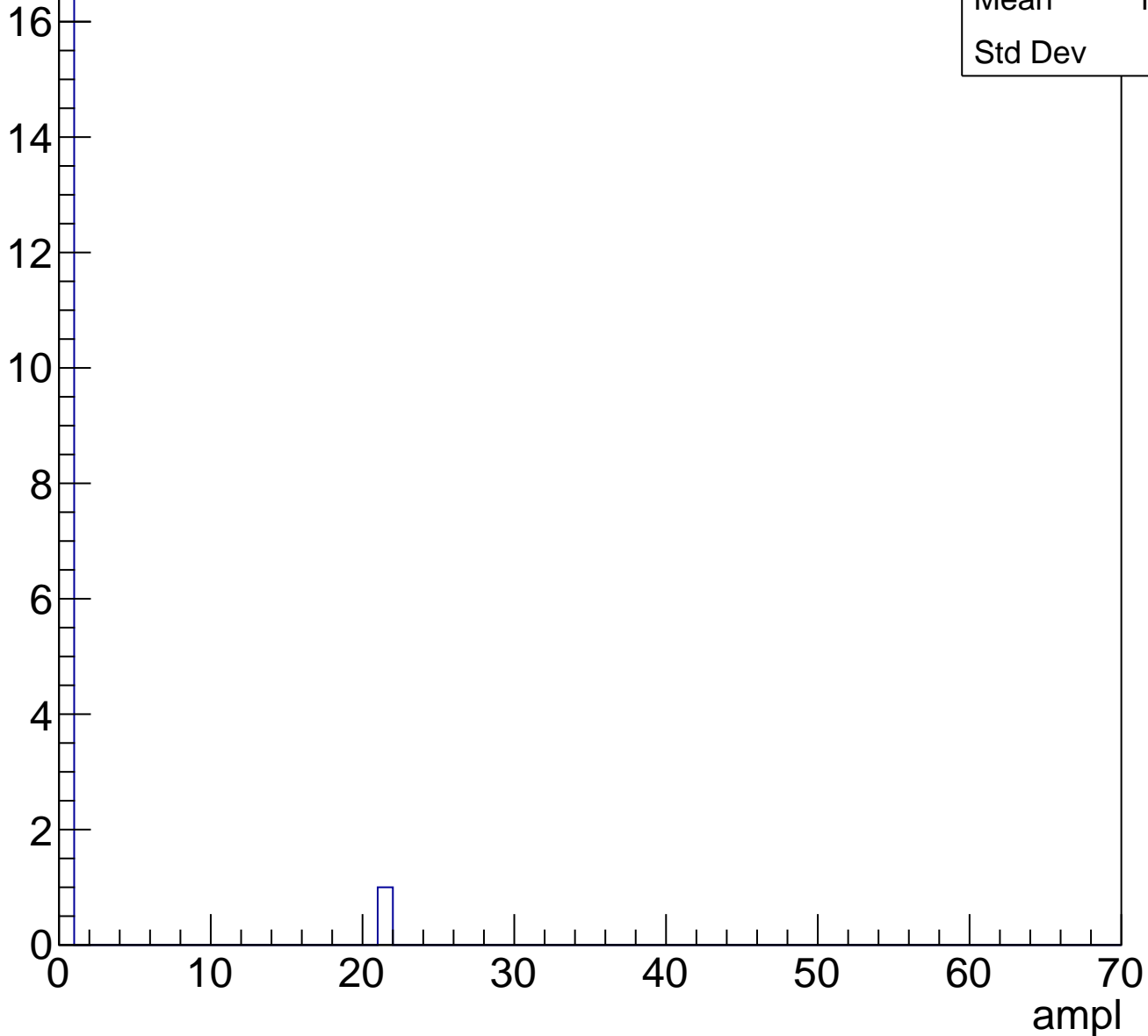
Entry



B1L103S, U6-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.167
Std Dev	4.81

B1L103S, U6-ch38, adc0

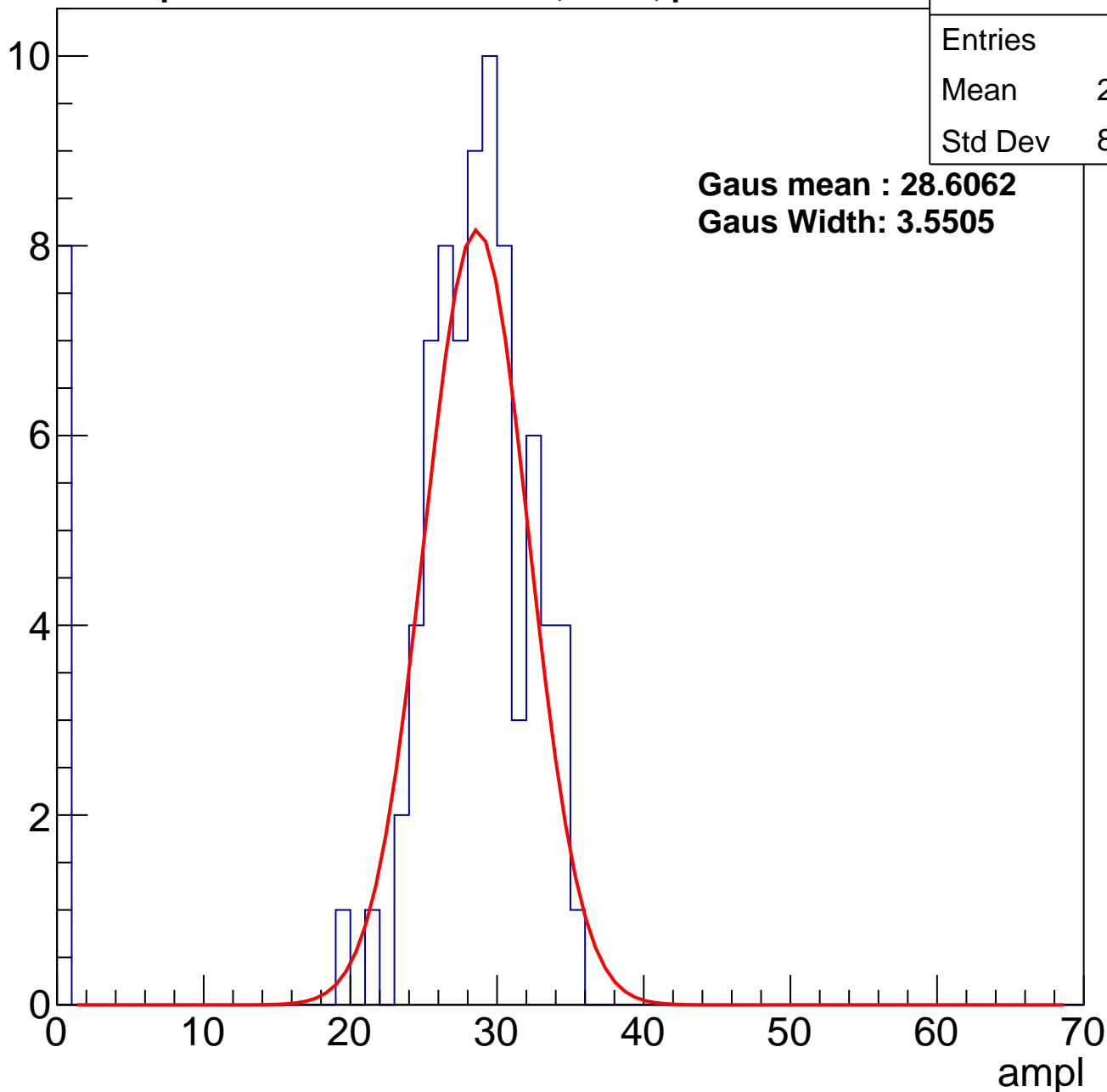
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	25.59
Std Dev	8.913

Gaus mean : 28.6062

Gaus Width: 3.5505

Entry



B1L103S, U6-ch38, adc1

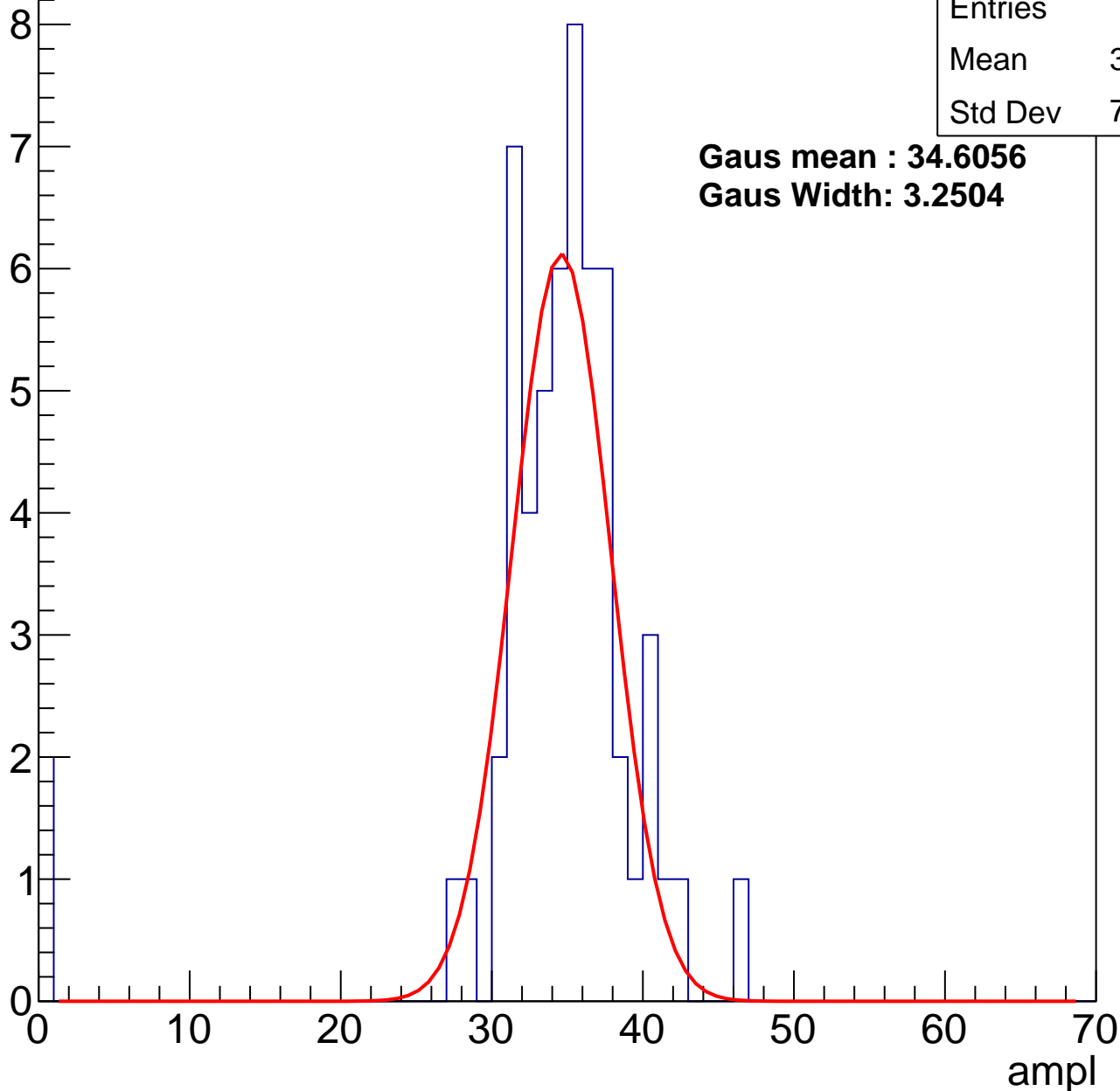
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	33.53
Std Dev	7.265

Gaus mean : 34.6056

Gaus Width: 3.2504



B1L103S, U6-ch38, adc2

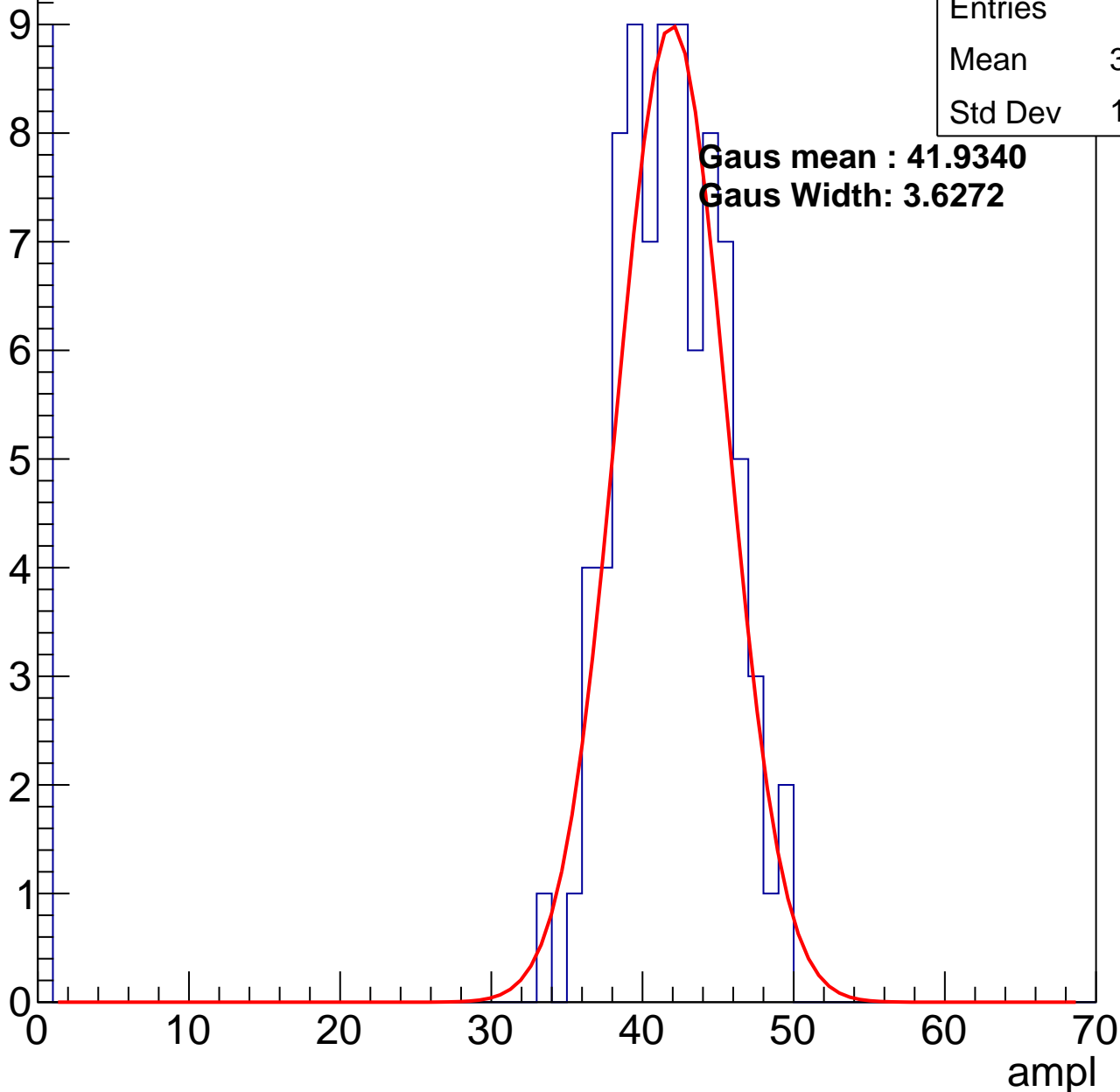
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	93
Mean	37.46
Std Dev	12.69

Gaus mean : 41.9340

Gaus Width: 3.6272

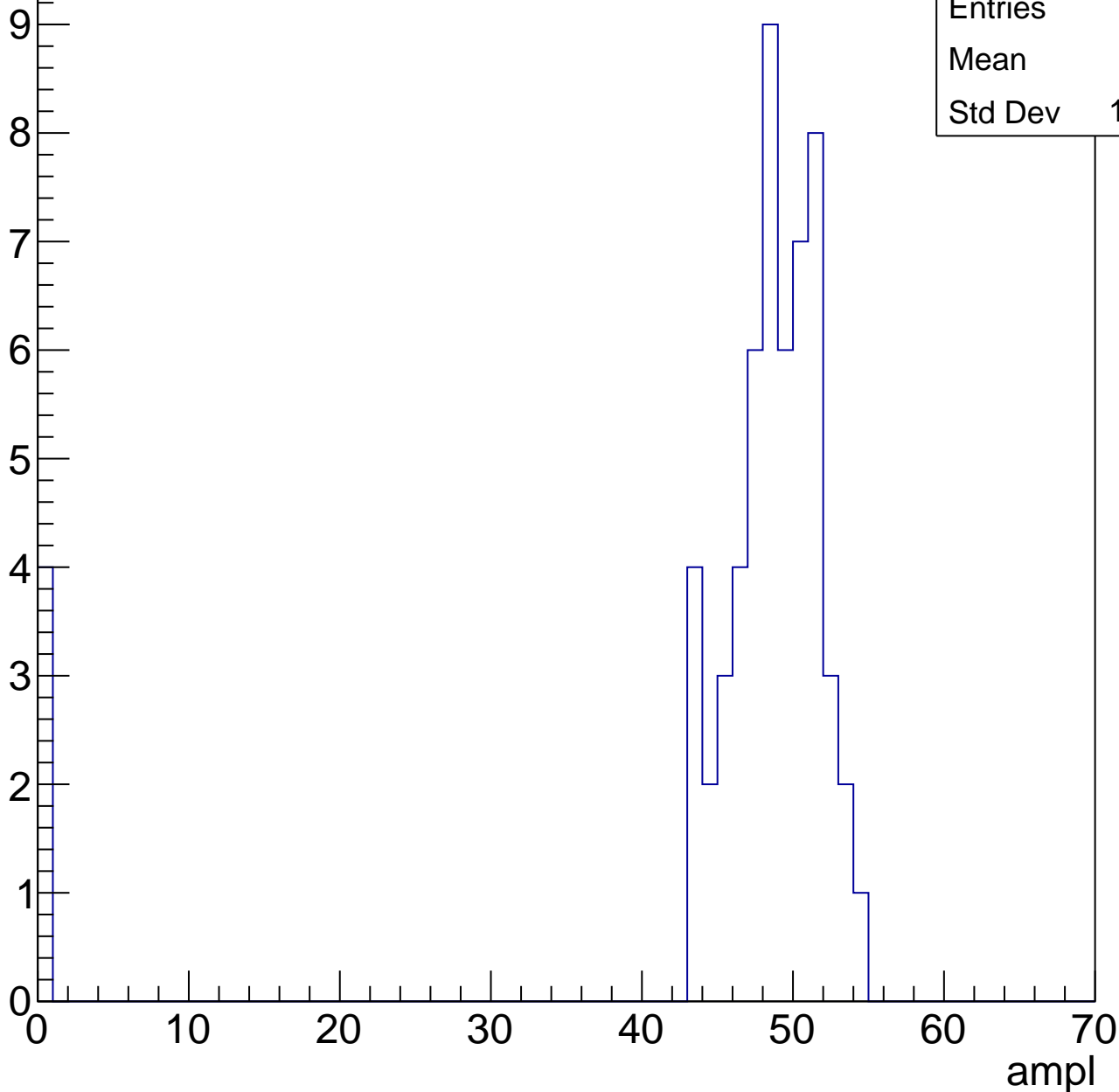


B1L103S, U6-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

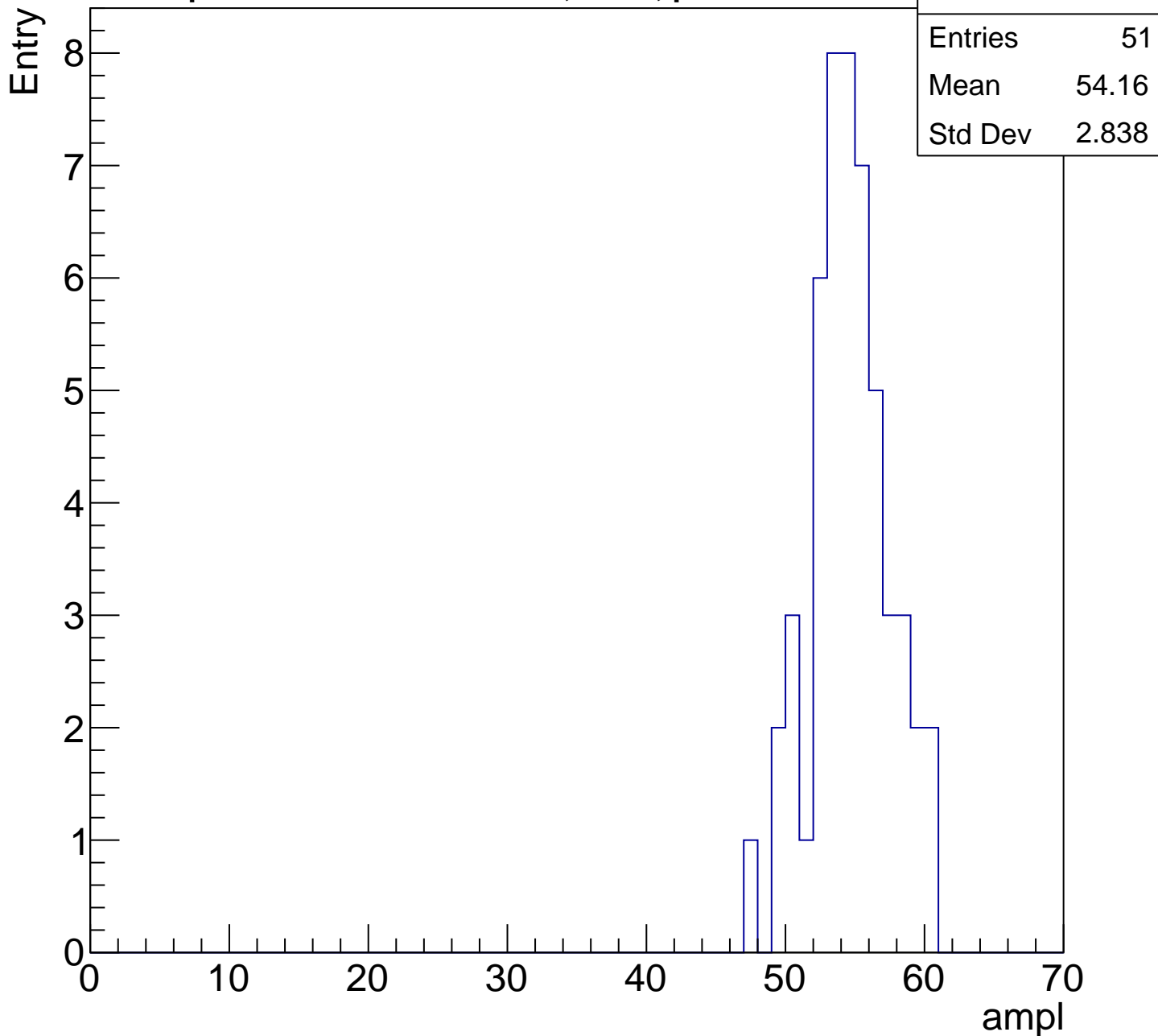
Entry

Entries	59
Mean	45.1
Std Dev	12.45



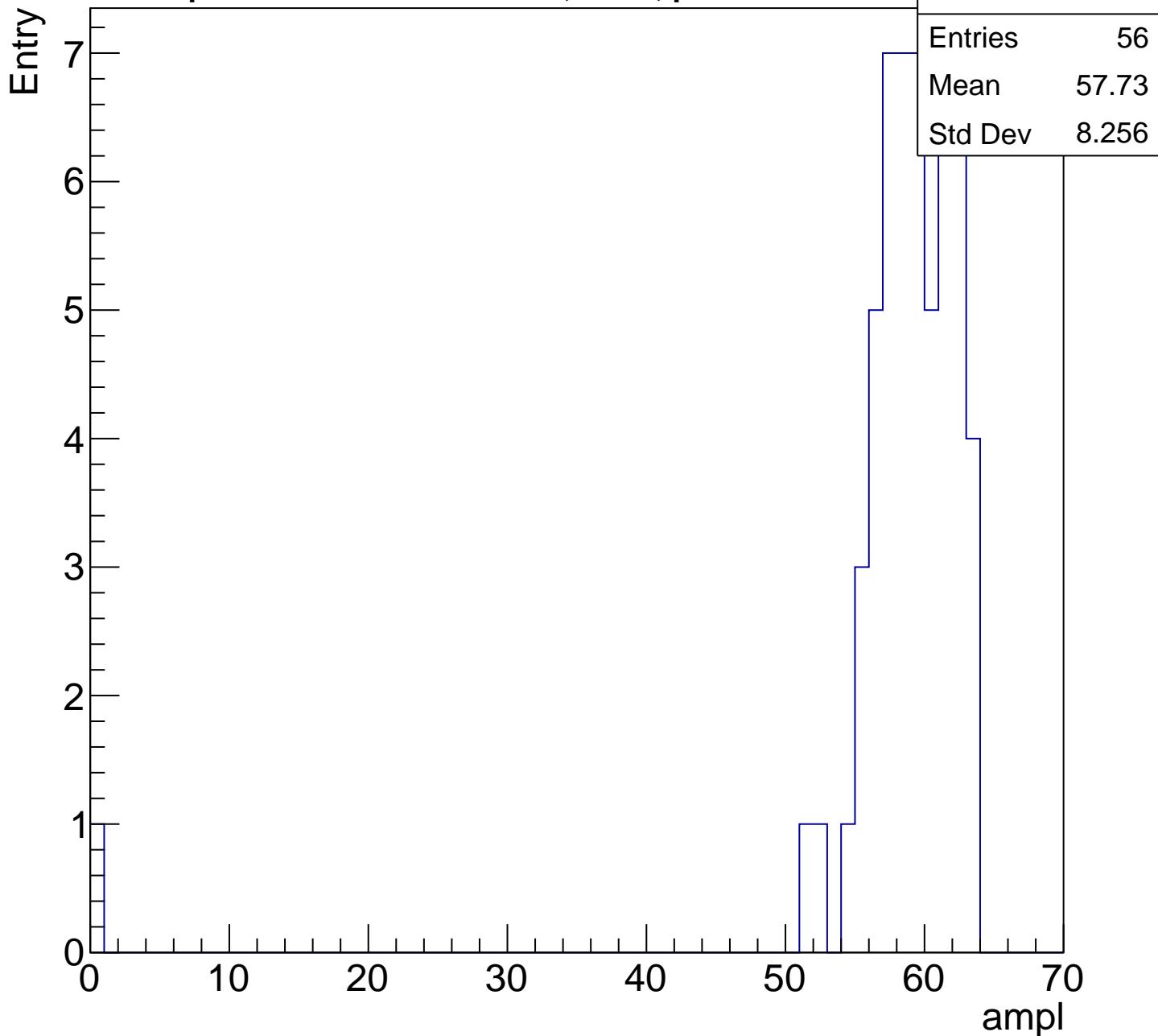
B1L103S, U6-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

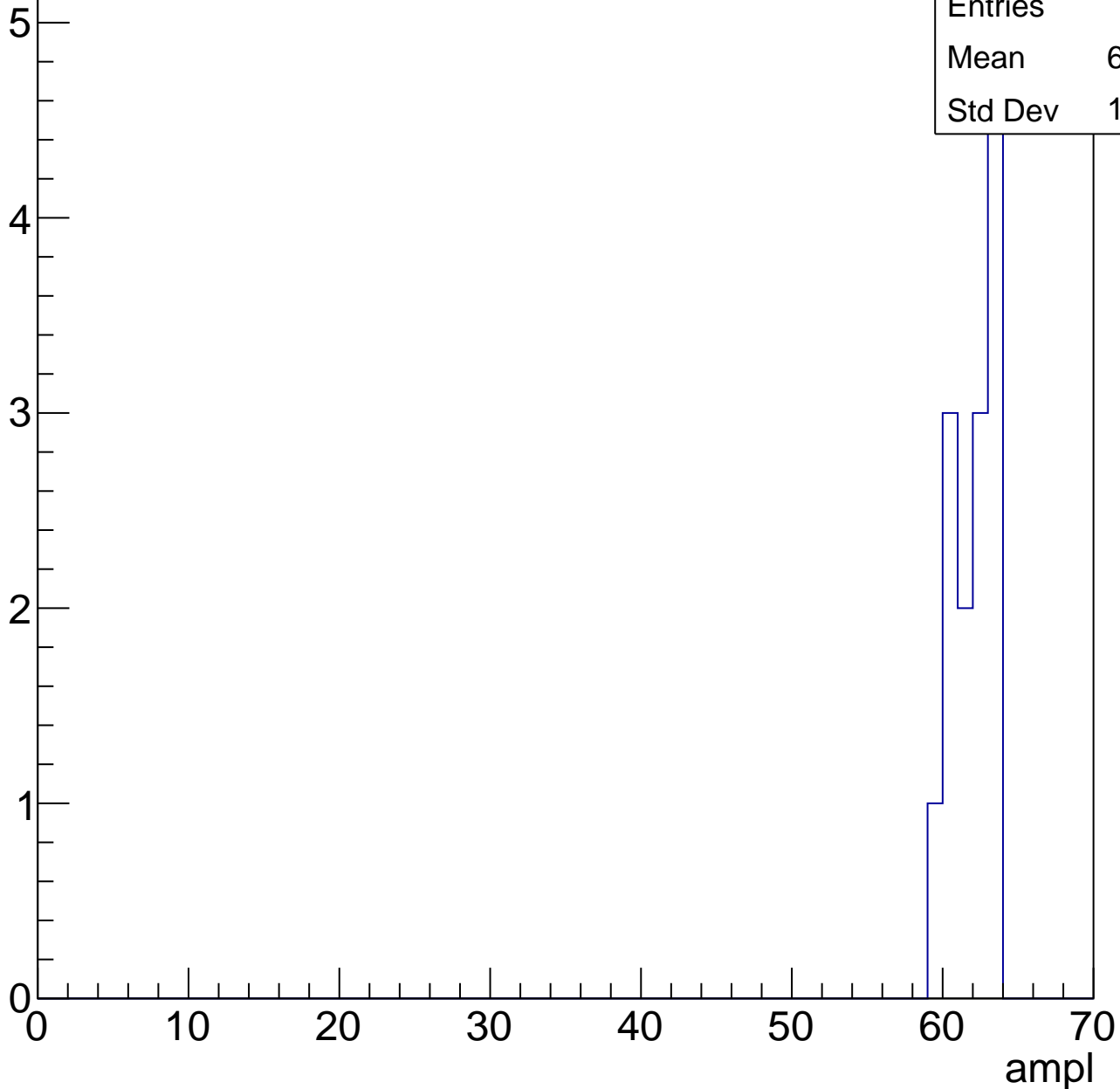


B1L103S, U6-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

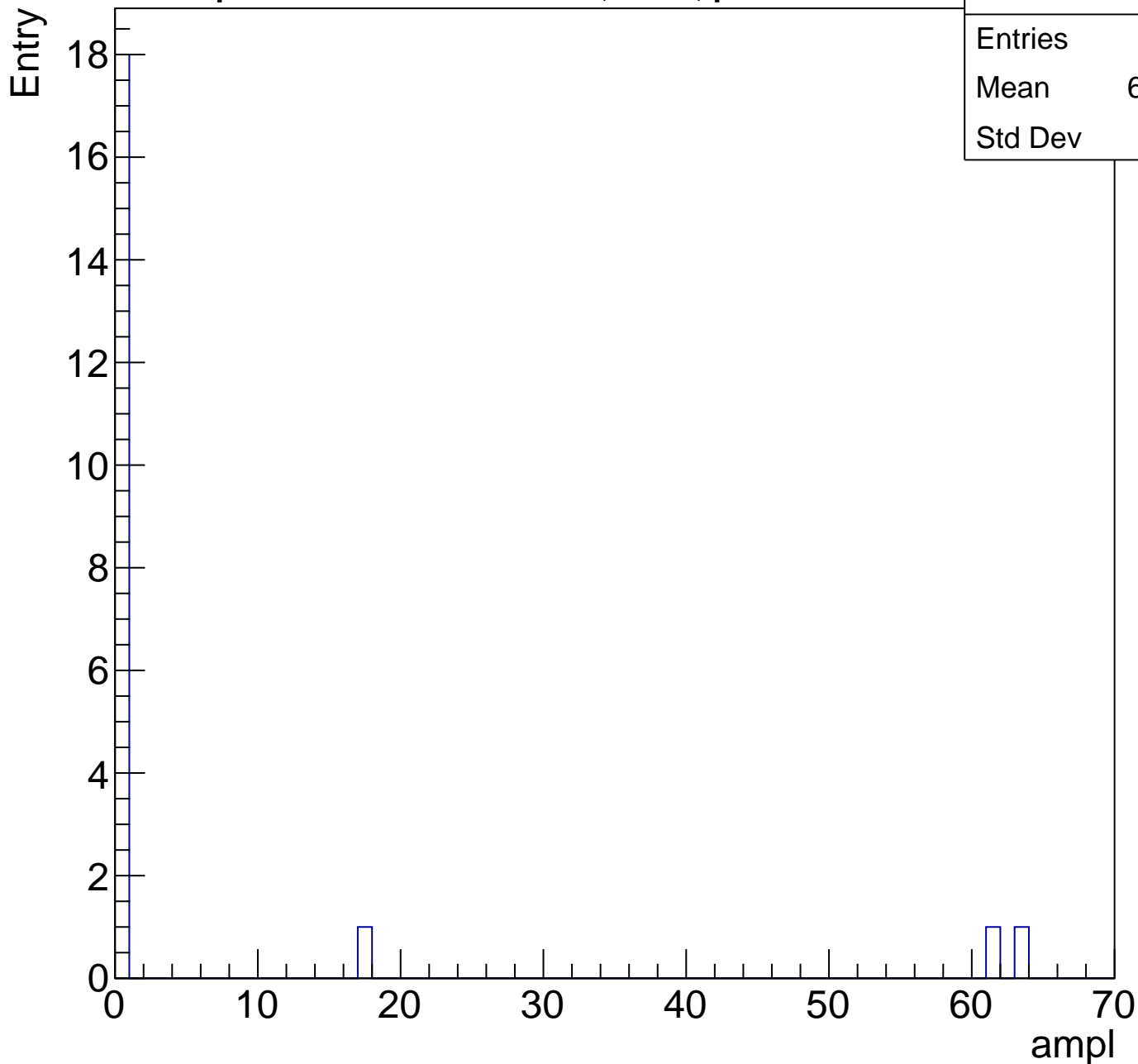
Entries	14
Mean	61.57
Std Dev	1.348



B1L103S, U6-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.714
Std Dev	18.3



B1L103S, U6-ch39, adc0

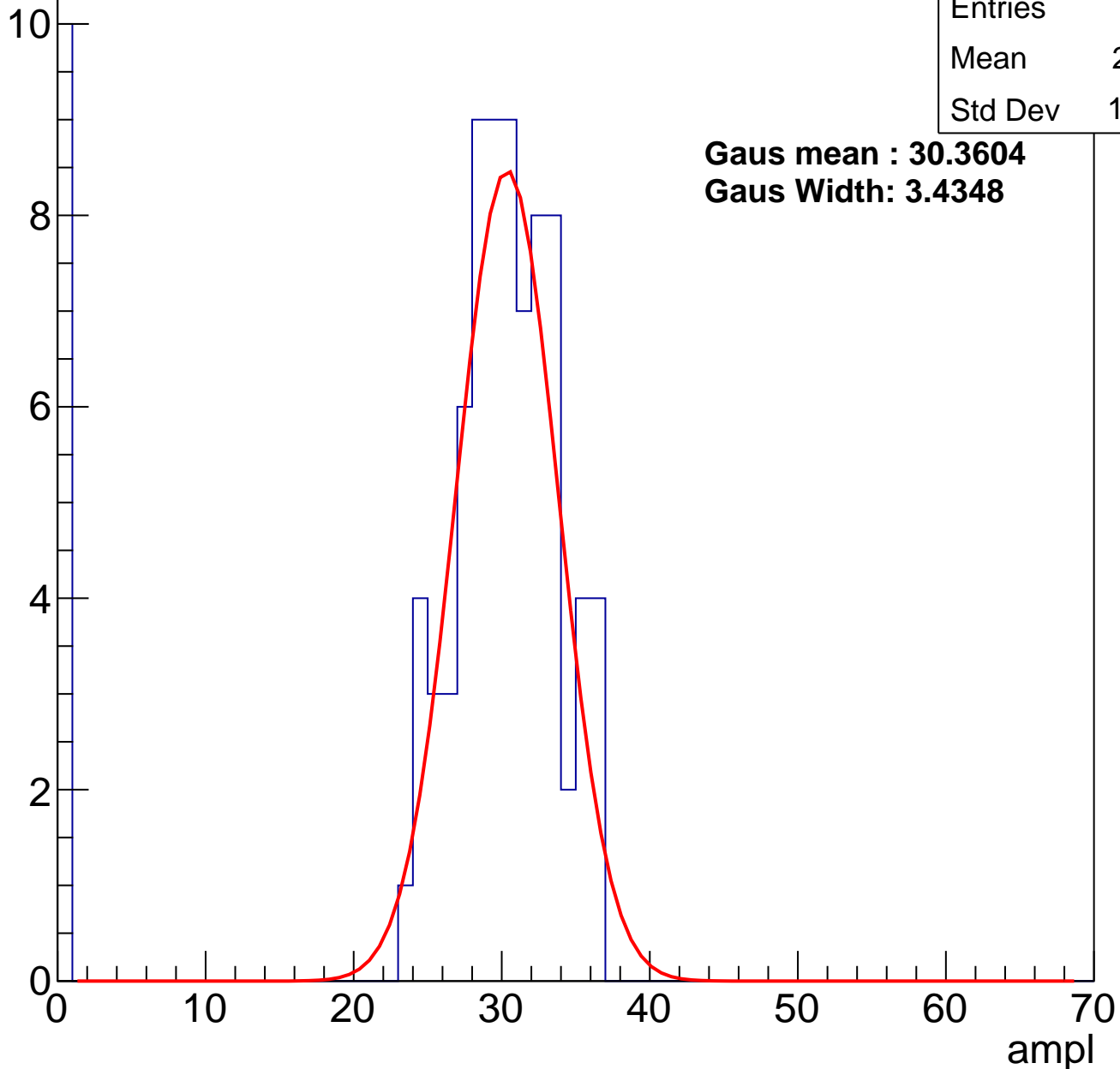
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	26.51
Std Dev	10.02

Gaus mean : 30.3604

Gaus Width: 3.4348

Entry



B1L103S, U6-ch39, adc1

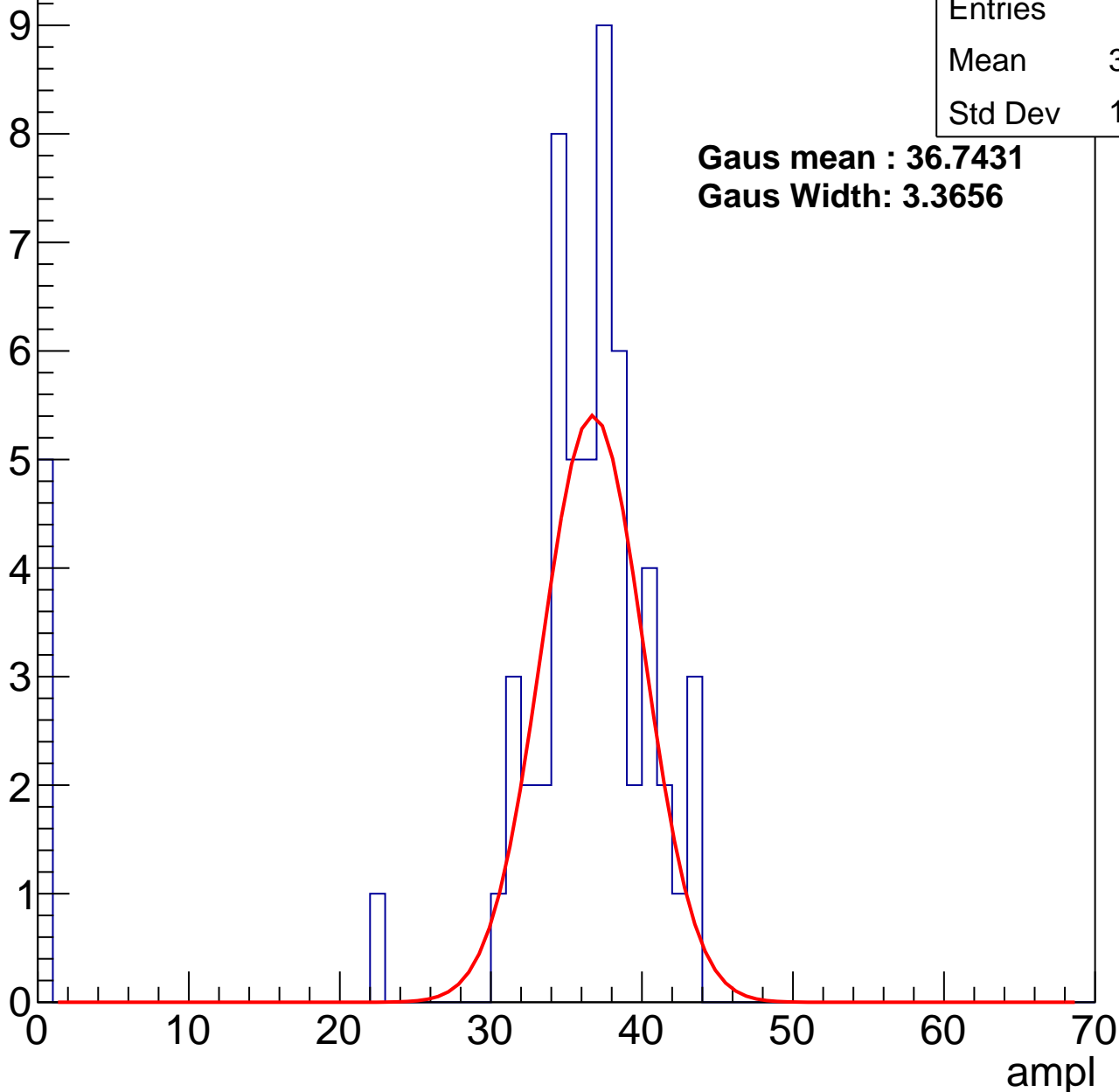
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	33.12
Std Dev	10.68

Gaus mean : 36.7431

Gaus Width: 3.3656



B1L103S, U6-ch39, adc2

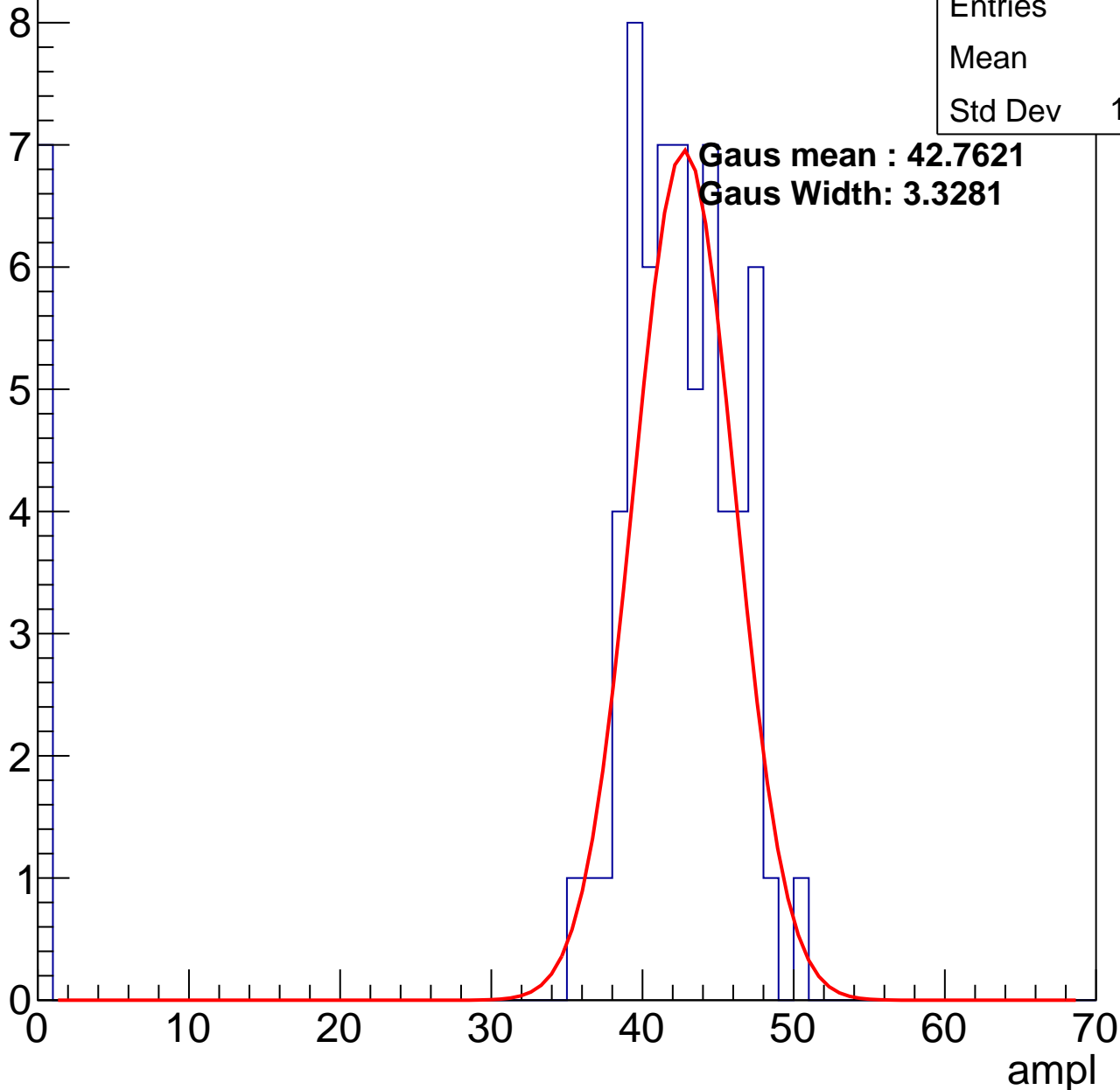
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	38
Std Dev	13.03

Gaus mean : 42.7621

Gaus Width: 3.3281

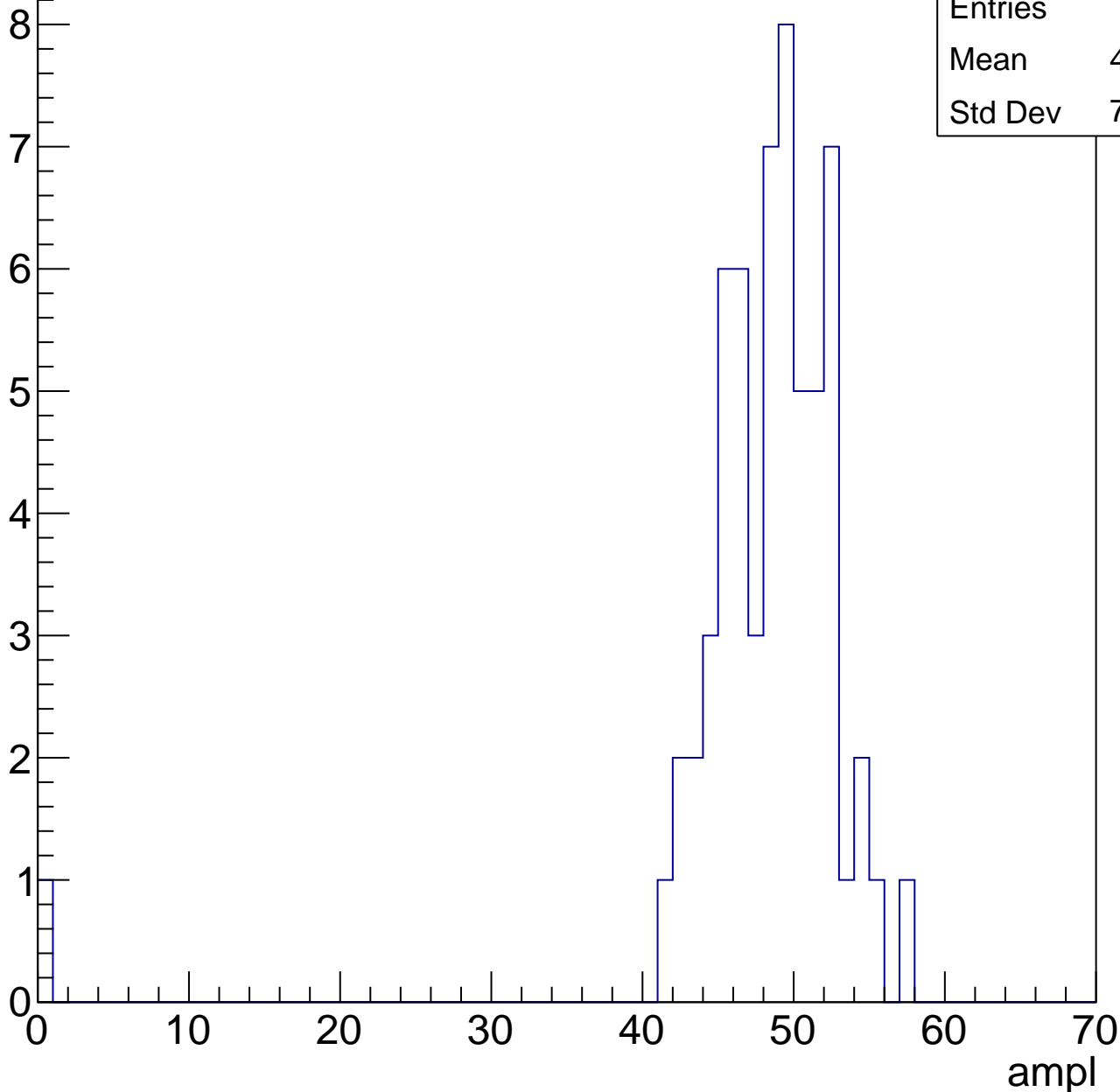


B1L103S, U6-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

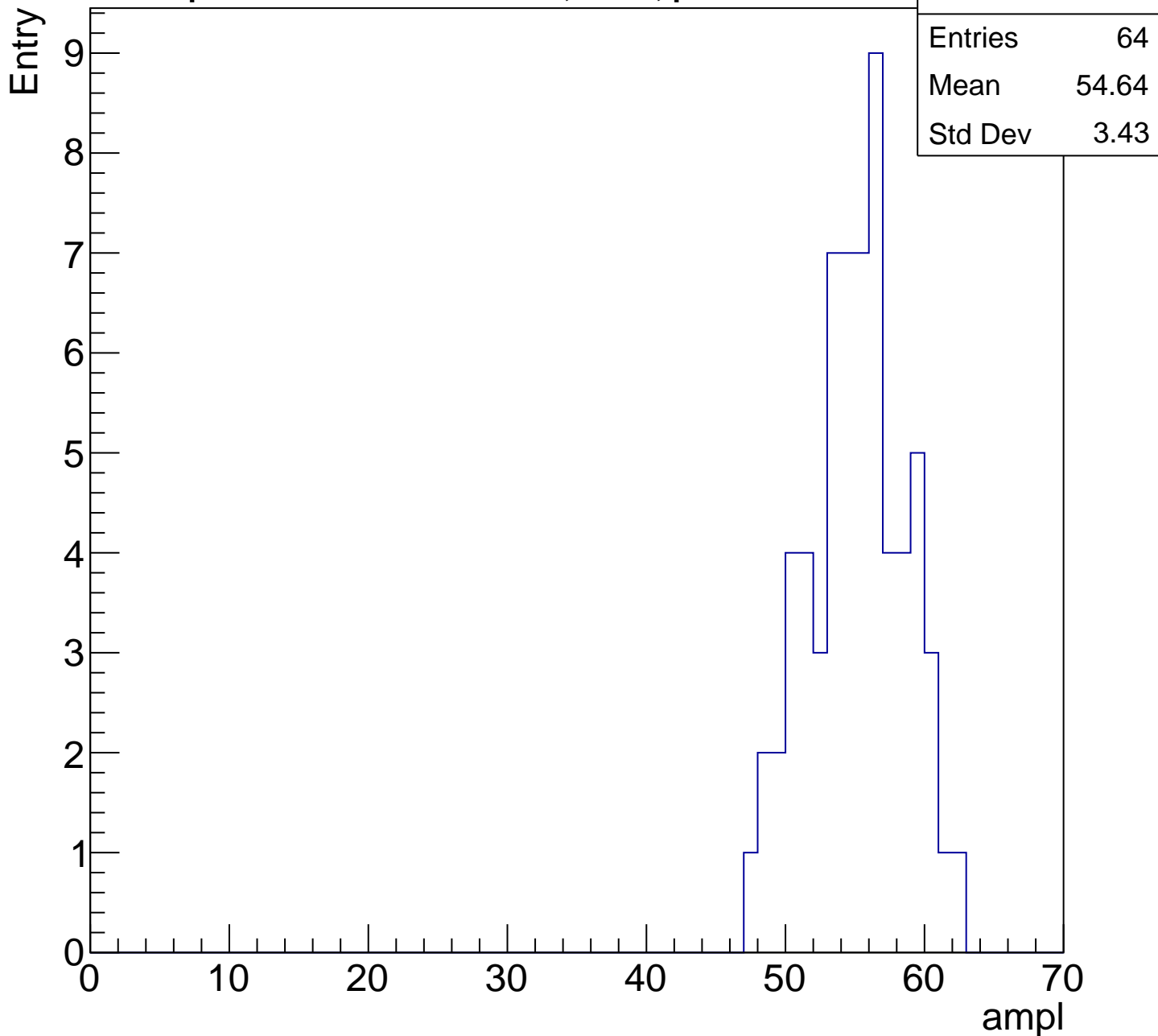
Entry

Entries	61
Mean	47.54
Std Dev	7.017



B1L103S, U6-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

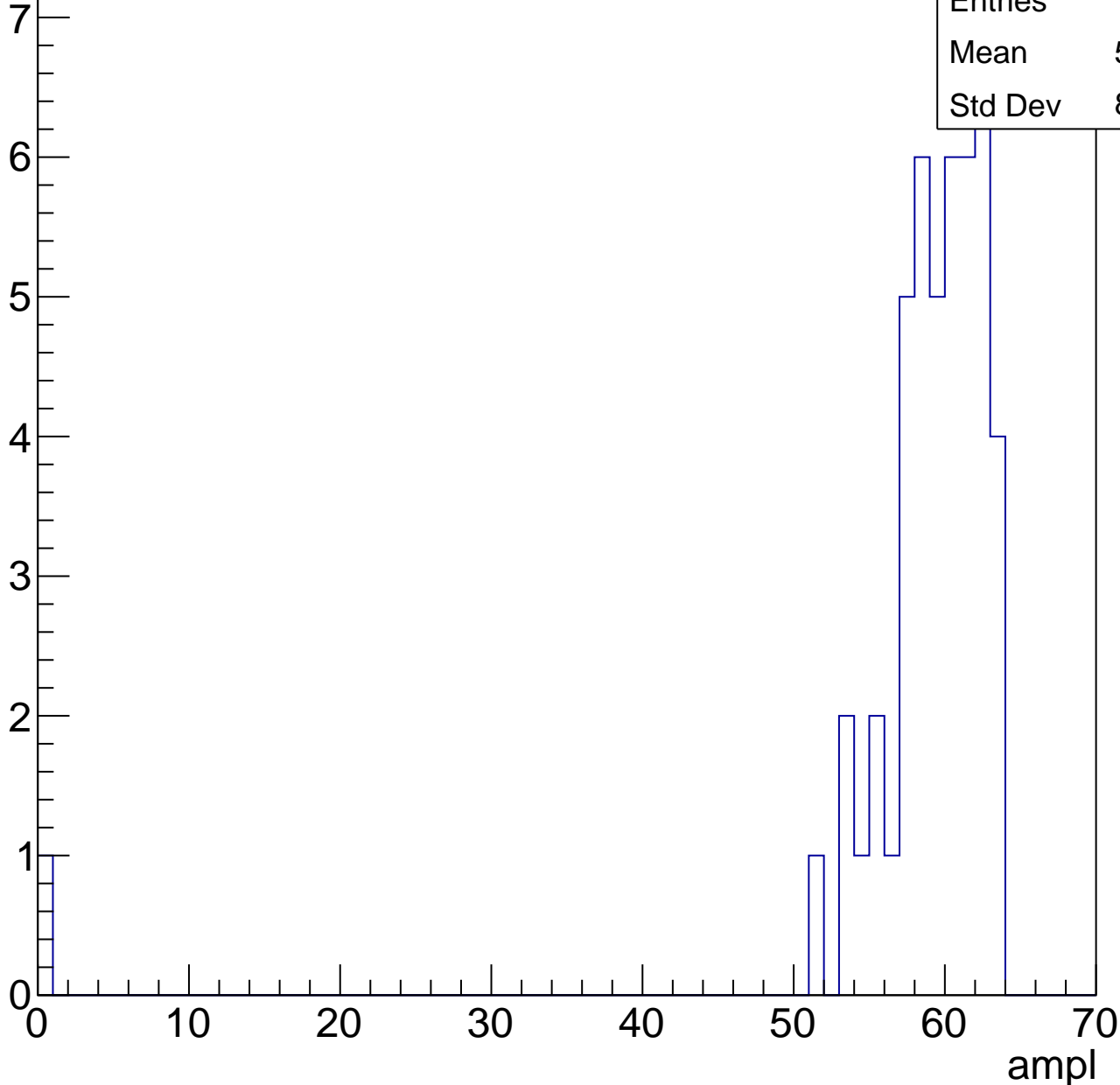


B1L103S, U6-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	57.81
Std Dev	8.991

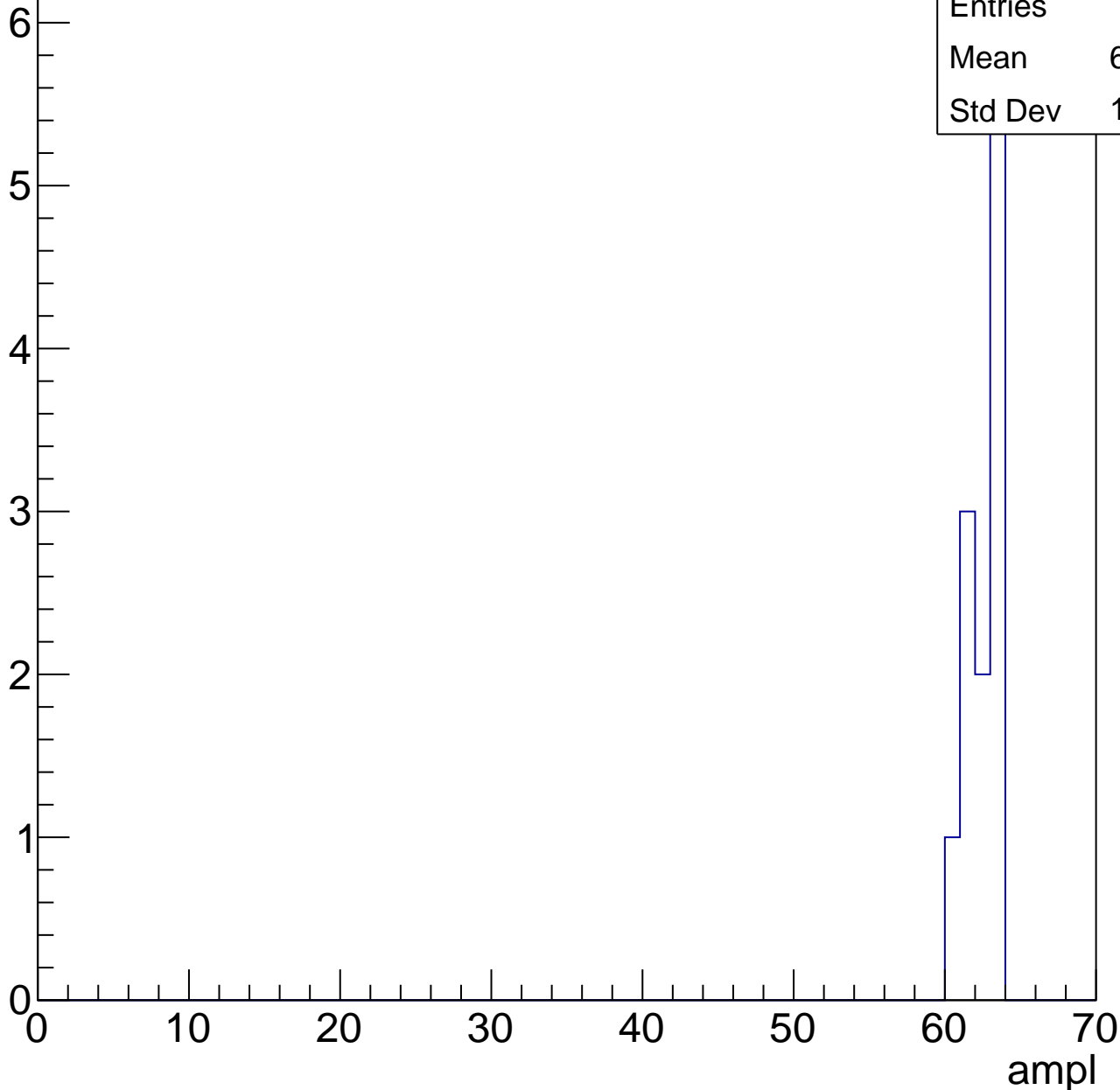


B1L103S, U6-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62.08
Std Dev	1.037



B1L103S, U6-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

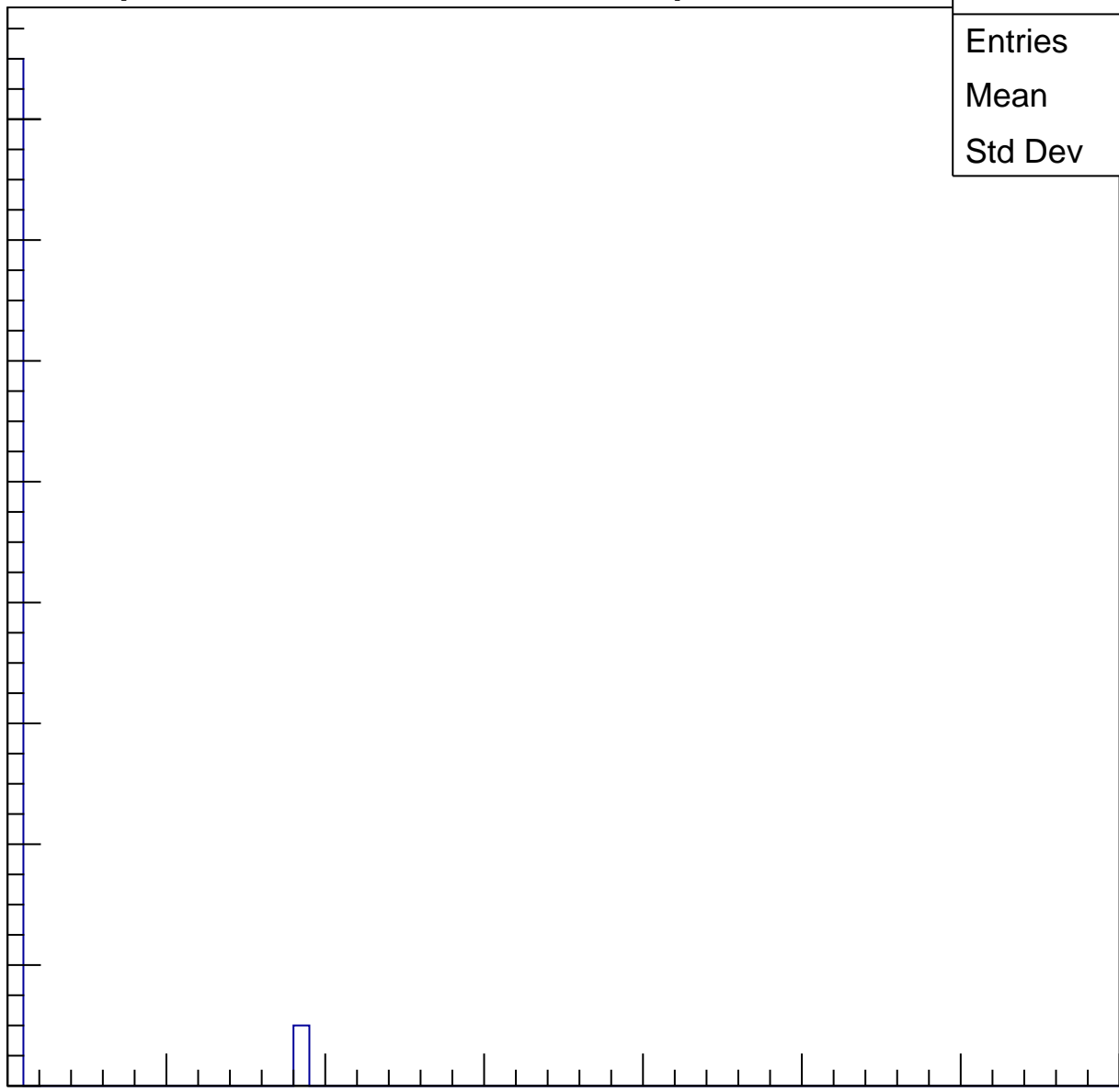
Entries	18
Mean	1
Std Dev	4.123

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch40, adc0

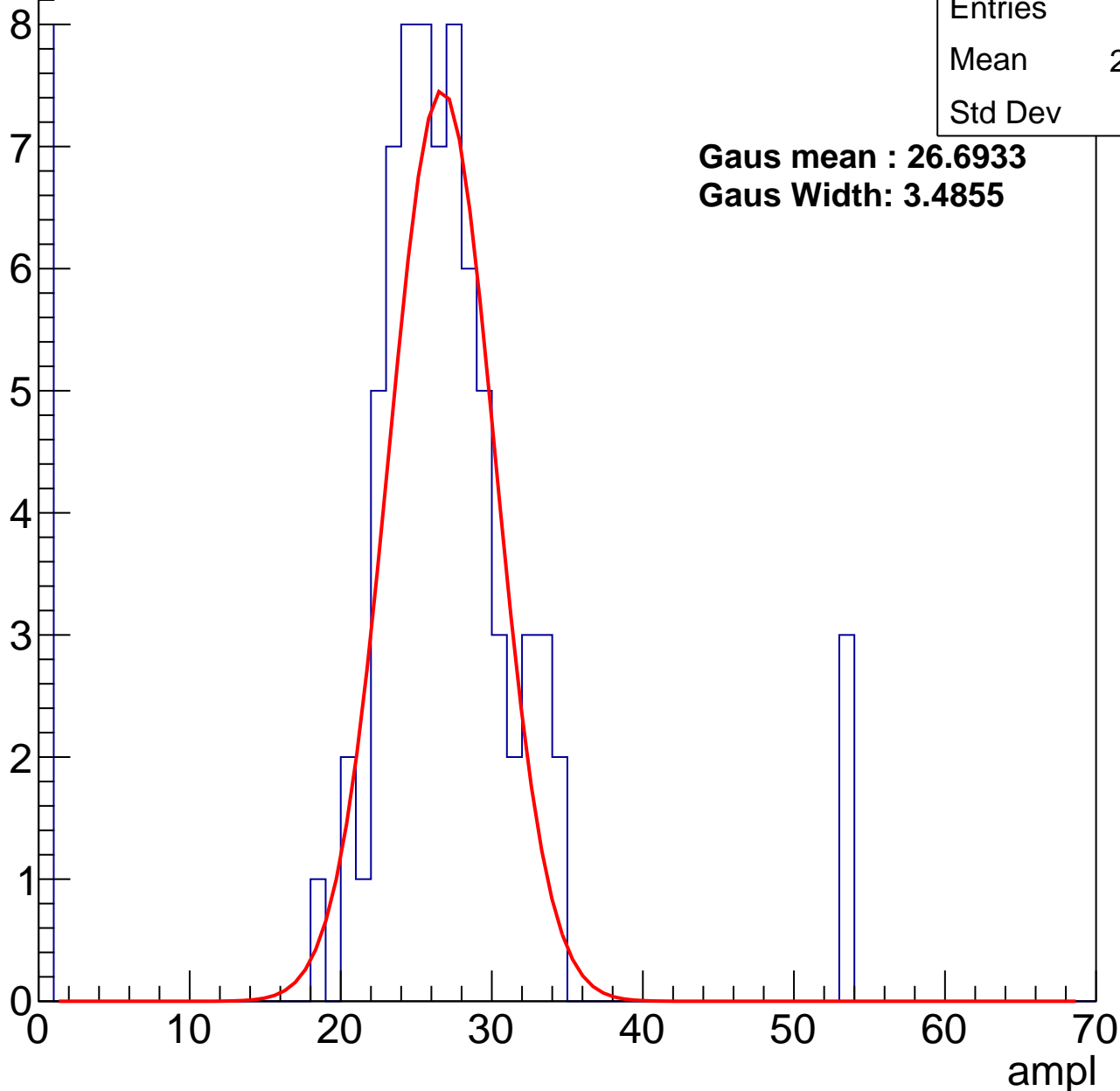
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	24.72
Std Dev	10.1

Gaus mean : 26.6933

Gaus Width: 3.4855



B1L103S, U6-ch40, adc1

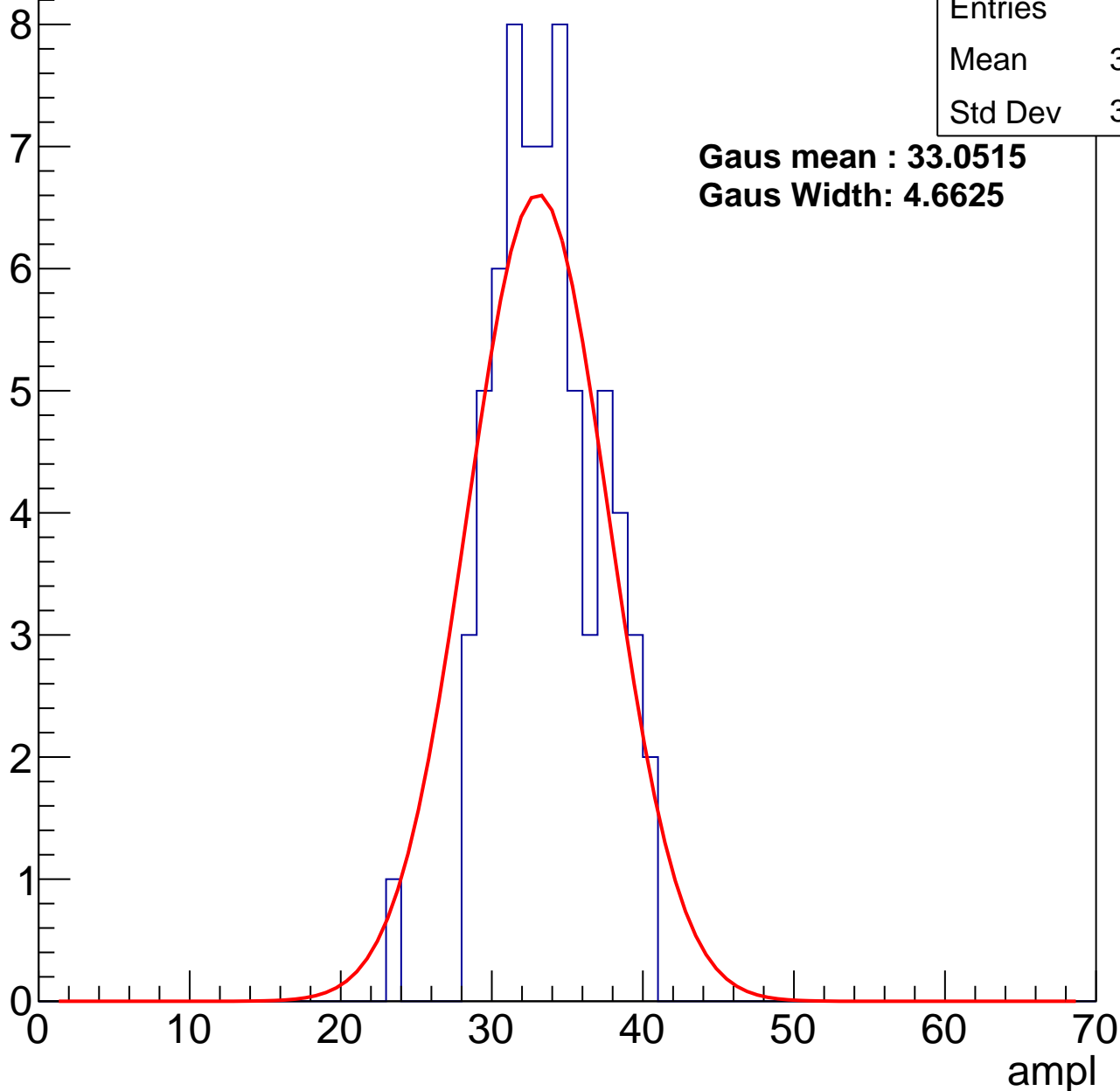
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.19
Std Dev	3.426

Gaus mean : 33.0515

Gaus Width: 4.6625



B1L103S, U6-ch40, adc2

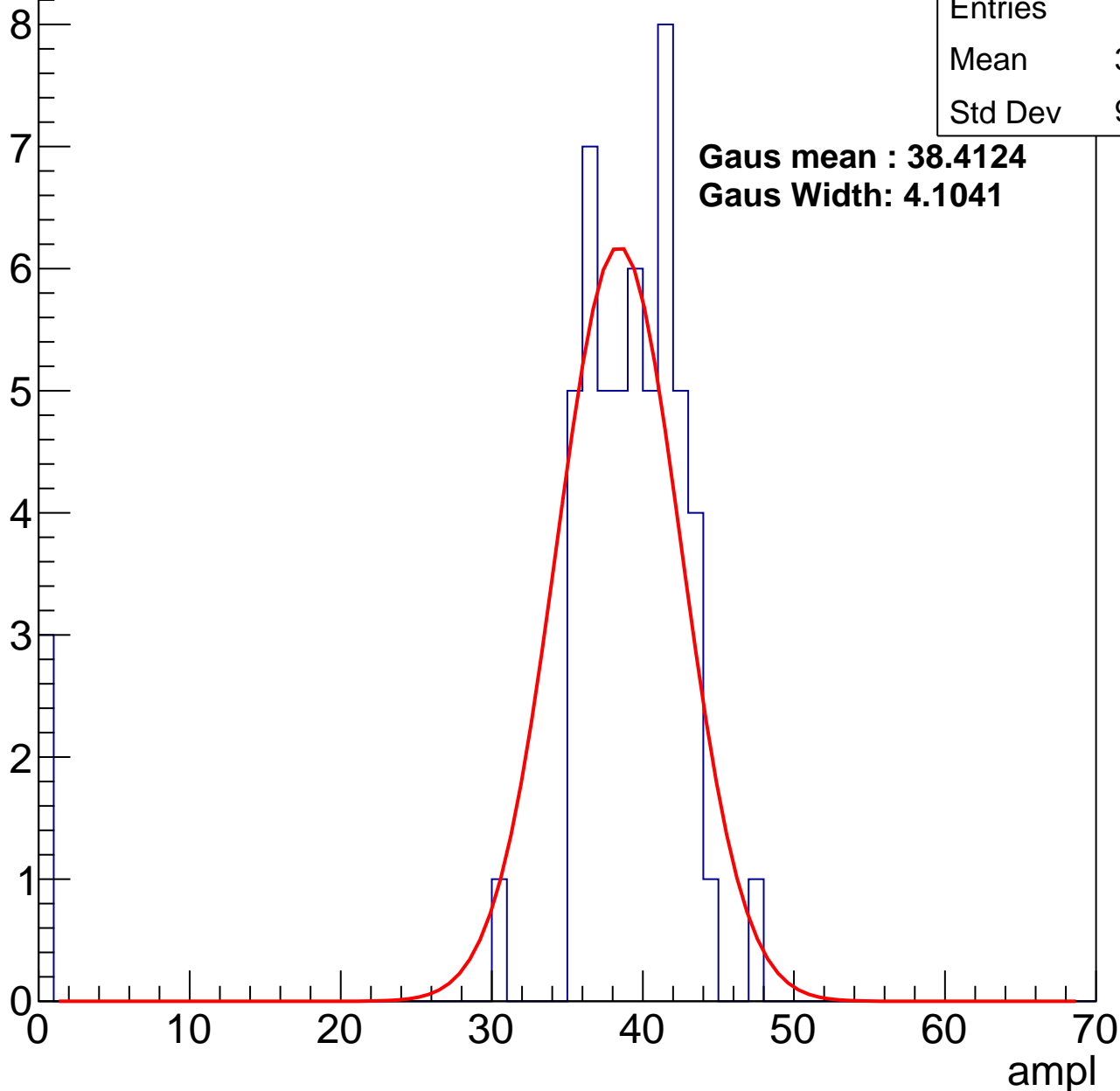
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	36.91
Std Dev	9.261

Gaus mean : 38.4124

Gaus Width: 4.1041

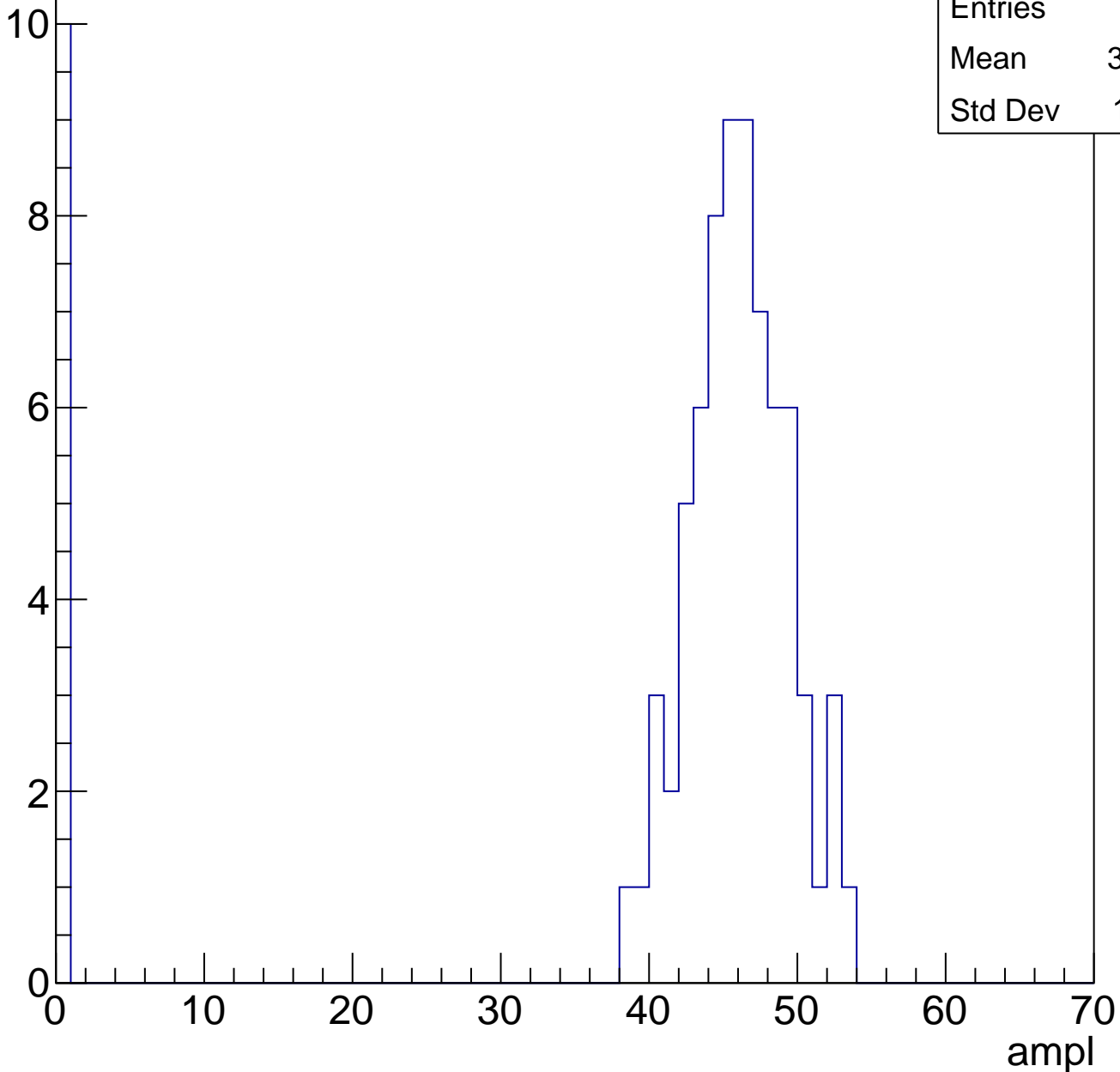


B1L103S, U6-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	39.99
Std Dev	15.31

Entry

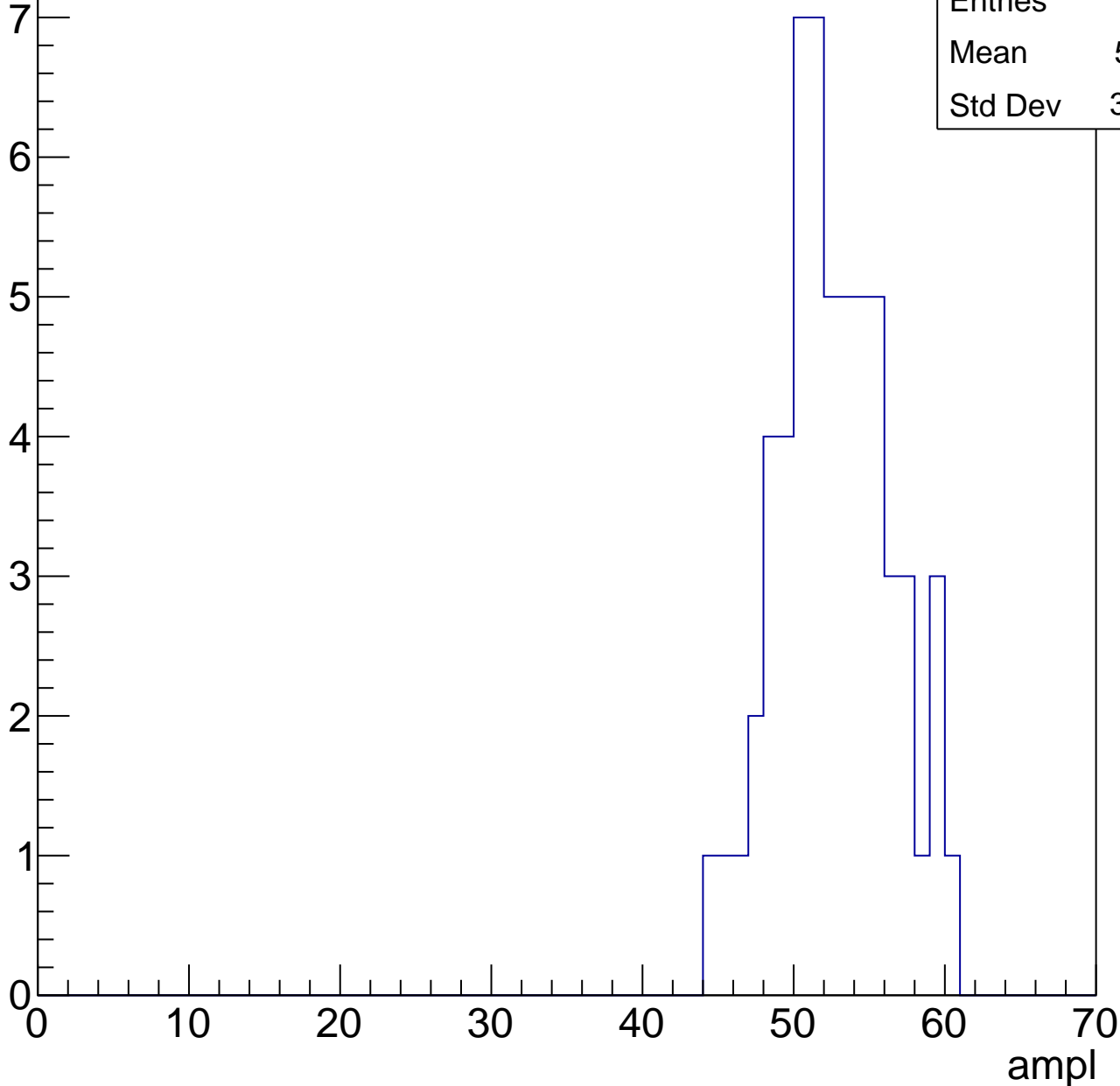


B1L103S, U6-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

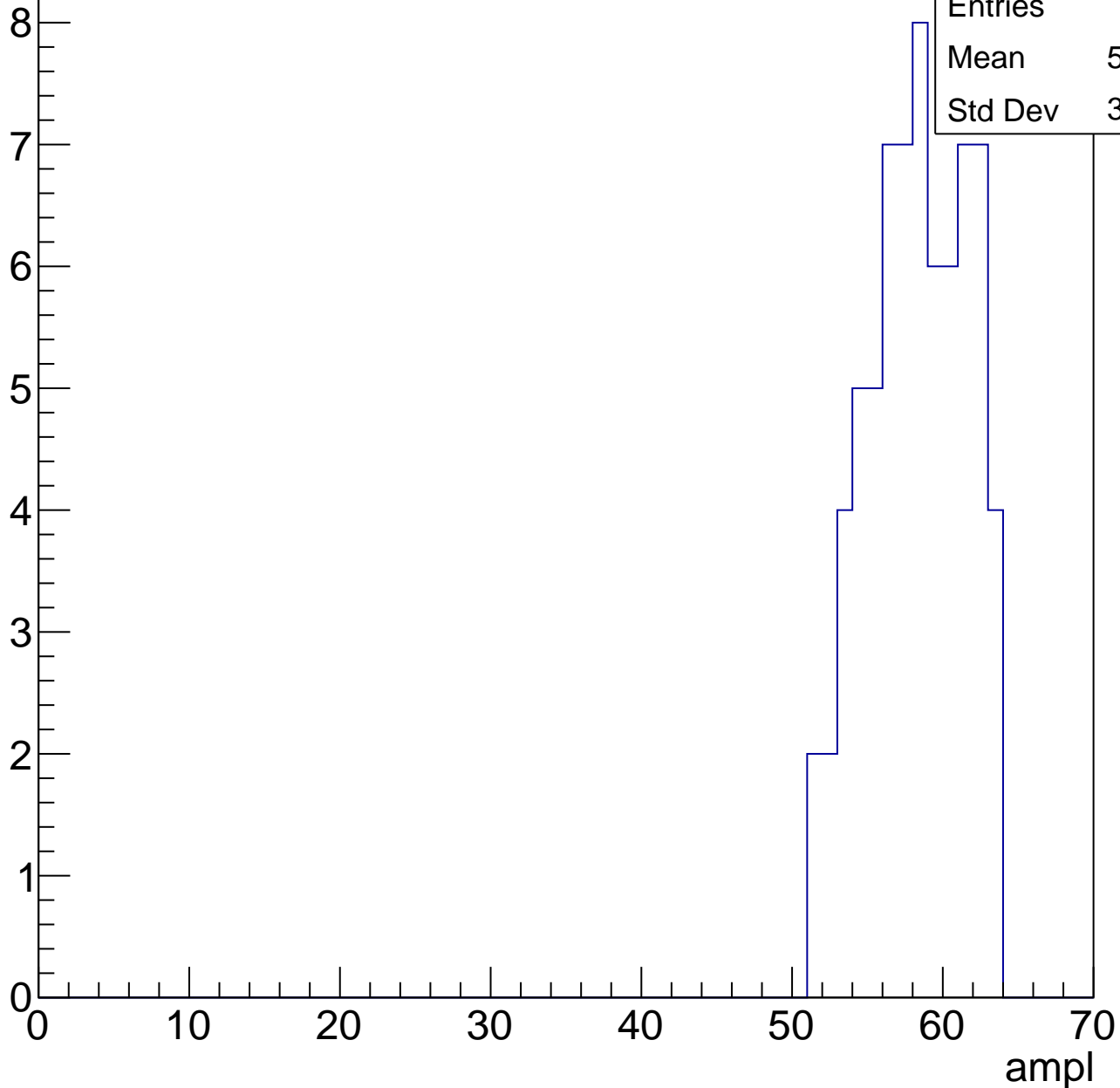
Entries	58
Mean	52.21
Std Dev	3.647



B1L103S, U6-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

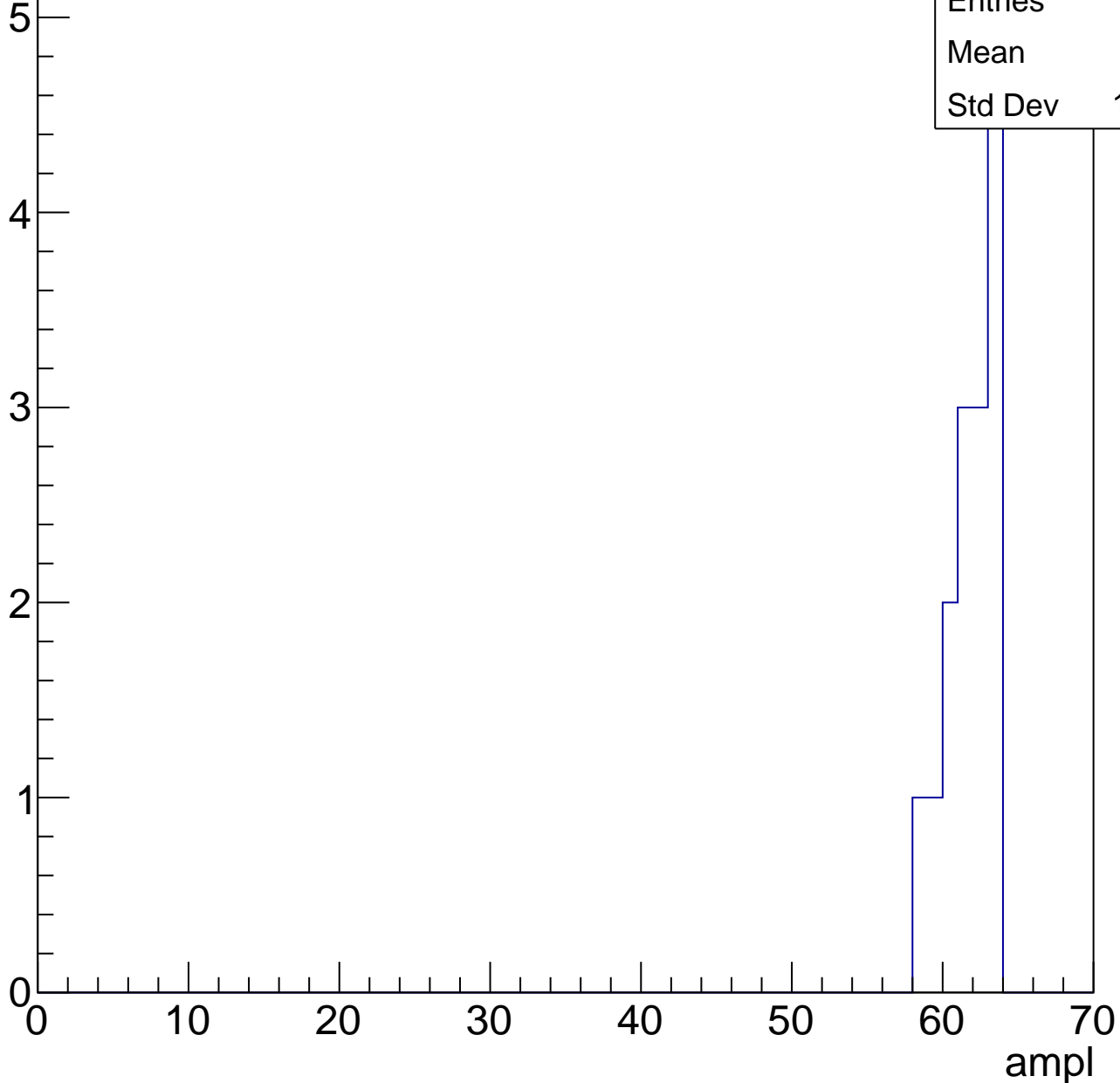


B1L103S, U6-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.4
Std Dev	1.541

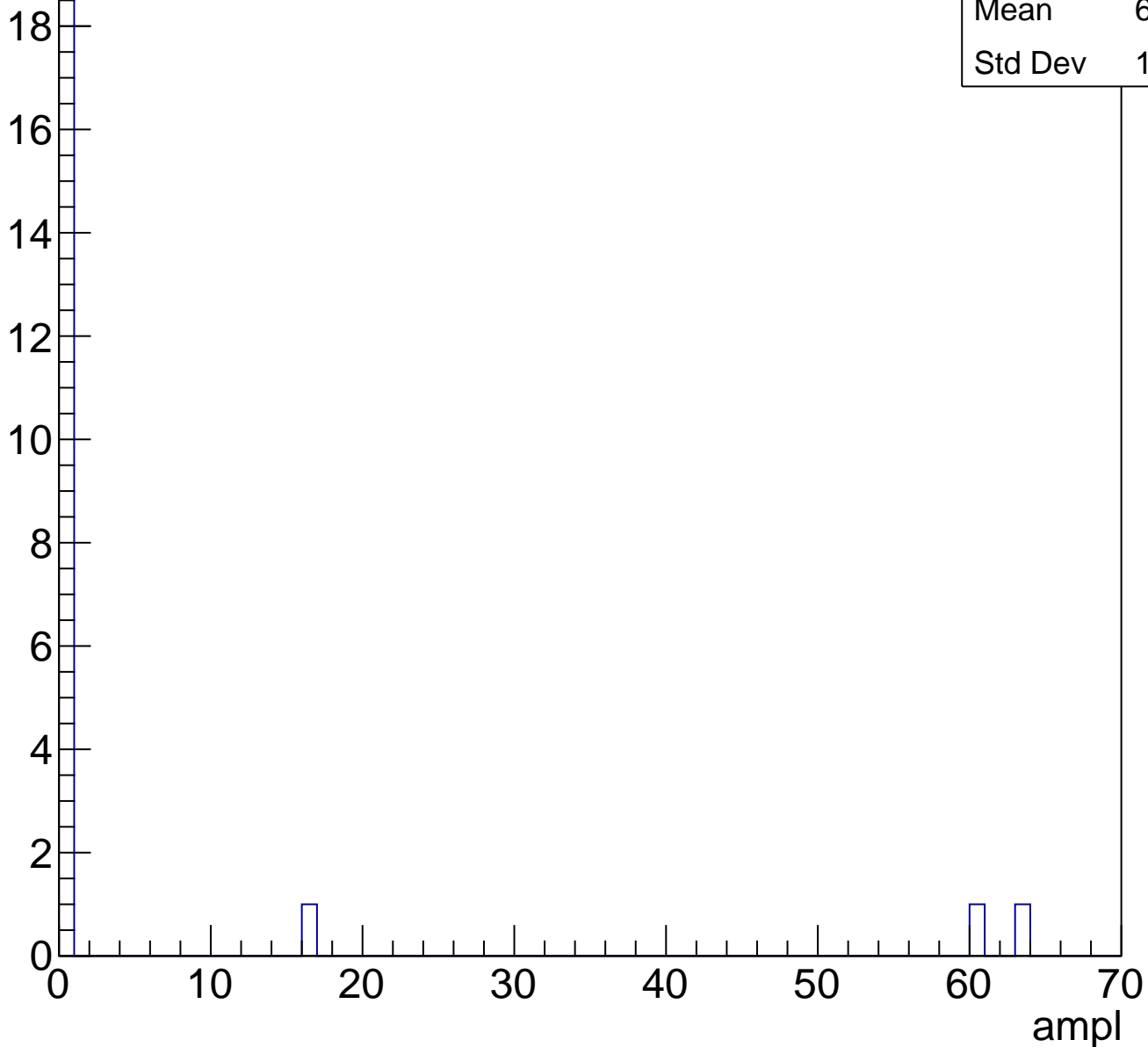


B1L103S, U6-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	6.318
Std Dev	17.77

Entry



B1L103S, U6-ch41, adc0

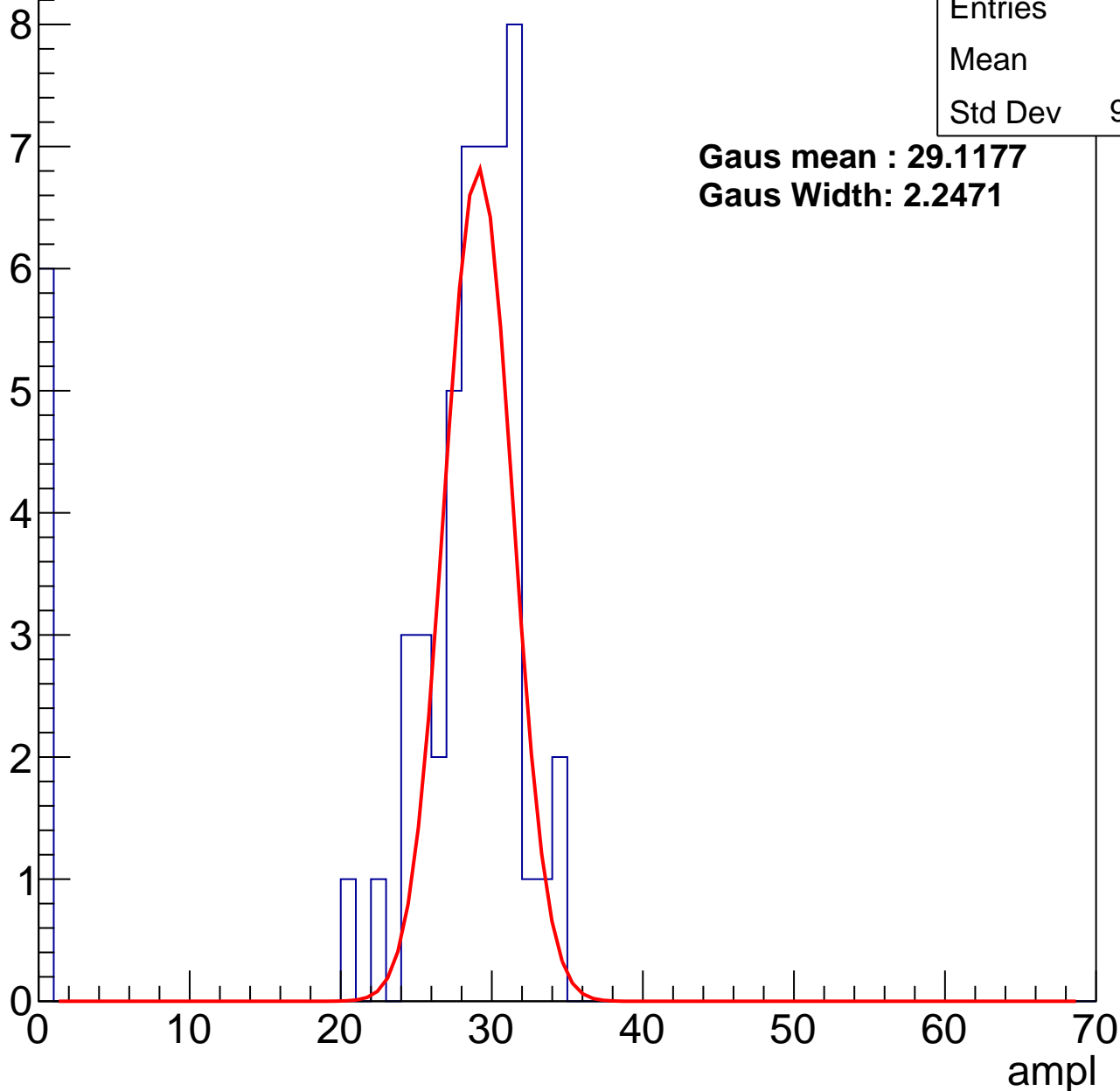
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	25.3
Std Dev	9.346

Gaus mean : 29.1177

Gaus Width: 2.2471



B1L103S, U6-ch41, adc1

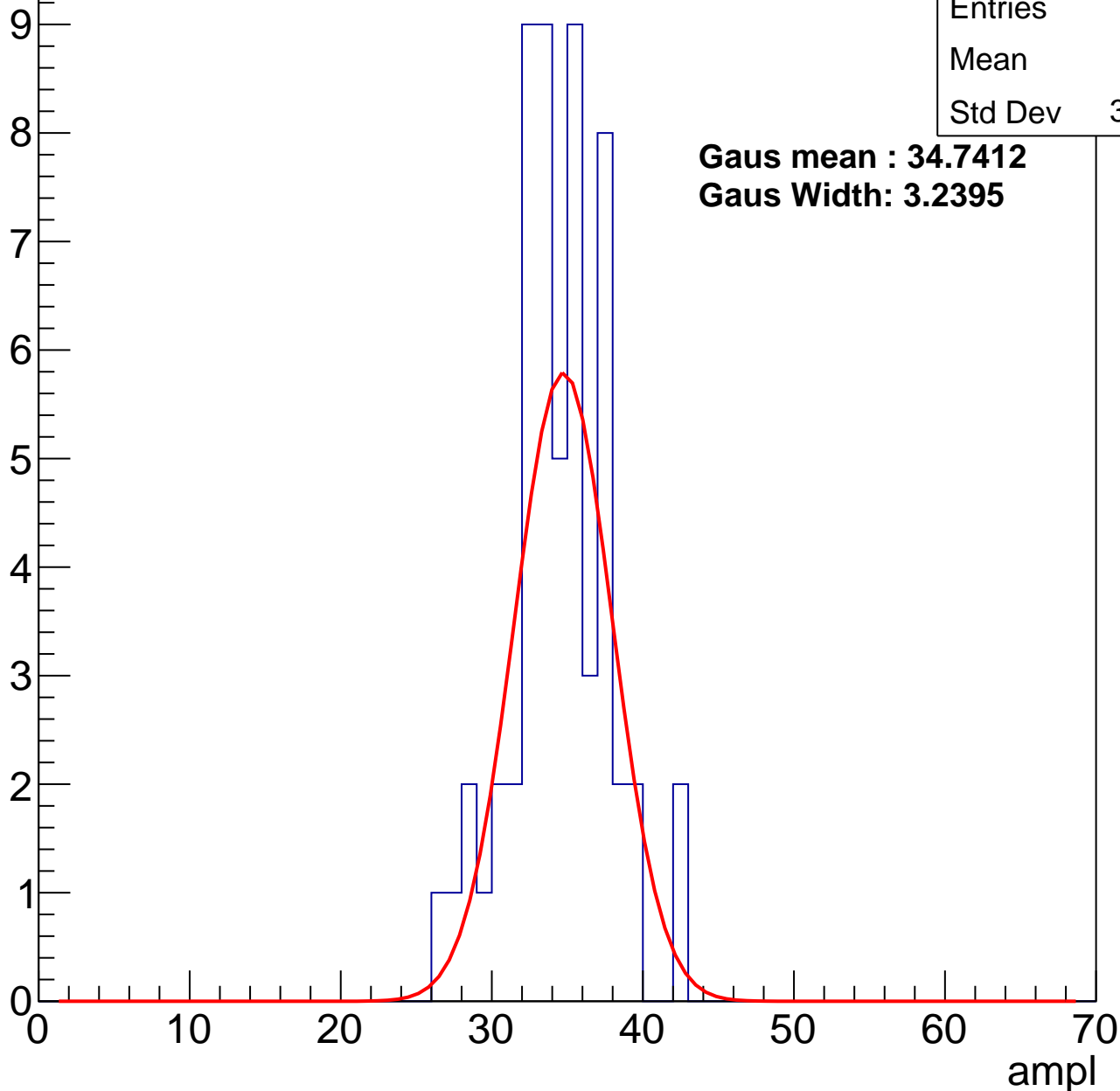
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	34
Std Dev	3.238

Gaus mean : 34.7412

Gaus Width: 3.2395



B1L103S, U6-ch41, adc2

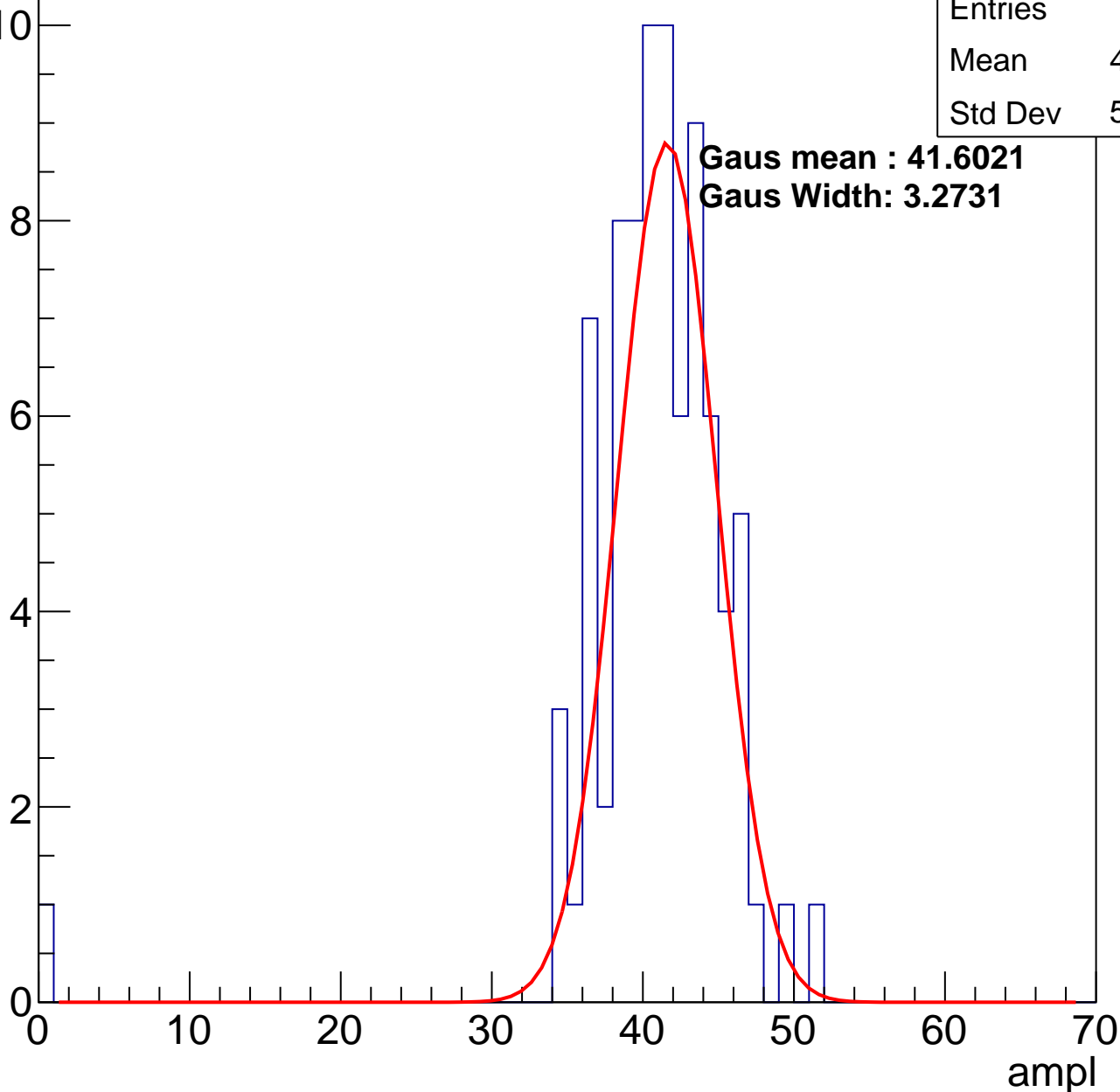
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	40.35
Std Dev	5.634

Gaus mean : 41.6021

Gaus Width: 3.2731

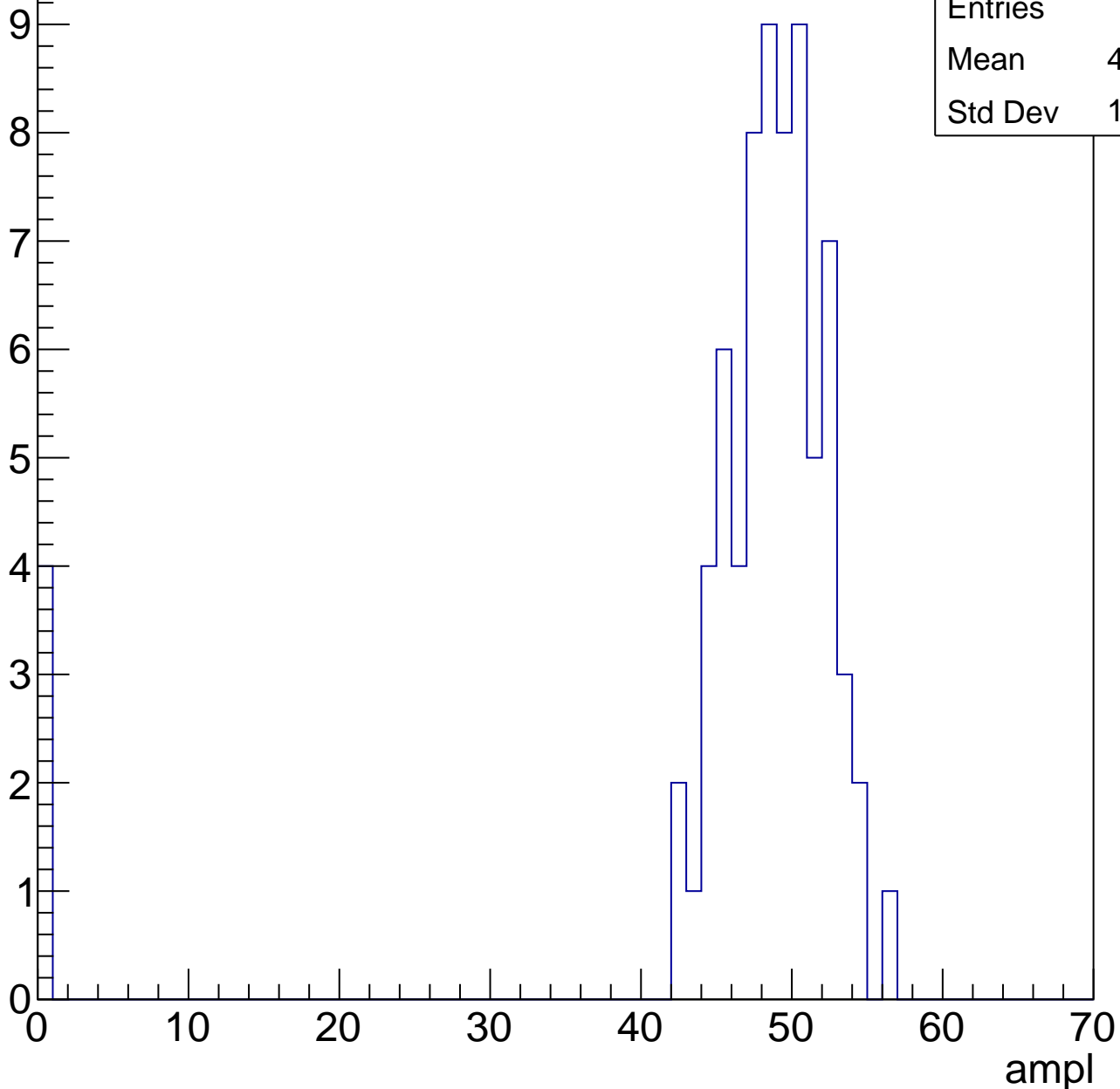


B1L103S, U6-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

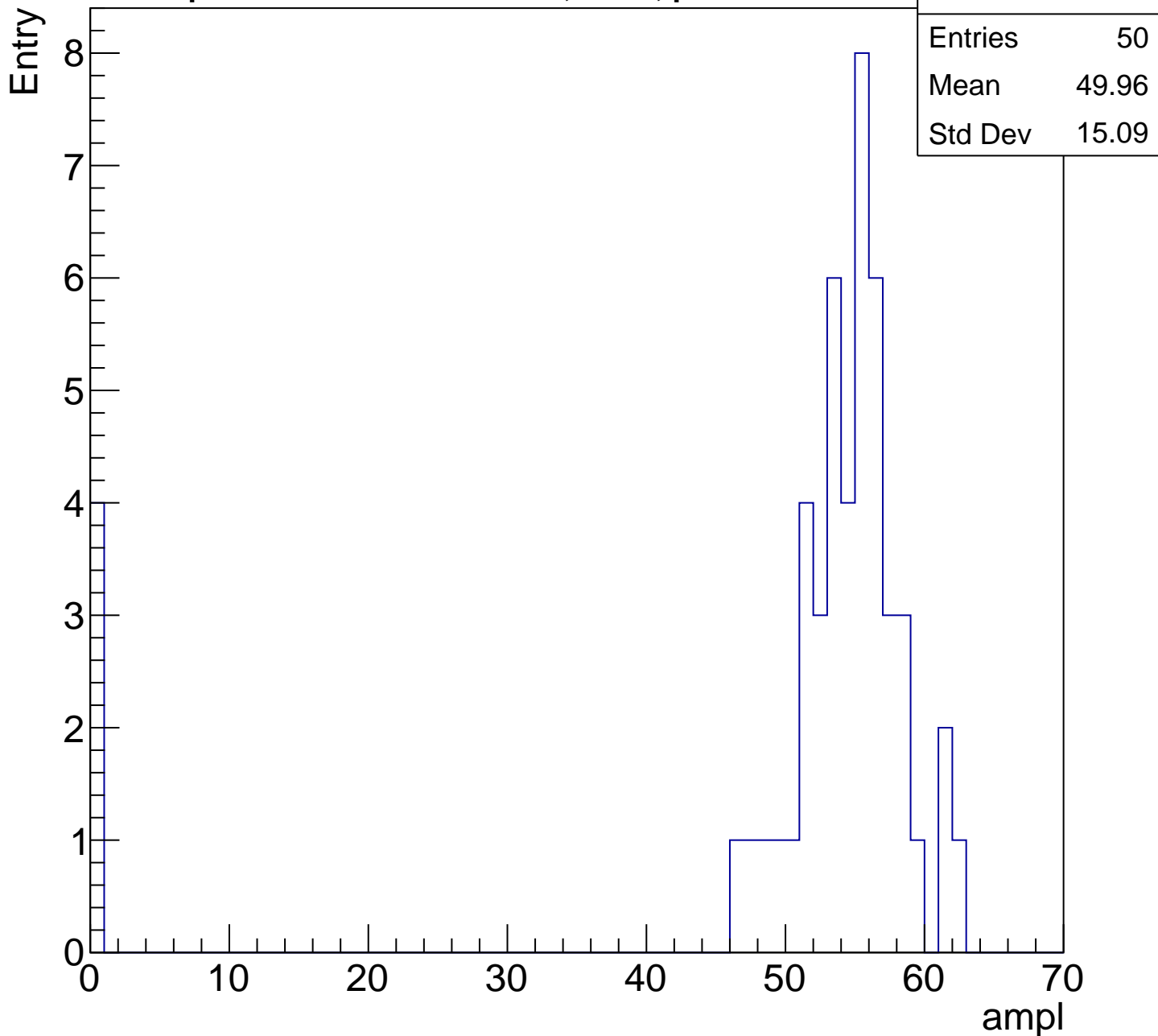
Entry

Entries	73
Mean	45.88
Std Dev	11.43



B1L103S, U6-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	57.7
Std Dev	8.109

ampl

0

10

20

30

40

50

60

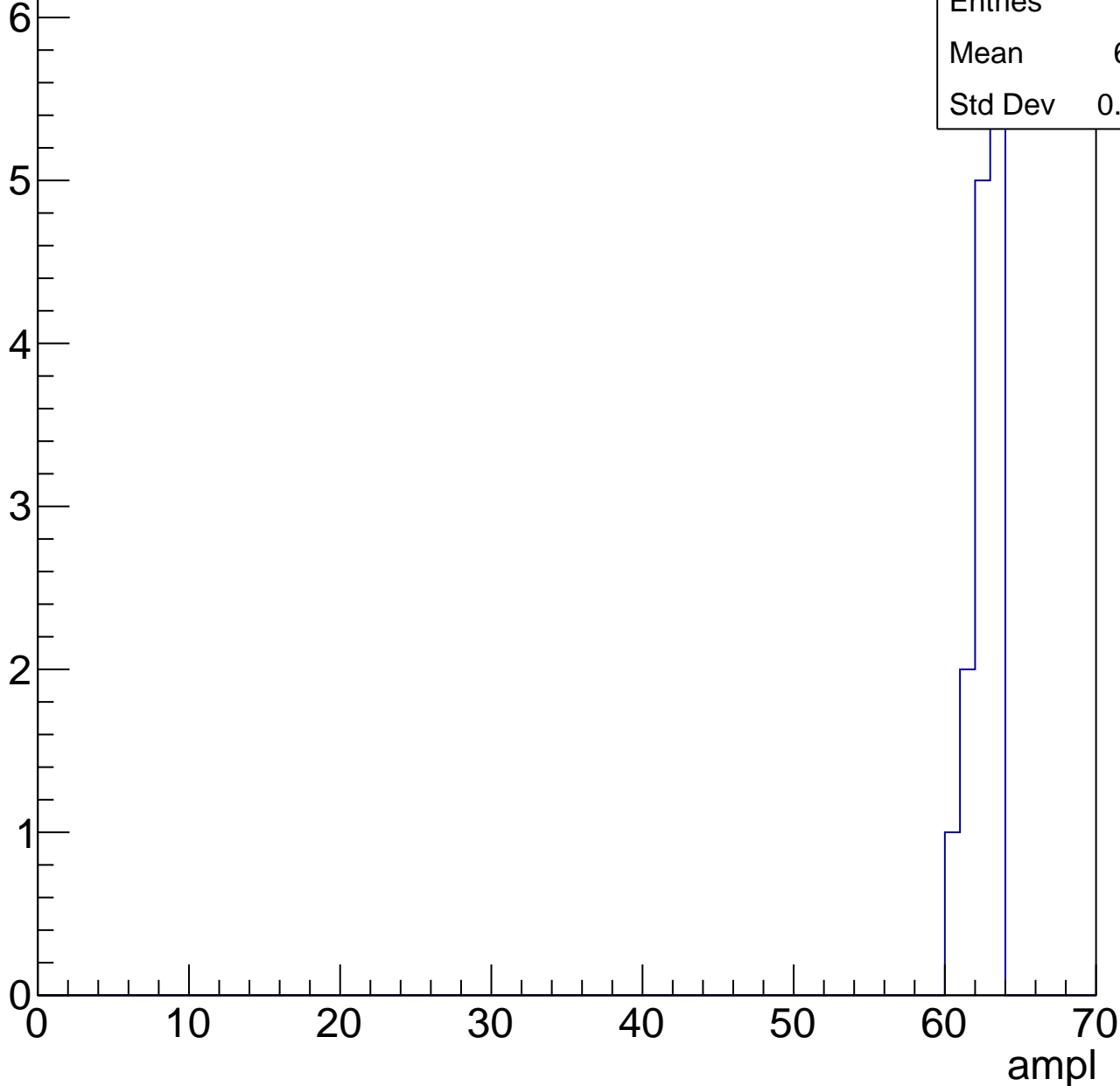
70

B1L103S, U6-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	62.14
Std Dev	0.9147



B1L103S, U6-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U6-ch42, adc0

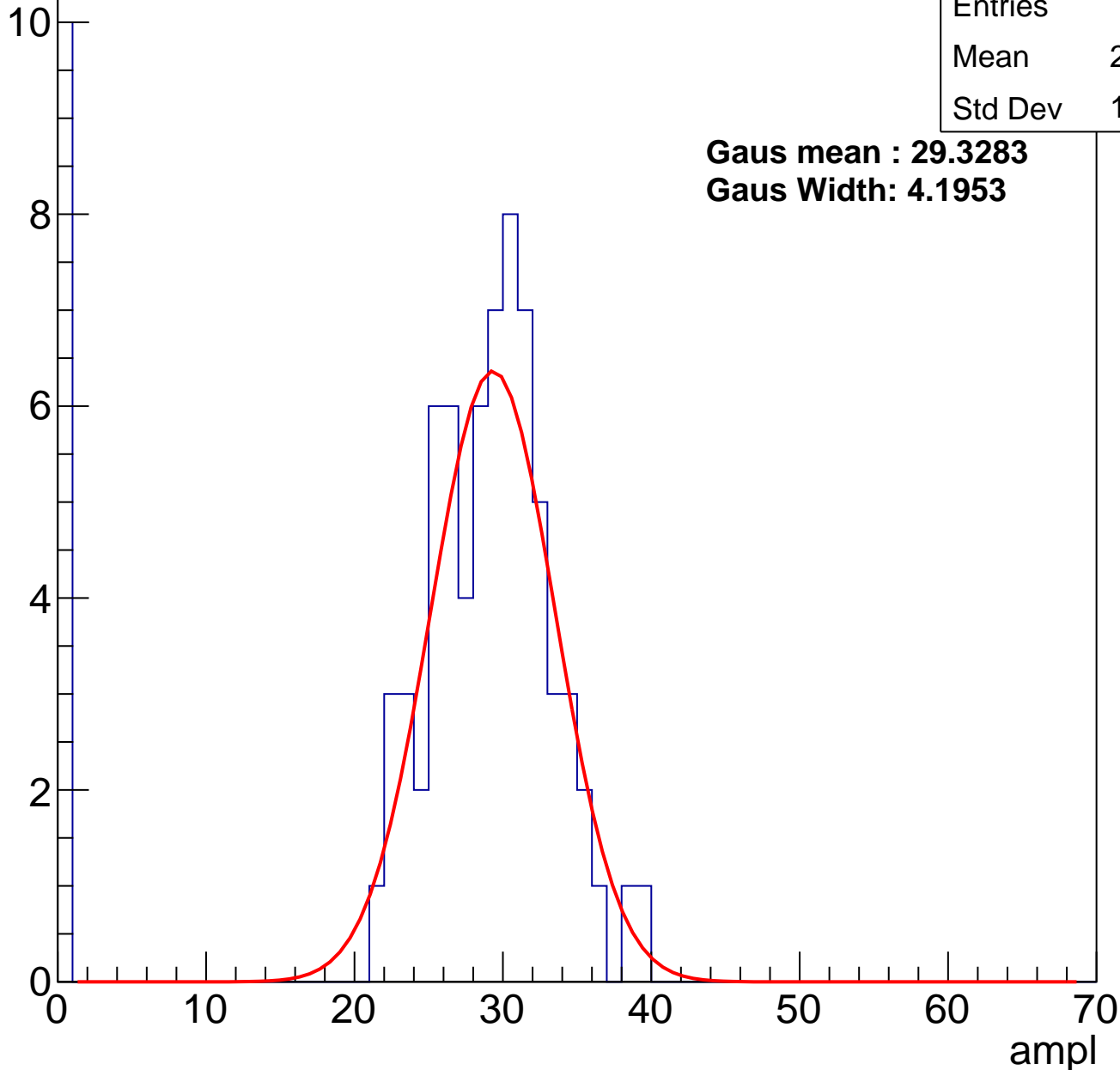
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	25.19
Std Dev	10.26

Gaus mean : 29.3283

Gaus Width: 4.1953

Entry



B1L103S, U6-ch42, adc1

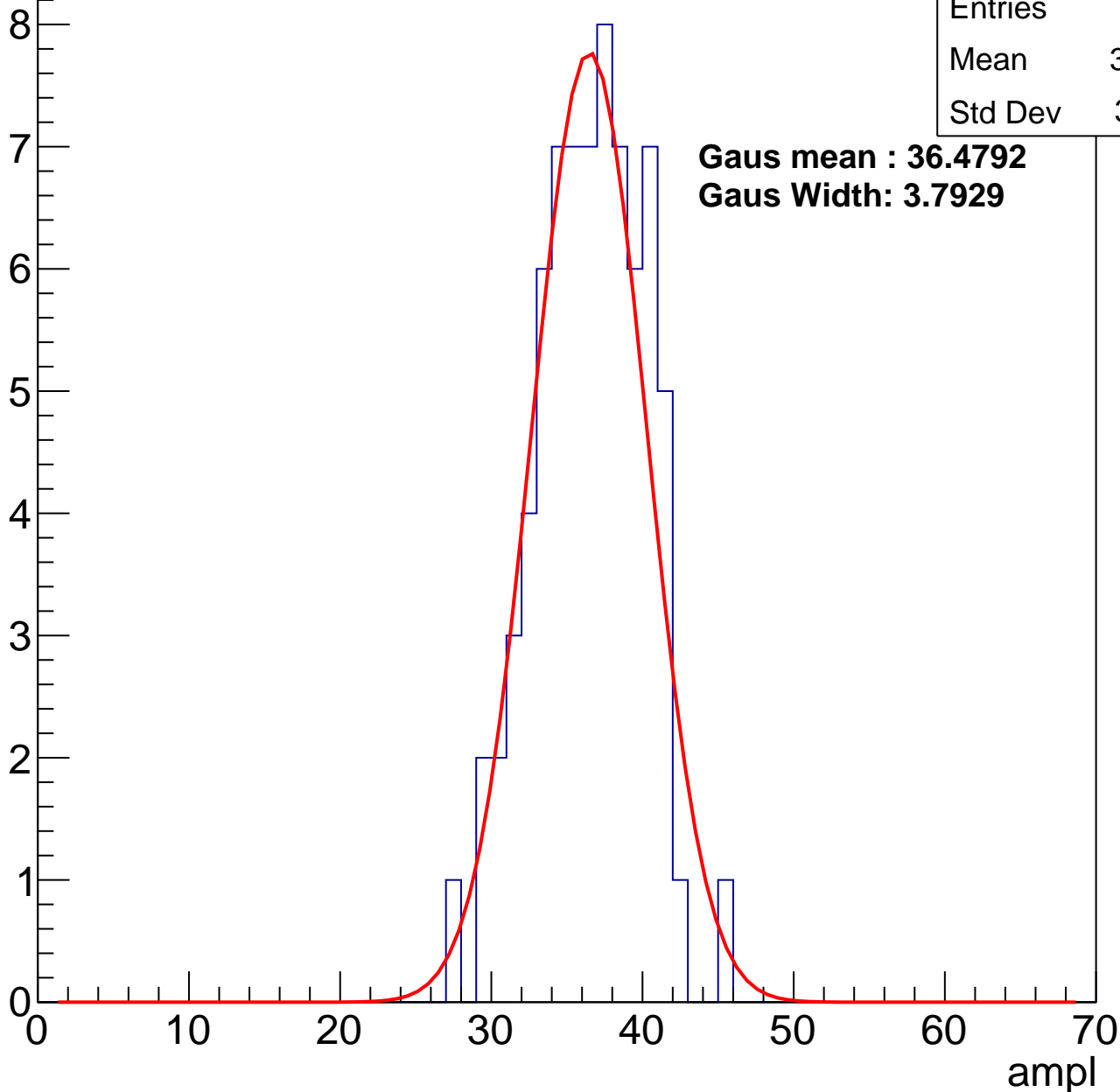
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.04
Std Dev	3.531

Gaus mean : 36.4792

Gaus Width: 3.7929



B1L103S, U6-ch42, adc2

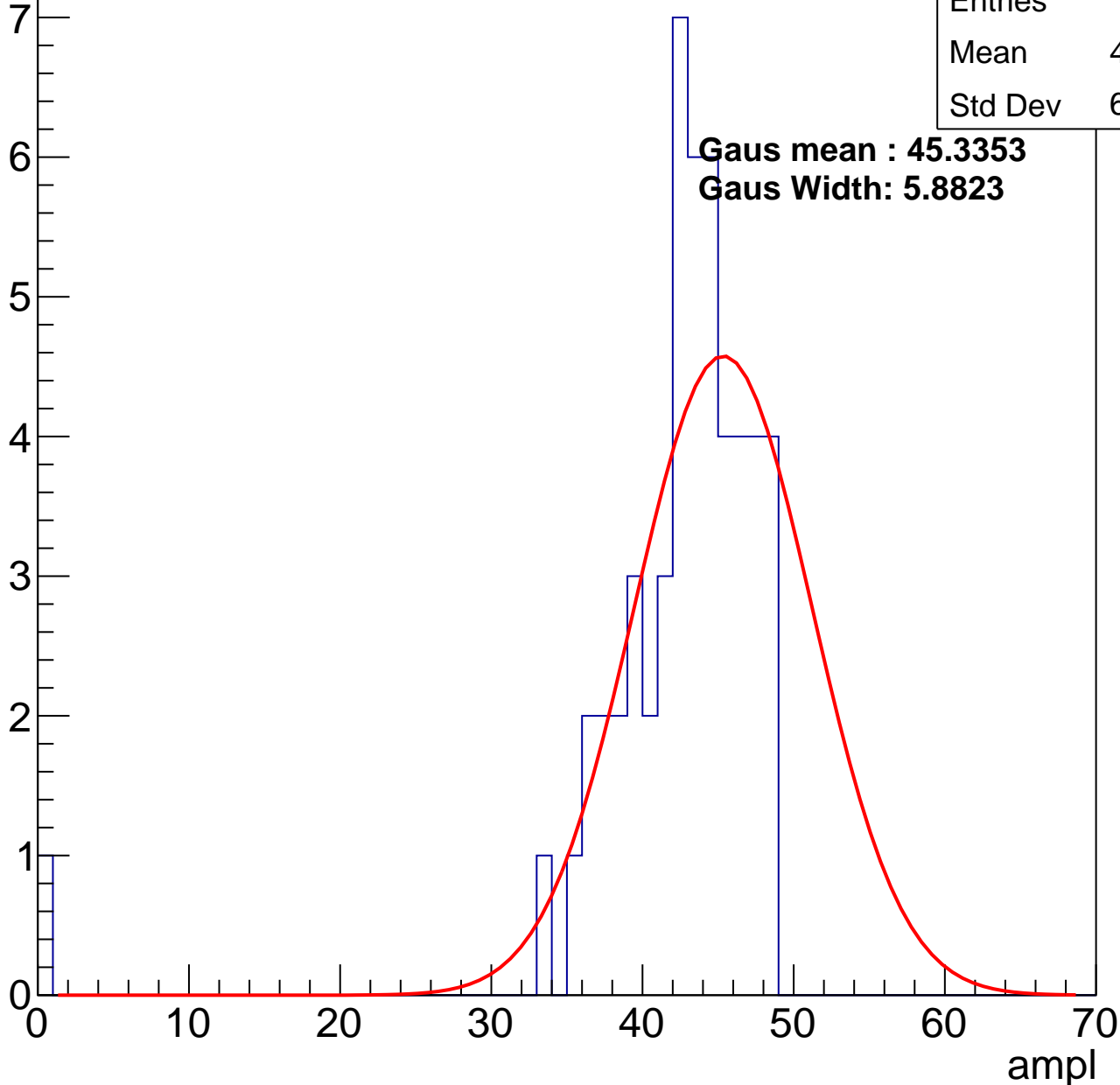
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	41.73
Std Dev	6.873

Gaus mean : 45.3353

Gaus Width: 5.8823

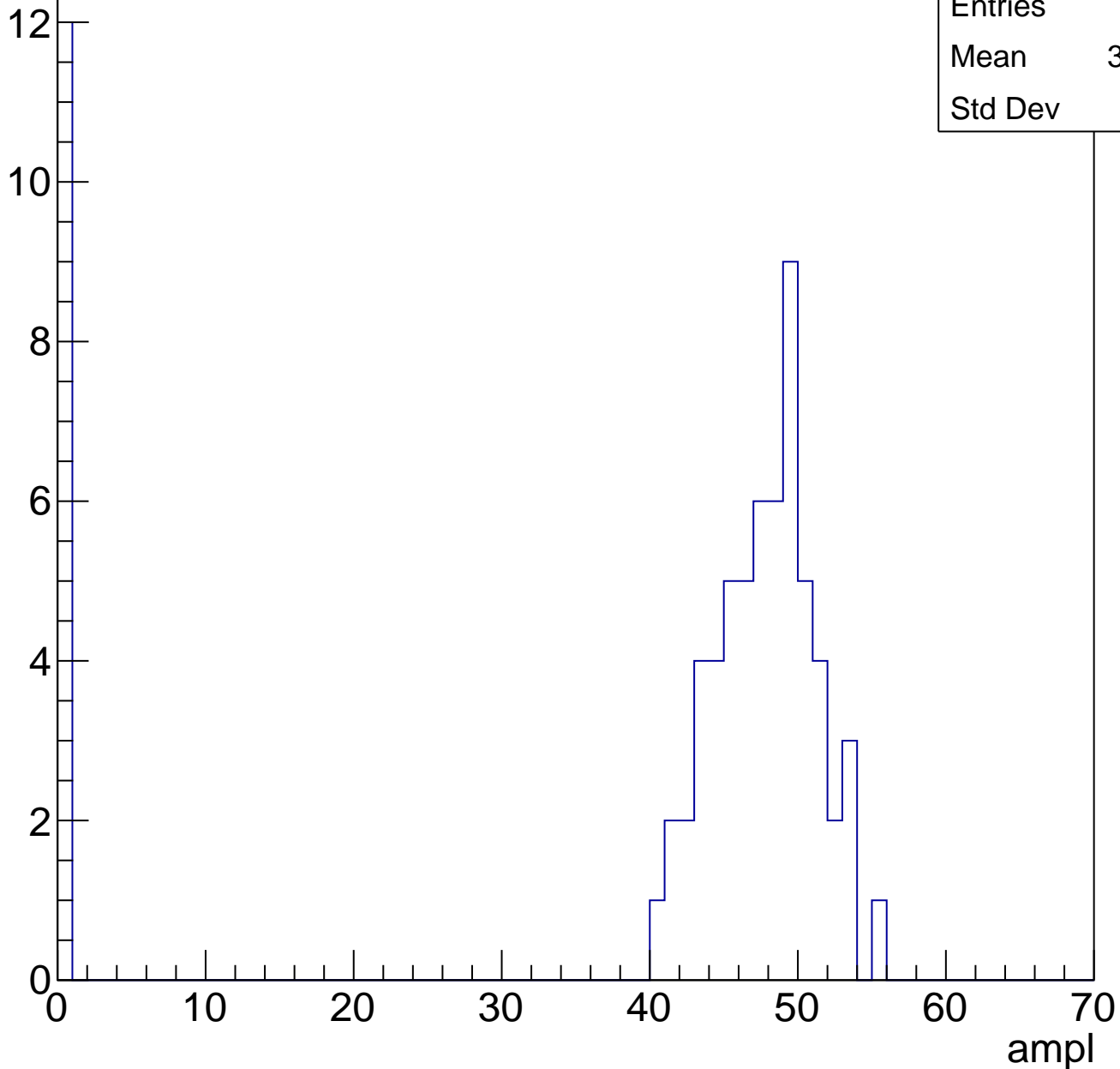


B1L103S, U6-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

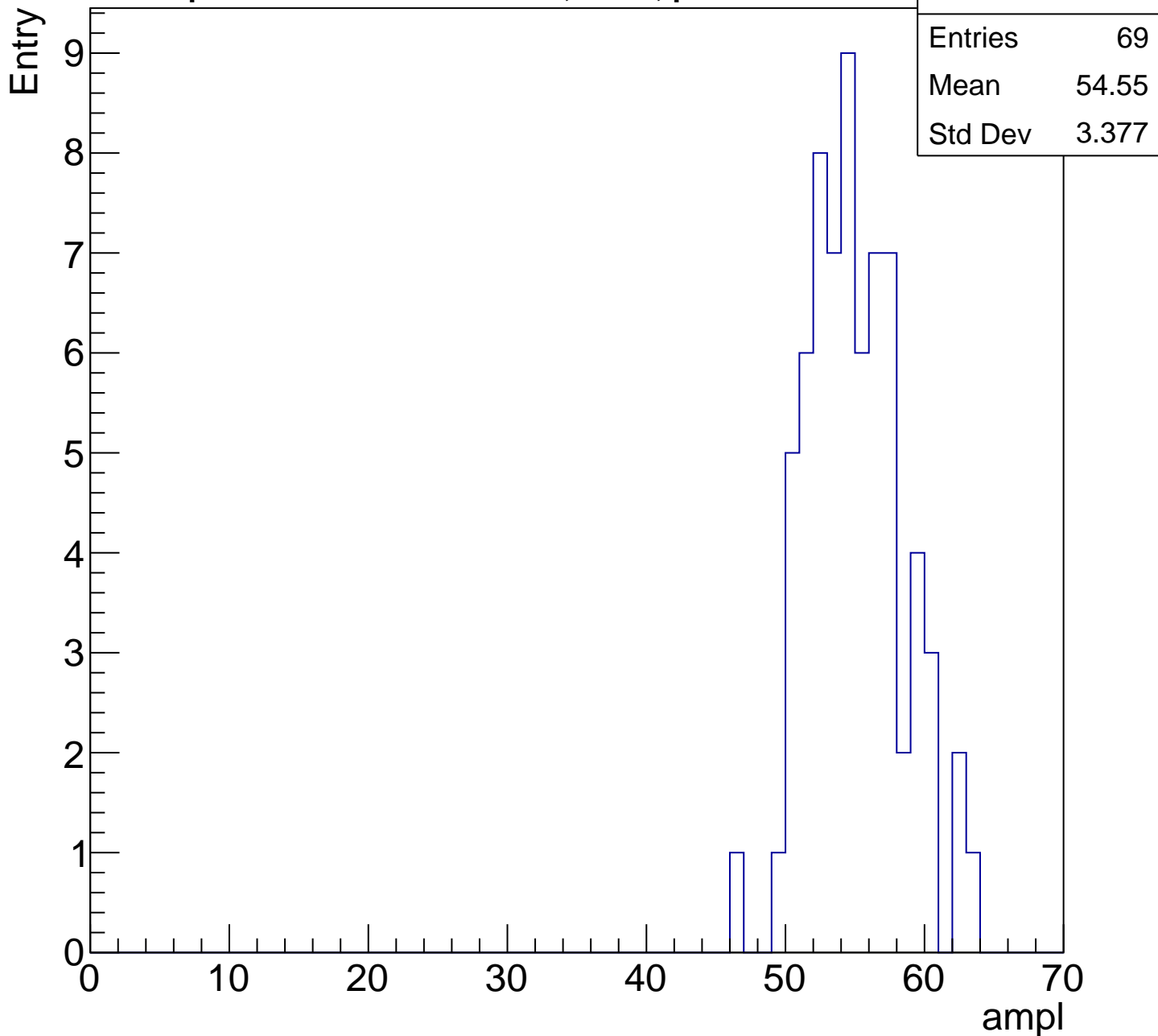
Entries	71
Mean	39.32
Std Dev	18

Entry



B1L103S, U6-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

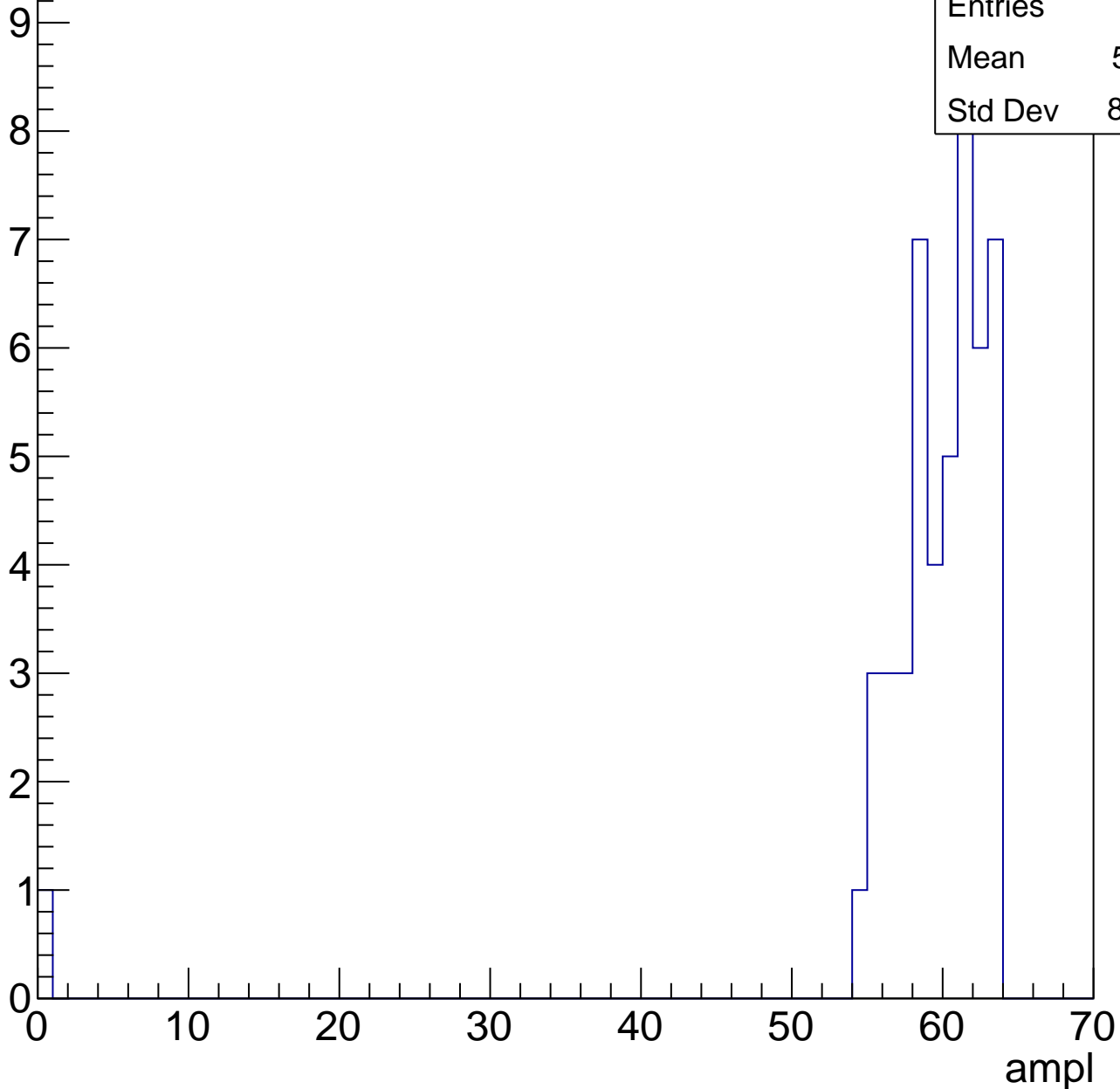


B1L103S, U6-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.41
Std Dev	8.795



B1L103S, U6-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

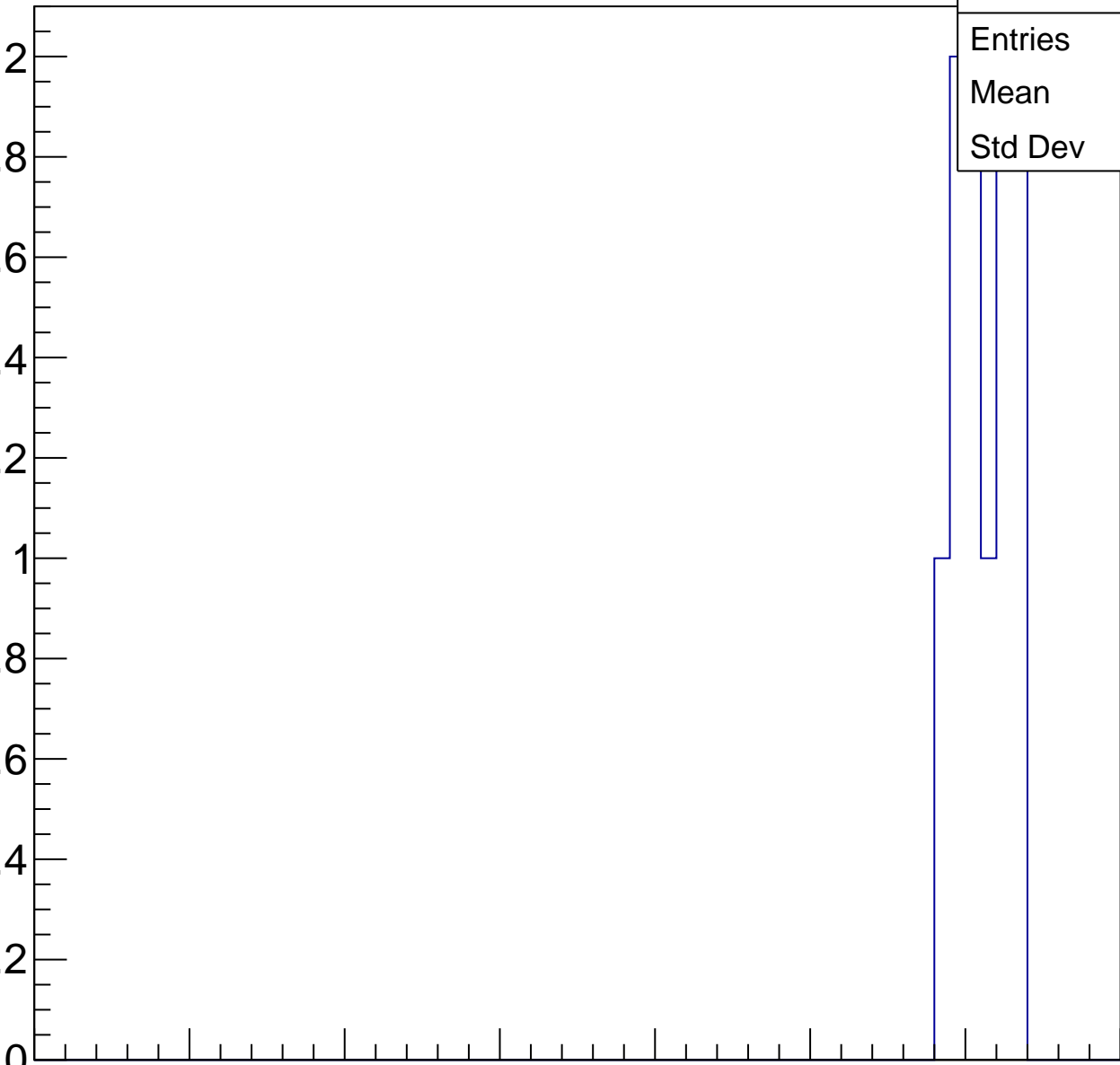
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60.7
Std Dev	1.676

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch43, adc0

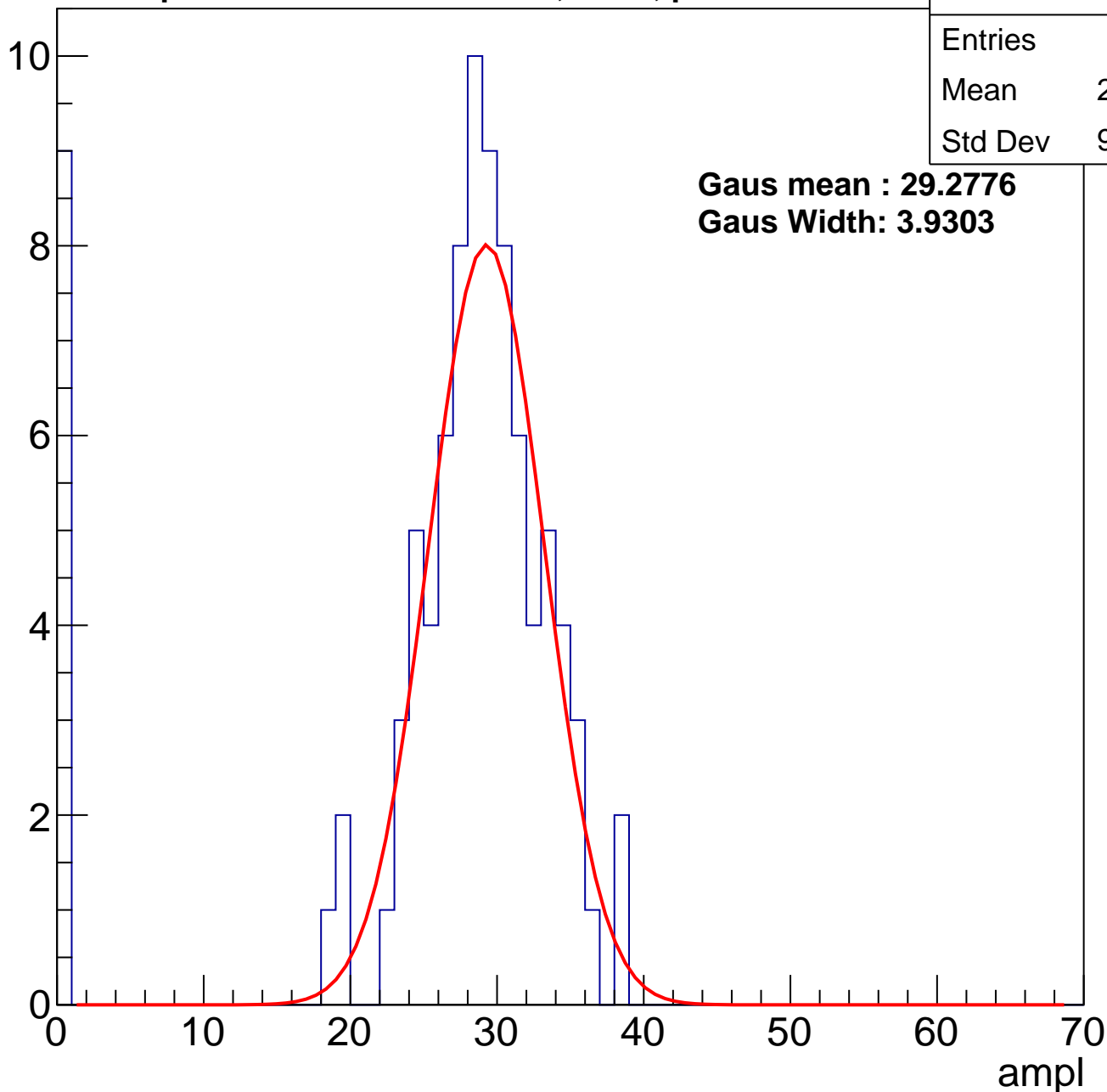
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	25.85
Std Dev	9.373

Gaus mean : 29.2776

Gaus Width: 3.9303

Entry



B1L103S, U6-ch43, adc1

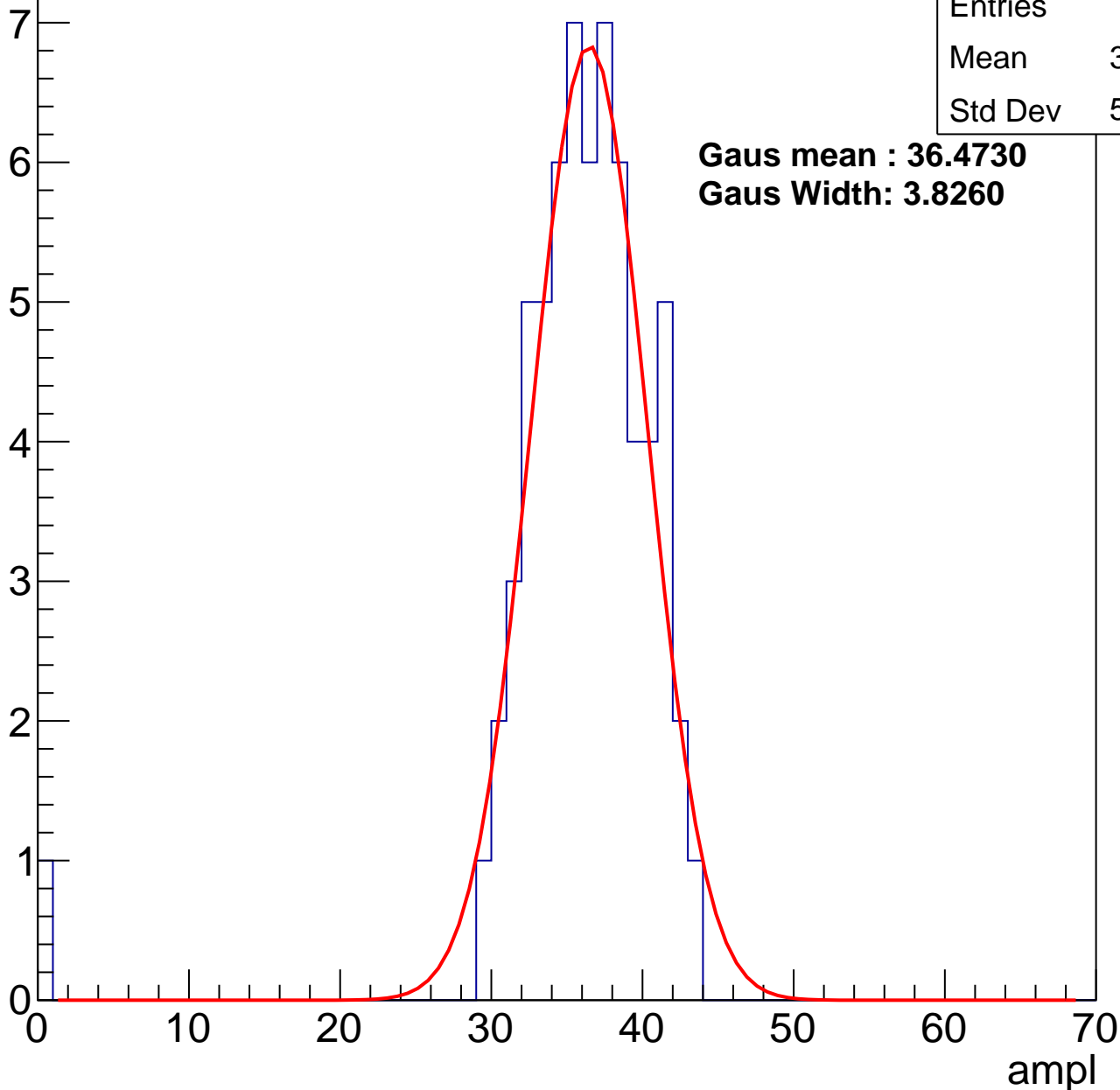
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.49
Std Dev	5.558

Gaus mean : 36.4730

Gaus Width: 3.8260



B1L103S, U6-ch43, adc2

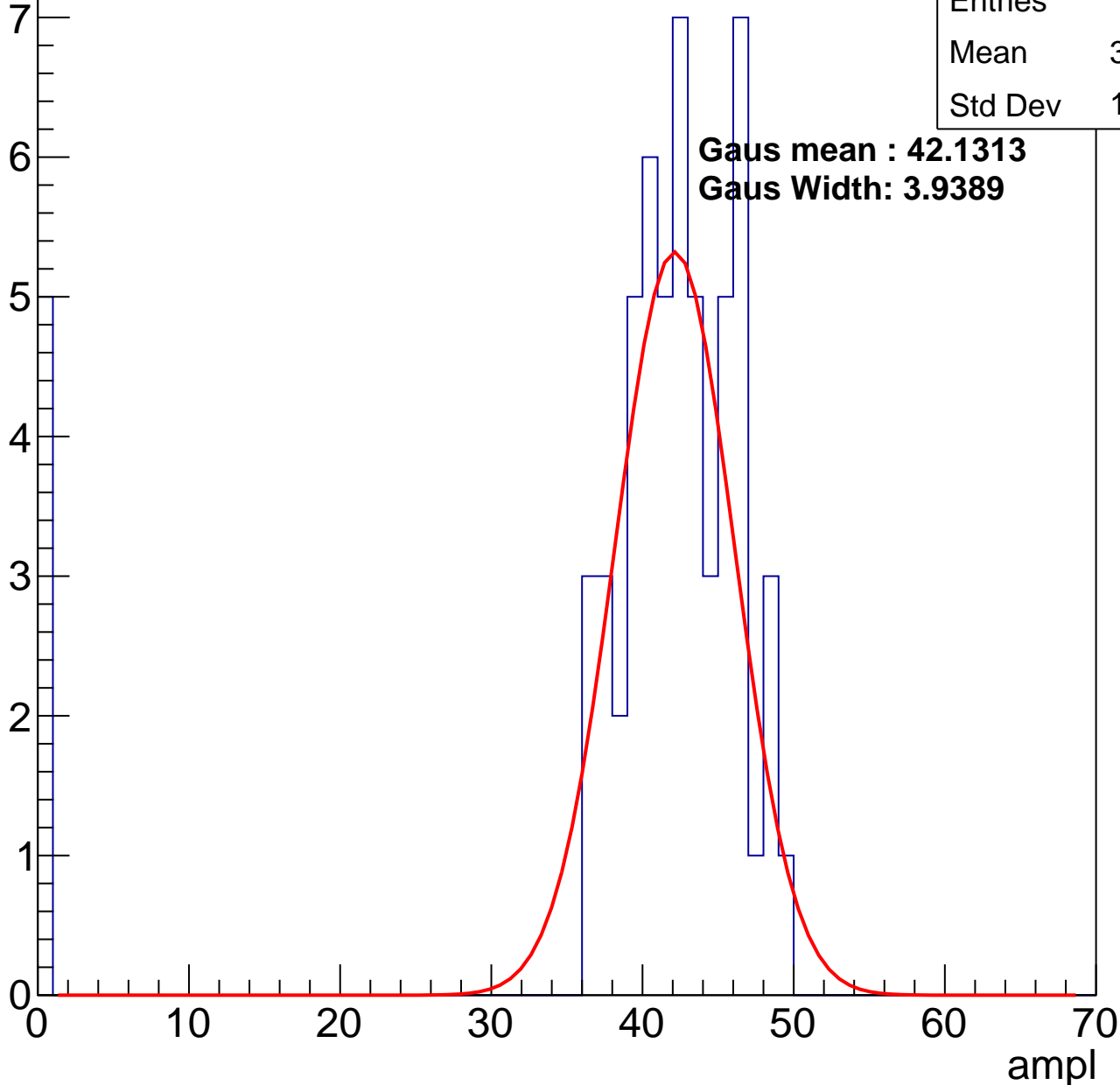
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.74
Std Dev	12.02

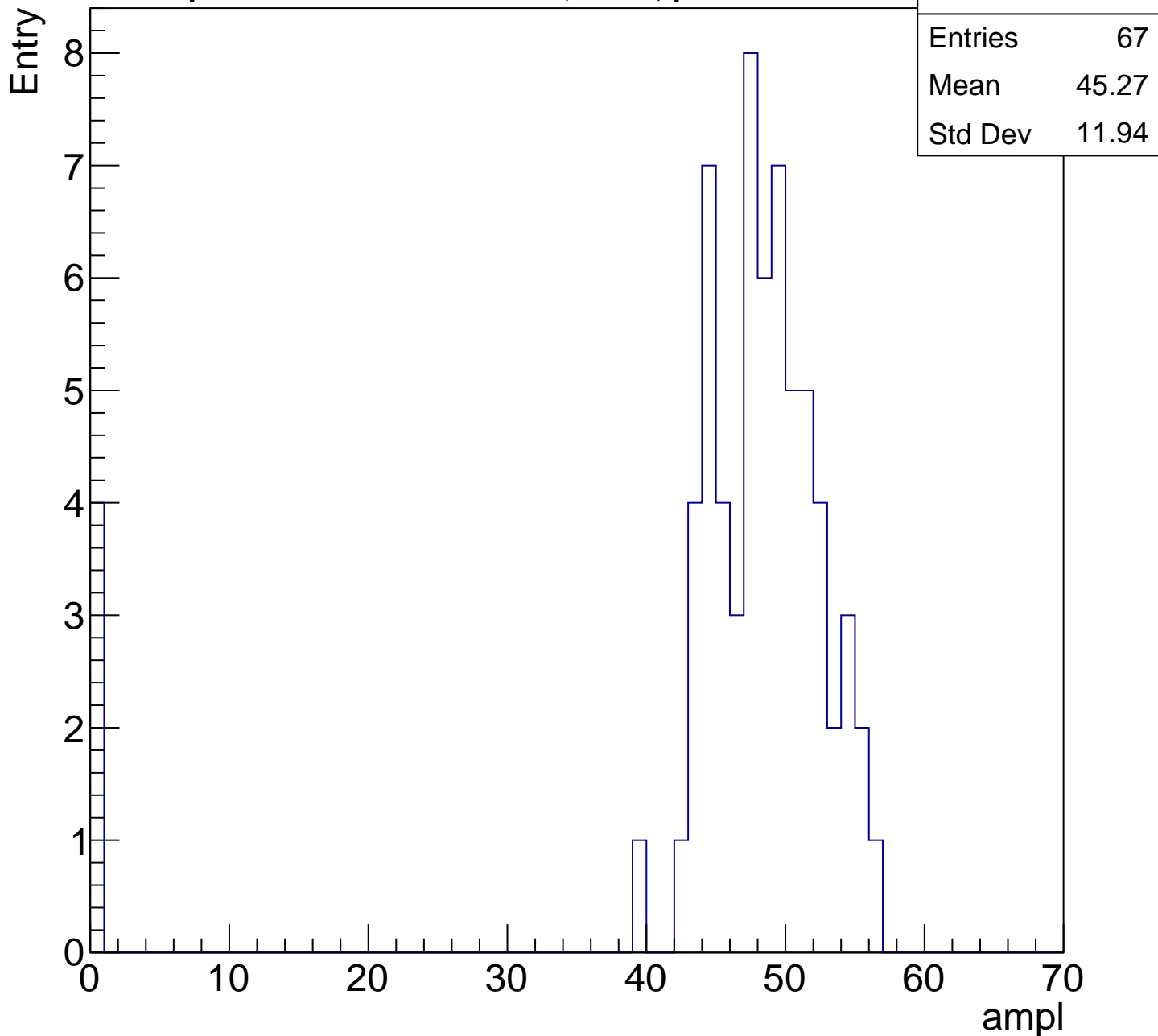
Gaus mean : 42.1313

Gaus Width: 3.9389



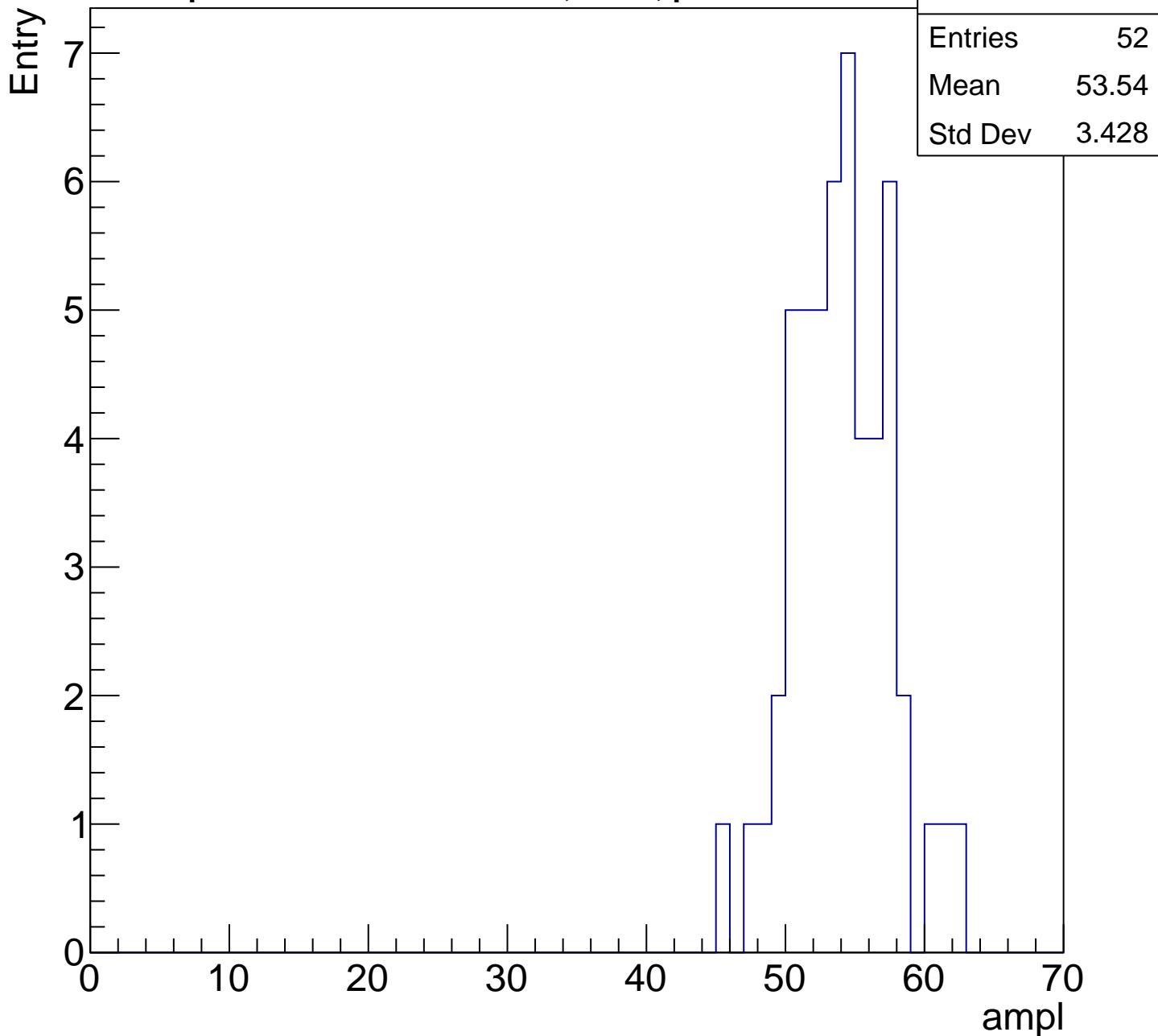
B1L103S, U6-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2



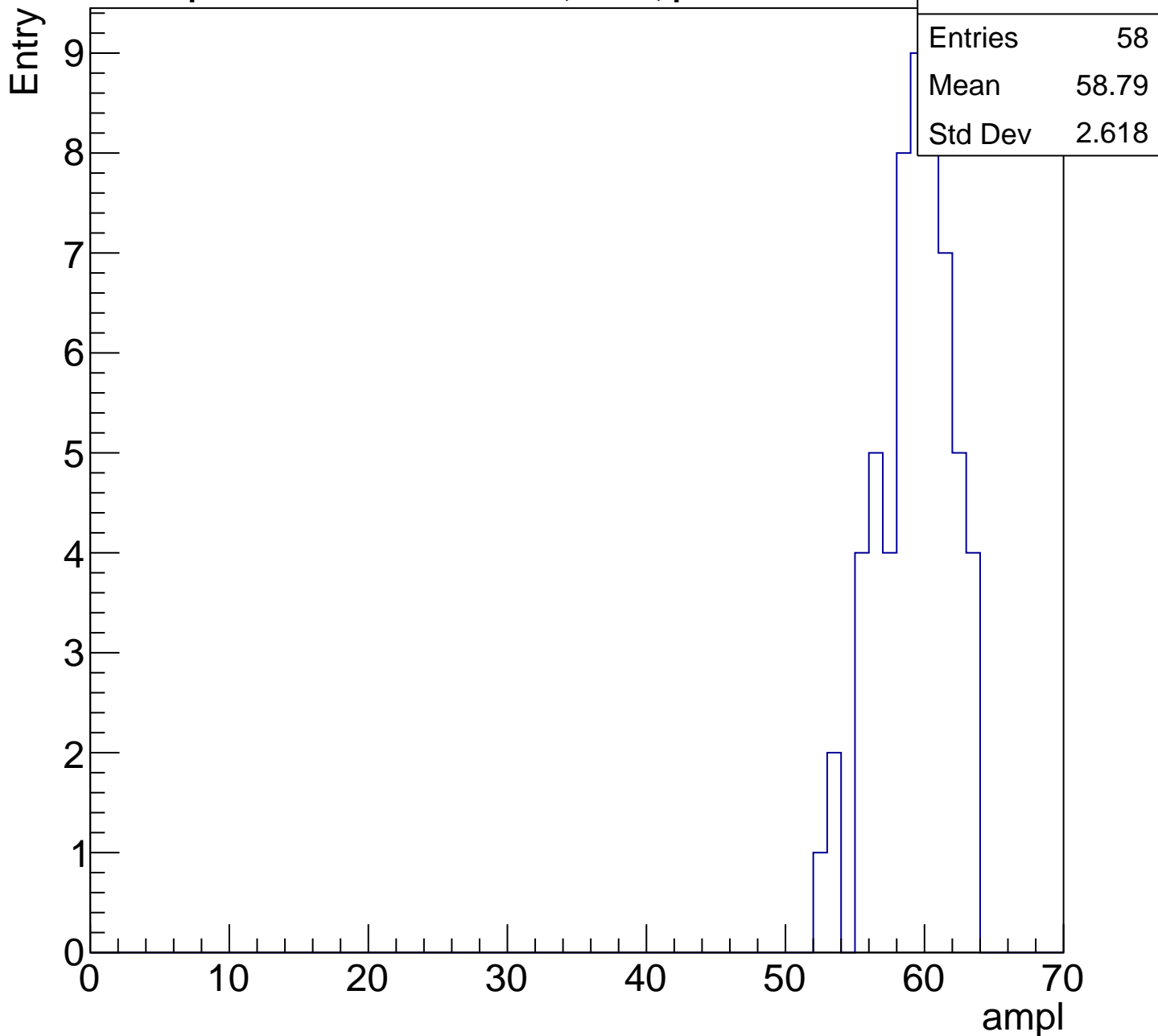
B1L103S, U6-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

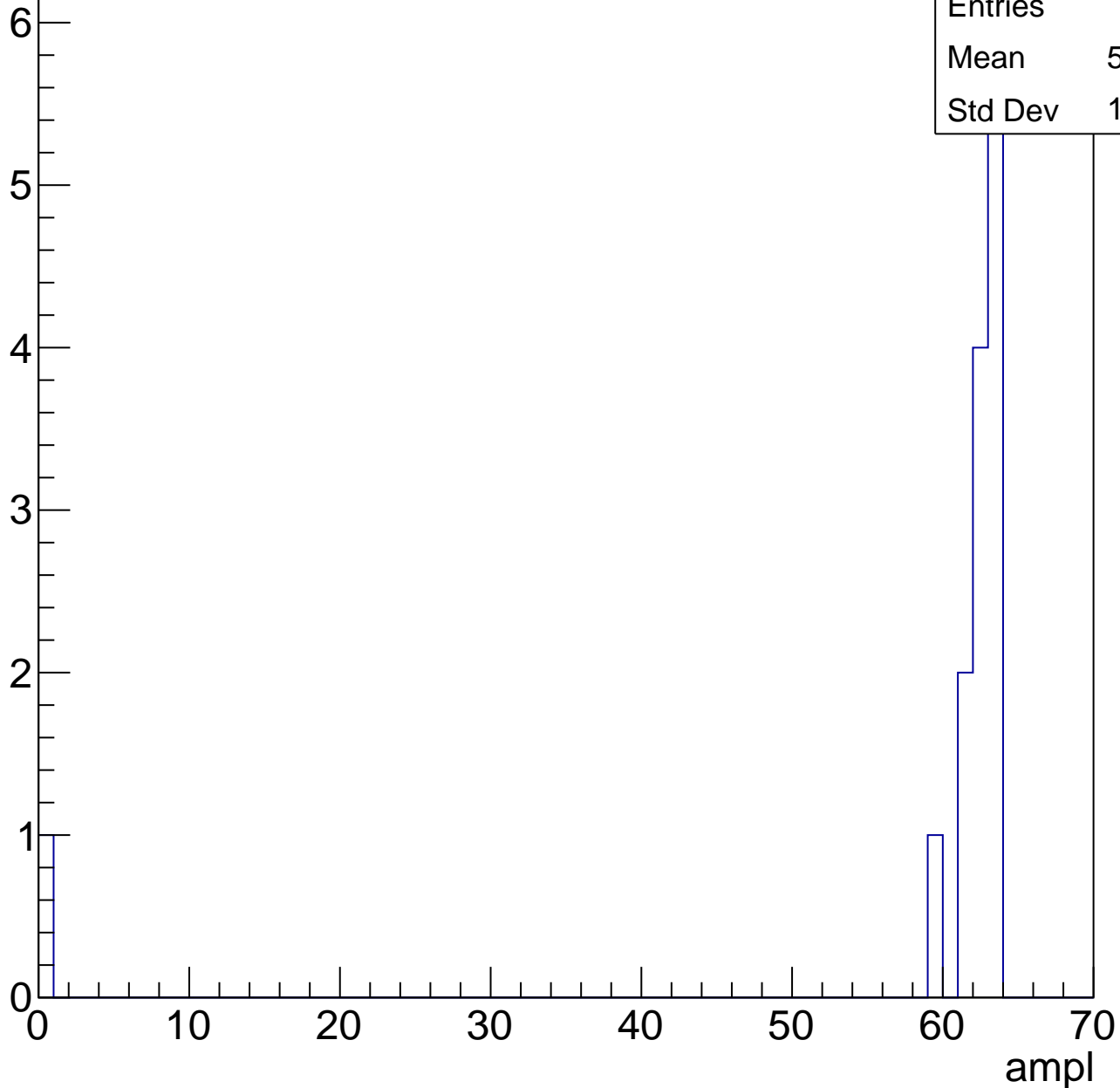


B1L103S, U6-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.03



B1L103S, U6-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch44, adc0

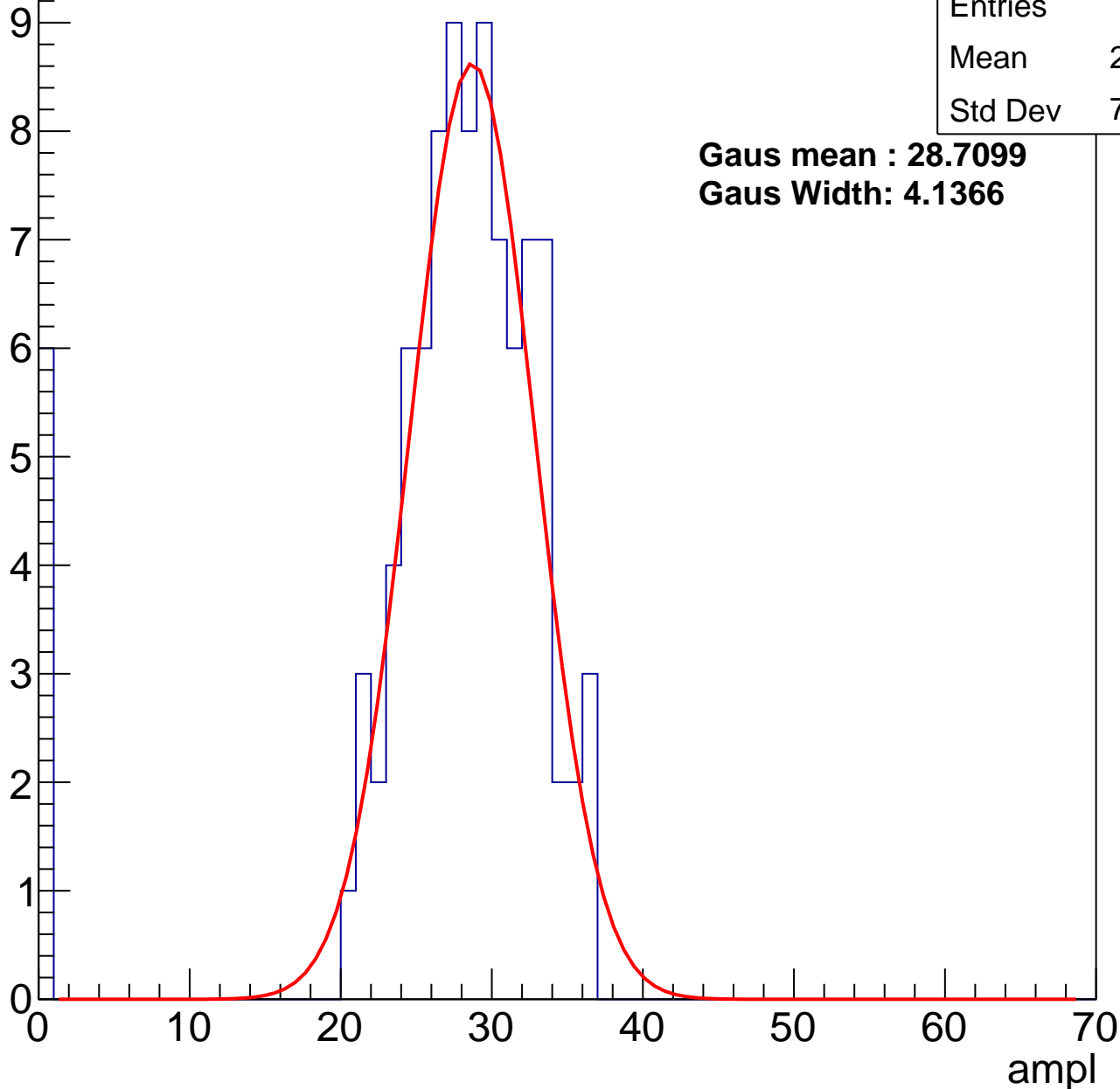
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	96
Mean	26.52
Std Dev	7.778

Gaus mean : 28.7099

Gaus Width: 4.1366



B1L103S, U6-ch44, adc1

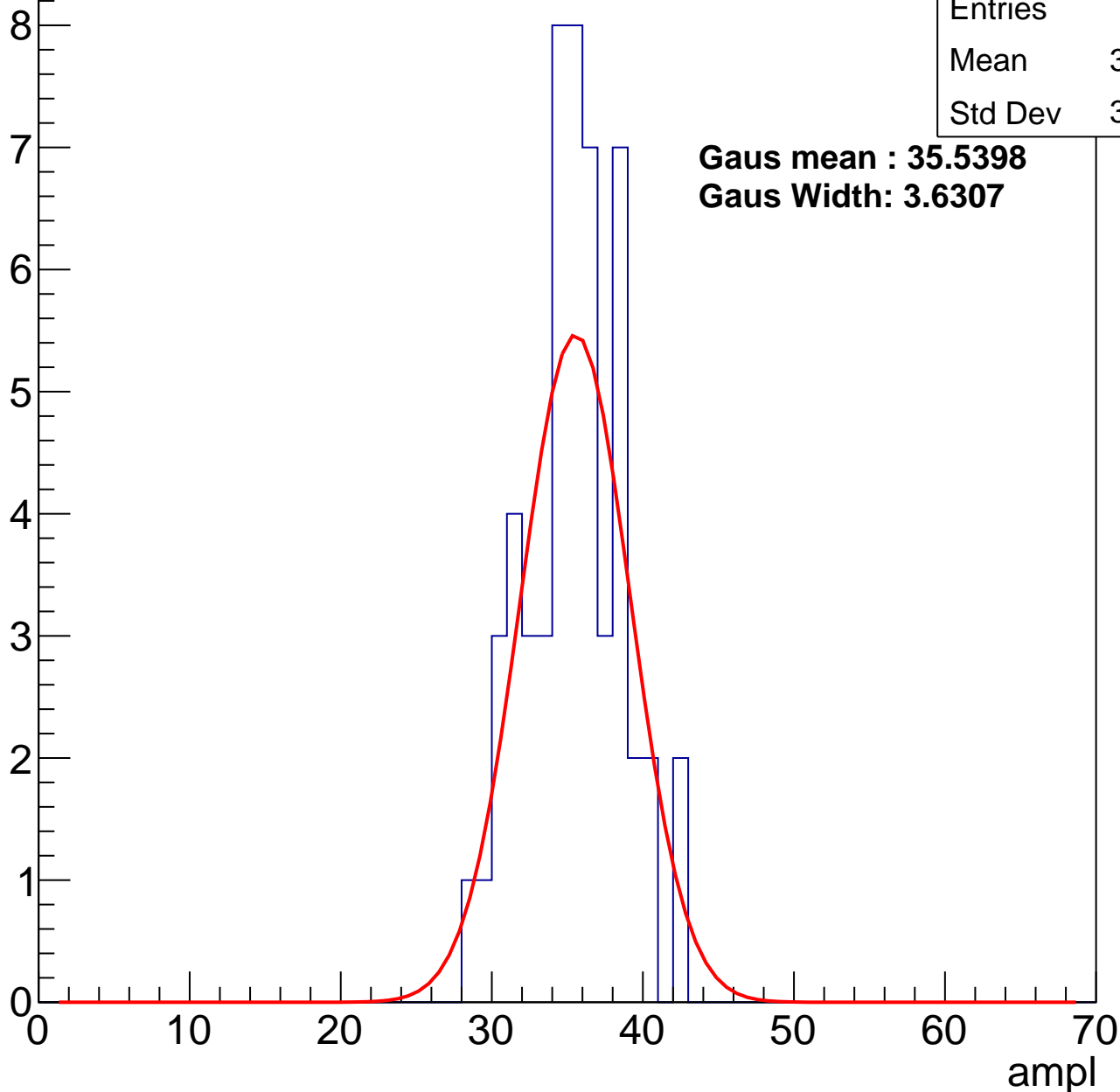
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	34.98
Std Dev	3.142

Gaus mean : 35.5398

Gaus Width: 3.6307



B1L103S, U6-ch44, adc2

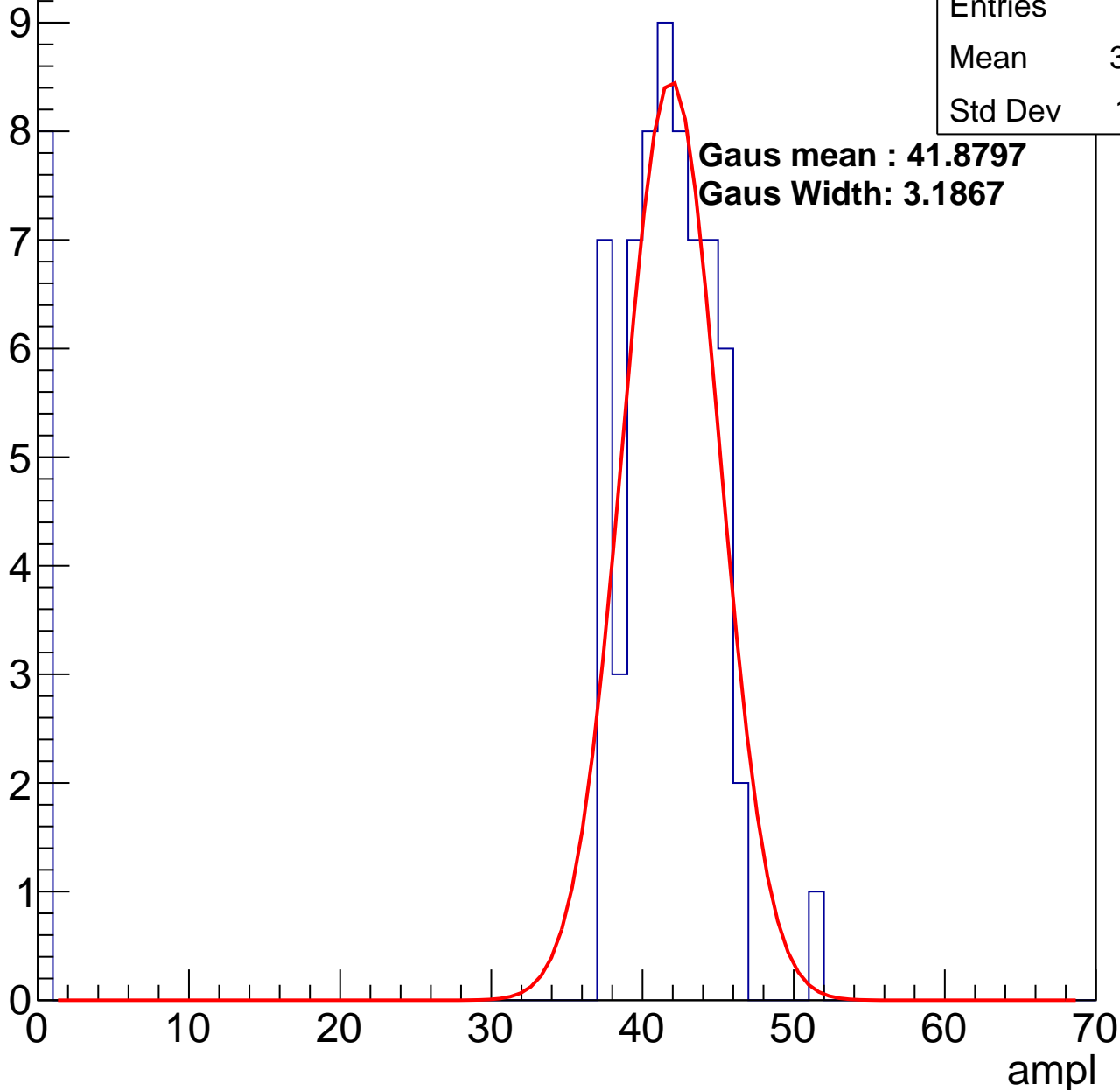
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.89
Std Dev	13.21

Gaus mean : 41.8797

Gaus Width: 3.1867



B1L103S, U6-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

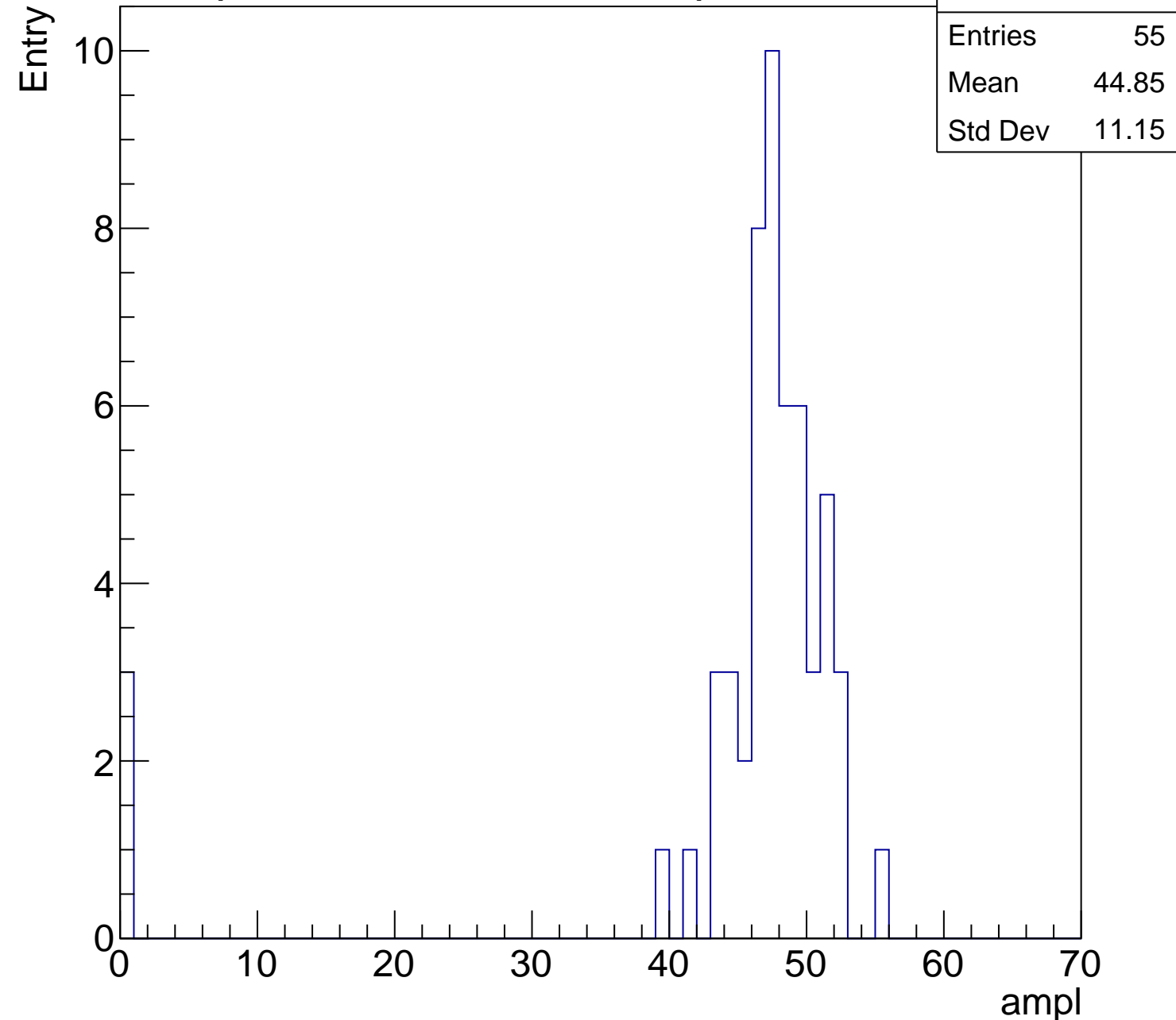
Entries	55
Mean	44.85
Std Dev	11.15

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

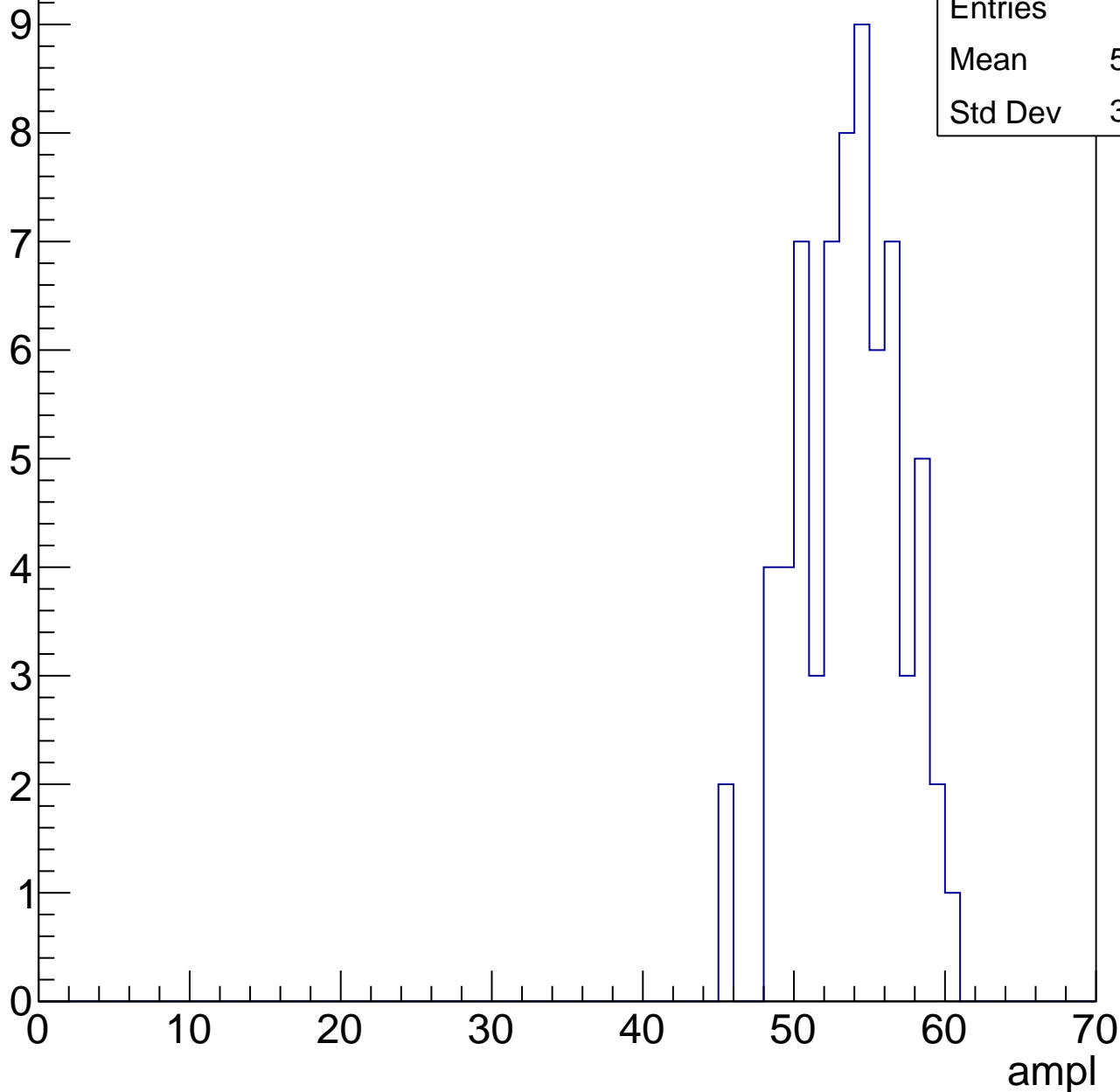


B1L103S, U6-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.18
Std Dev	3.347

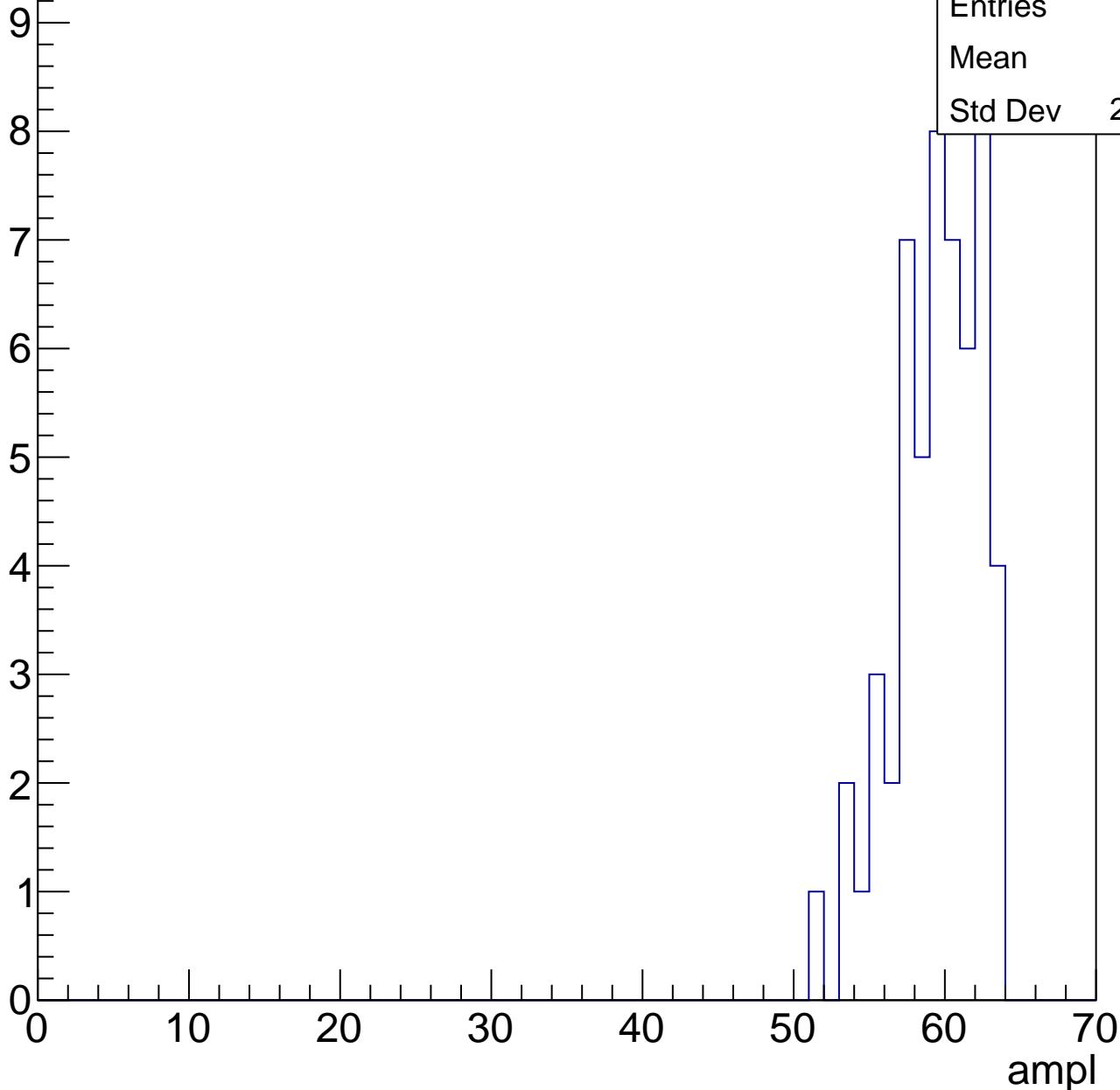


B1L103S, U6-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	59
Std Dev	2.816

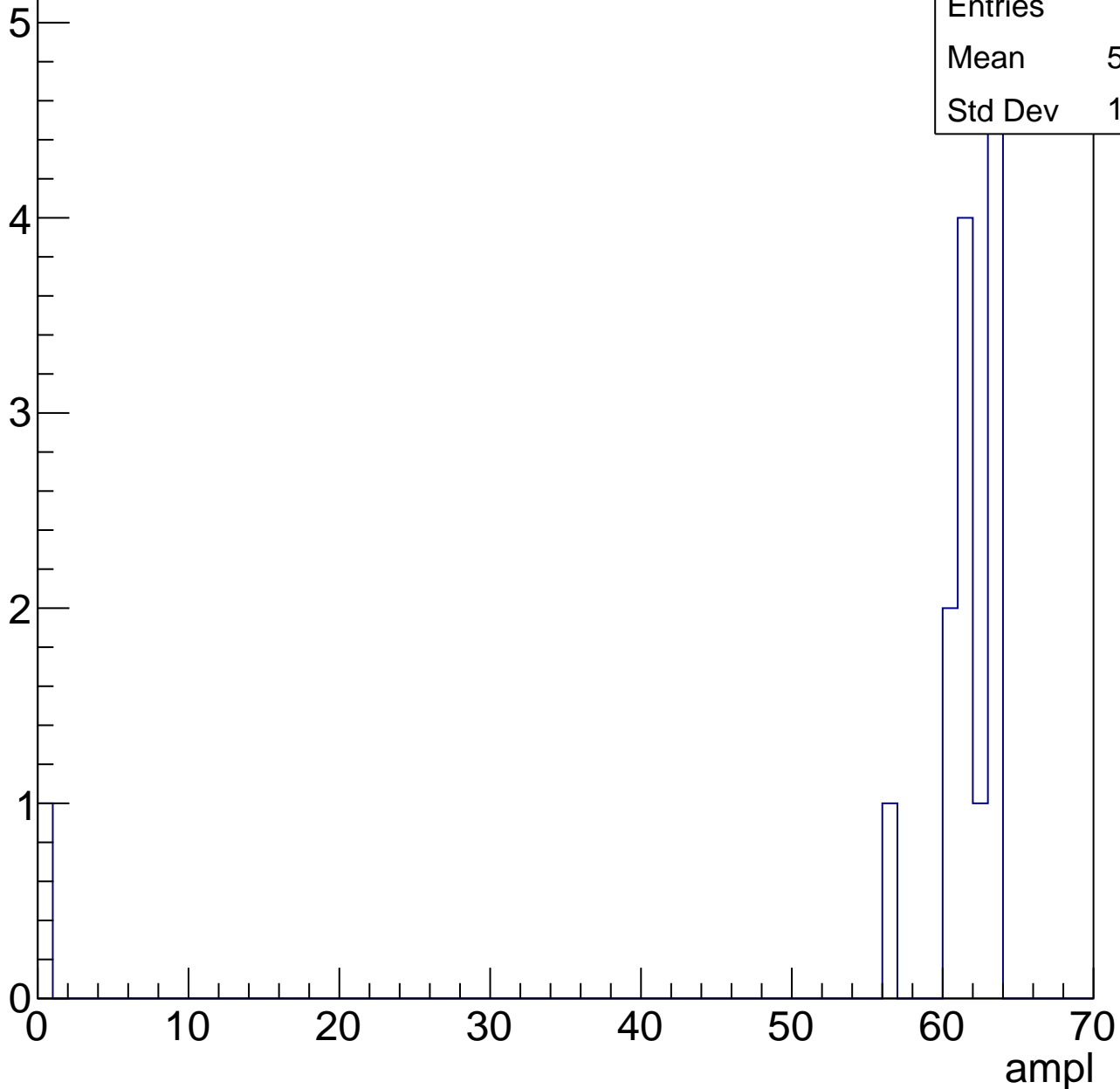


B1L103S, U6-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

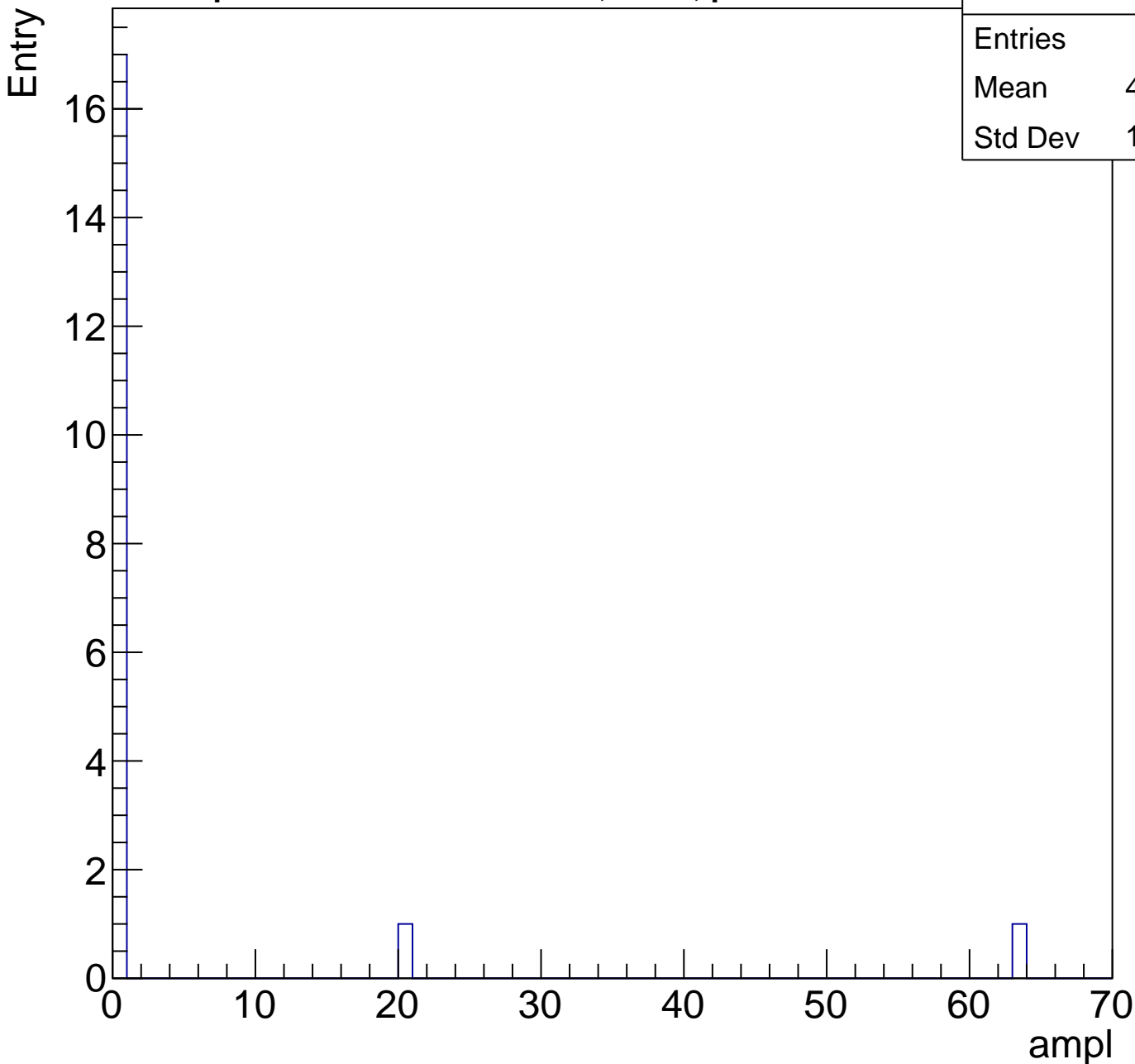
Entries	14
Mean	56.93
Std Dev	15.89



B1L103S, U6-ch44, adc7

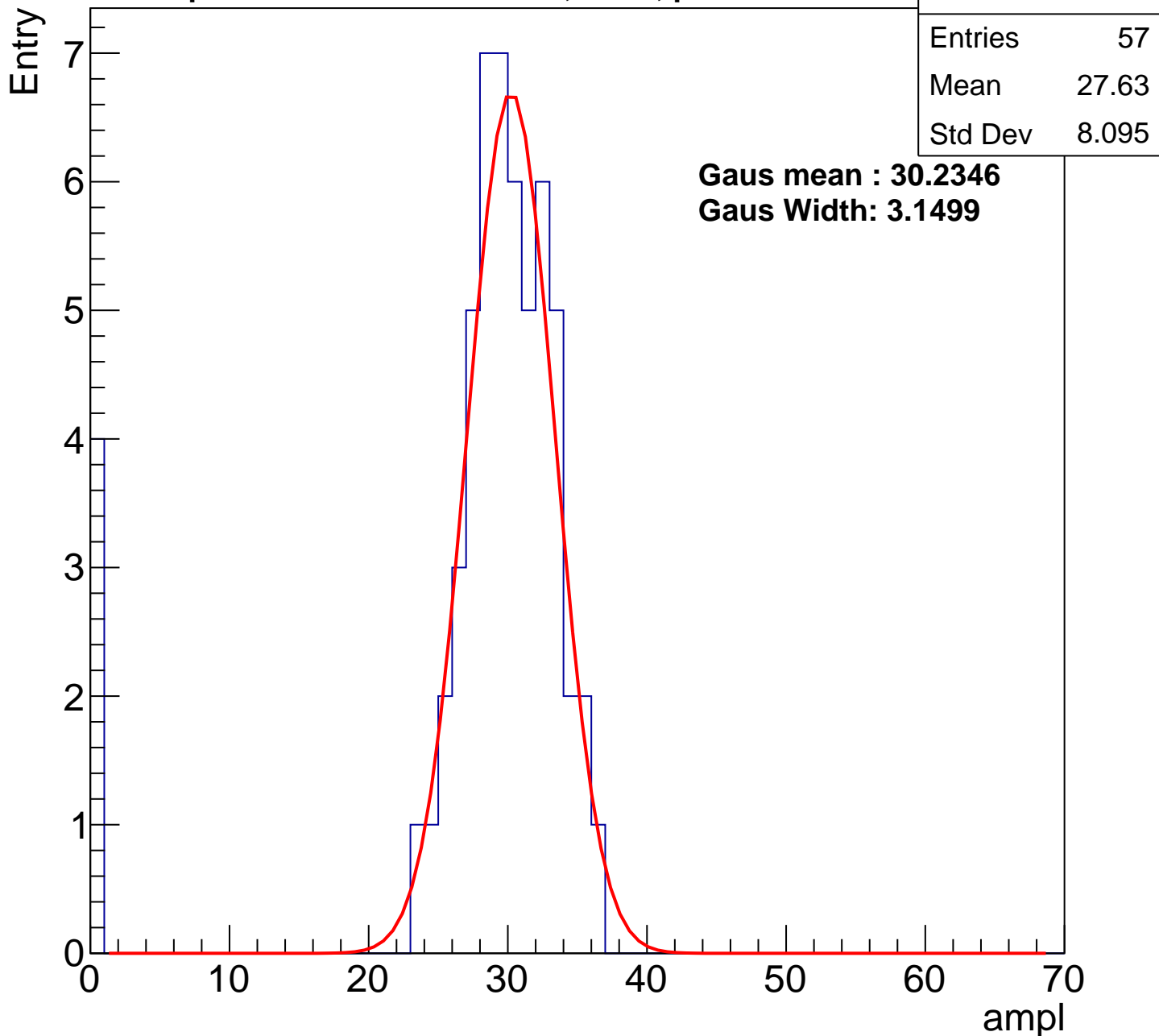
calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.368
Std Dev	14.52



B1L103S, U6-ch45, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch45, adc1

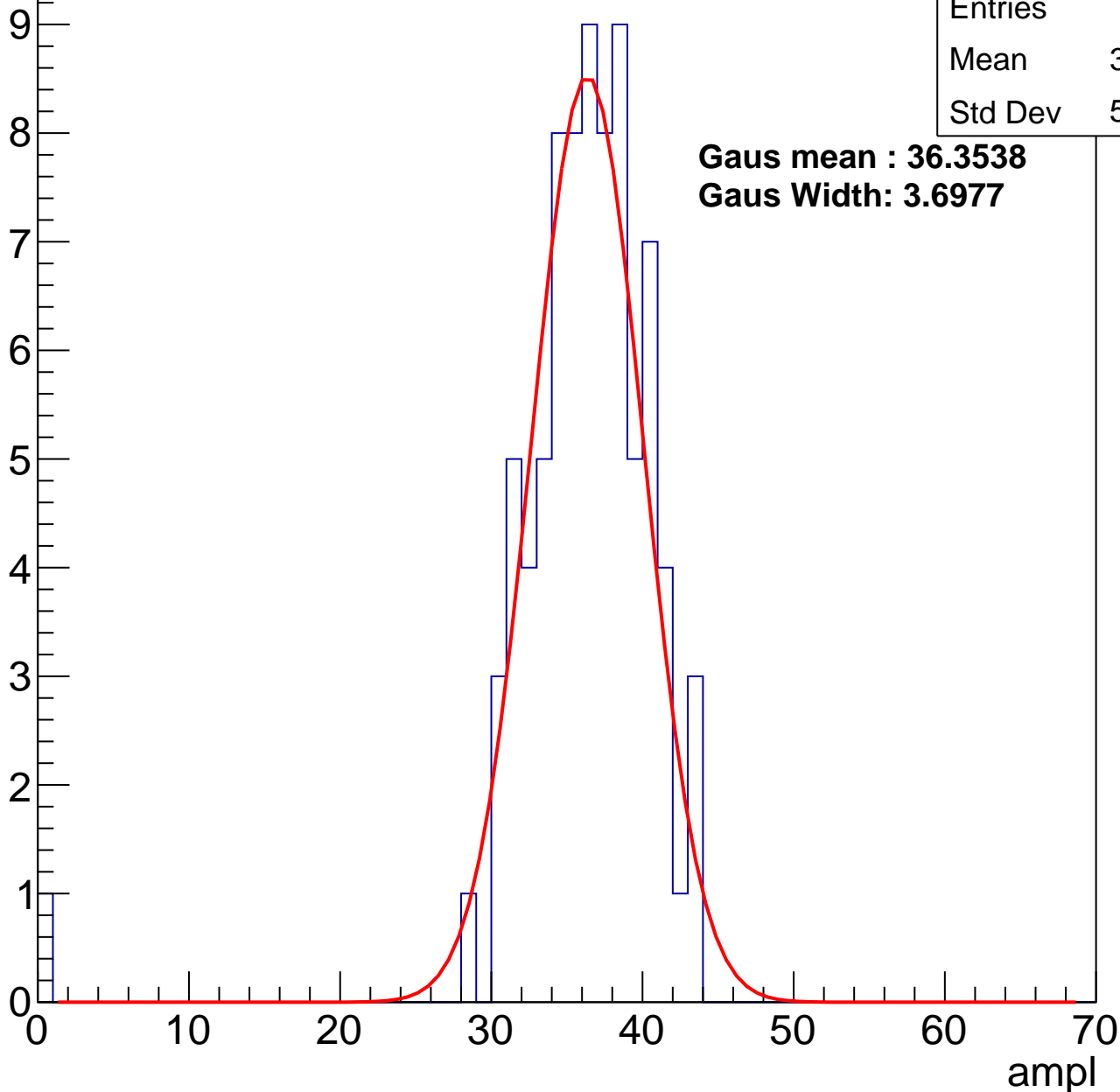
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	35.68
Std Dev	5.232

Gaus mean : 36.3538

Gaus Width: 3.6977



B1L103S, U6-ch45, adc2

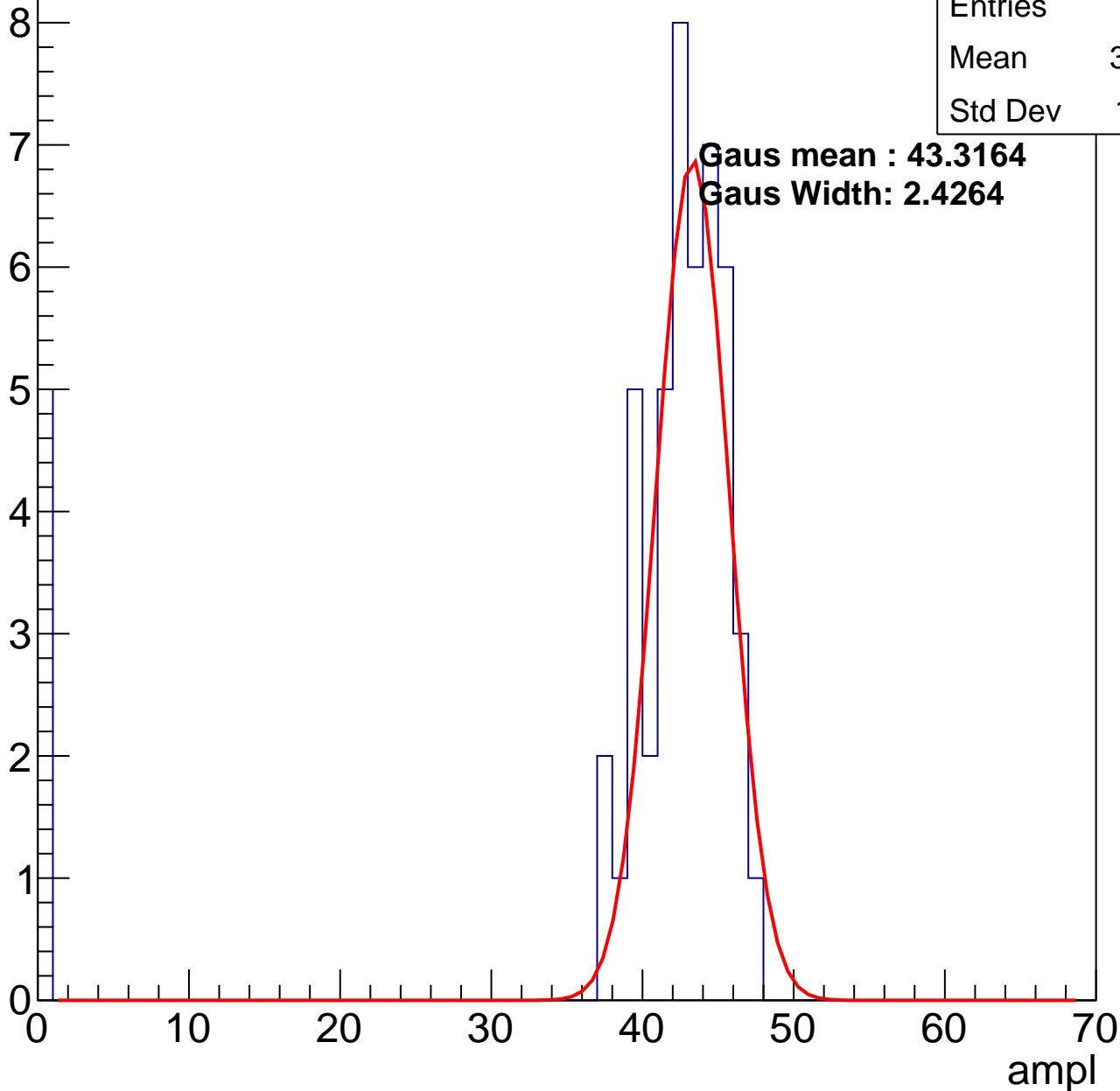
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	38.22
Std Dev	12.81

Gaus mean : 43.3164

Gaus Width: 2.4264

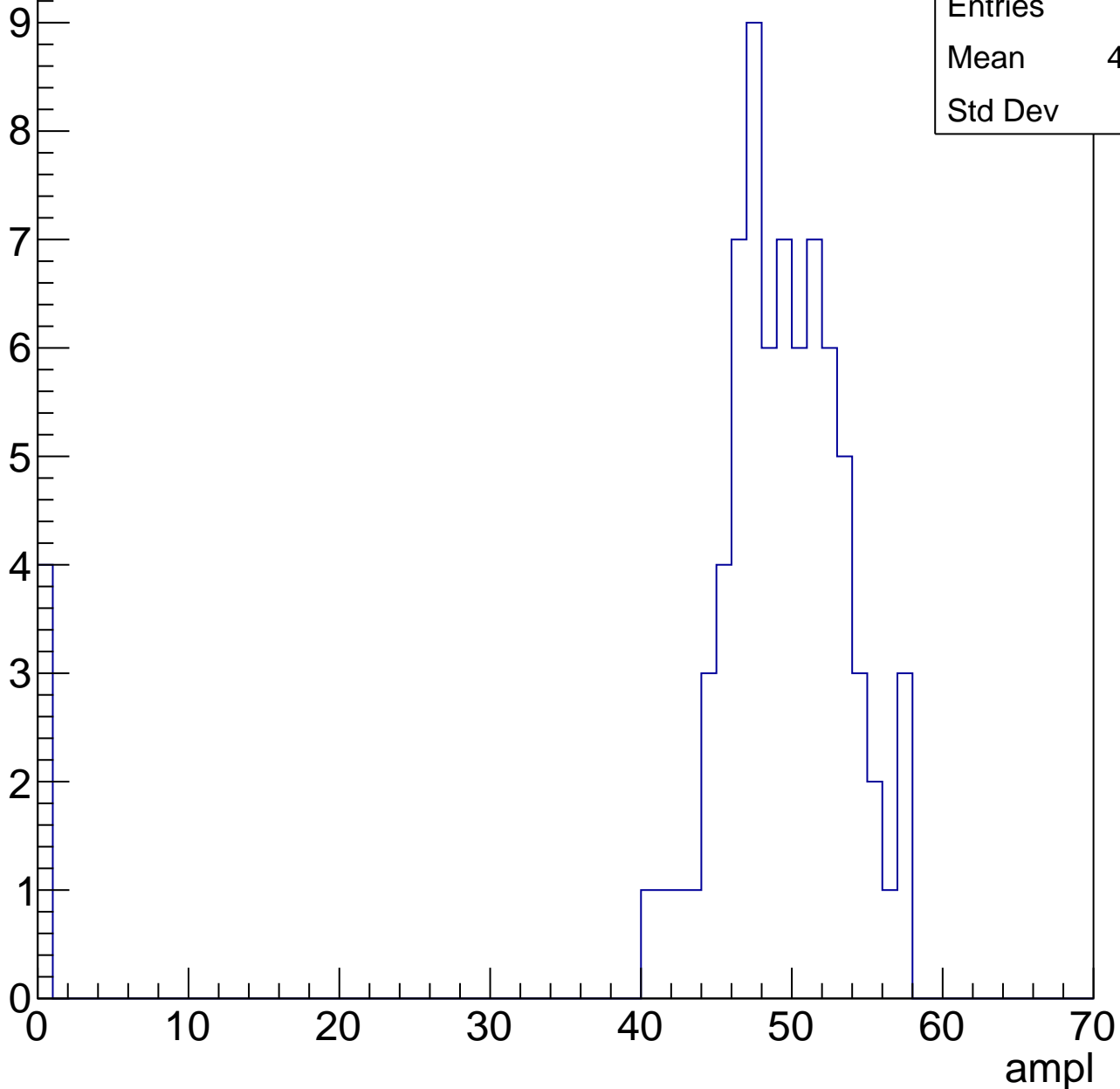


B1L103S, U6-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

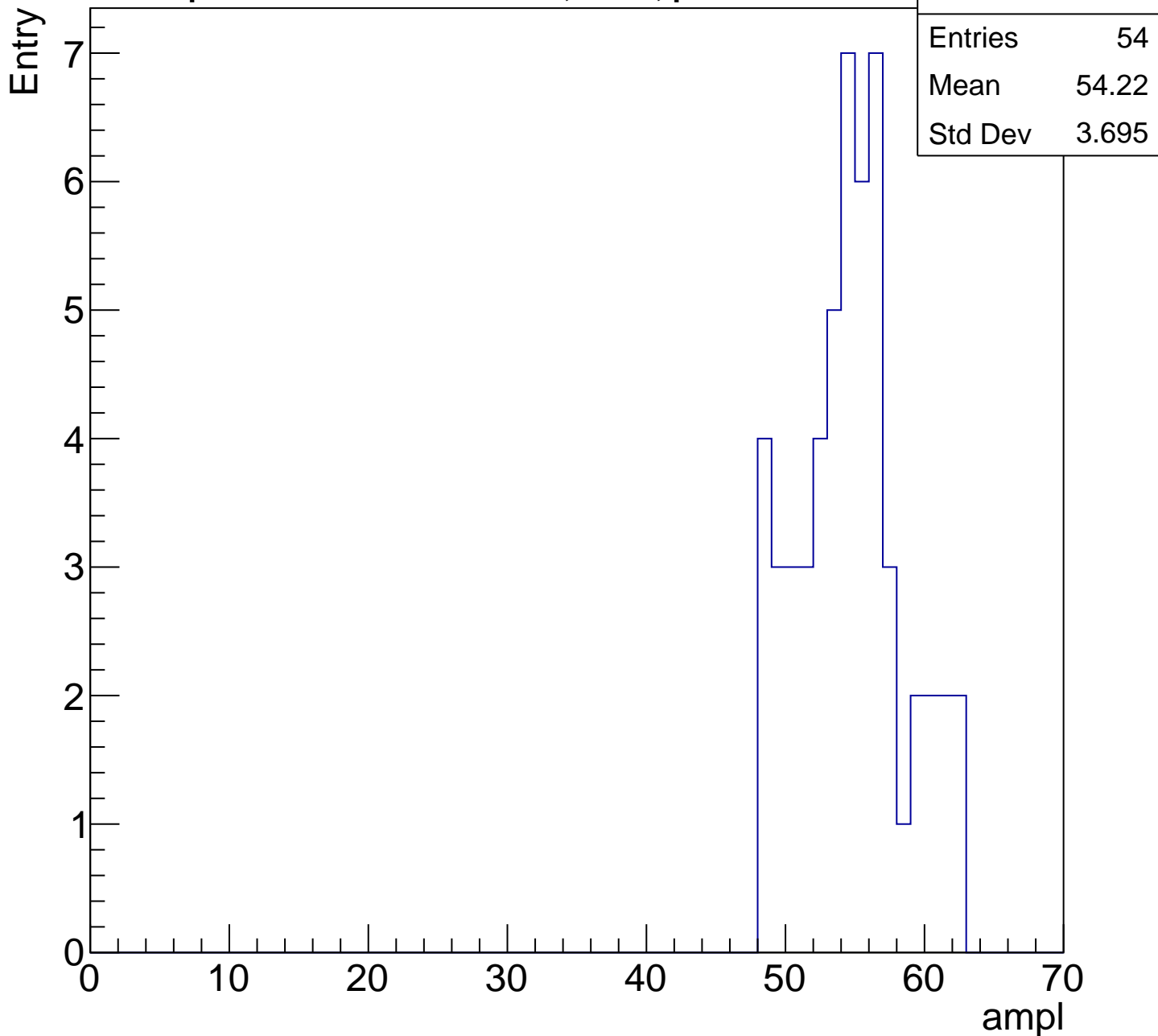
Entry

Entries	77
Mean	46.58
Std Dev	11.5



B1L103S, U6-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

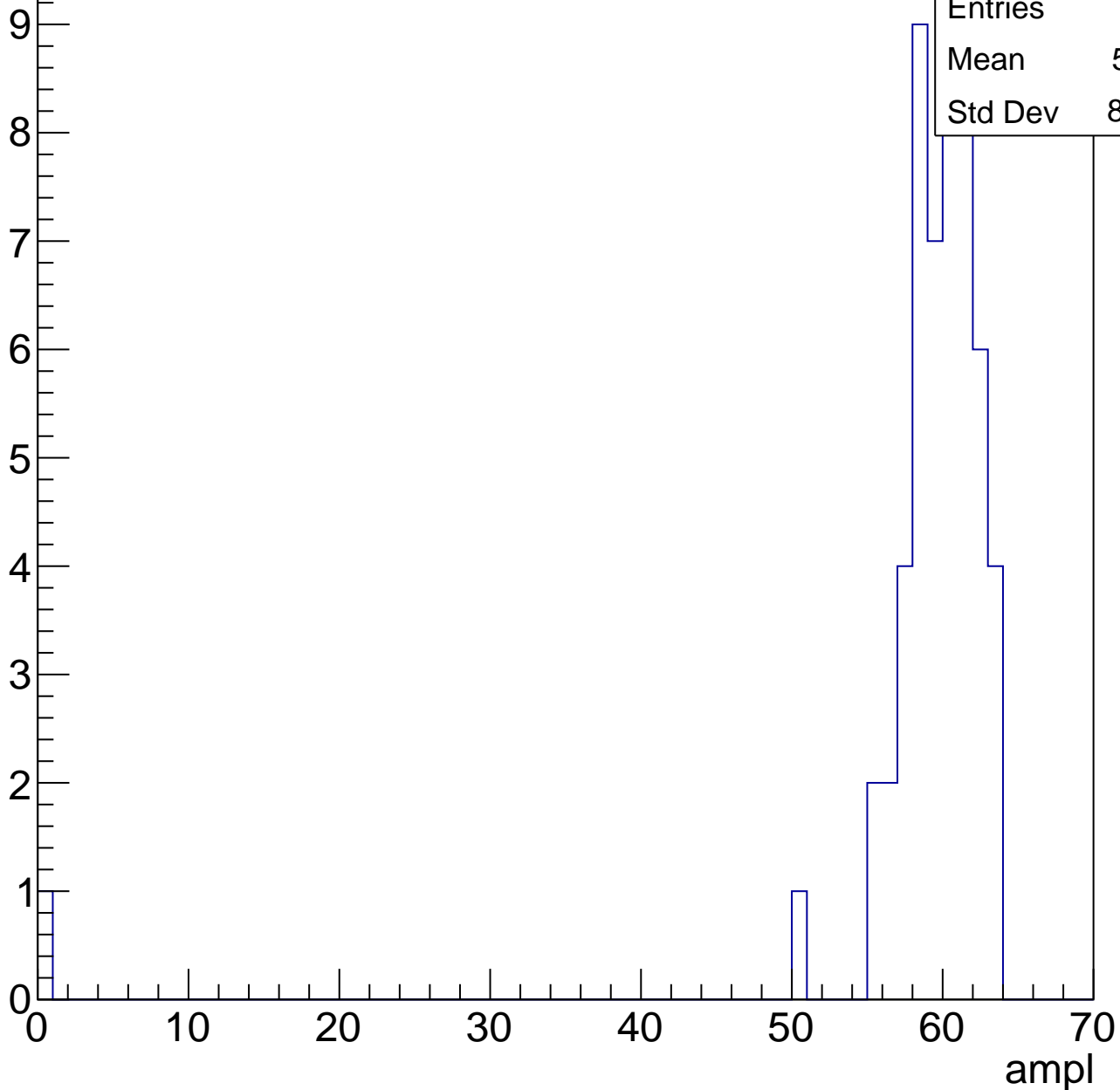


B1L103S, U6-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.21
Std Dev	8.506

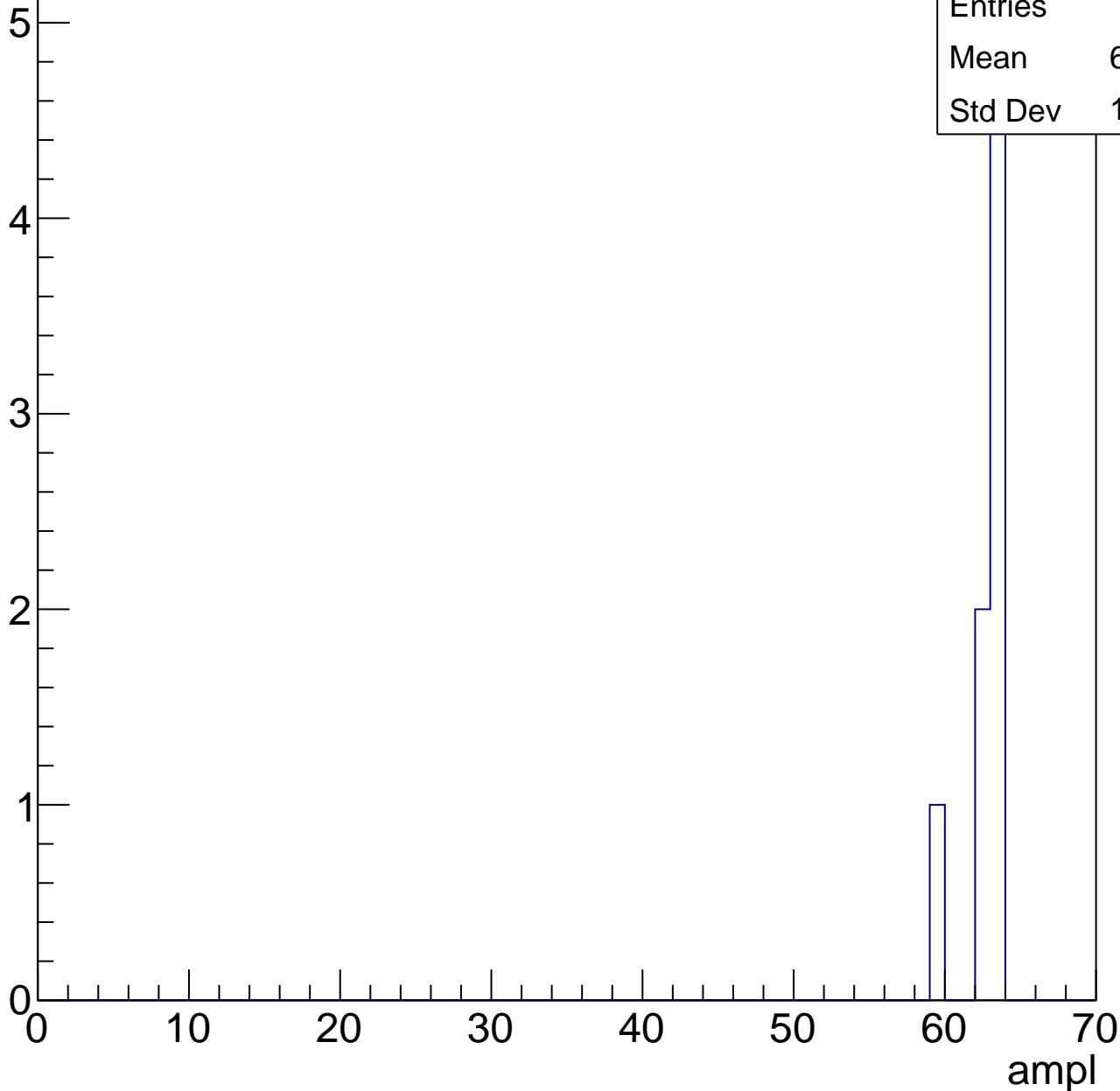


B1L103S, U6-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	8
Mean	62.25
Std Dev	1.299

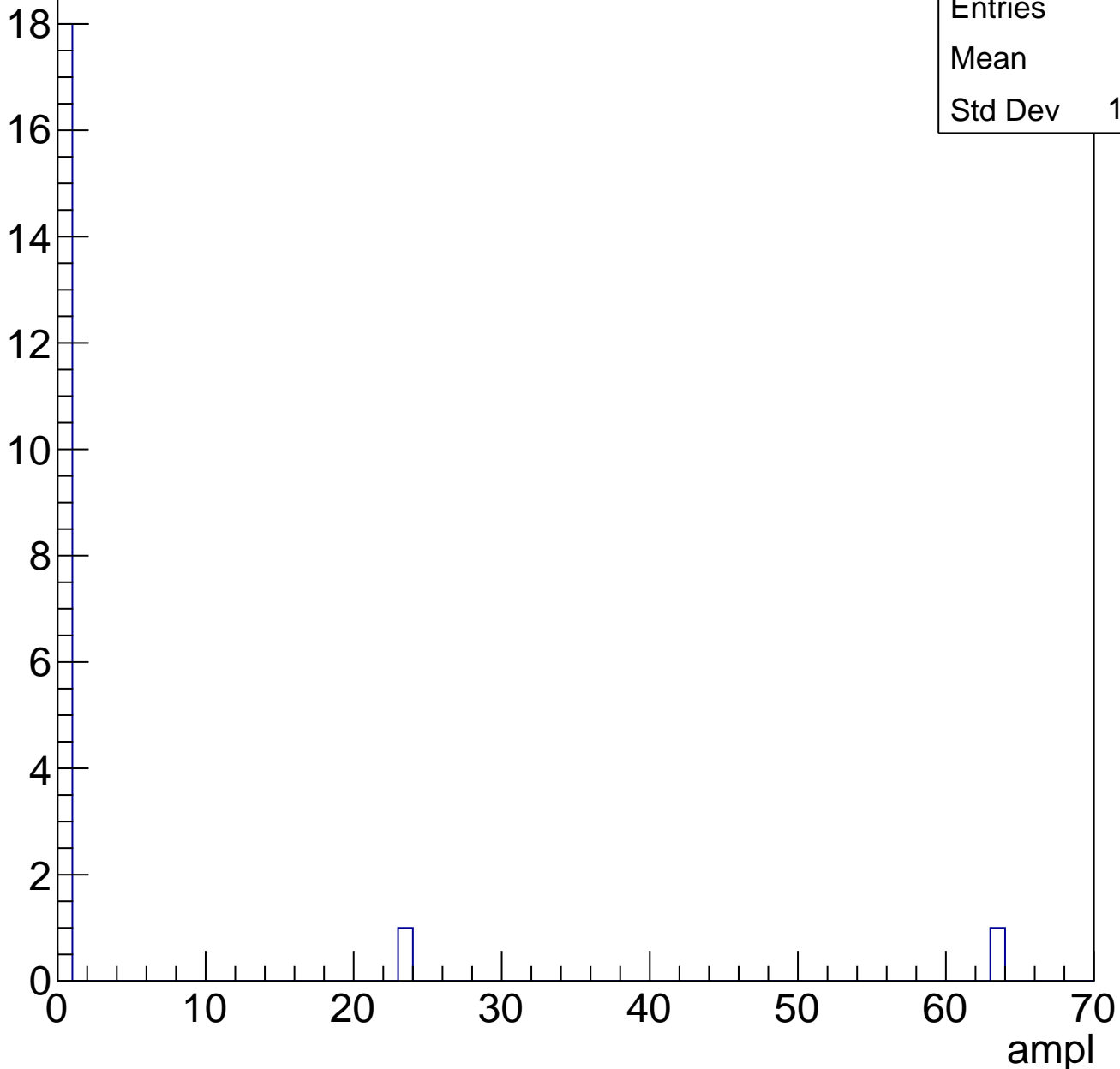


B1L103S, U6-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.3
Std Dev	14.37

Entry



B1L103S, U6-ch46, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	108
Mean	23.69
Std Dev	10.39

Gaus mean : 28.2736

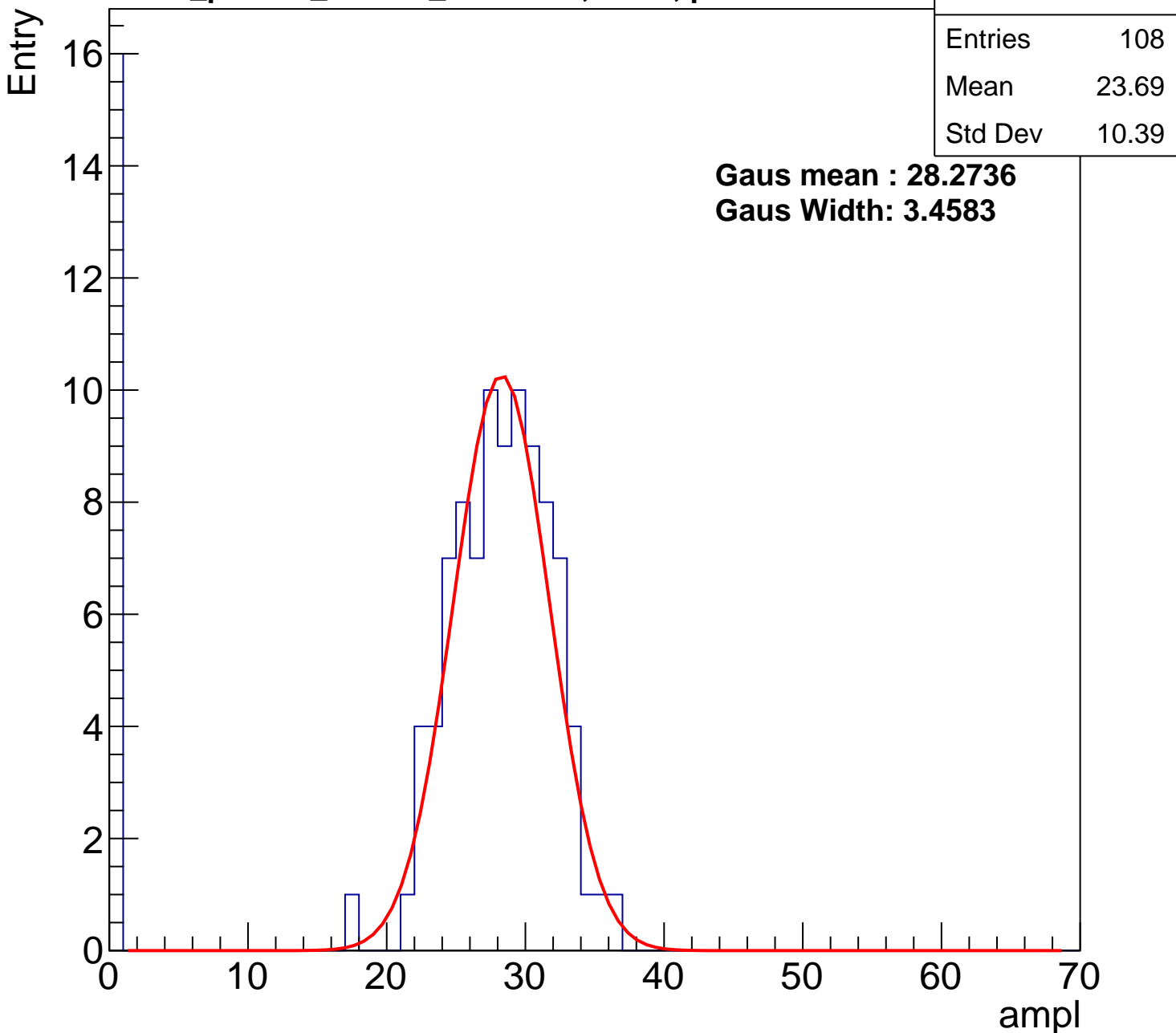
Gaus Width: 3.4583

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch46, adc1

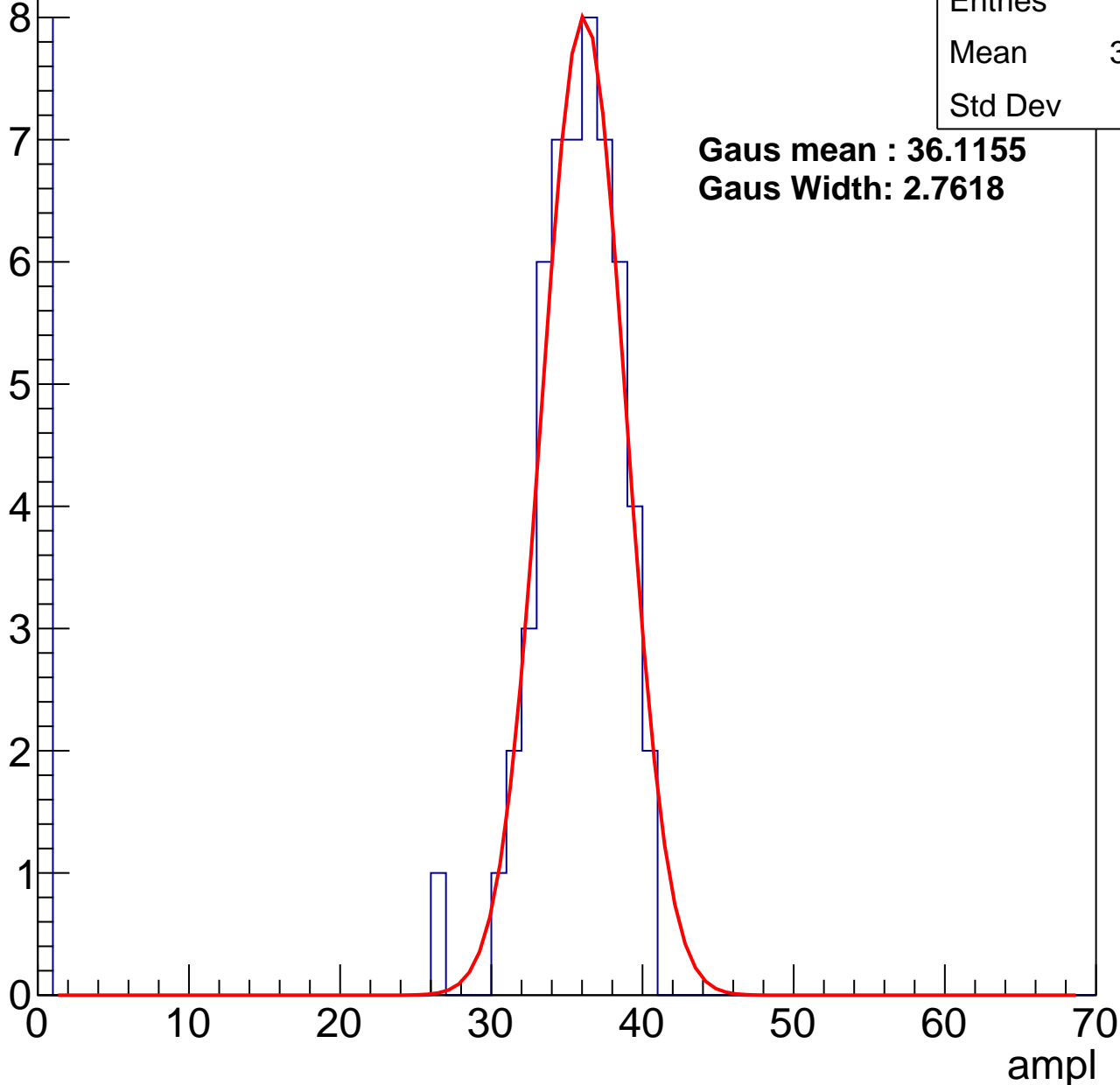
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	30.74
Std Dev	12.1

Gaus mean : 36.1155

Gaus Width: 2.7618



B1L103S, U6-ch46, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	37.03
Std Dev	13.18

Gaus mean : 41.9467

Gaus Width: 3.2801

Entry

10

8

6

4

2

0

0

10

20

30

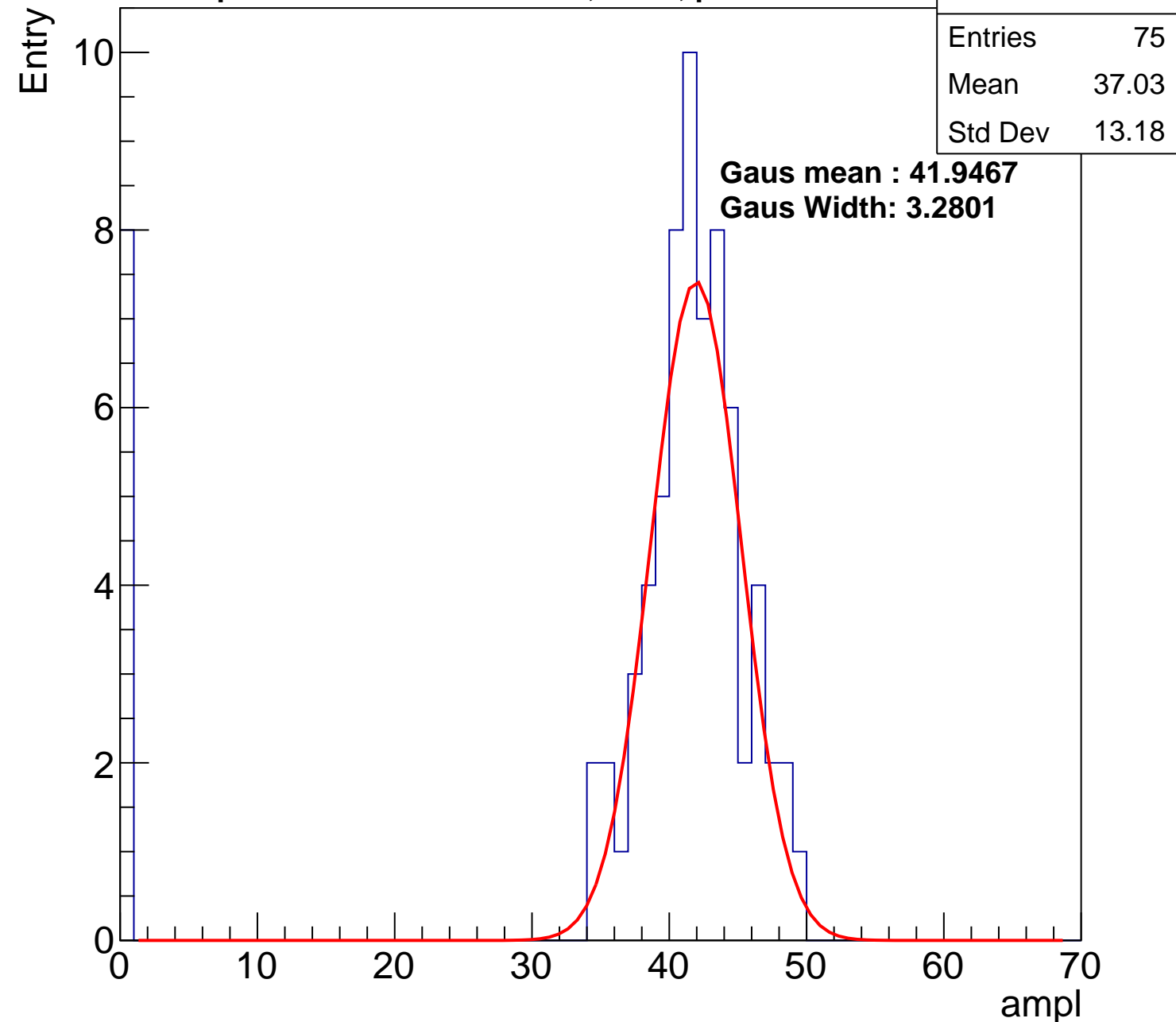
40

50

60

70

ampl

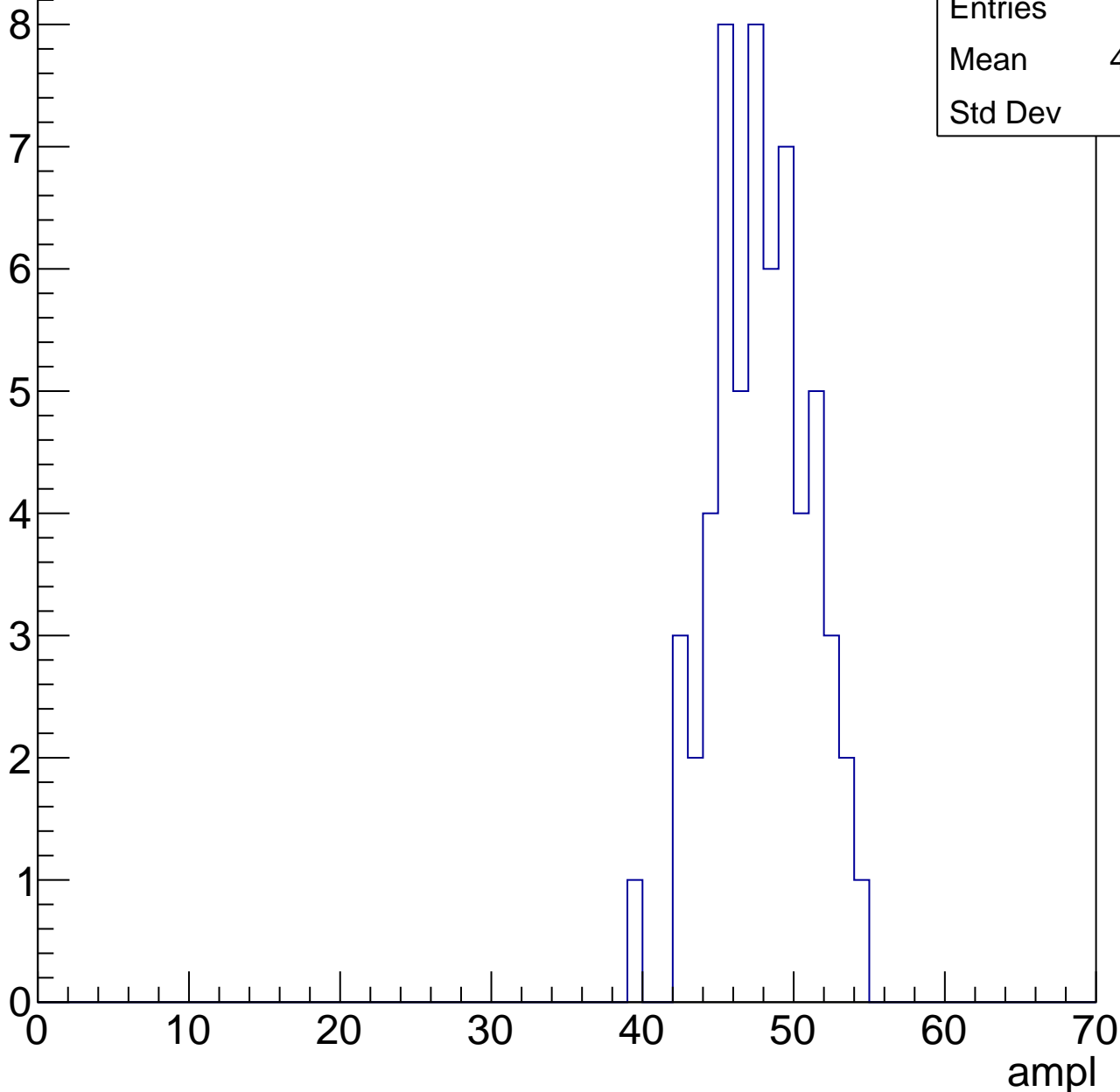


B1L103S, U6-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.37
Std Dev	3.14

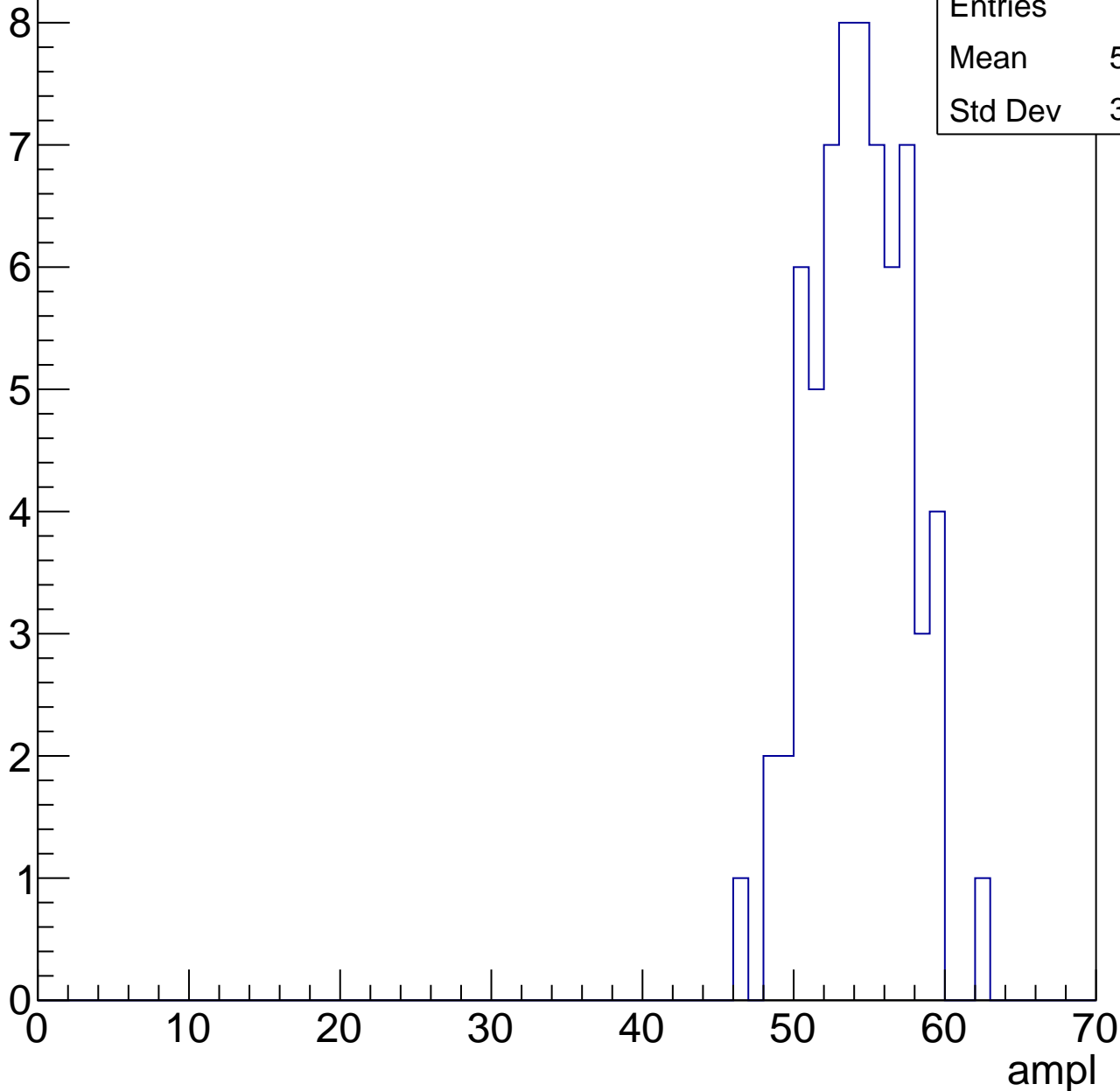


B1L103S, U6-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	53.84
Std Dev	3.156



B1L103S, U6-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

Entries	53
Mean	59.4
Std Dev	2.342

ampl

0

10

20

30

40

50

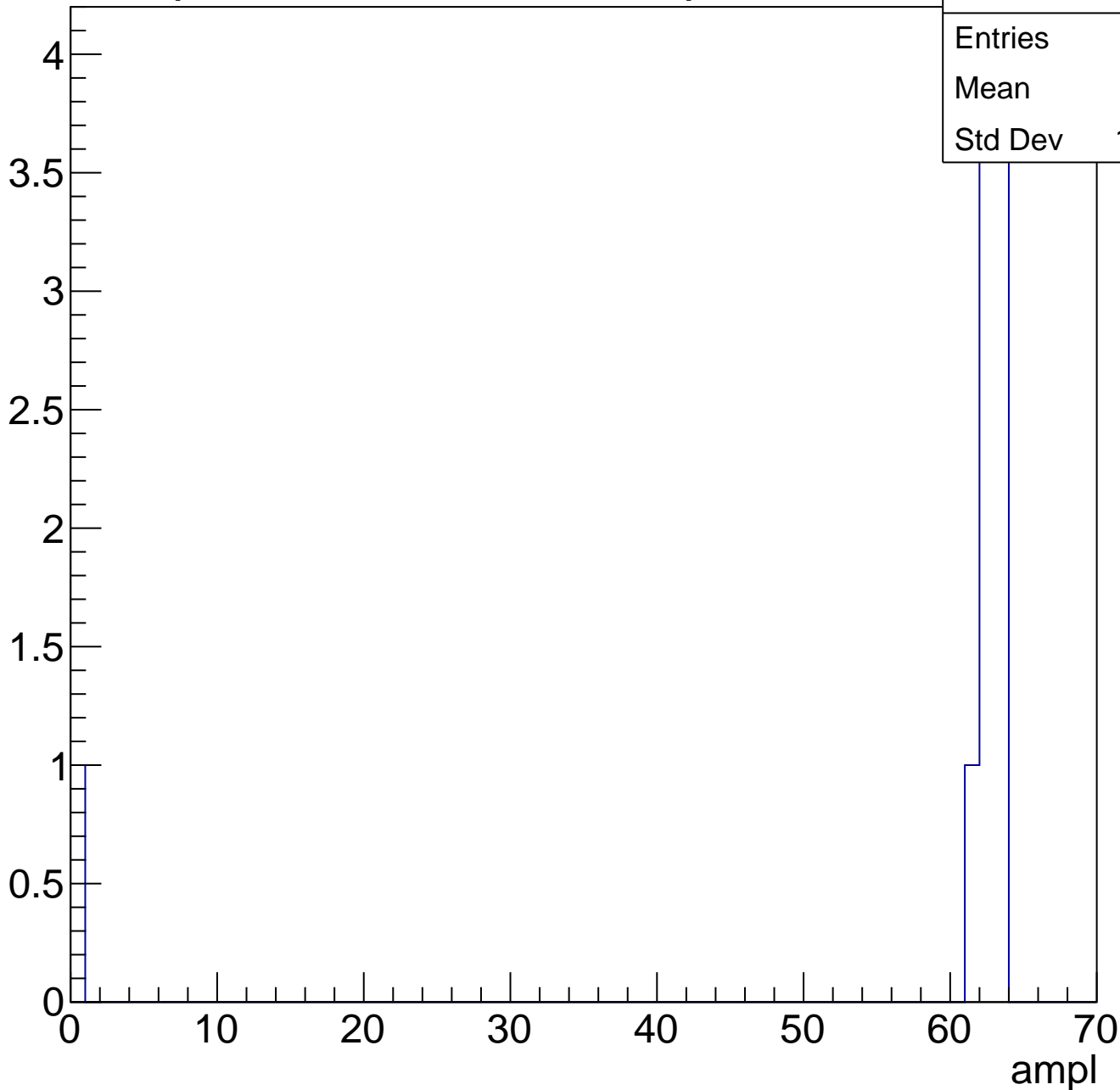
60

70

B1L103S, U6-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



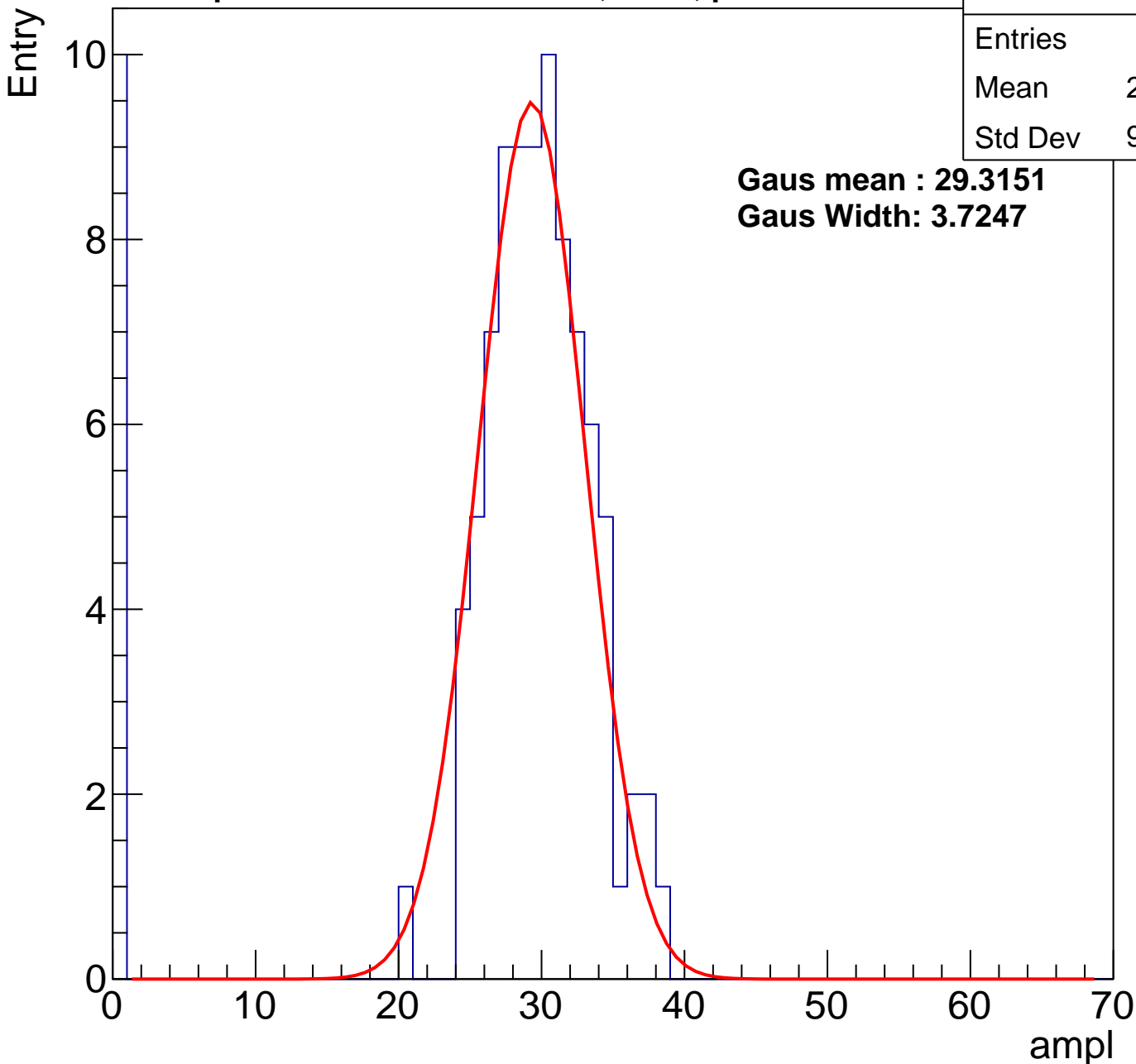
B1L103S, U6-ch47, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	26.44
Std Dev	9.584

Gaus mean : 29.3151

Gaus Width: 3.7247



B1L103S, U6-ch47, adc1

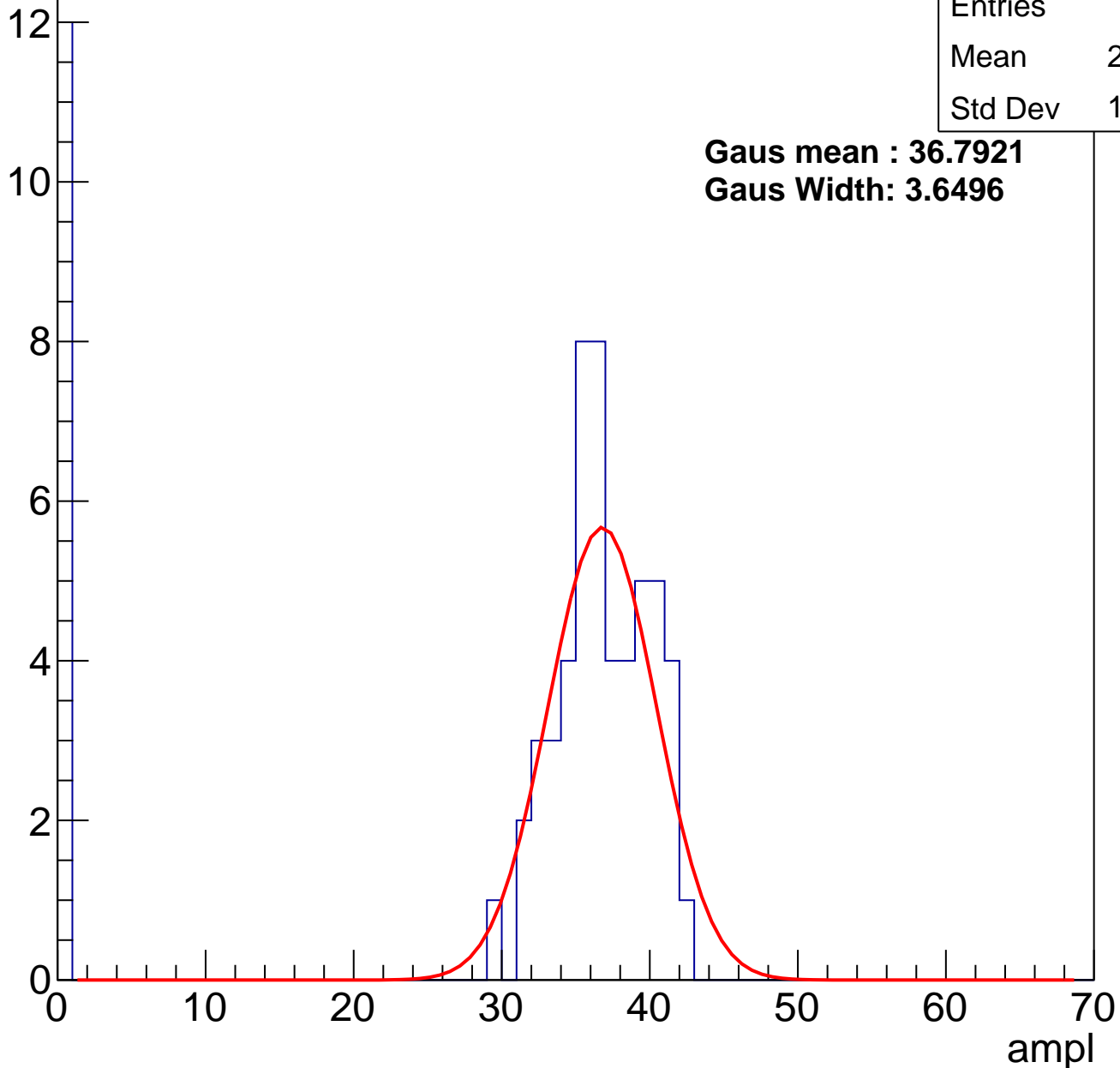
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	29.55
Std Dev	14.45

Gaus mean : 36.7921

Gaus Width: 3.6496

Entry



B1L103S, U6-ch47, adc2

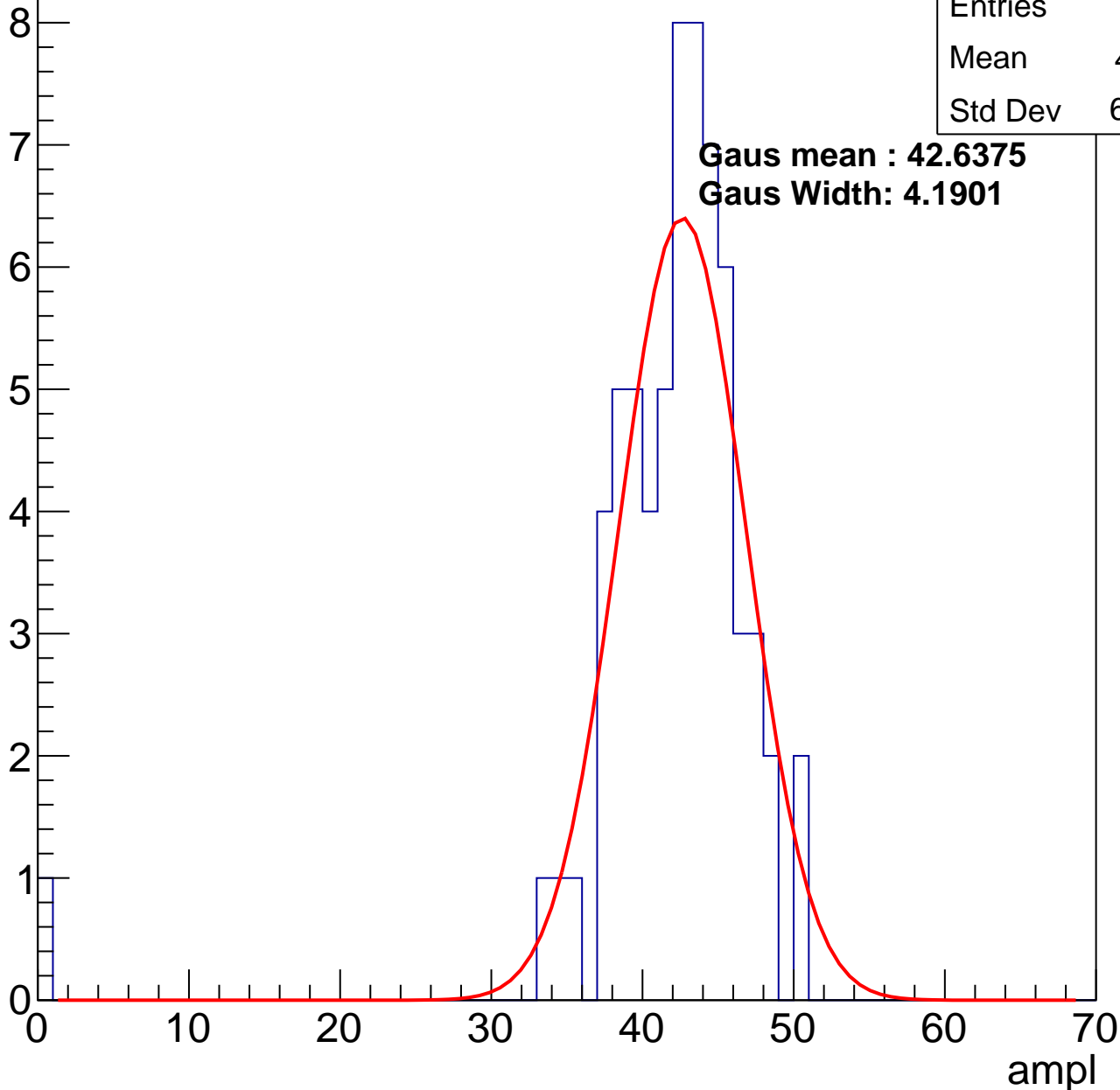
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.41
Std Dev	6.269

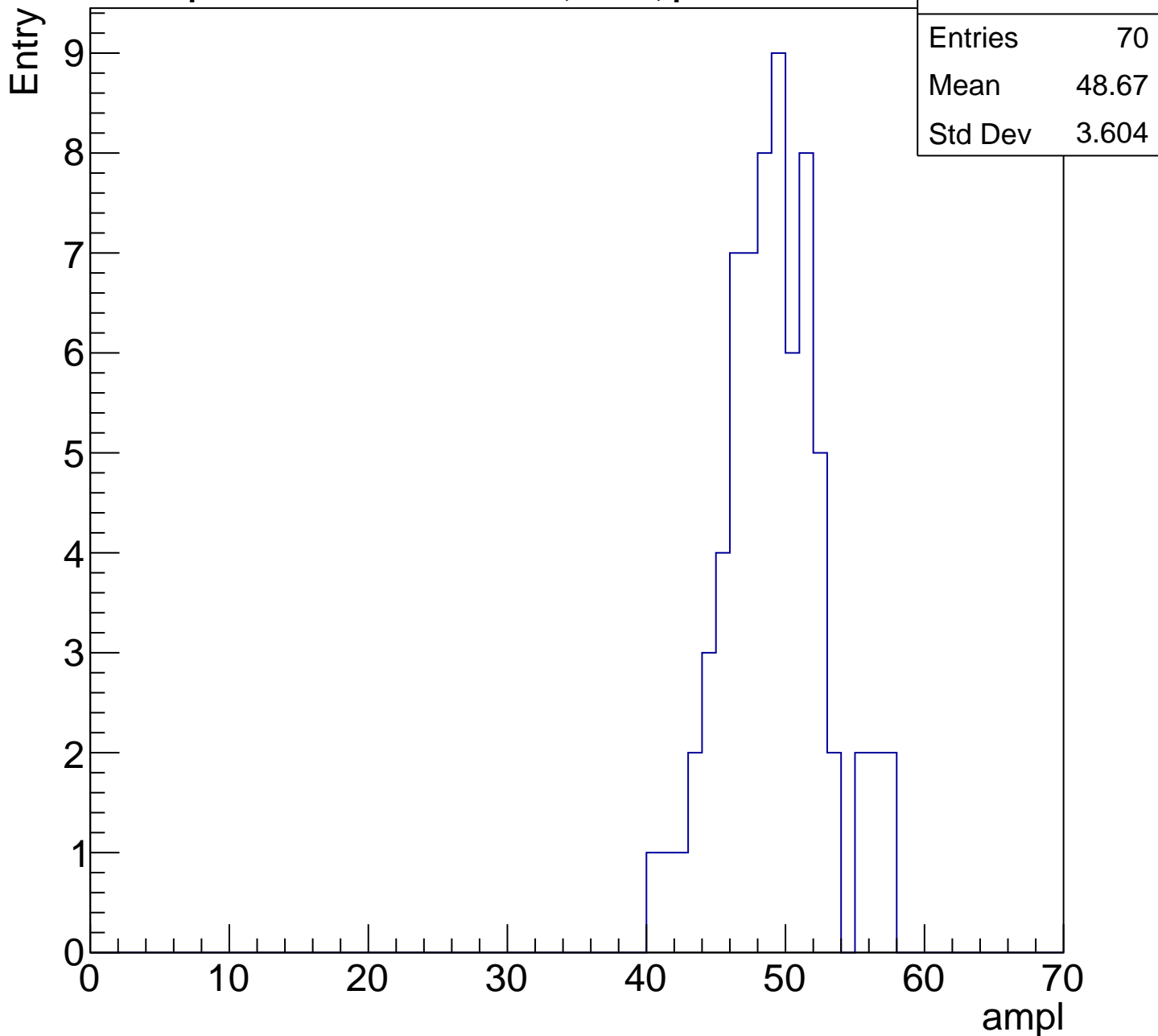
Gaus mean : 42.6375

Gaus Width: 4.1901



B1L103S, U6-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

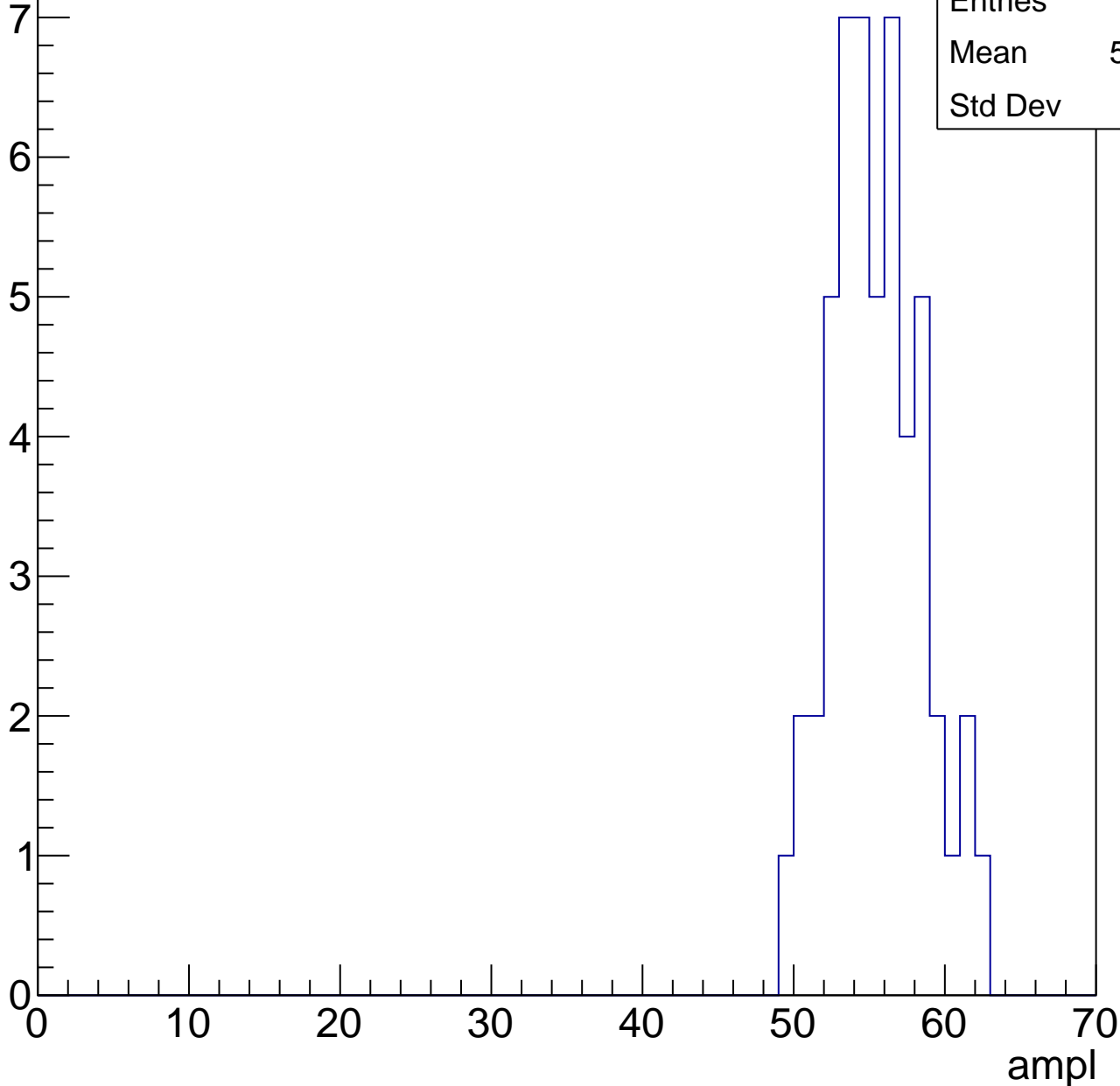


B1L103S, U6-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	55.04
Std Dev	2.95

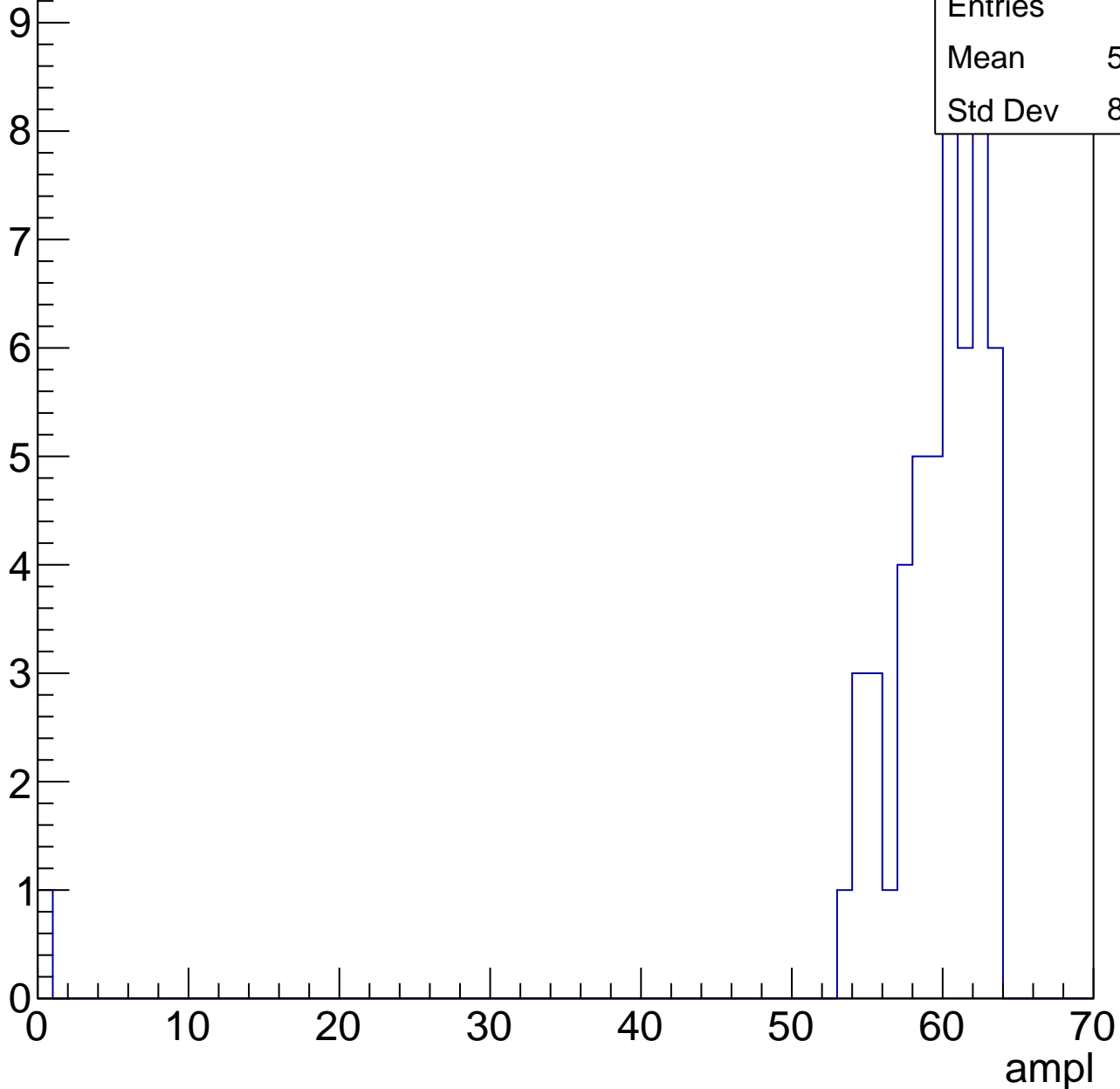


B1L103S, U6-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

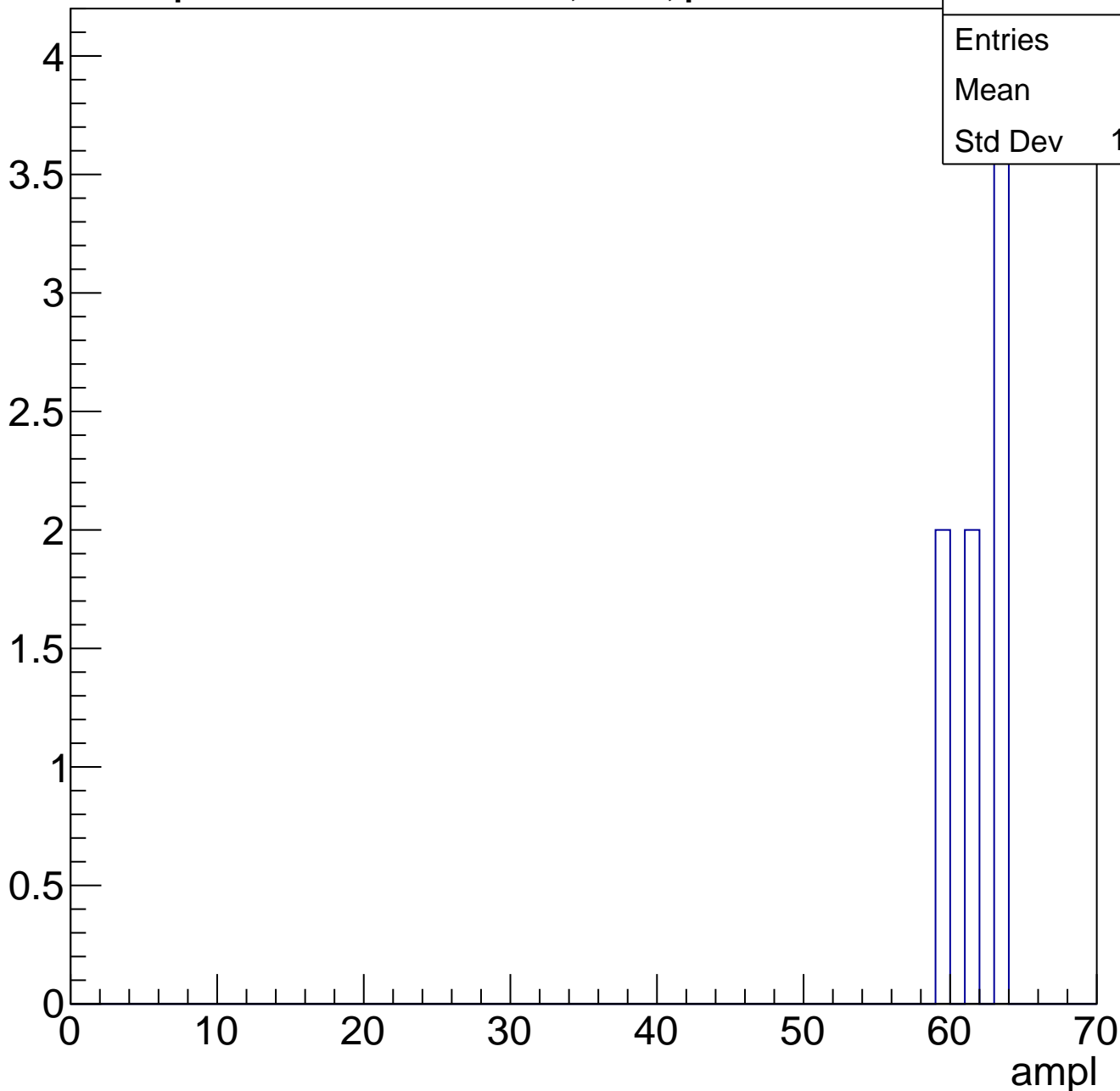
Entries	53
Mean	58.32
Std Dev	8.529



B1L103S, U6-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

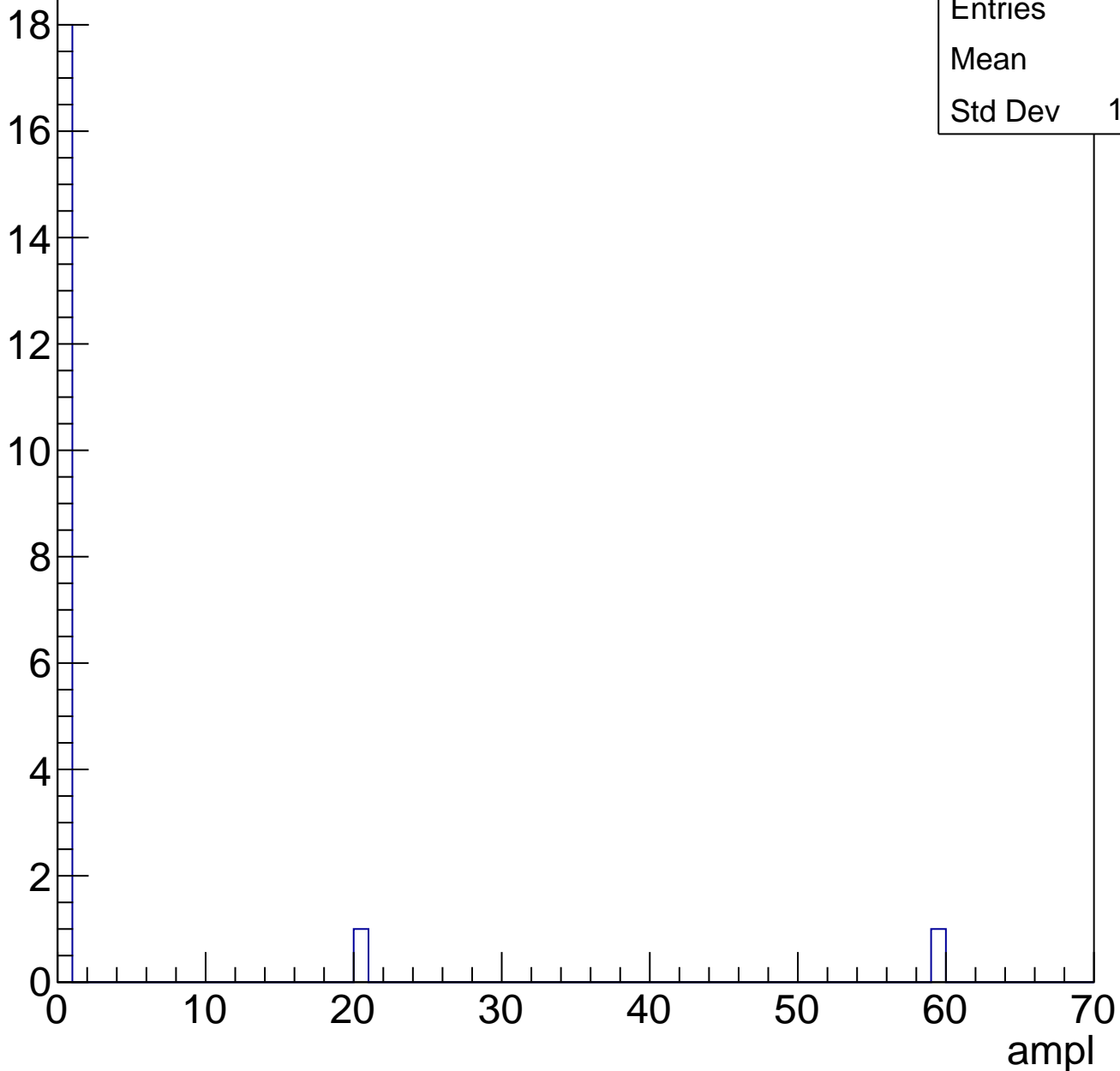


B1L103S, U6-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.95
Std Dev	13.36

Entry



B1L103S, U6-ch48, adc0

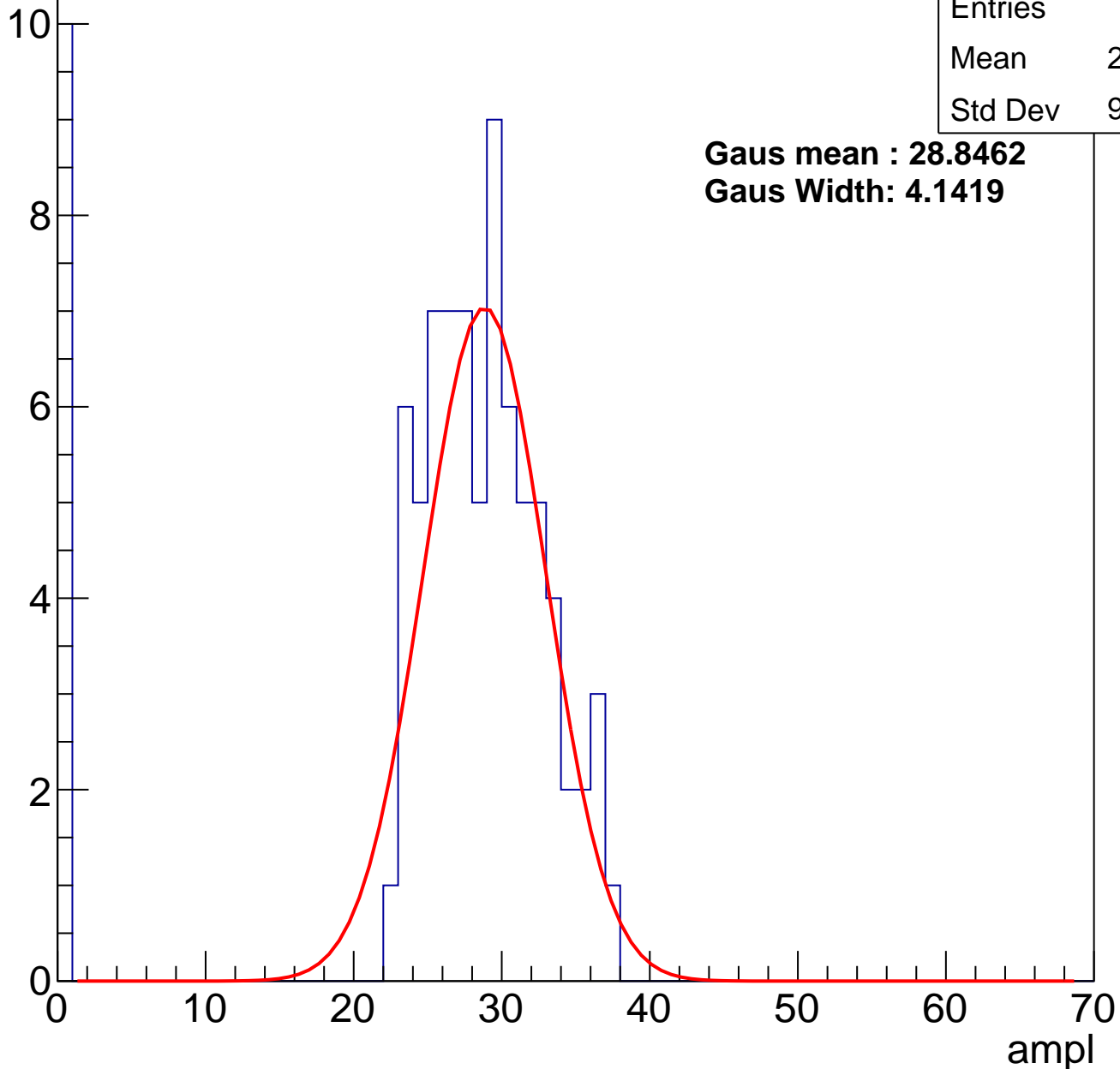
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.14
Std Dev	9.828

Gaus mean : 28.8462

Gaus Width: 4.1419

Entry



B1L103S, U6-ch48, adc1

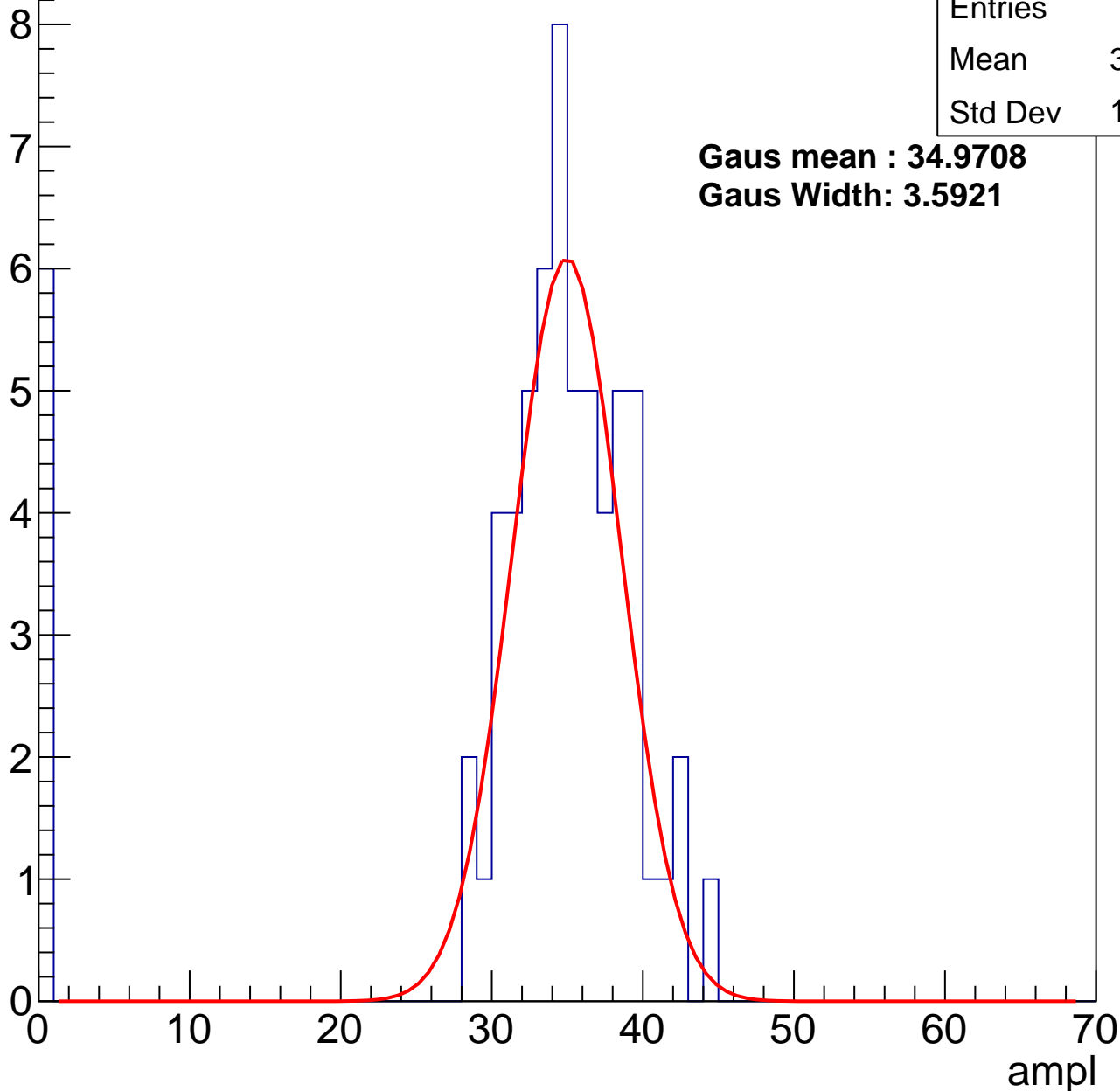
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	31.63
Std Dev	10.65

Gaus mean : 34.9708

Gaus Width: 3.5921



B1L103S, U6-ch48, adc2

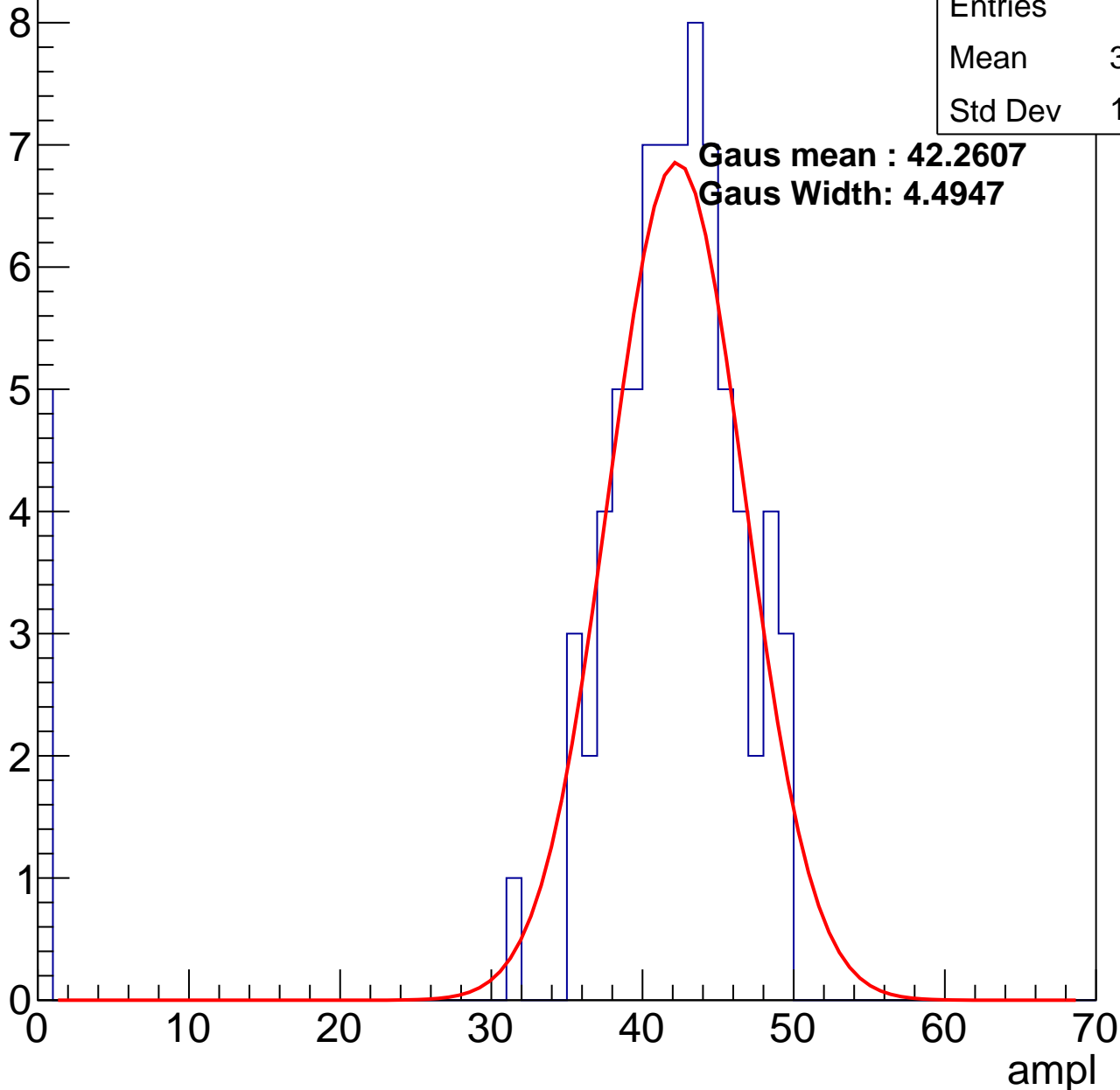
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	39.19
Std Dev	10.84

Gaus mean : 42.2607

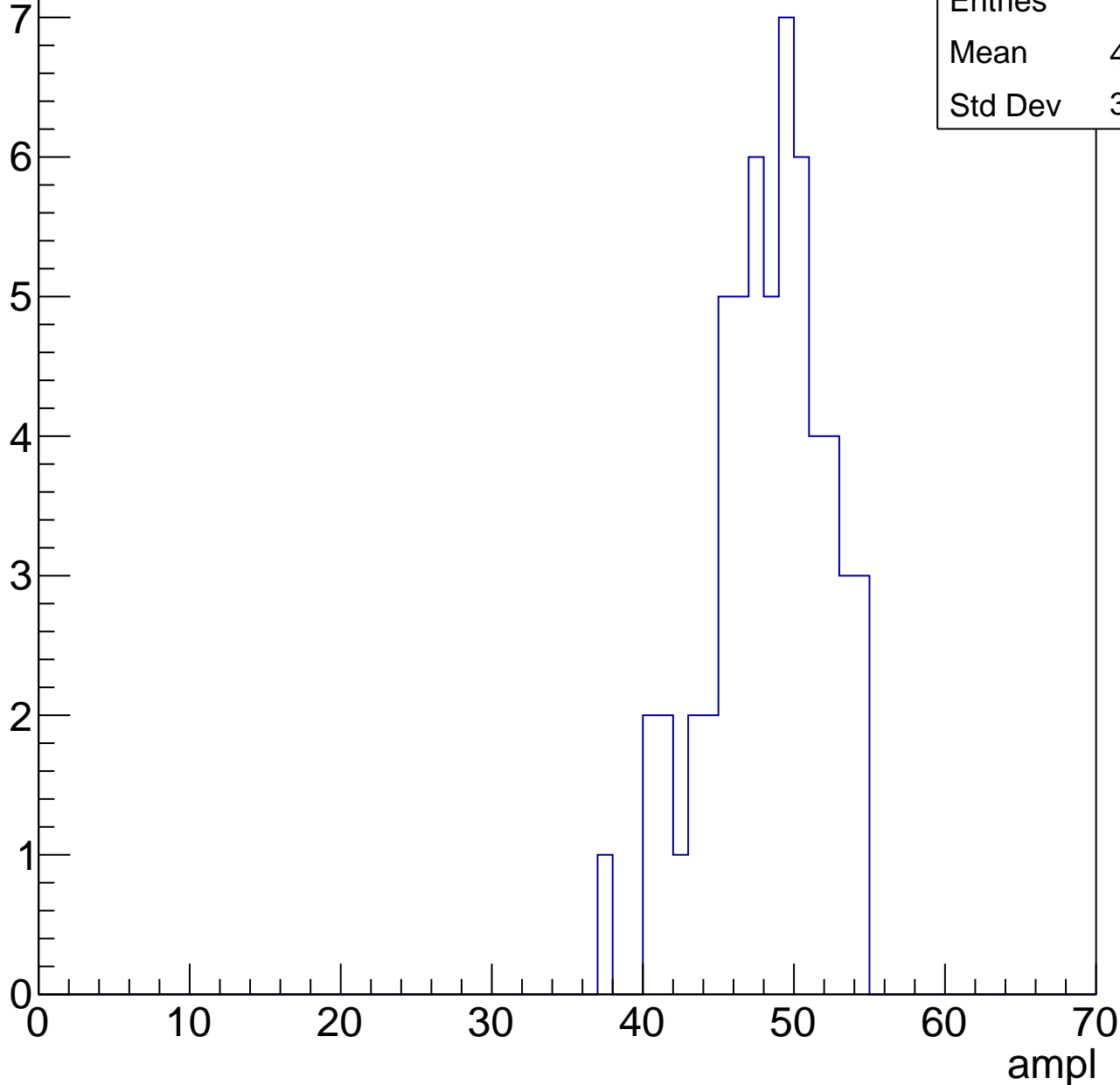
Gaus Width: 4.4947



B1L103S, U6-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

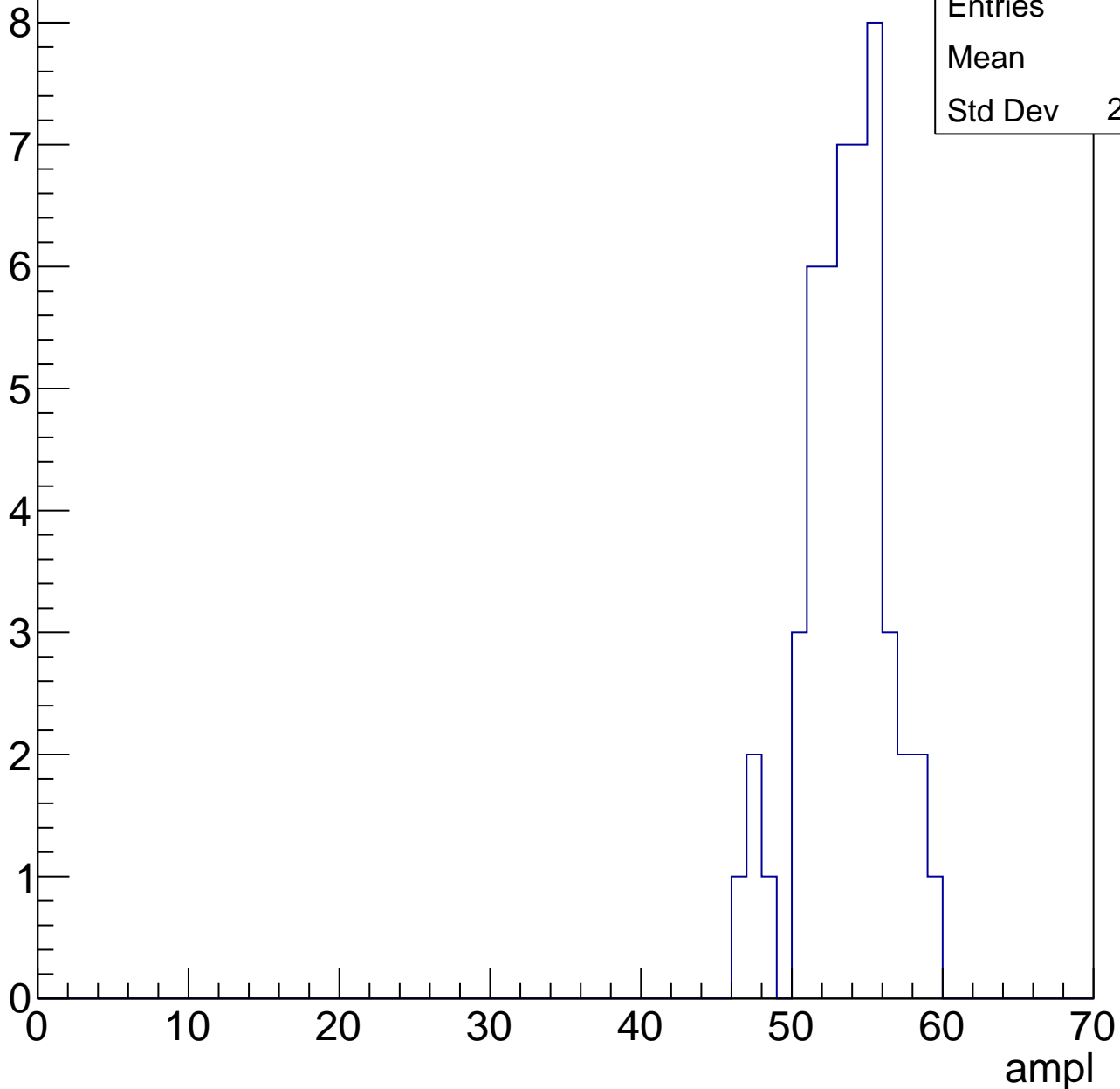


B1L103S, U6-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.1
Std Dev	2.808

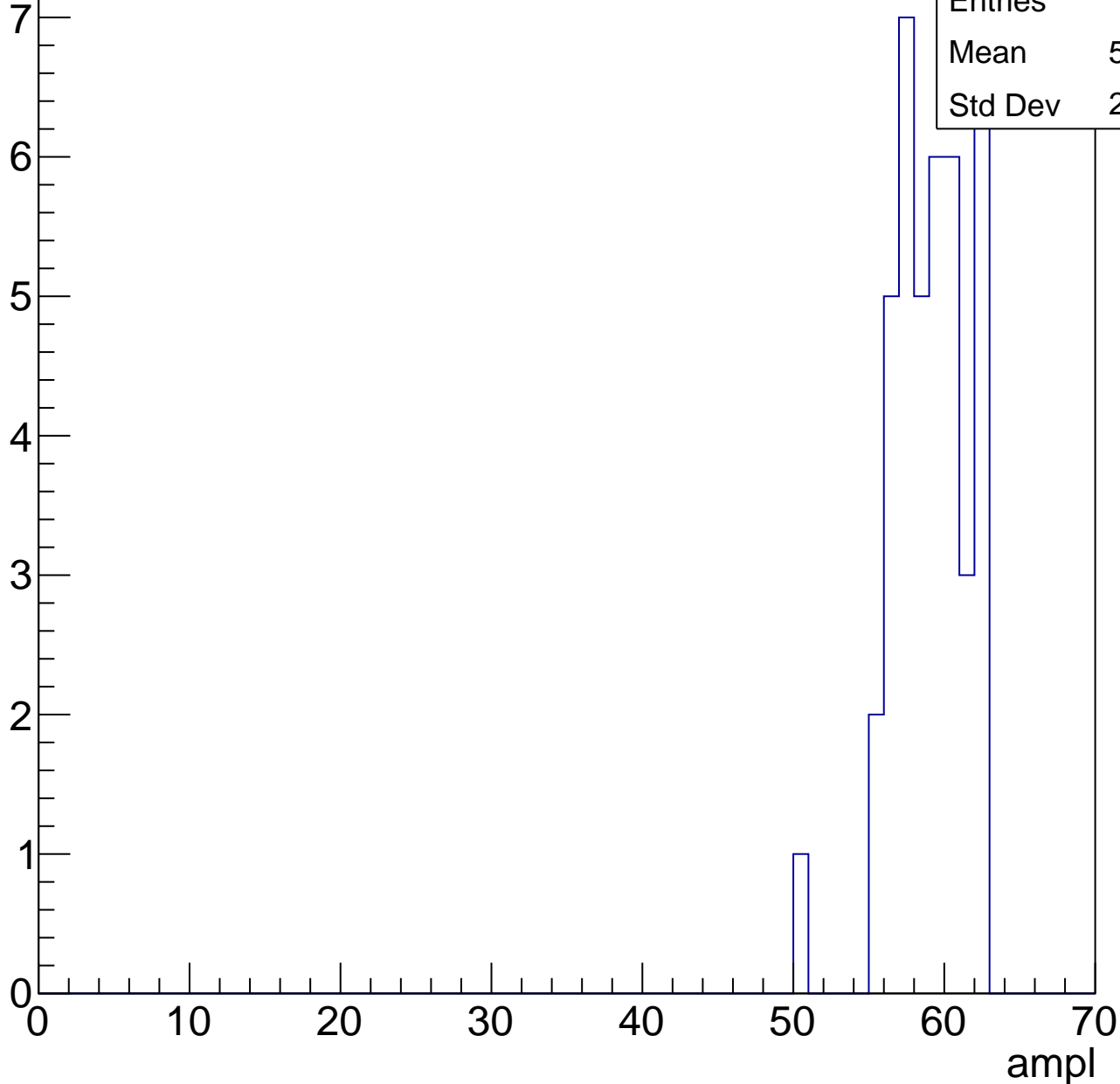


B1L103S, U6-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.57
Std Dev	2.508

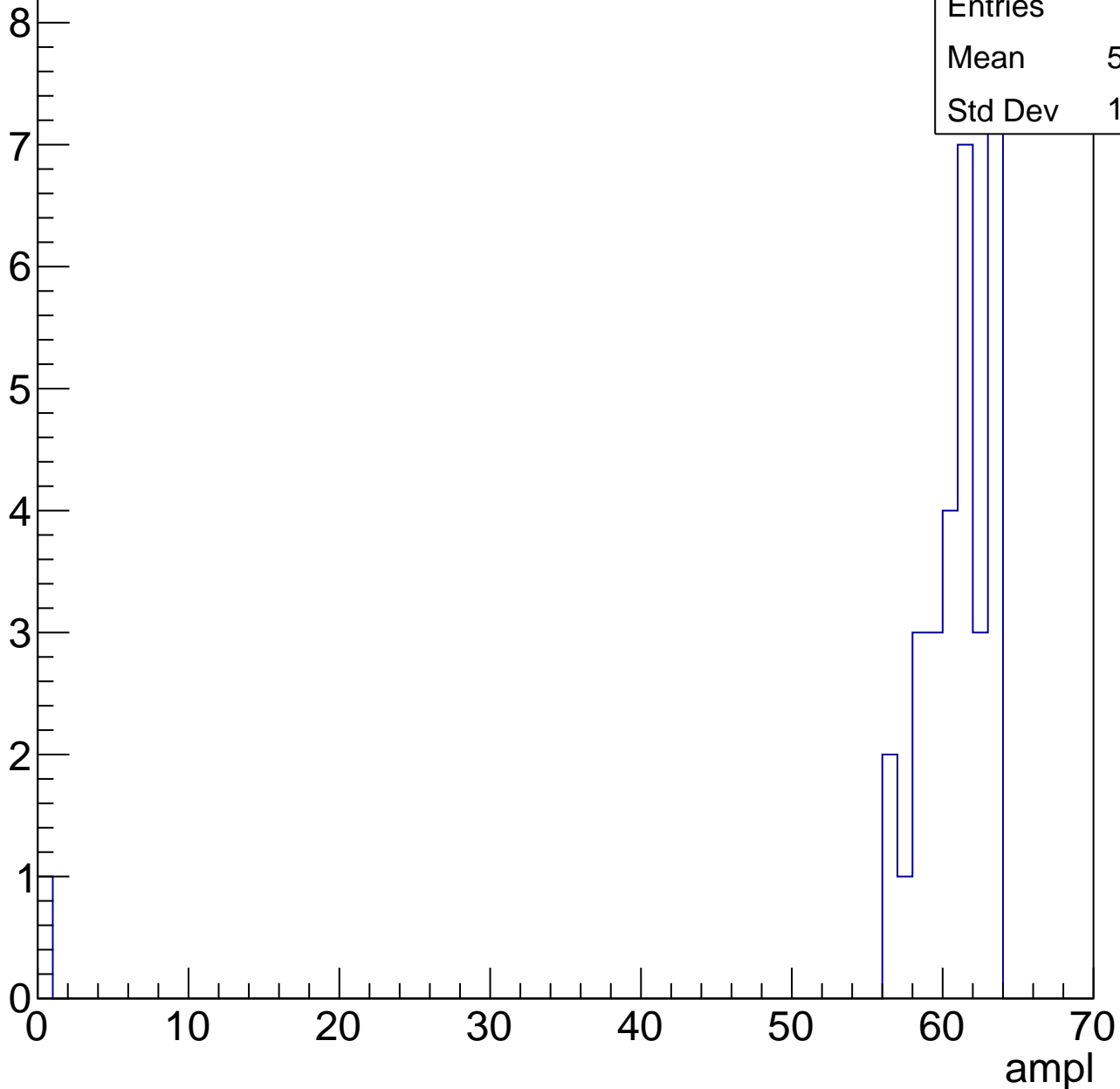


B1L103S, U6-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.66
Std Dev	10.74



B1L103S, U6-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

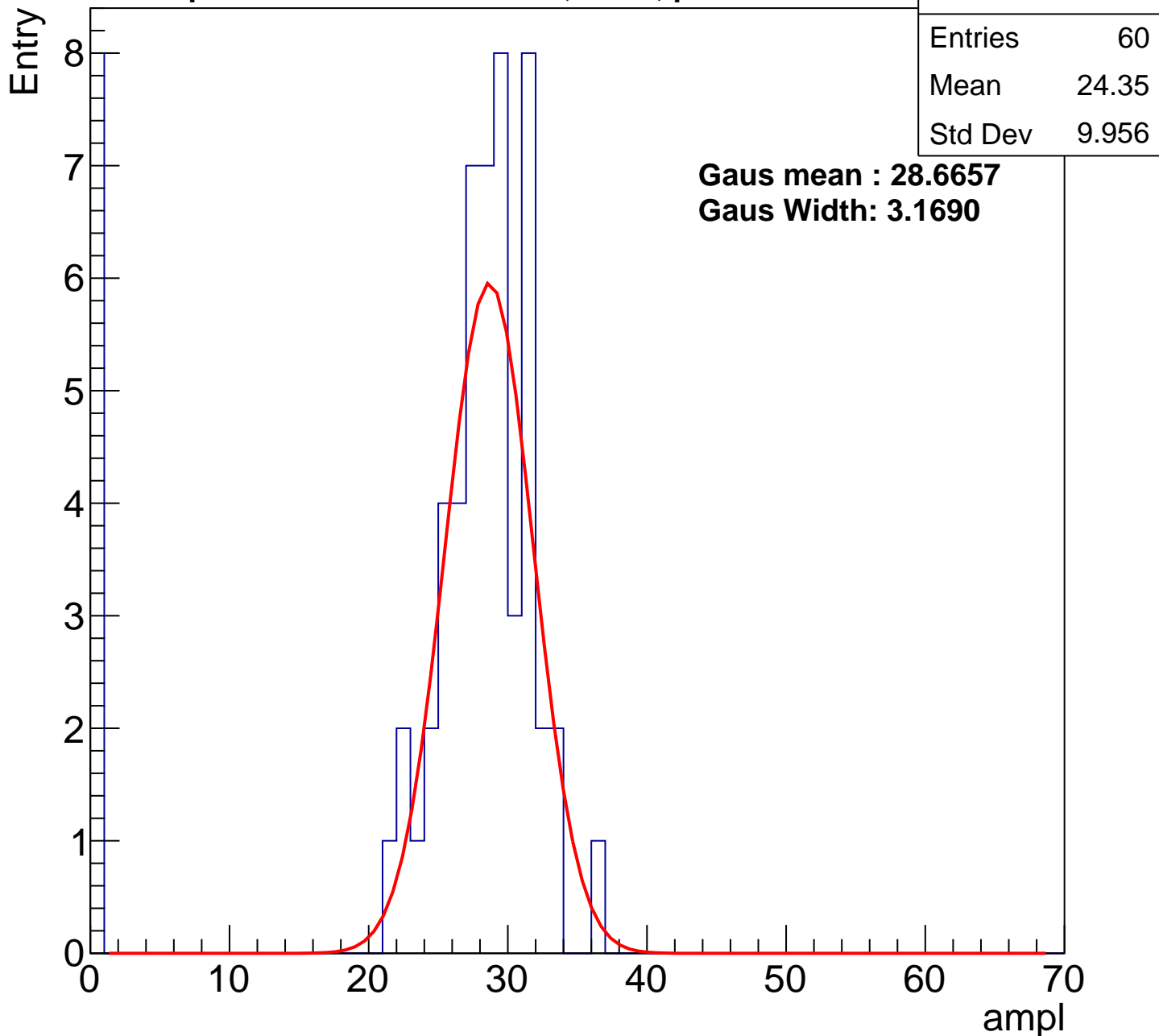
Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U6-ch49, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch49, adc1

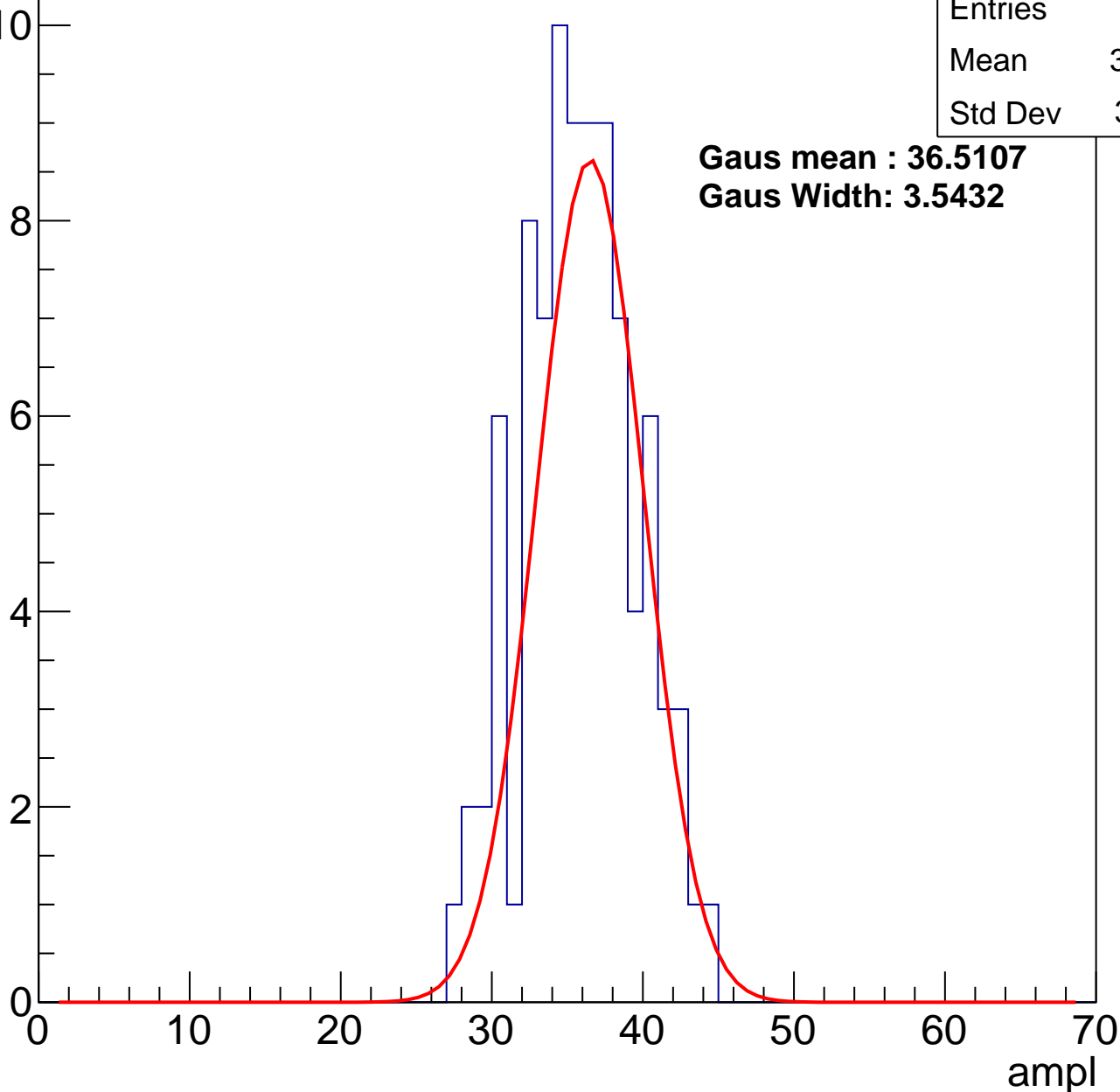
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	35.38
Std Dev	3.701

Gaus mean : 36.5107

Gaus Width: 3.5432



B1L103S, U6-ch49, adc2

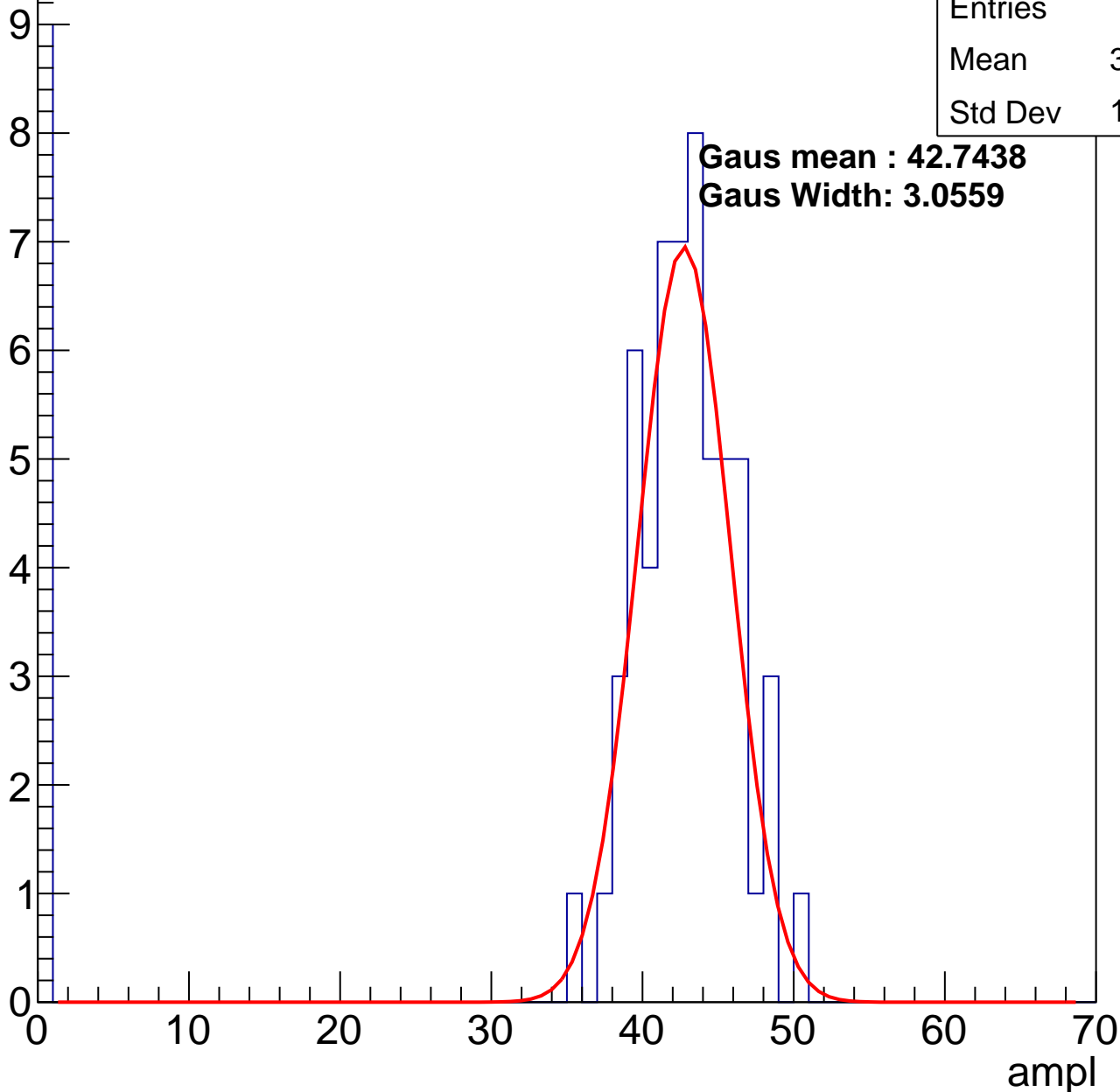
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.68
Std Dev	14.85

Gaus mean : 42.7438

Gaus Width: 3.0559



B1L103S, U6-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	46.54
Std Dev	12.31

Entry

10

8

6

4

2

0

0

10

20

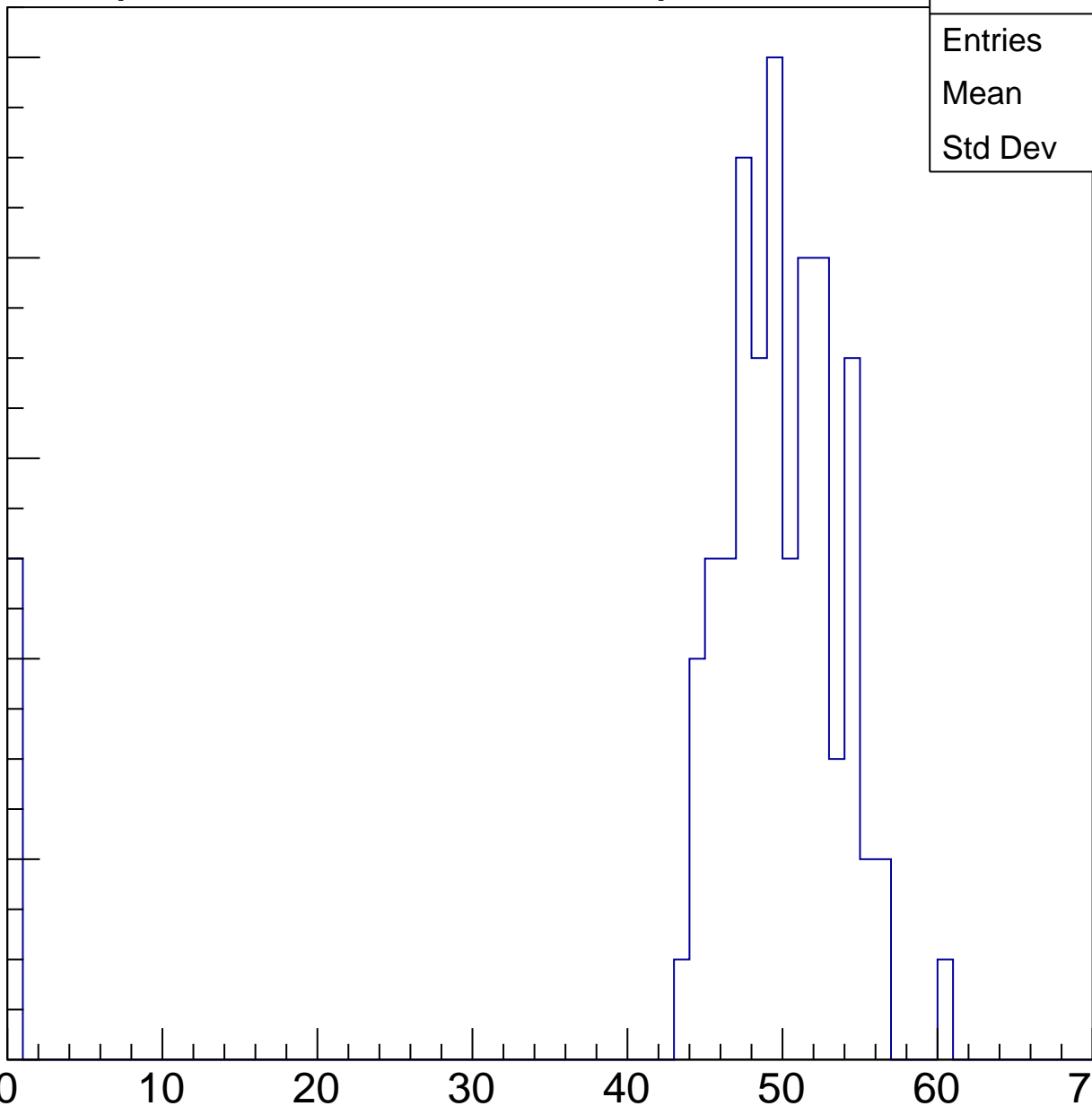
30

40

50

60

ampl

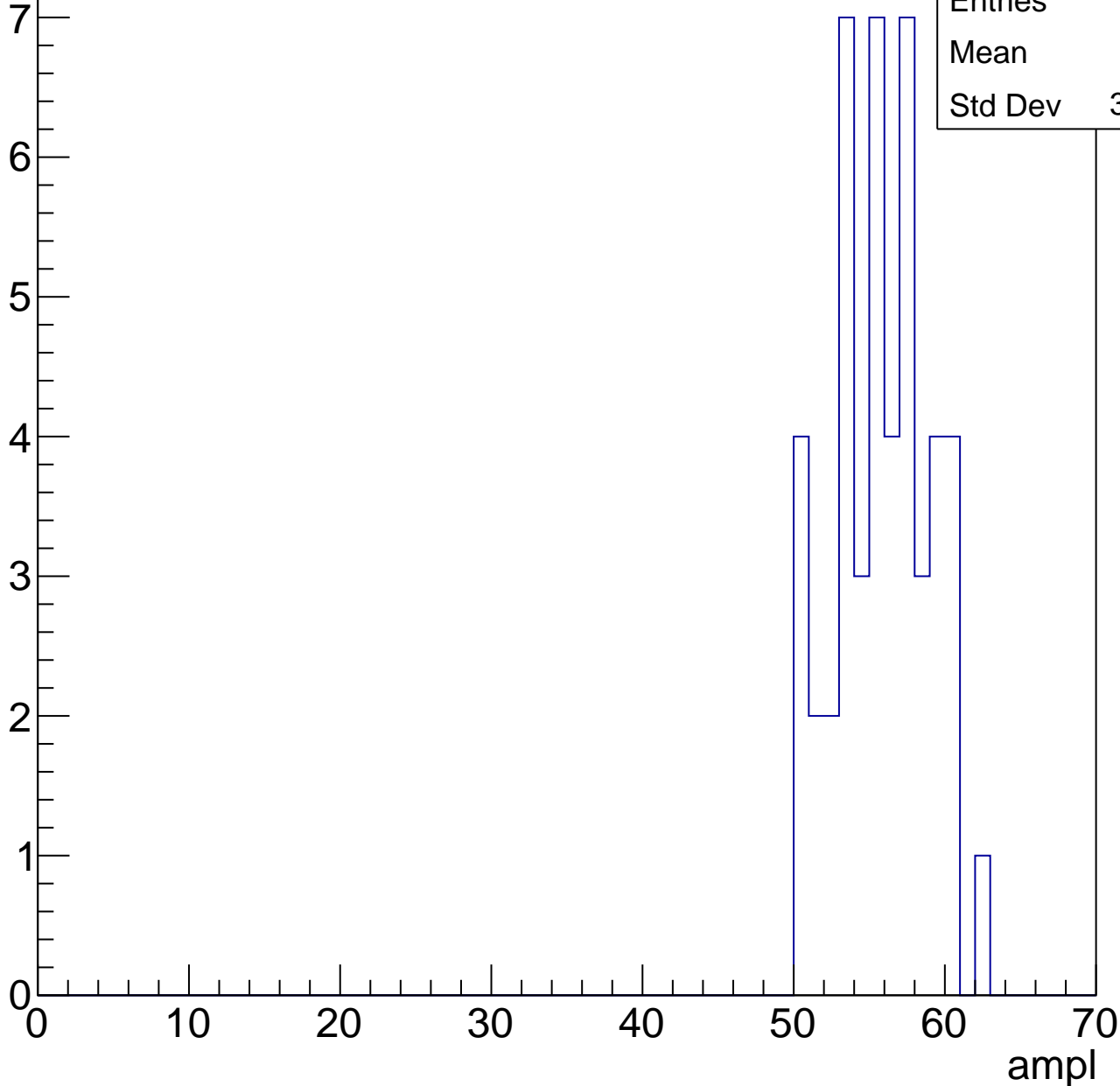


B1L103S, U6-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.4
Std Dev	3.046

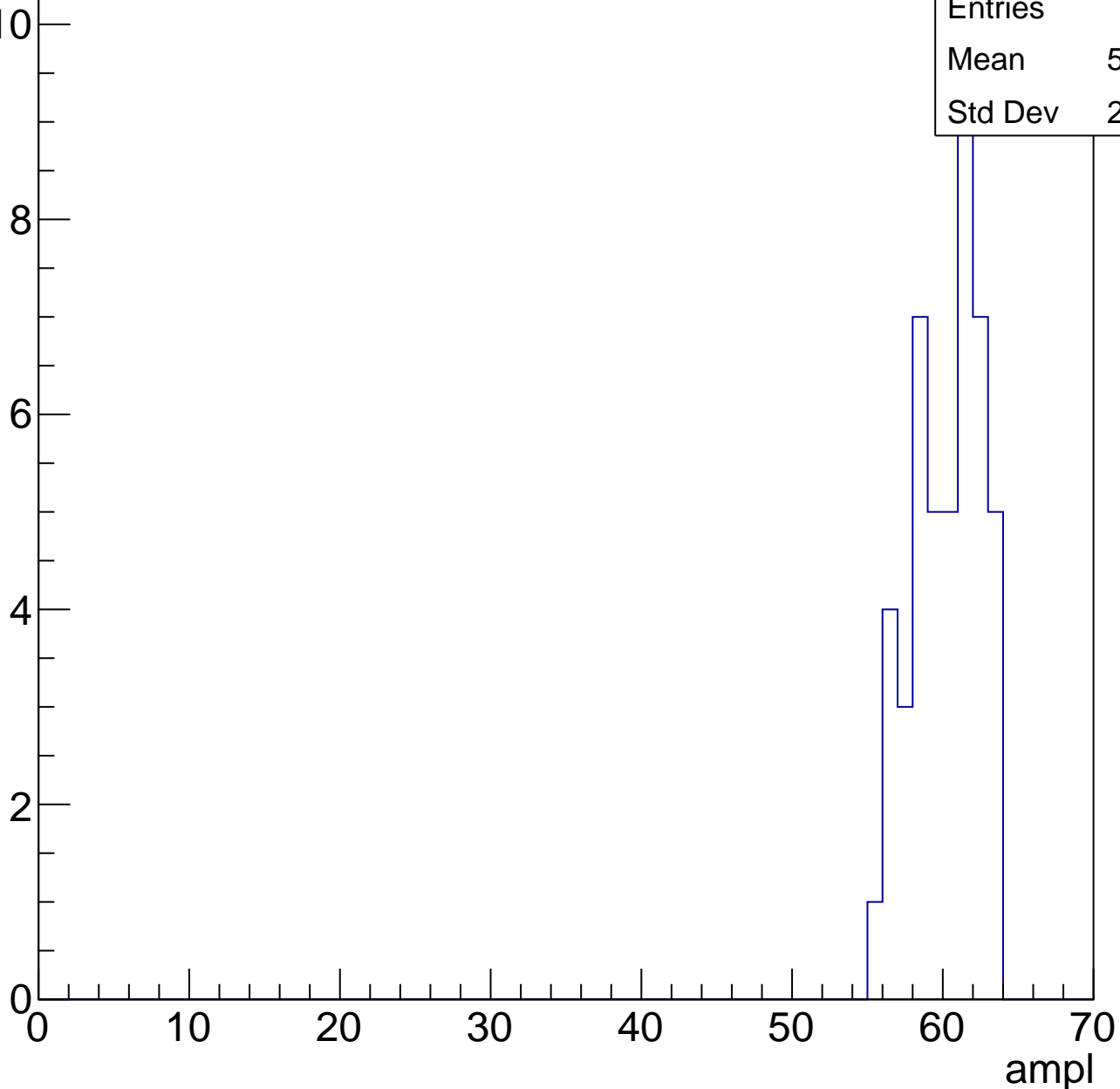


B1L103S, U6-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	59.79
Std Dev	2.212

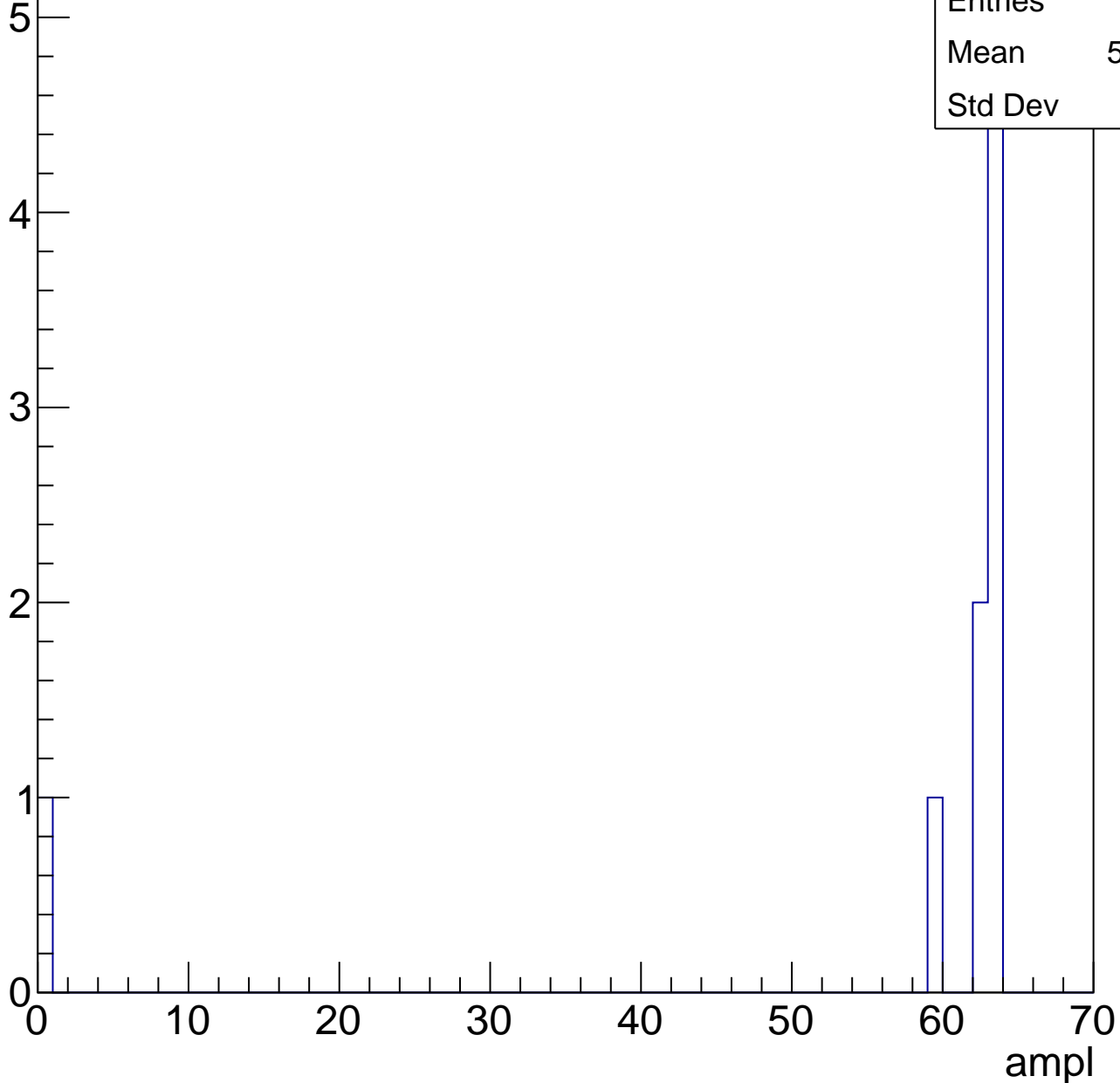


B1L103S, U6-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	55.33
Std Dev	19.6



B1L103S, U6-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

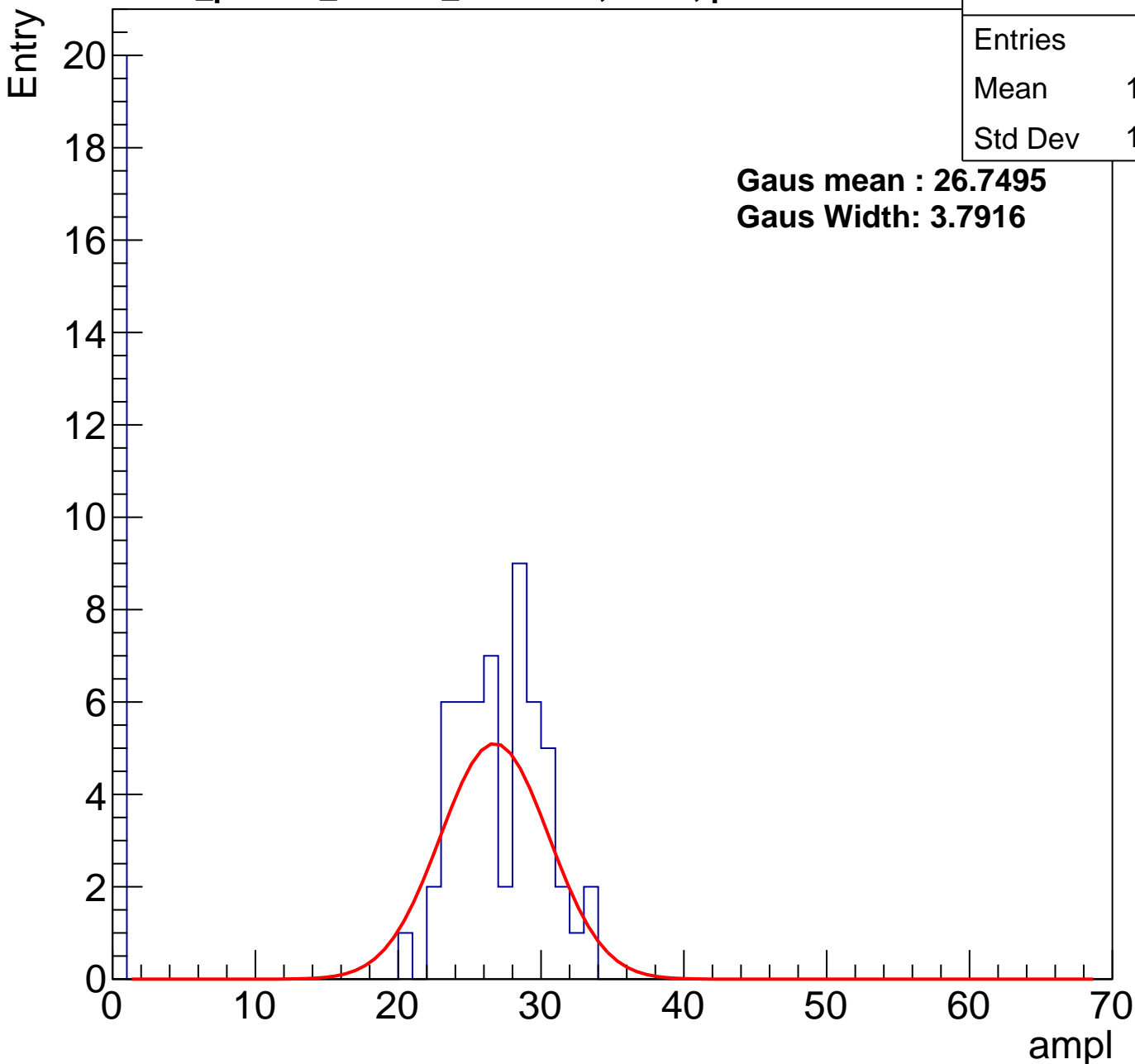
B1L103S, U6-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	19.57
Std Dev	12.07

Gaus mean : 26.7495

Gaus Width: 3.7916



B1L103S, U6-ch50, adc1

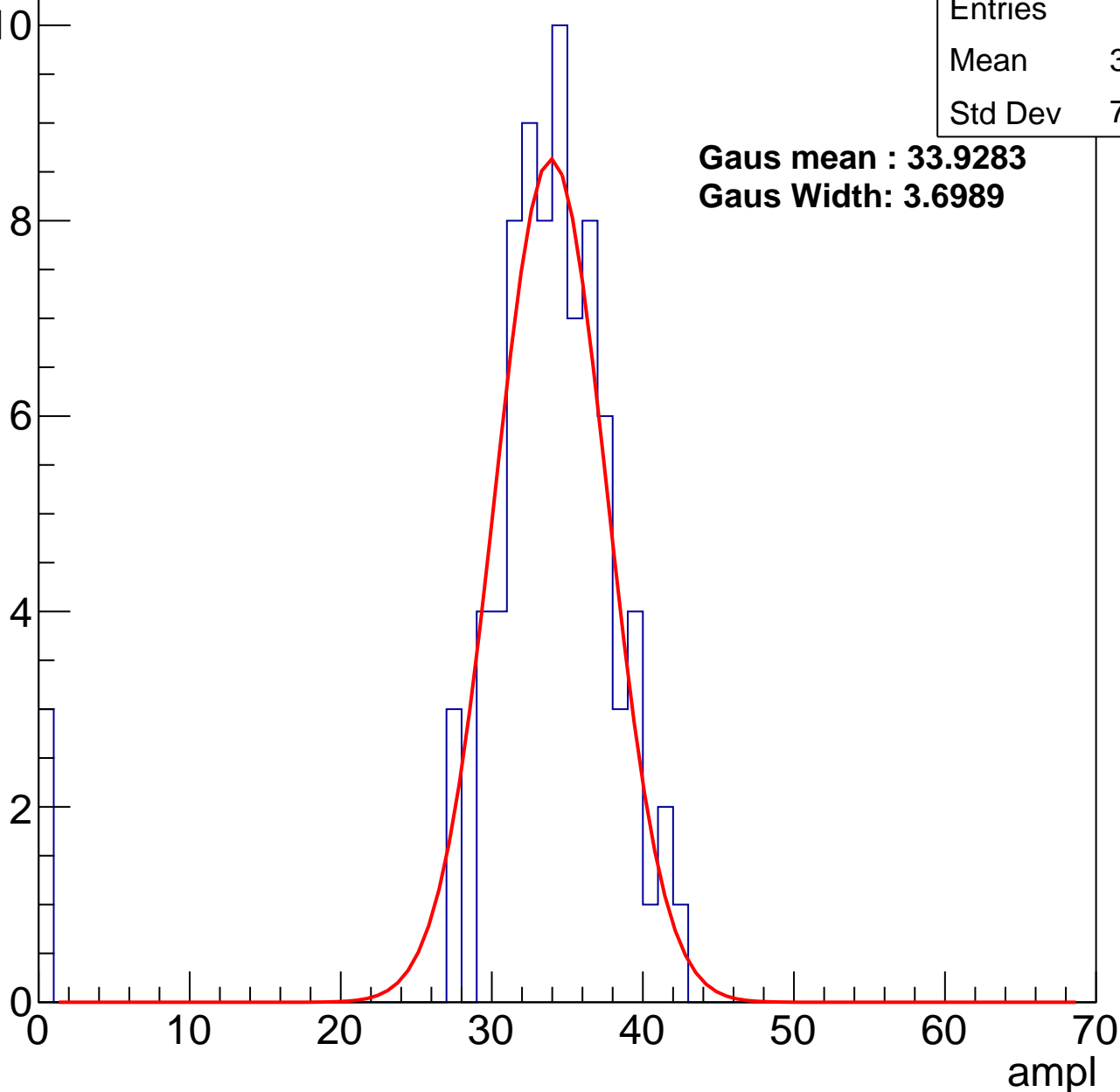
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	32.67
Std Dev	7.192

Gaus mean : 33.9283

Gaus Width: 3.6989



B1L103S, U6-ch50, adc2

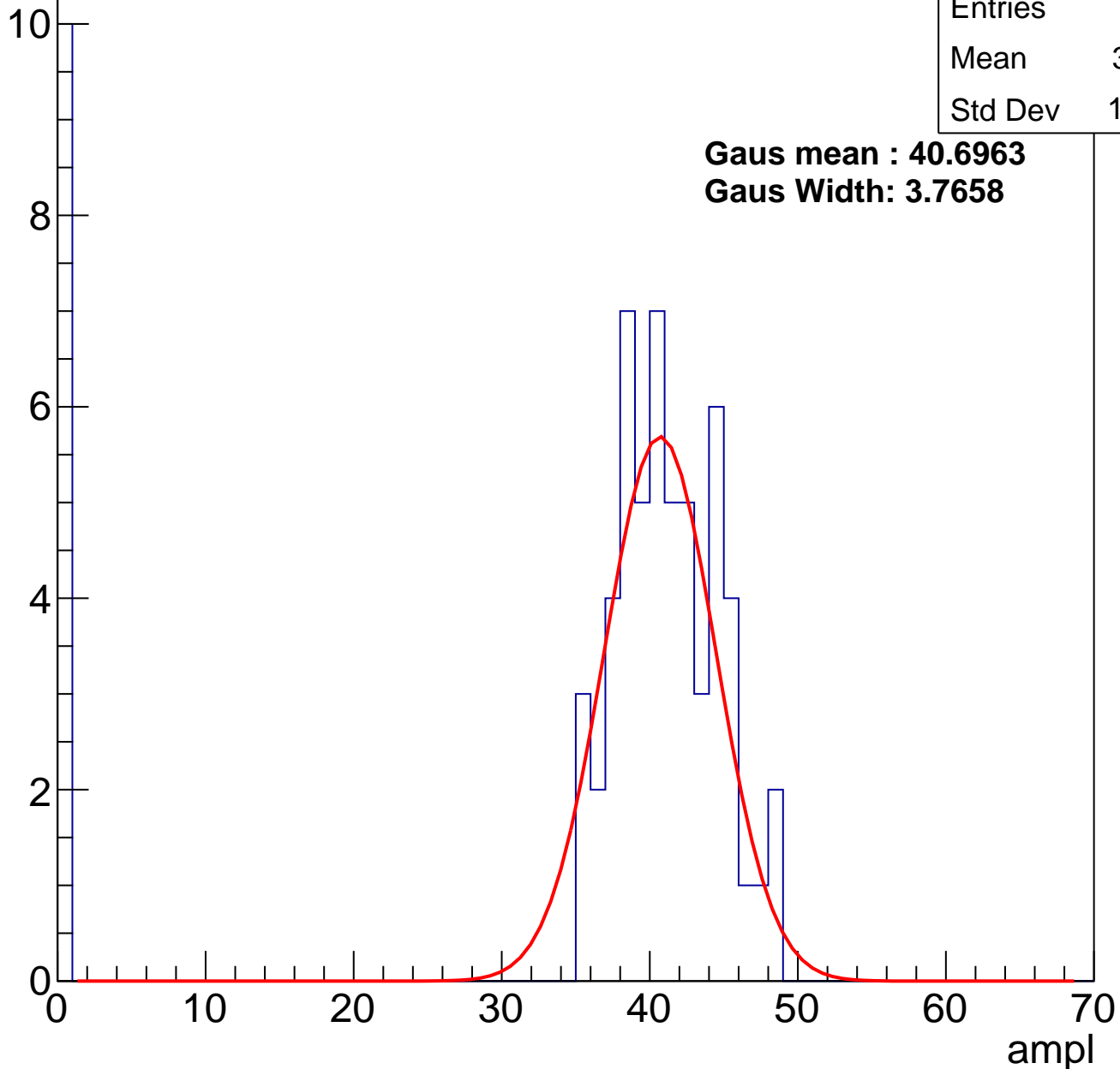
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	34.51
Std Dev	15.03

Gaus mean : 40.6963

Gaus Width: 3.7658

Entry

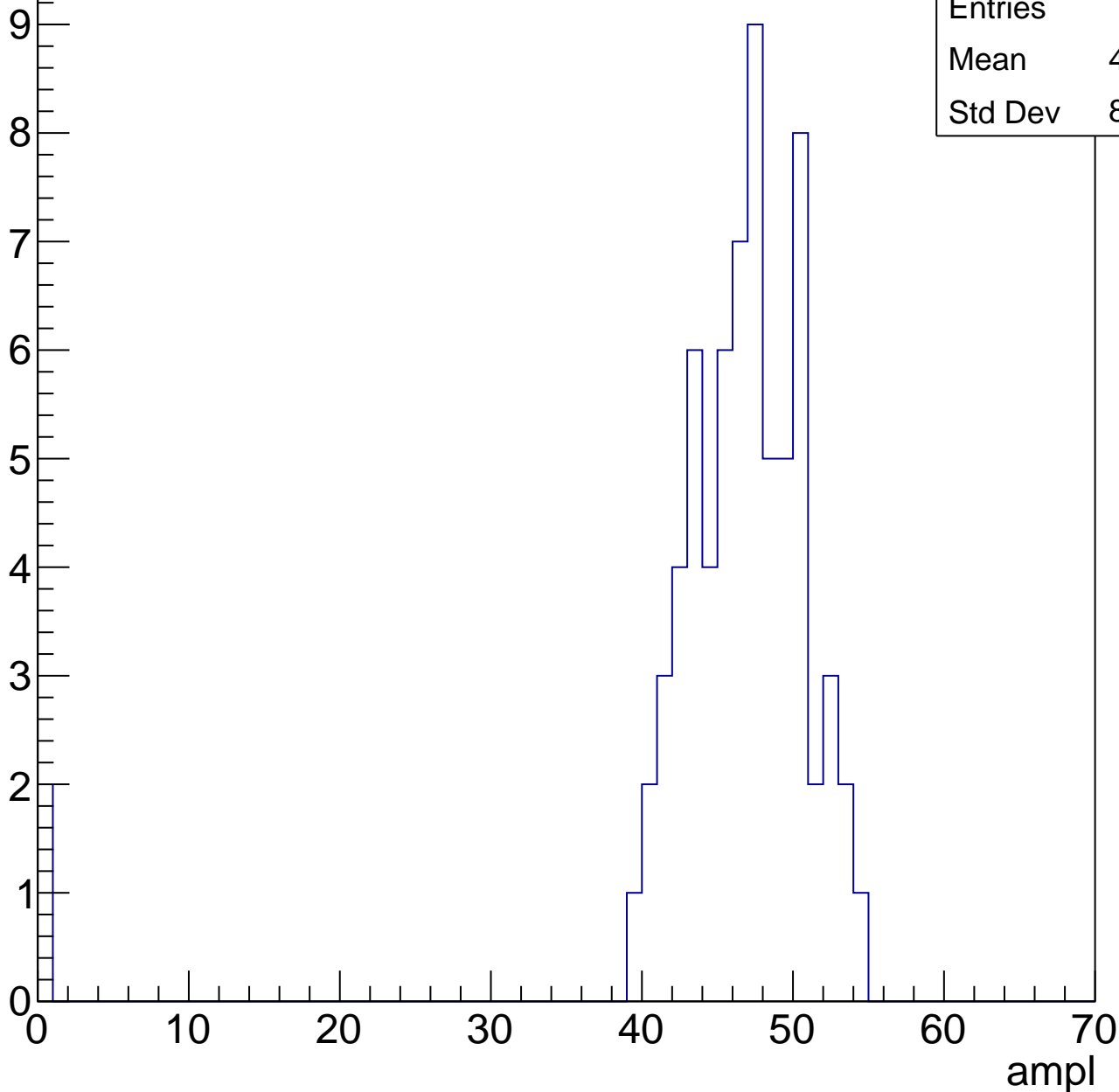


B1L103S, U6-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

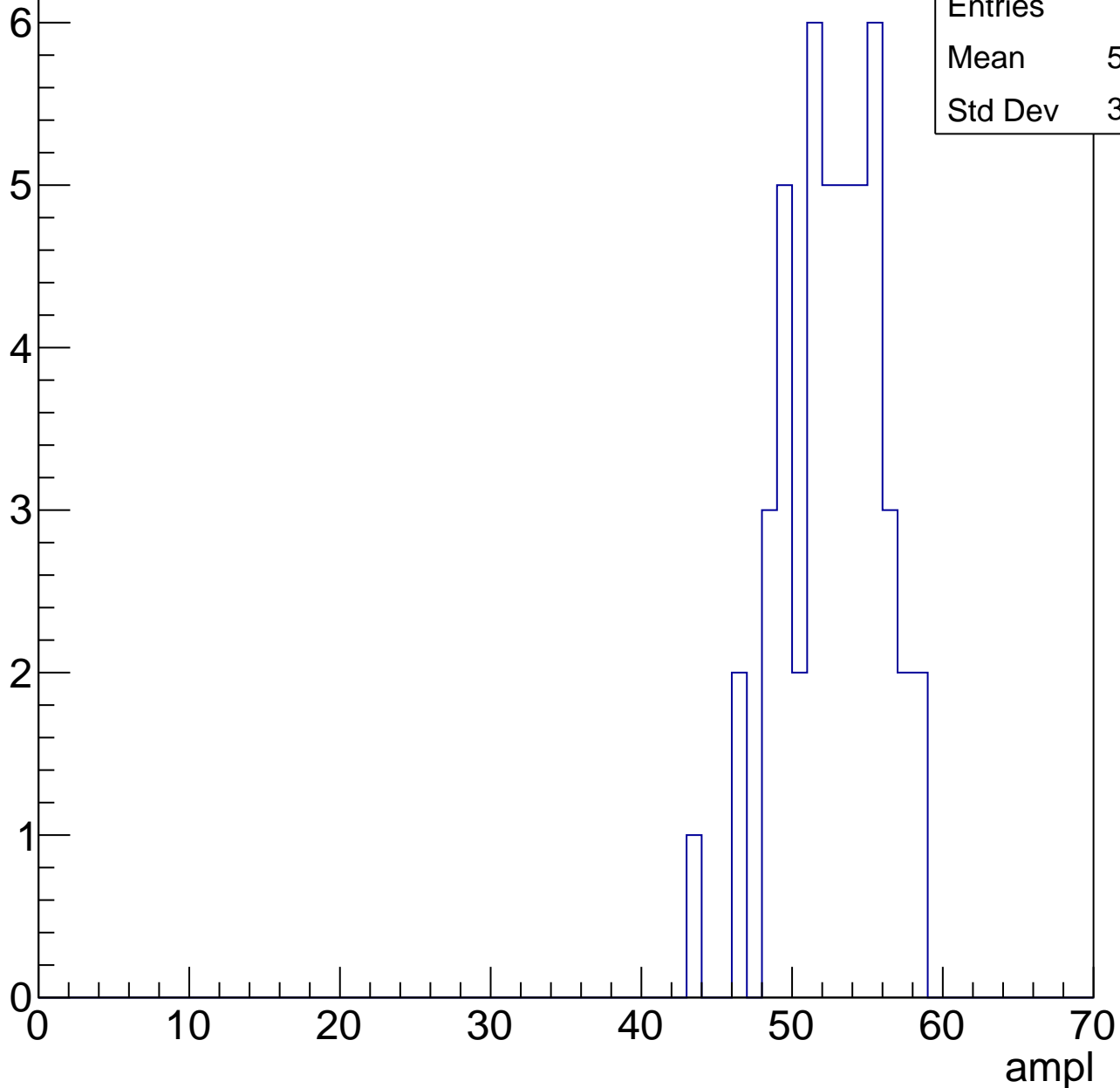
Entries	70
Mean	45.17
Std Dev	8.487



B1L103S, U6-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

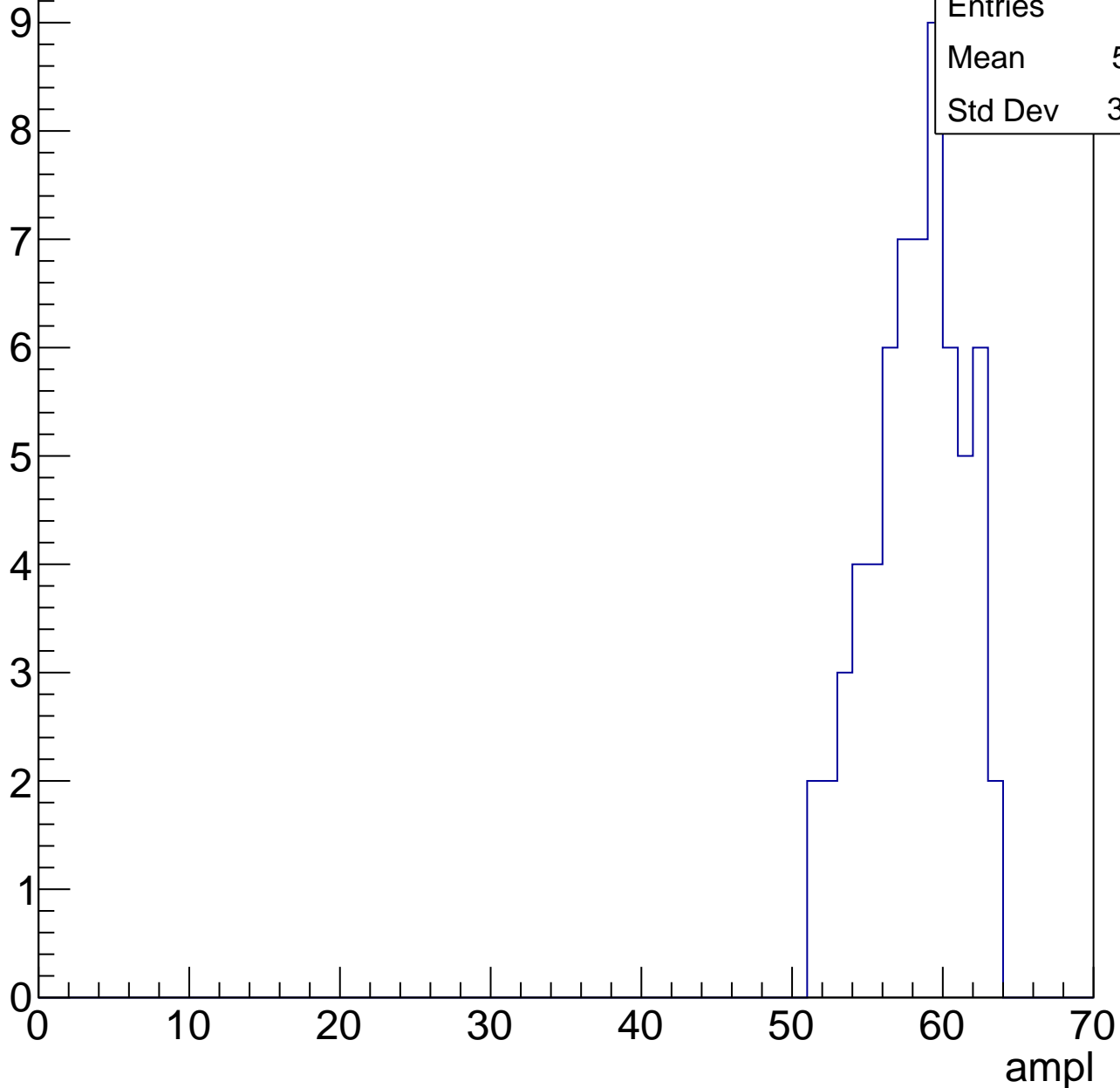


Entries	47
Mean	52.19
Std Dev	3.285

B1L103S, U6-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



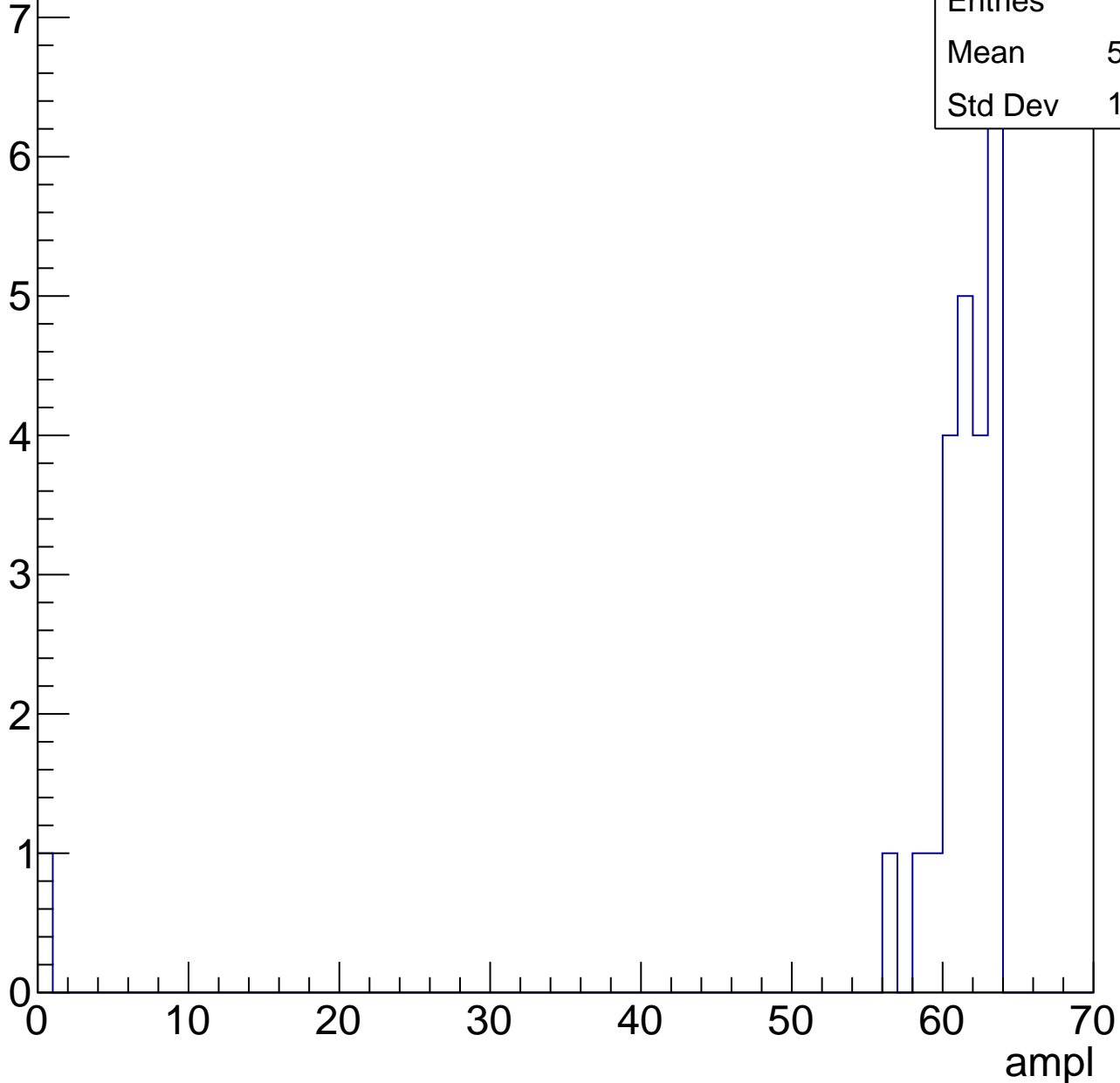
Entries	63
Mean	57.71
Std Dev	3.073

B1L103S, U6-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

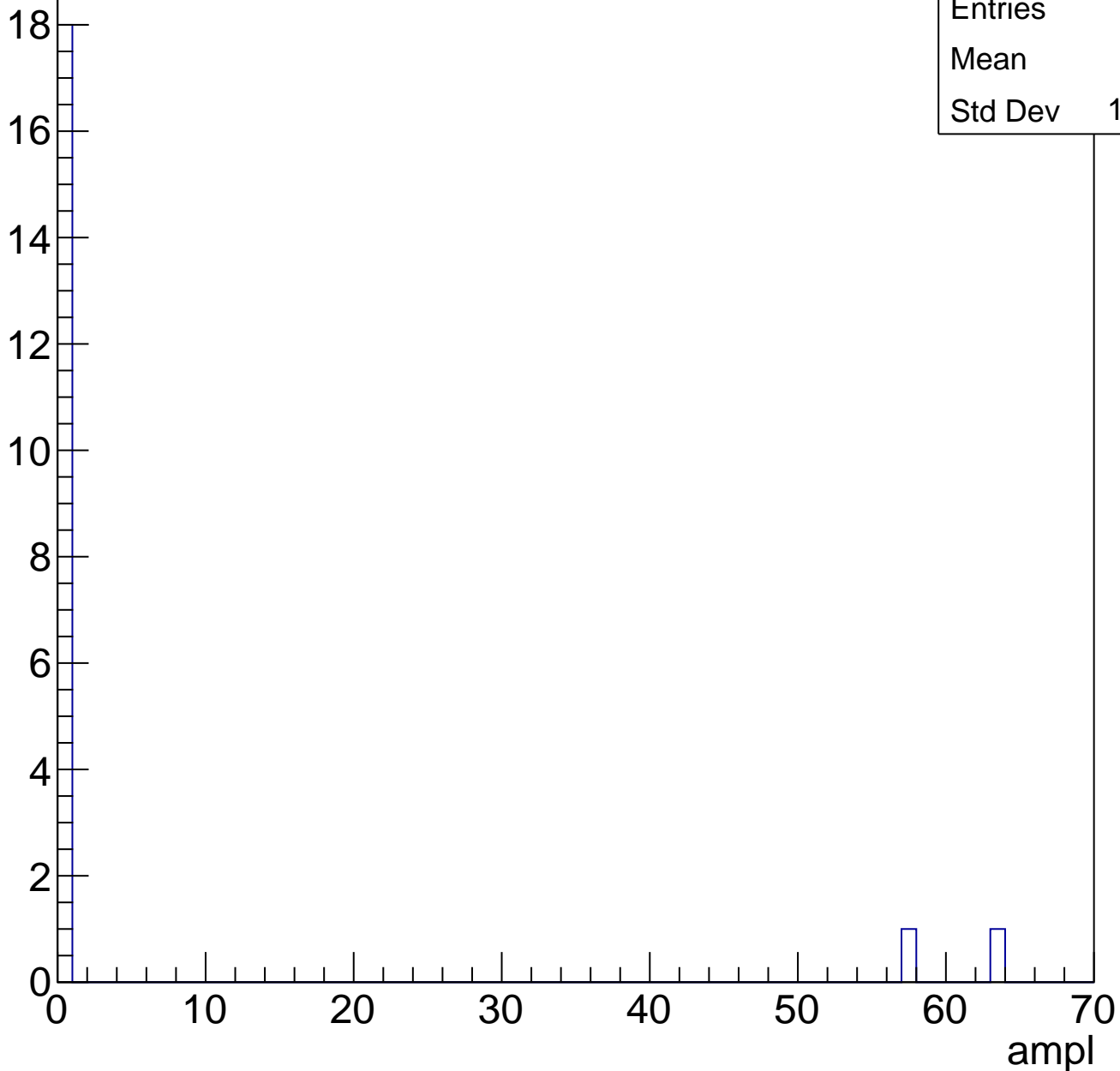
Entries	24
Mean	58.62
Std Dev	12.35



B1L103S, U6-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch51, adc0

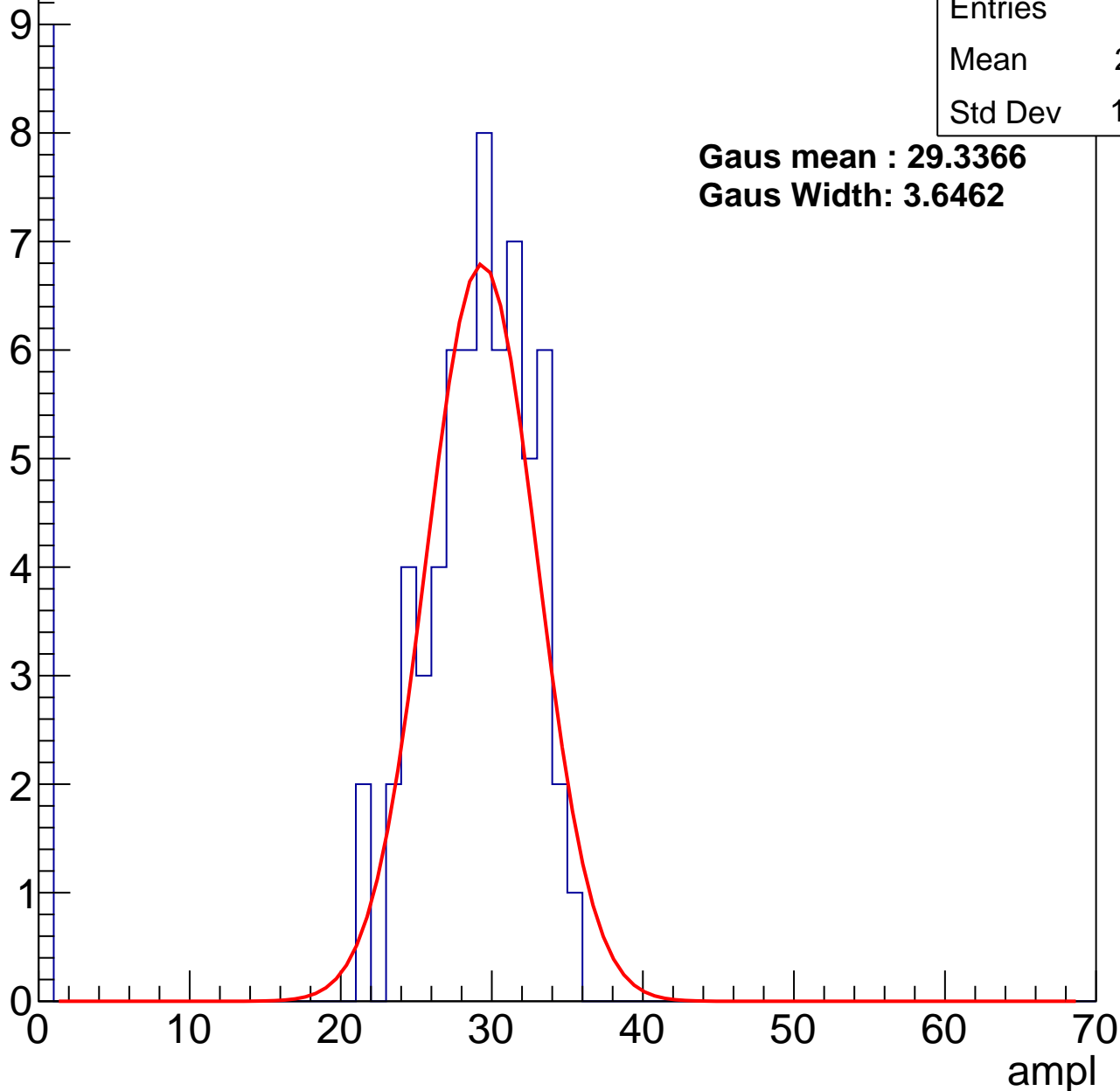
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	25.11
Std Dev	10.05

Gaus mean : 29.3366

Gaus Width: 3.6462



B1L103S, U6-ch51, adc1

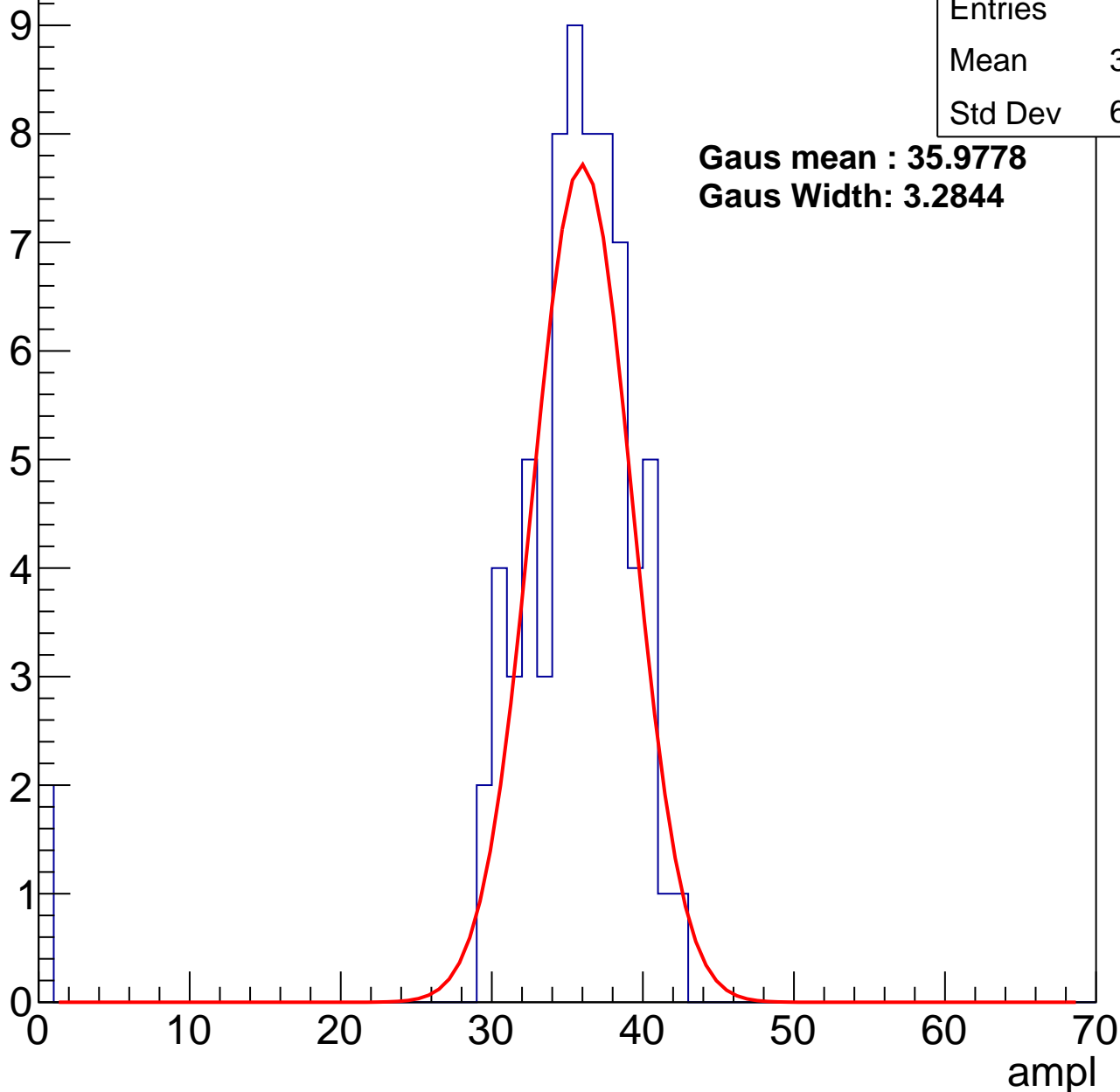
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.37
Std Dev	6.638

Gaus mean : 35.9778

Gaus Width: 3.2844



B1L103S, U6-ch51, adc2

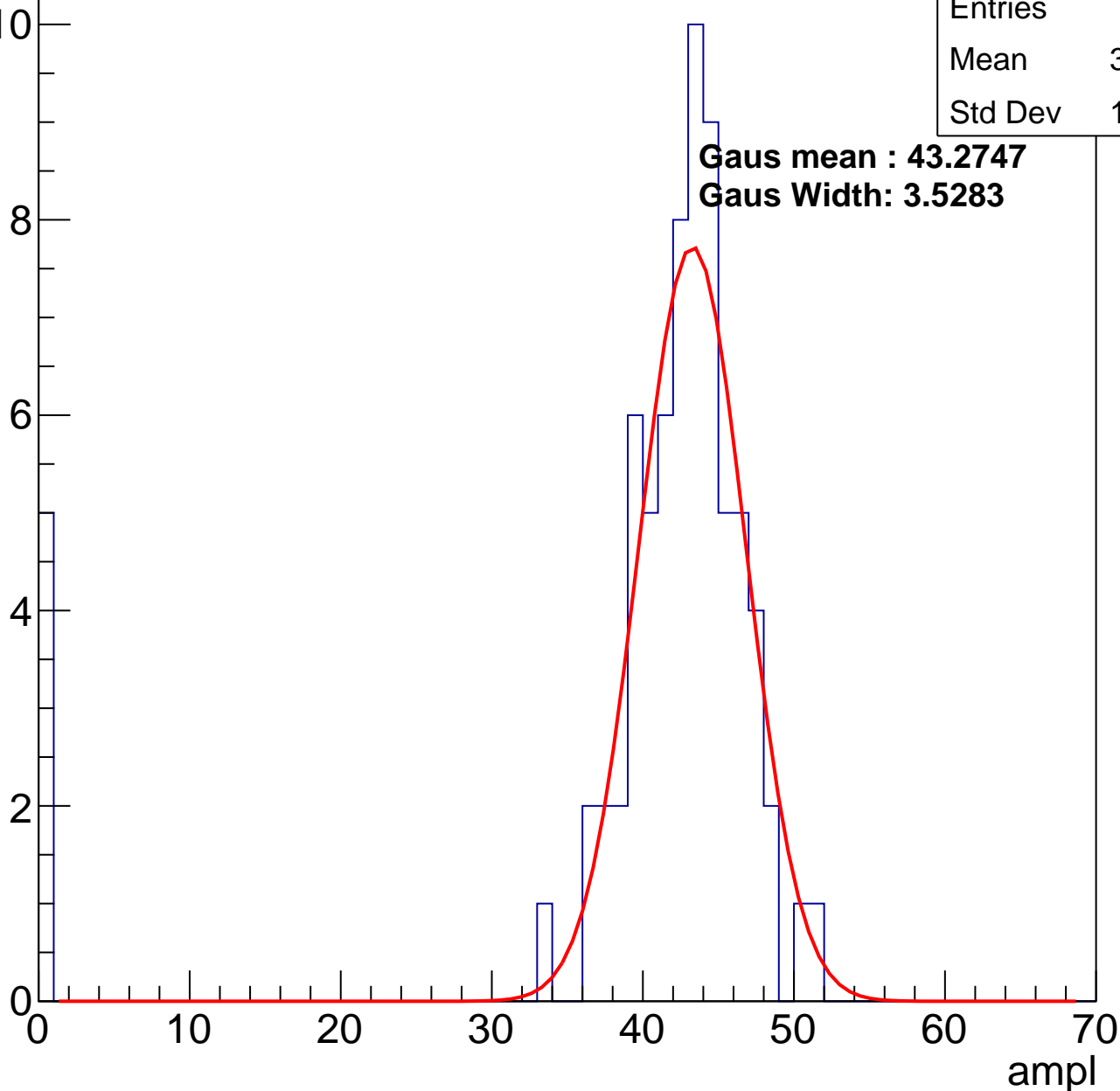
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	39.69
Std Dev	11.17

Gaus mean : 43.2747

Gaus Width: 3.5283

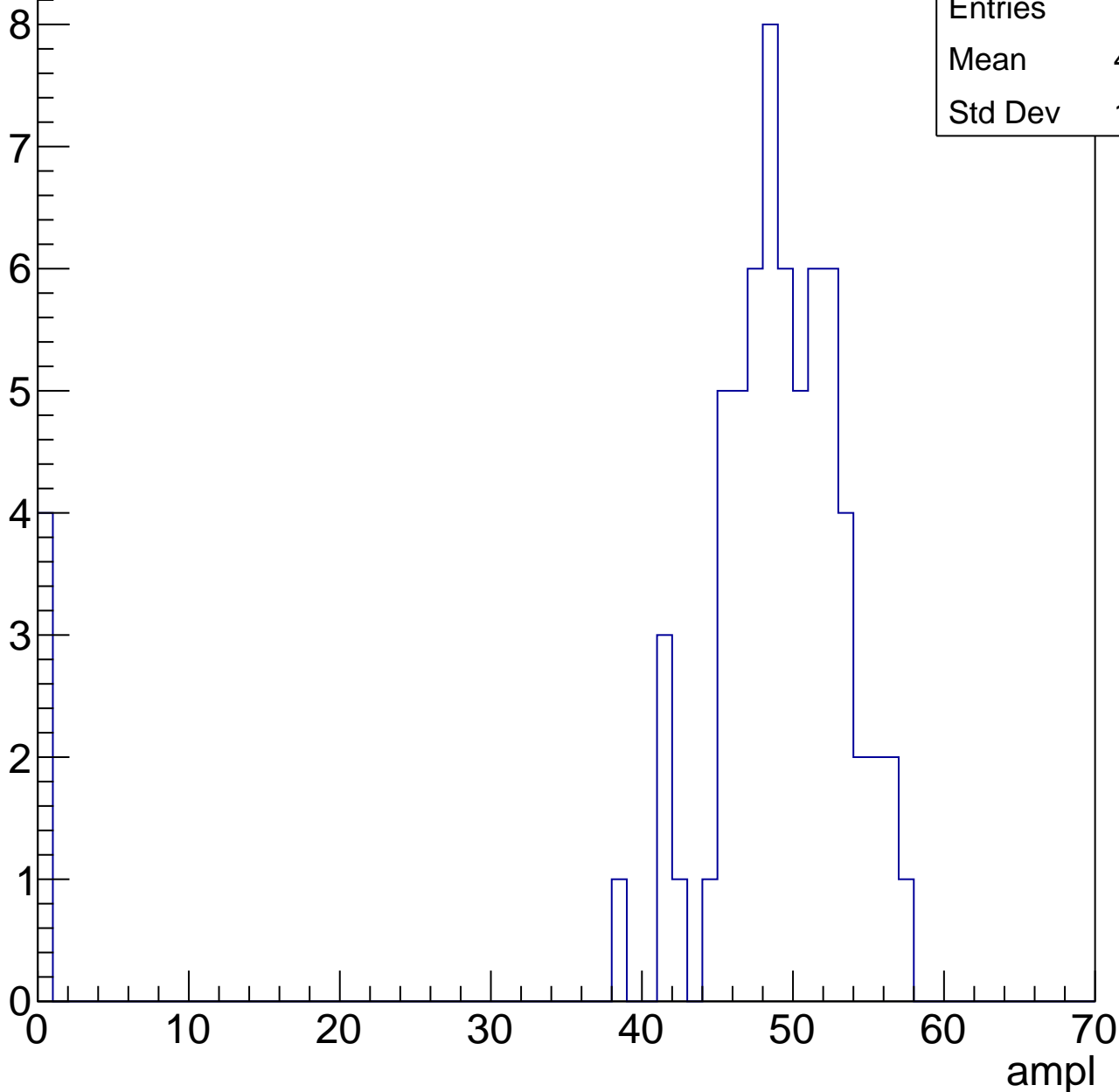


B1L103S, U6-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	46.01
Std Dev	12.11

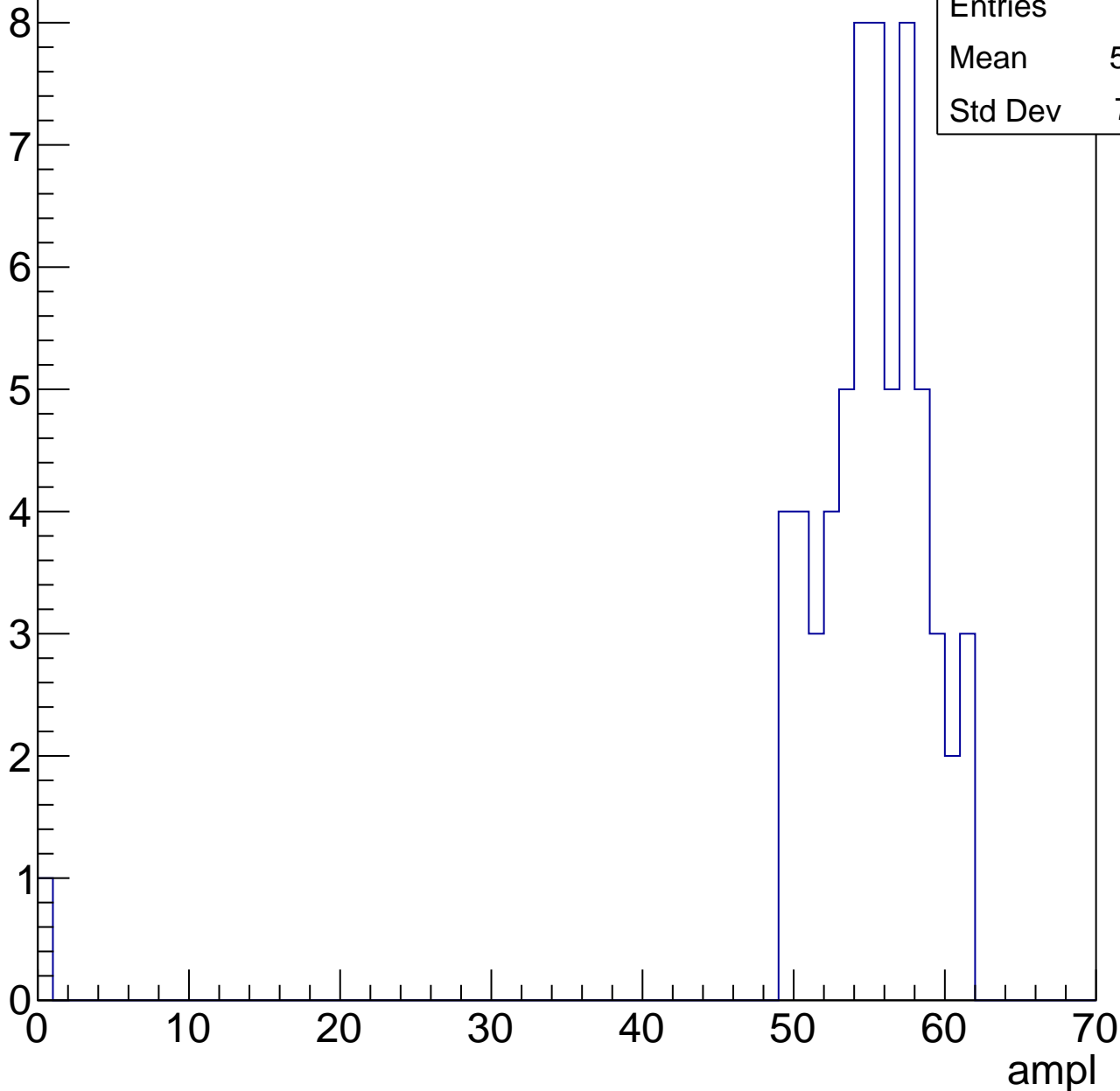


B1L103S, U6-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.97
Std Dev	7.561



B1L103S, U6-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

59.79

Std Dev

2.361

0

0

10

20

30

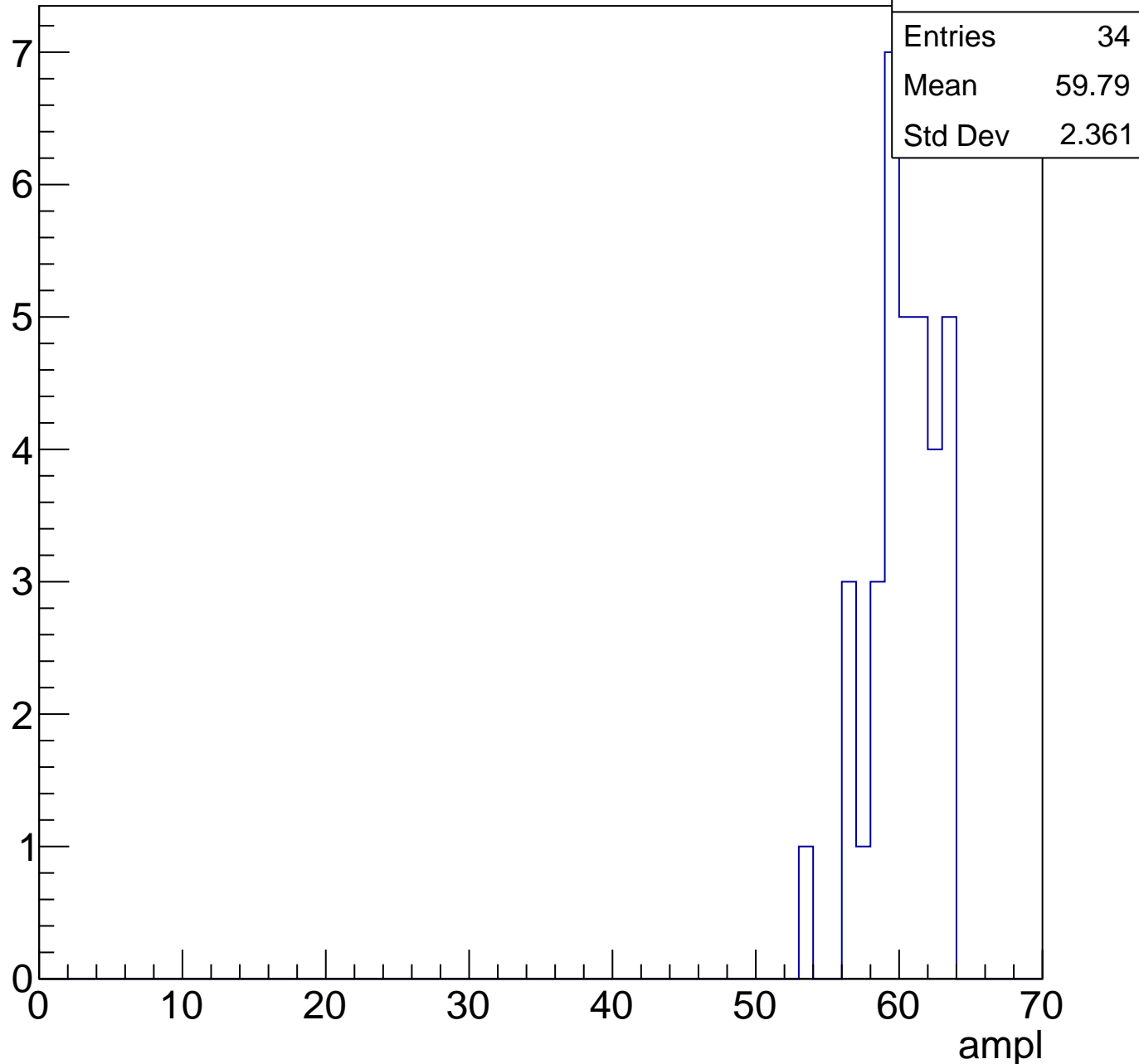
40

50

60

70

ampl

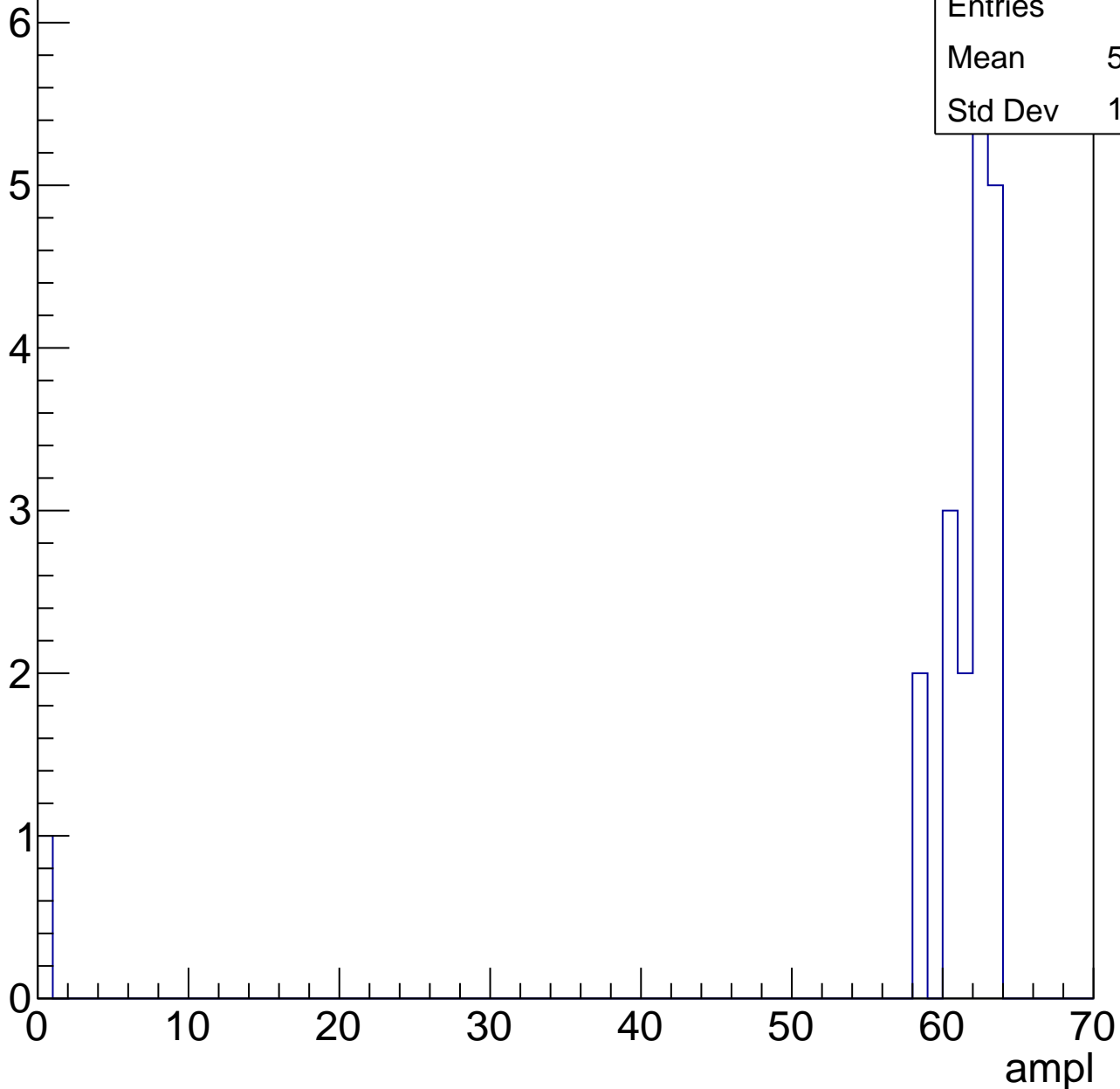


B1L103S, U6-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.16
Std Dev	13.79



B1L103S, U6-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch52, adc0

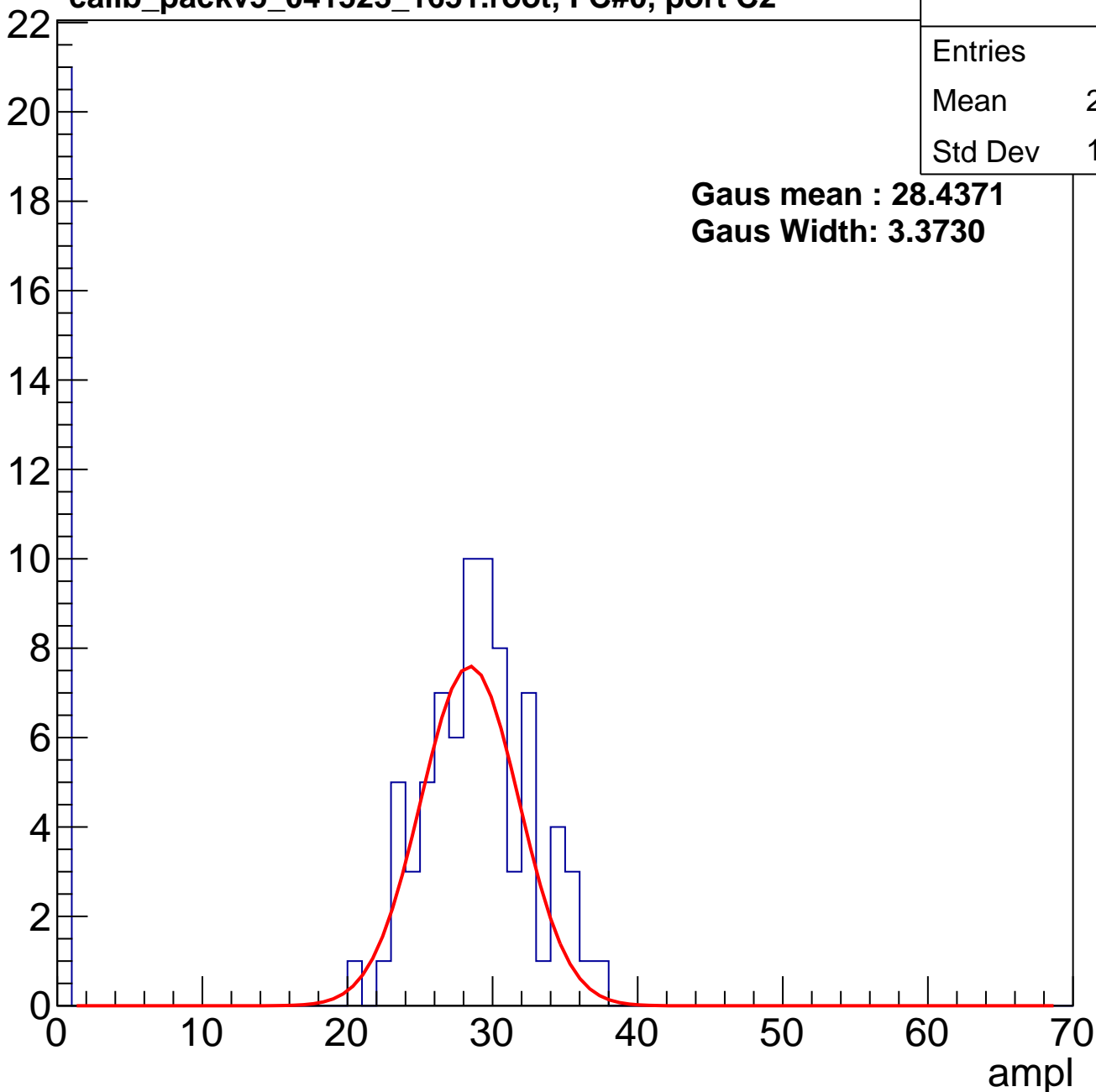
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	22.39
Std Dev	12.19

Gaus mean : 28.4371

Gaus Width: 3.3730

Entry



B1L103S, U6-ch52, adc1

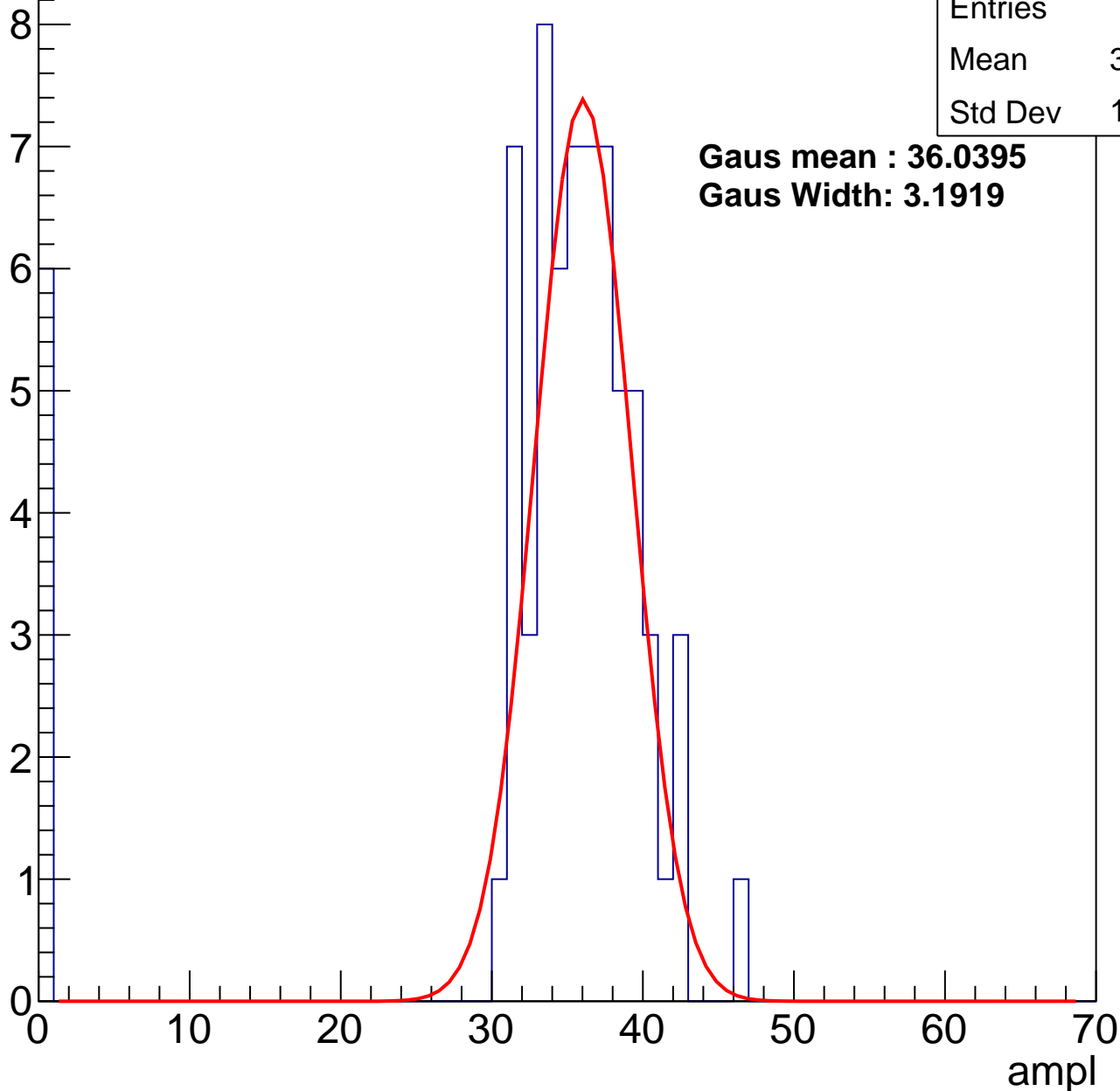
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.64
Std Dev	10.49

Gaus mean : 36.0395

Gaus Width: 3.1919



B1L103S, U6-ch52, adc2

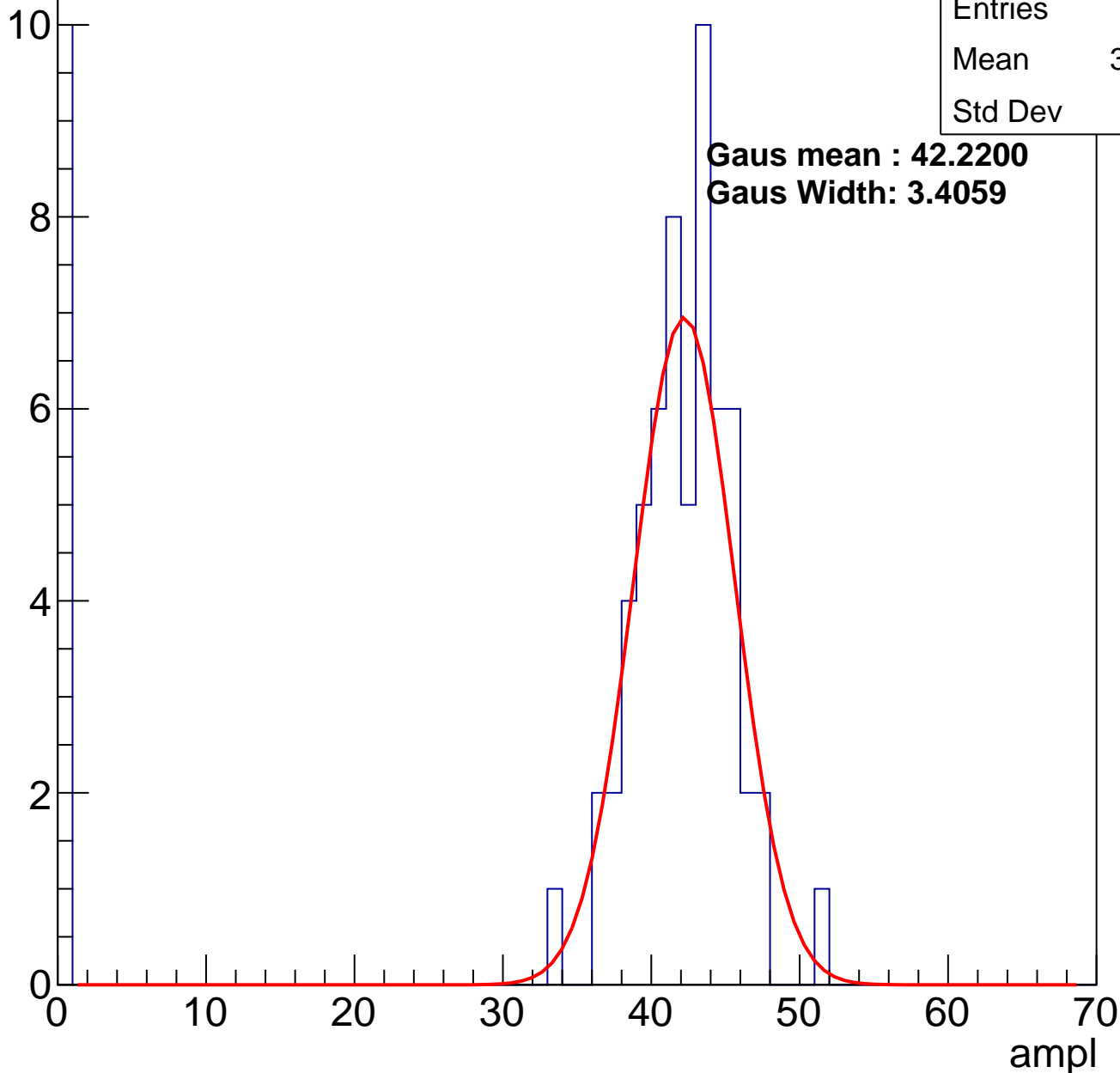
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	35.79
Std Dev	14.9

Gaus mean : 42.2200

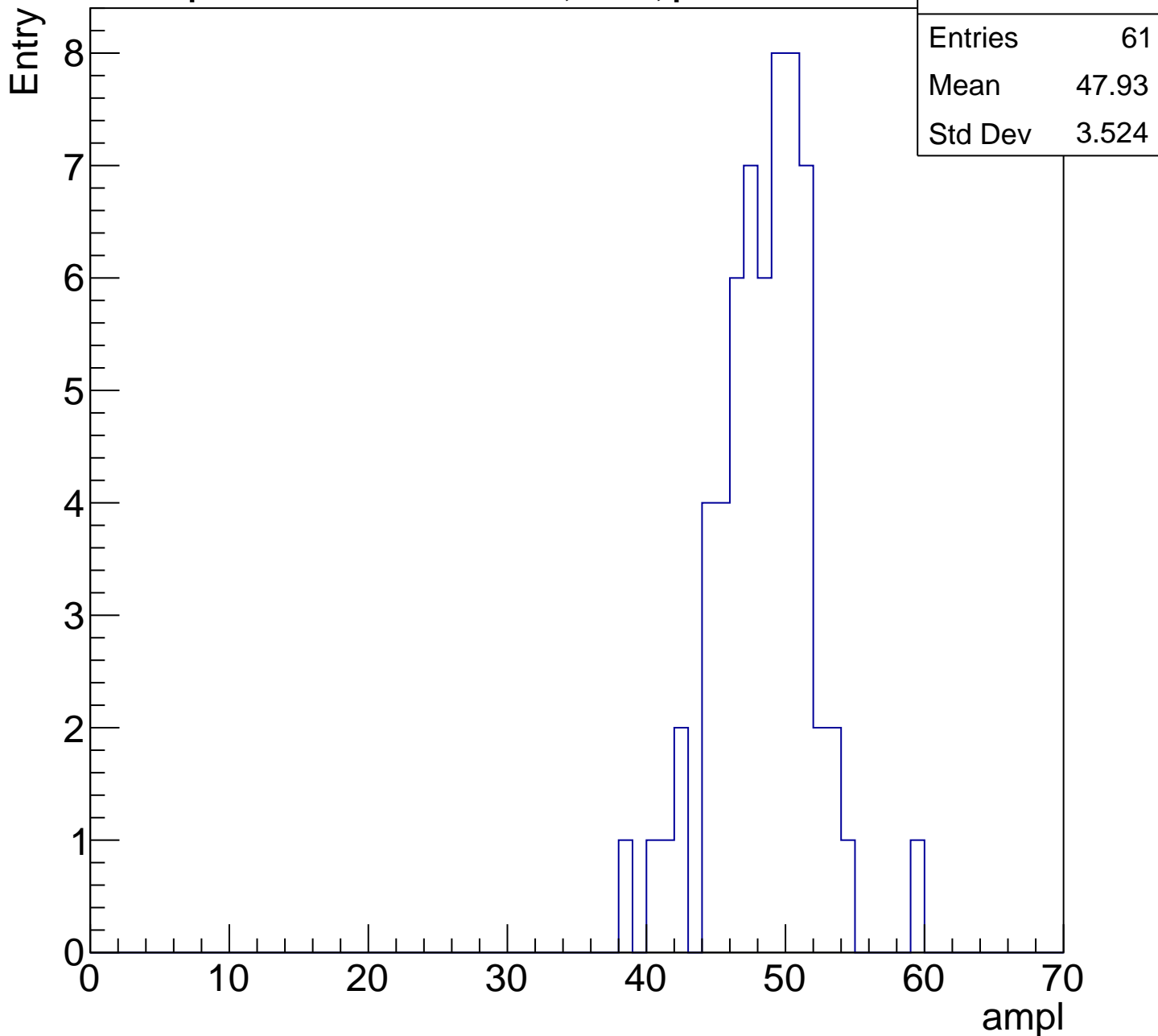
Gaus Width: 3.4059

Entry



B1L103S, U6-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

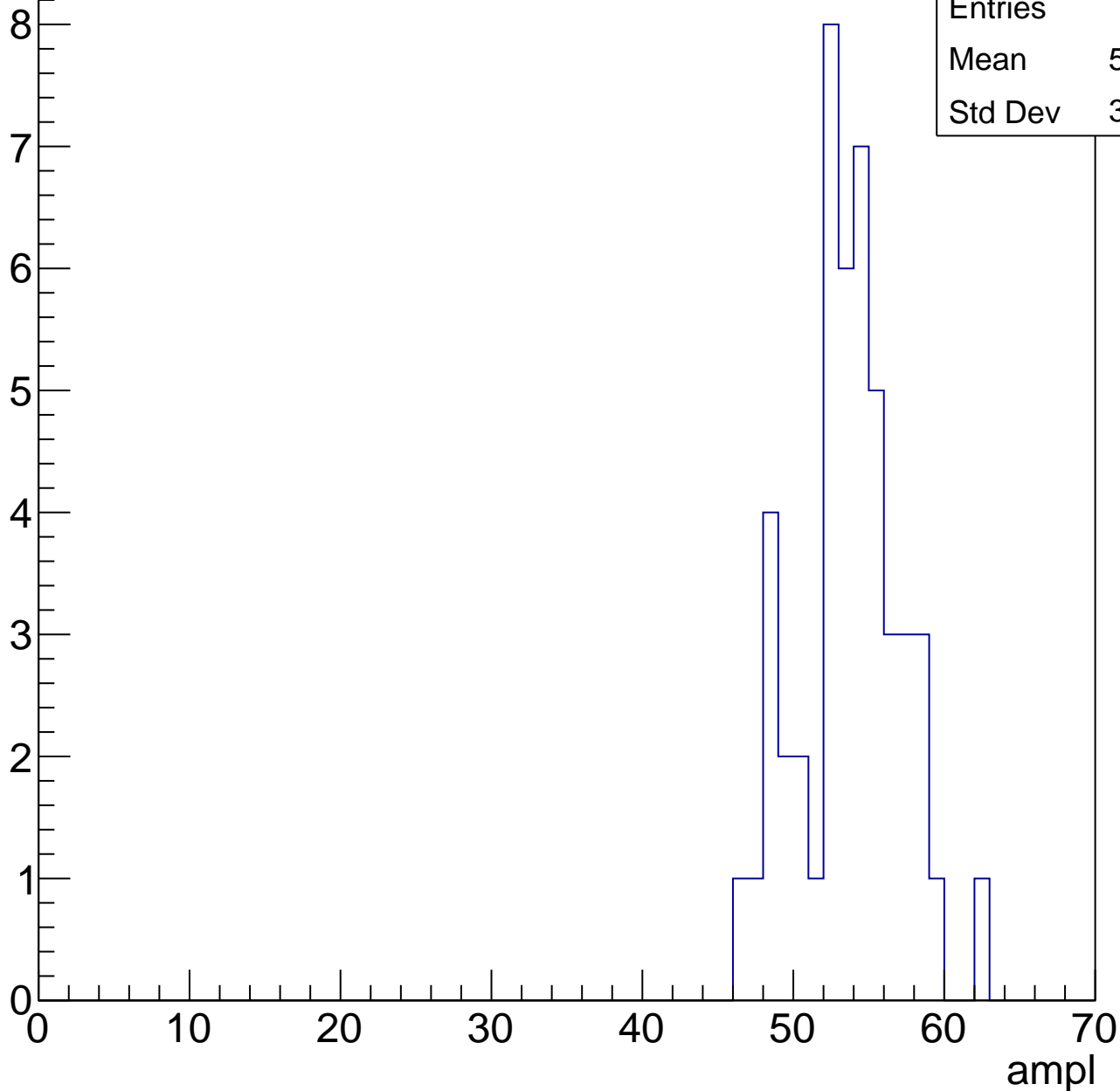


B1L103S, U6-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.23
Std Dev	3.349

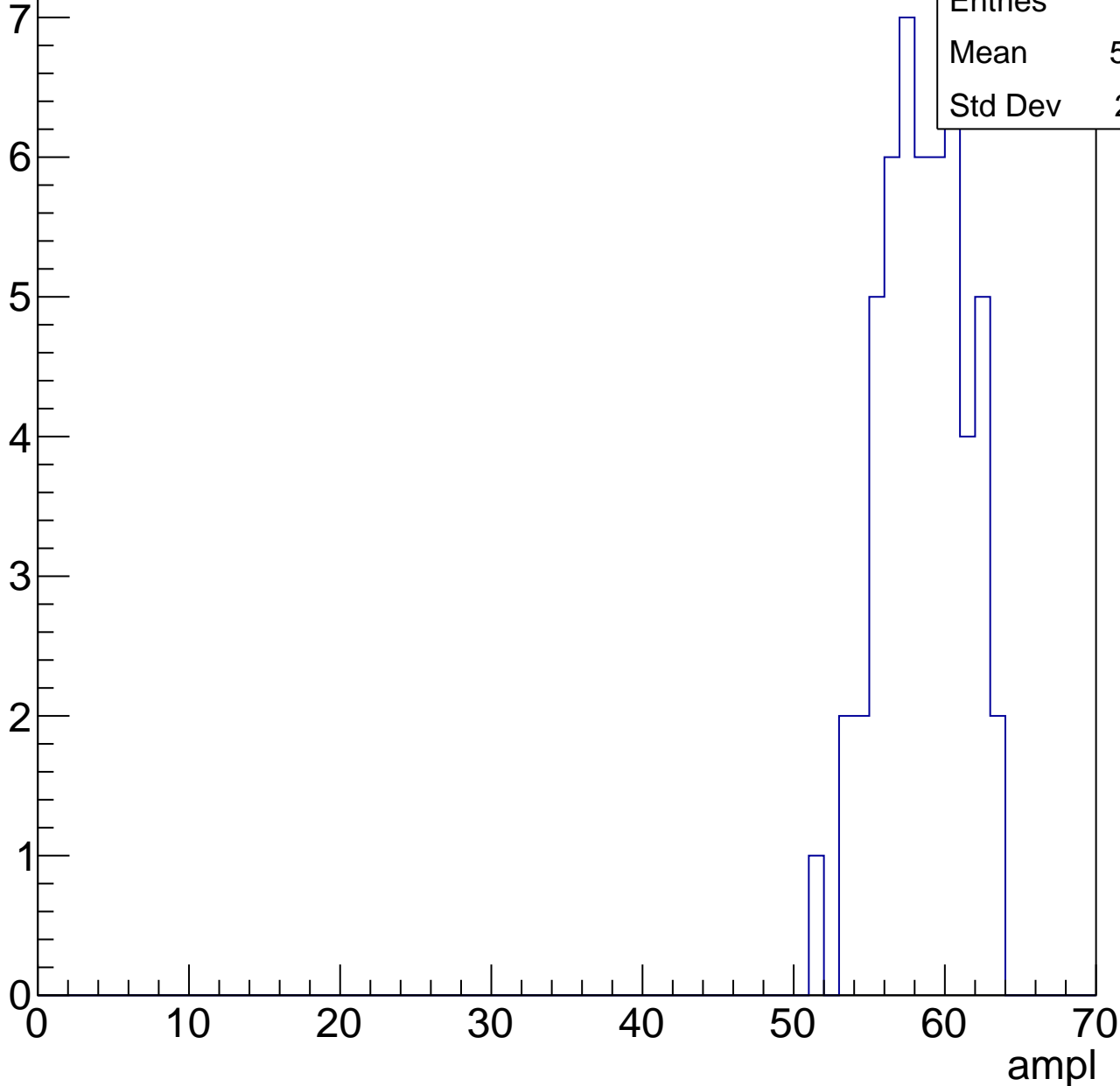


B1L103S, U6-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.06
Std Dev	2.771

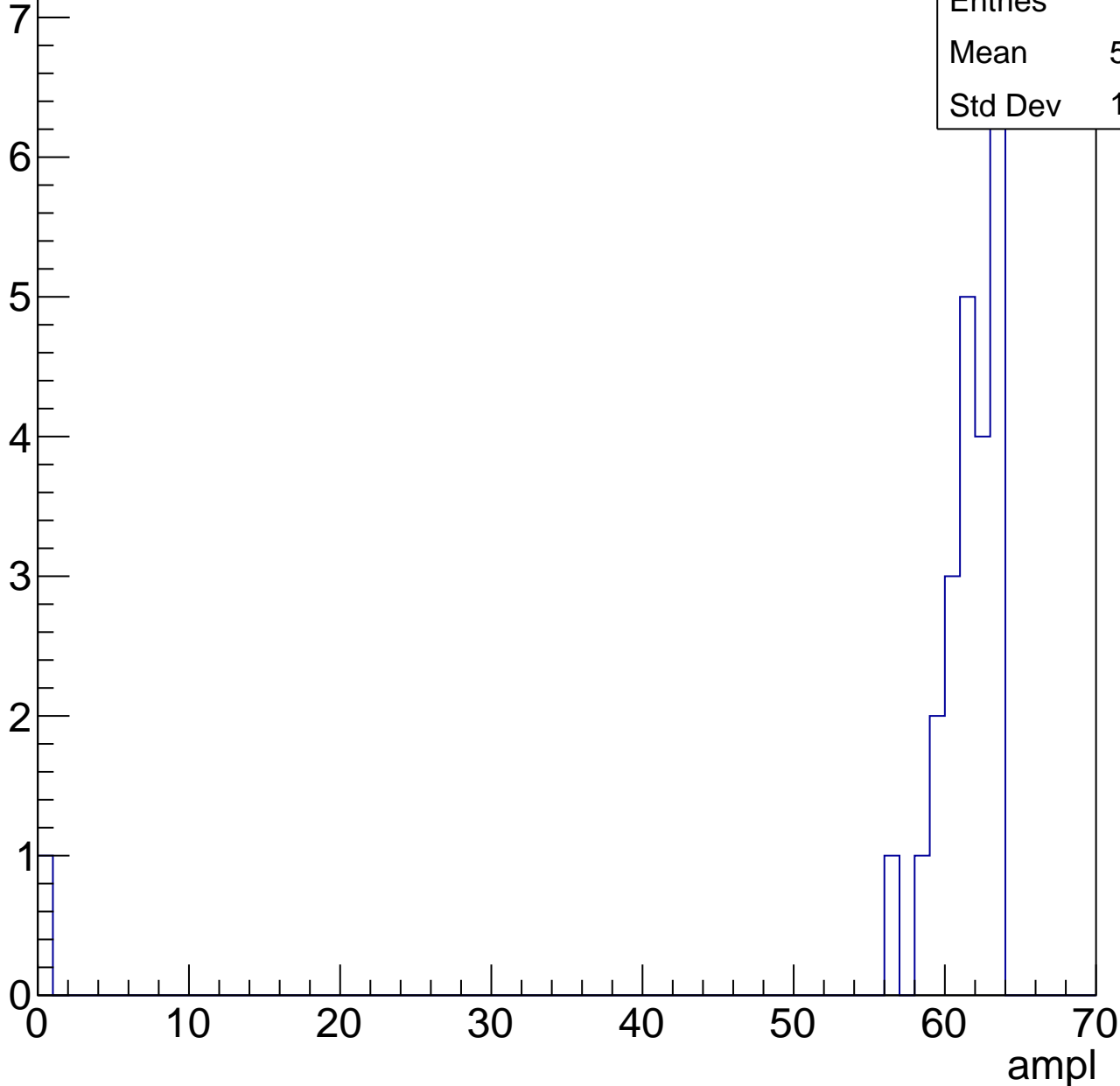


B1L103S, U6-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.58
Std Dev	12.35



B1L103S, U6-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U6-ch53, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	23.66
Std Dev	13.35

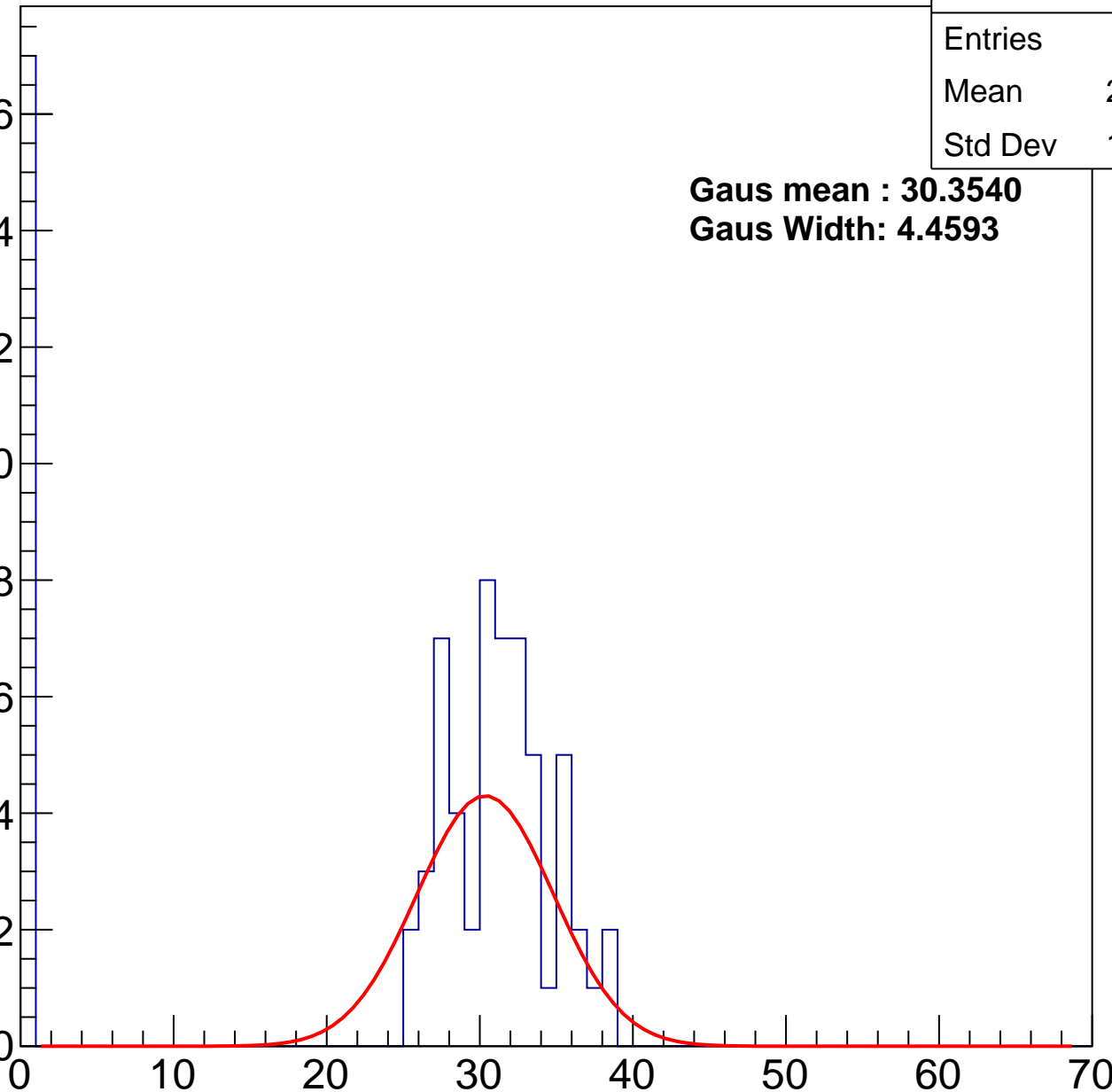
Gaus mean : 30.3540

Gaus Width: 4.4593

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch53, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	31.07
Std Dev	13.36

Gaus mean : 37.4914

Gaus Width: 3.3515

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

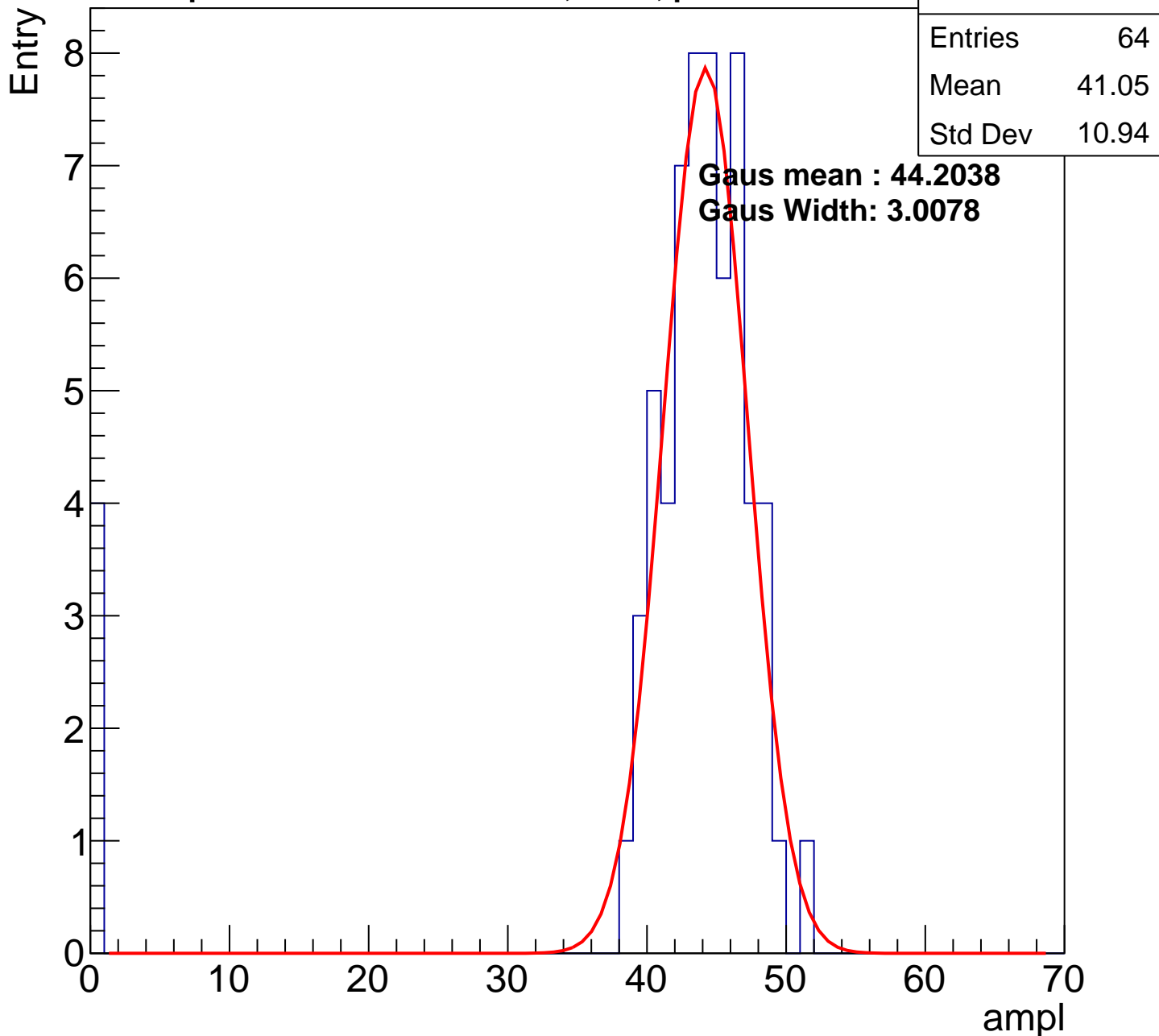
8

10

12

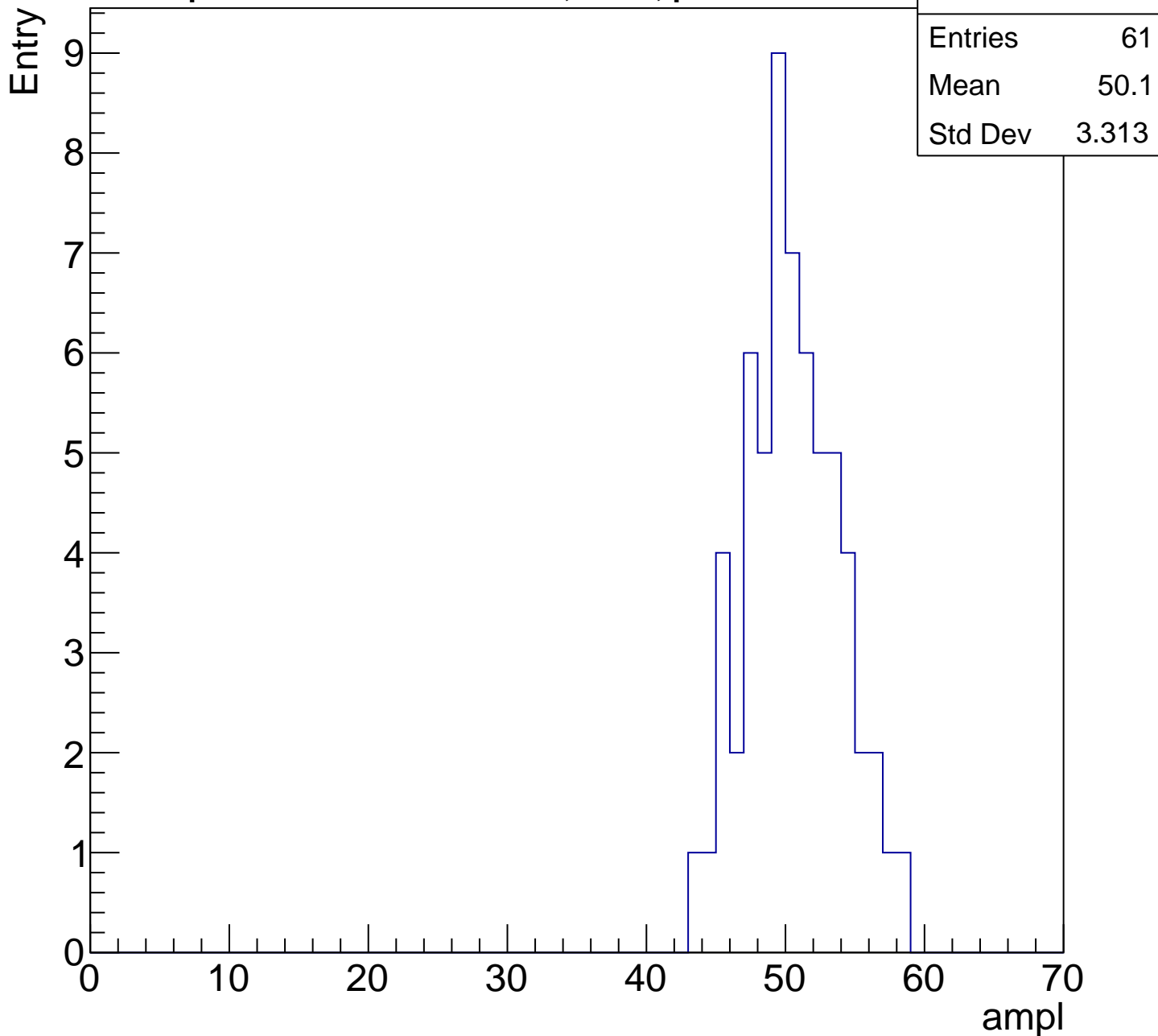
B1L103S, U6-ch53, adc2

calib_packv5_041523_1651.root, FC#0, port C2



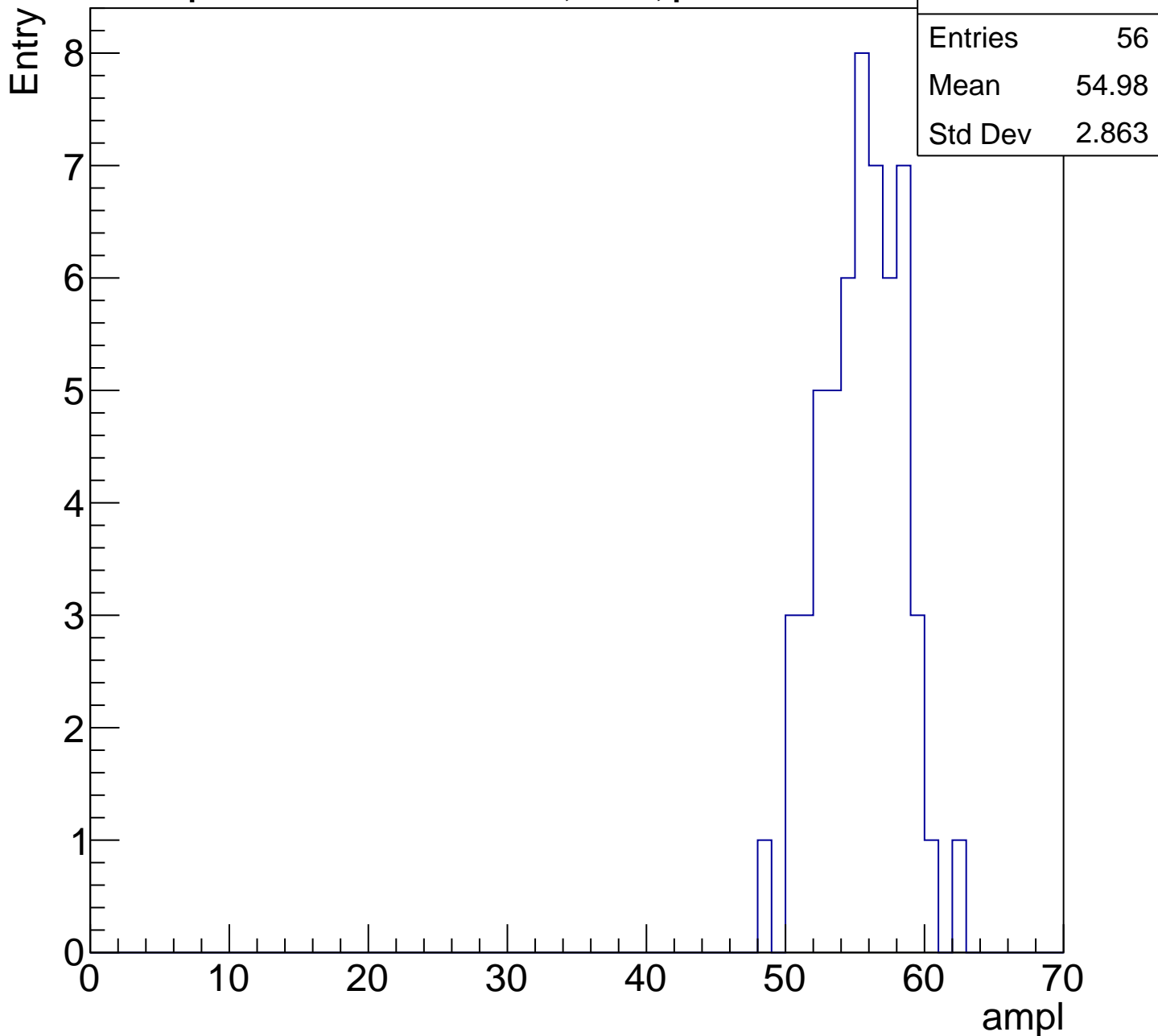
B1L103S, U6-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2



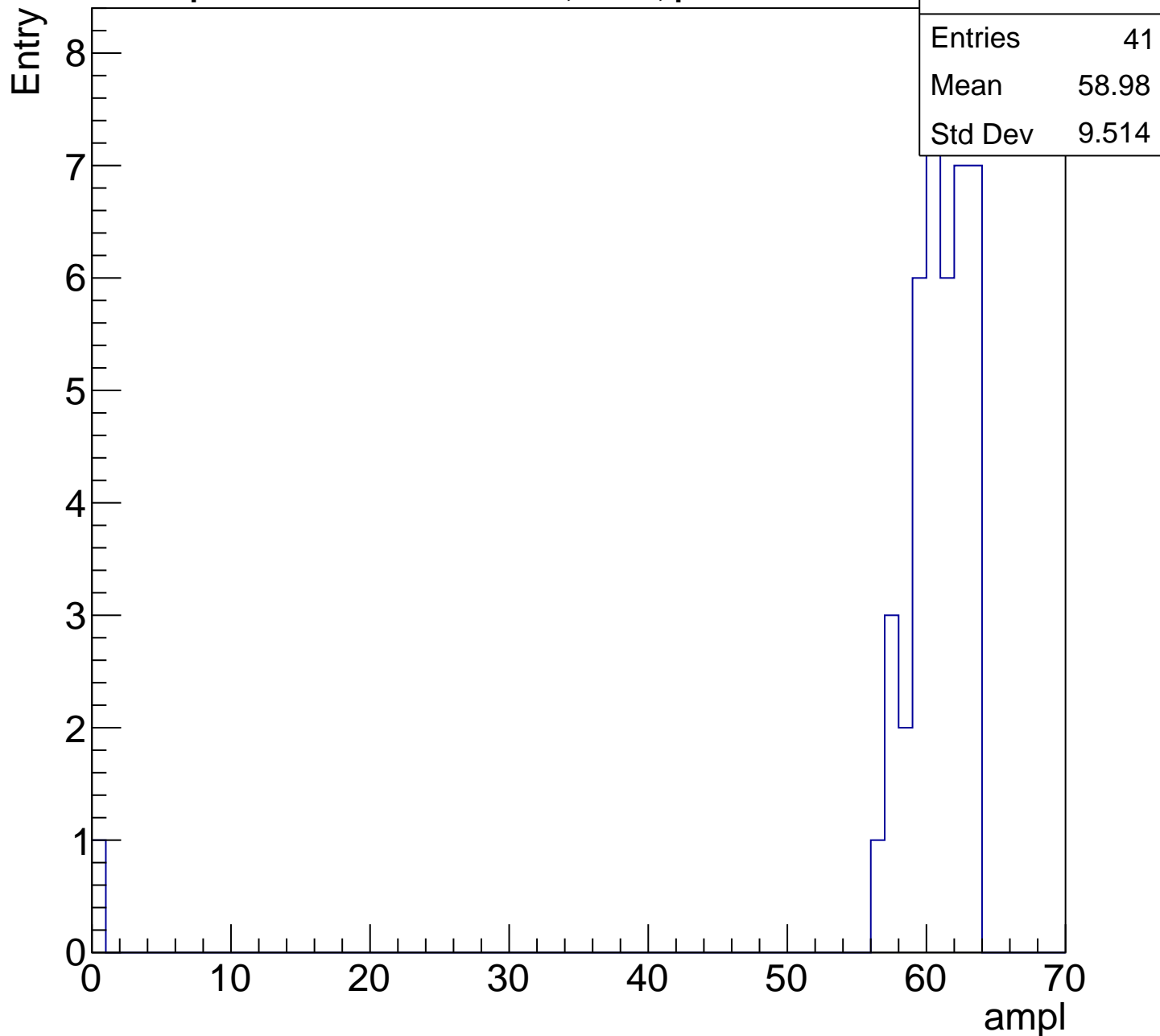
B1L103S, U6-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch53, adc5

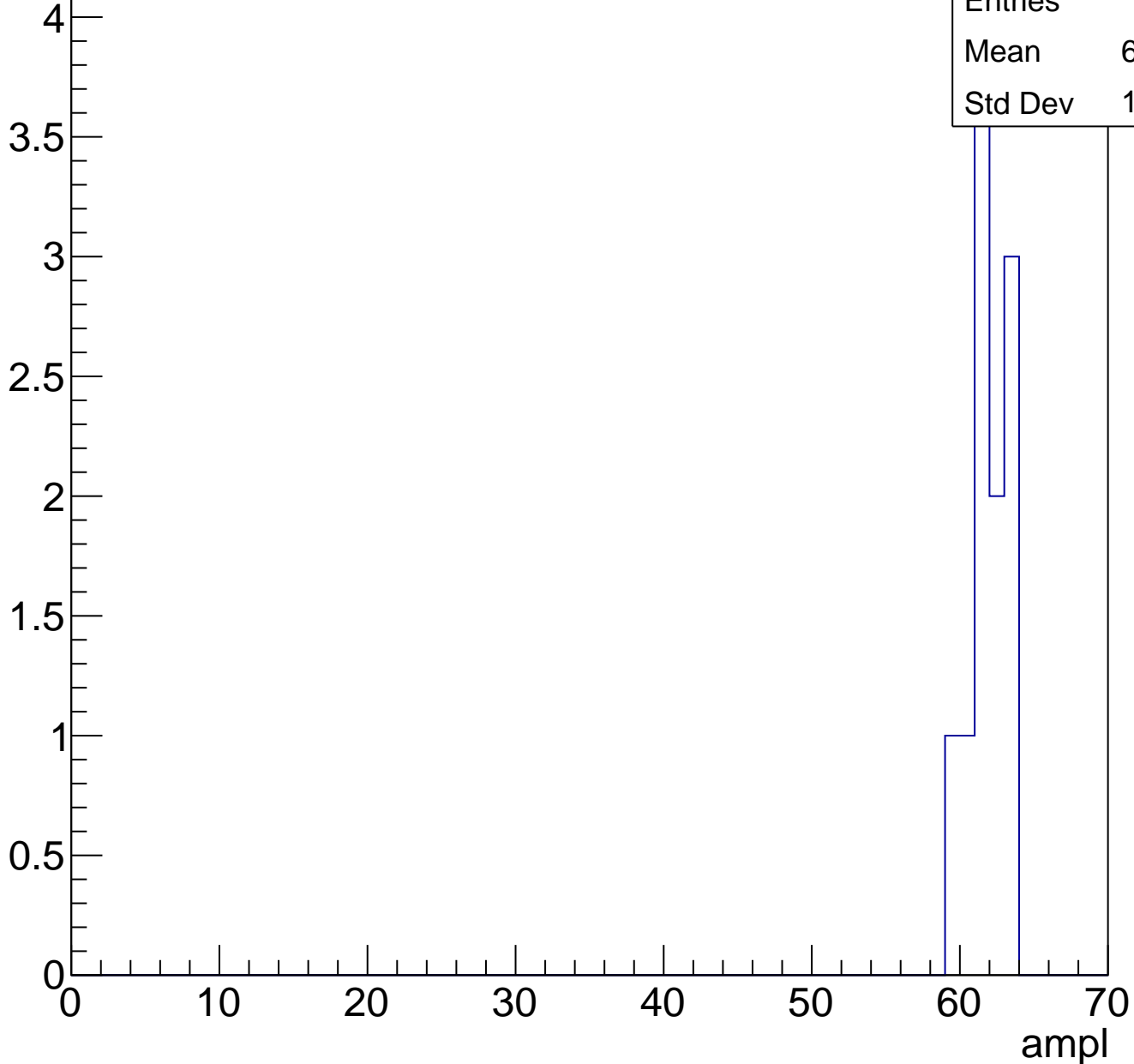
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

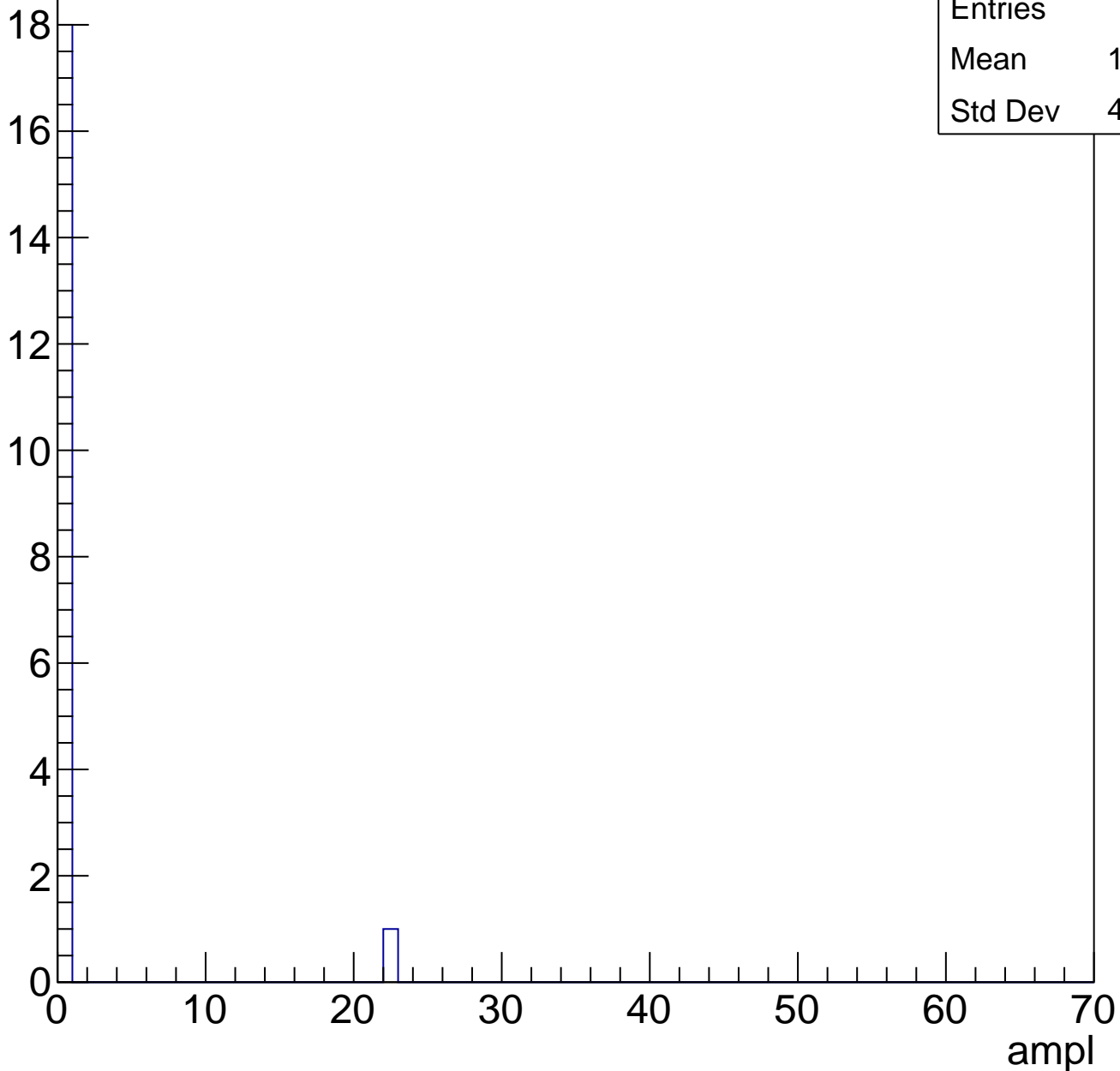
Entry



B1L103S, U6-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.158
Std Dev	4.913

B1L103S, U6-ch54, adc0

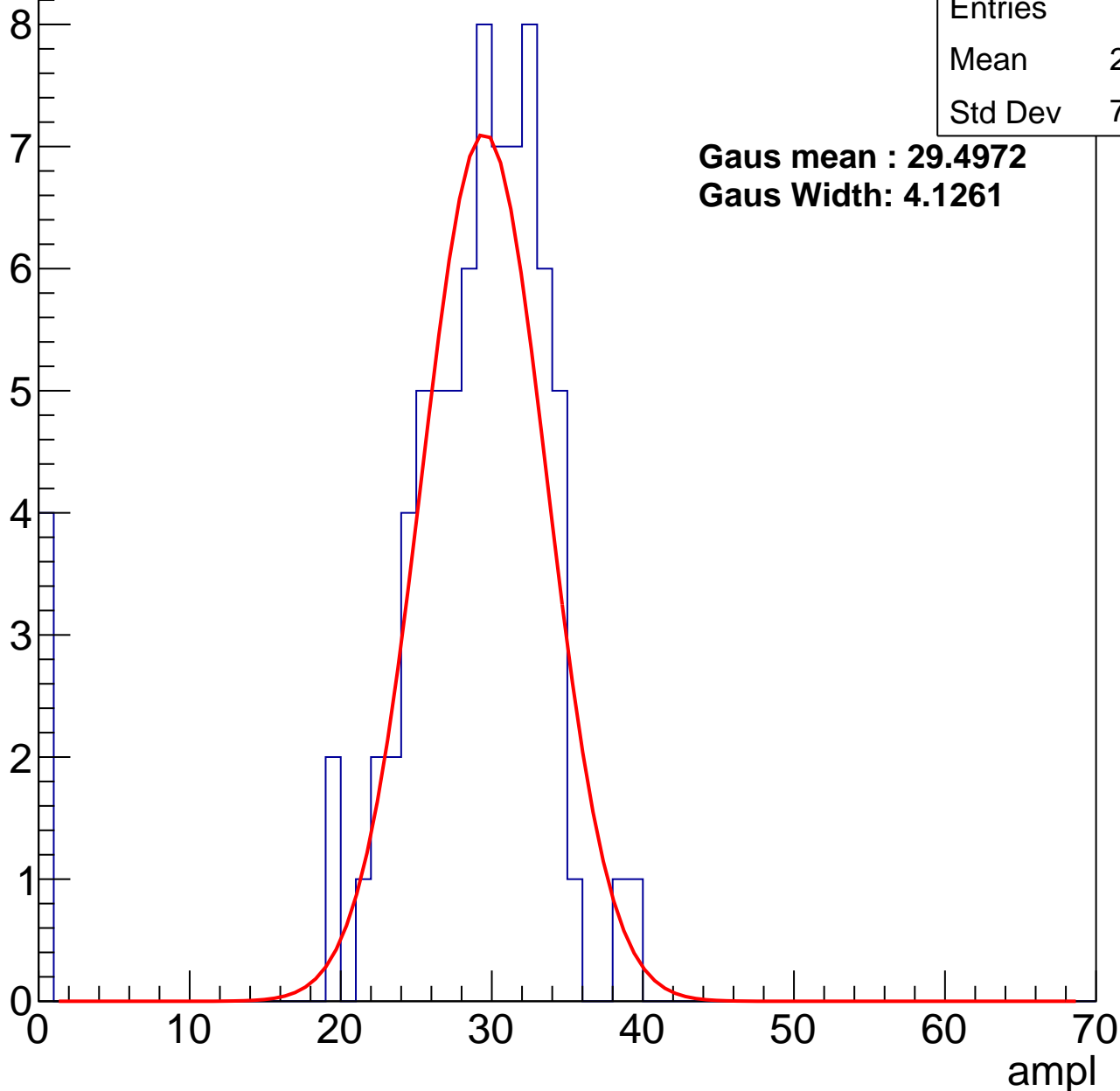
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	27.48
Std Dev	7.423

Gaus mean : 29.4972

Gaus Width: 4.1261



B1L103S, U6-ch54, adc1

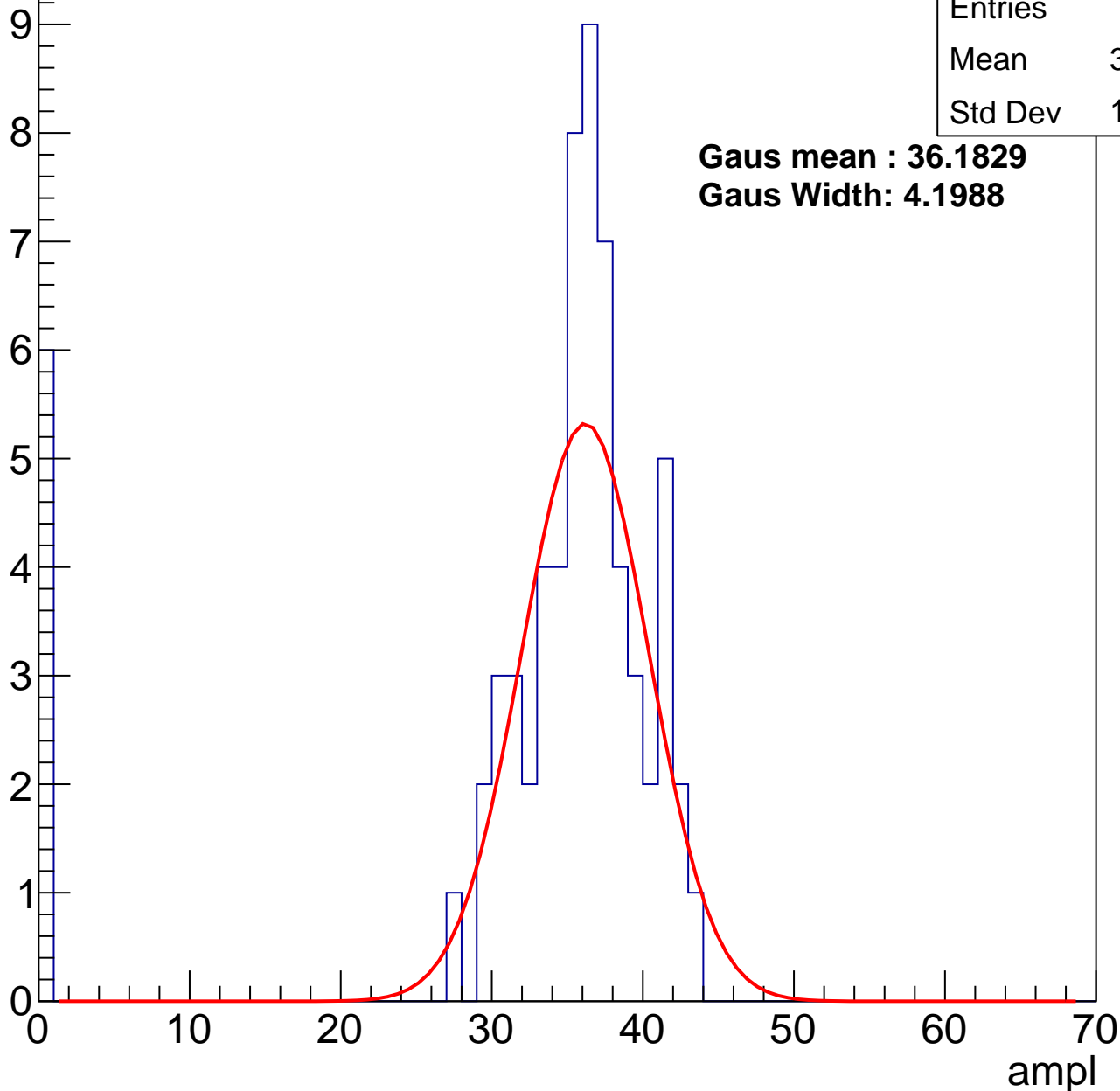
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.48
Std Dev	10.83

Gaus mean : 36.1829

Gaus Width: 4.1988



B1L103S, U6-ch54, adc2

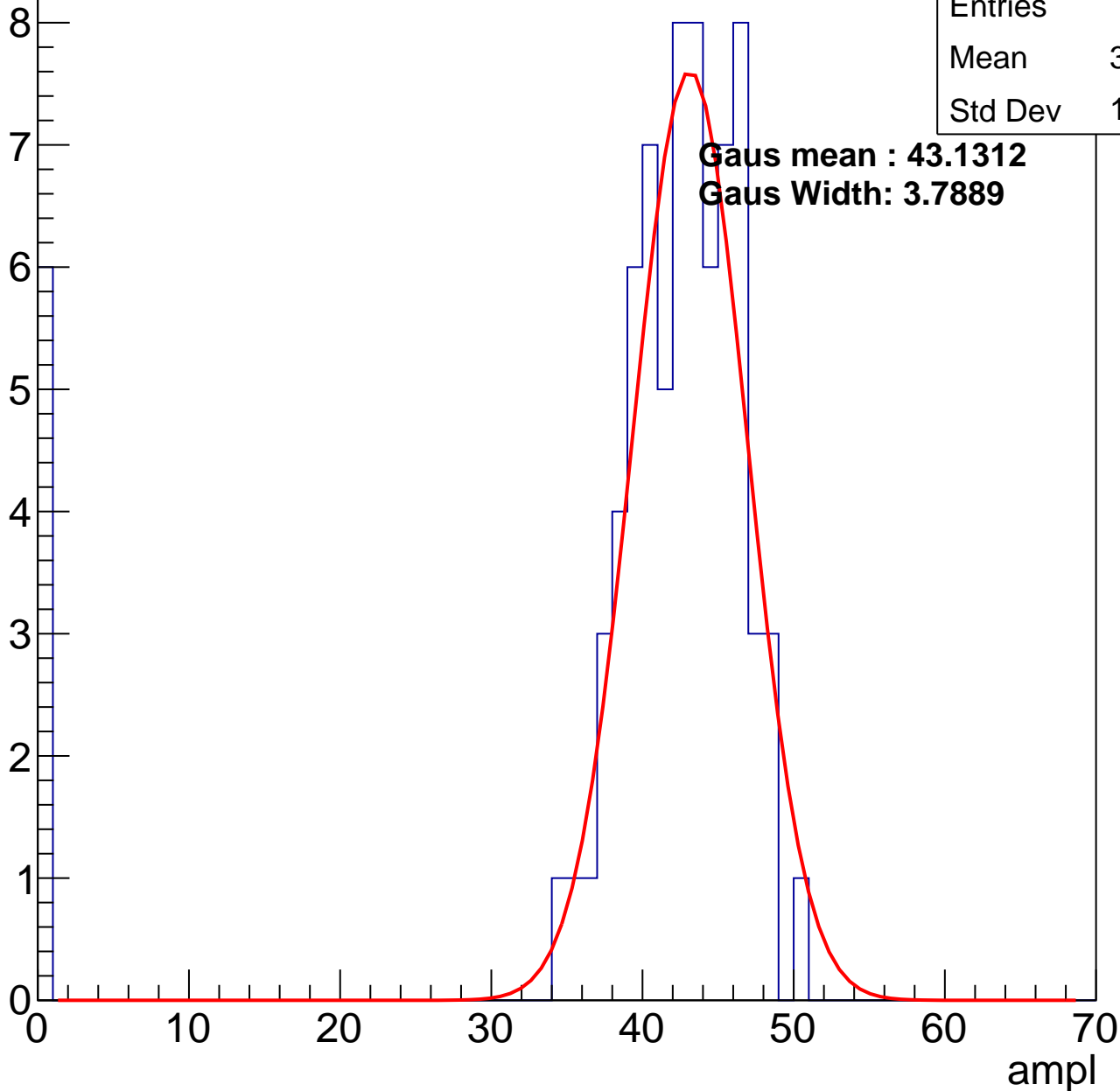
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	39.09
Std Dev	11.75

Gaus mean : 43.1312

Gaus Width: 3.7889

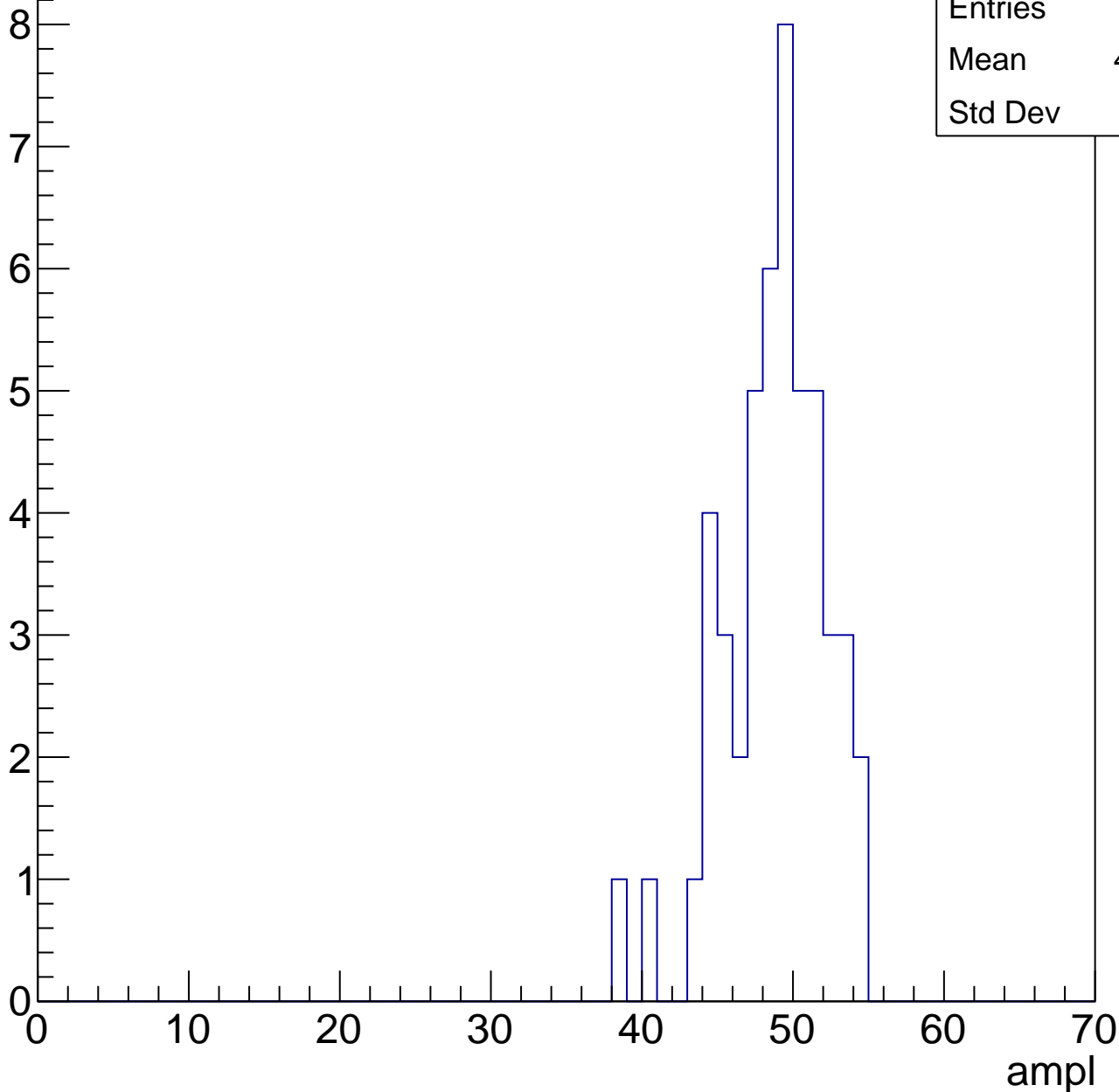


B1L103S, U6-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	48.31
Std Dev	3.37

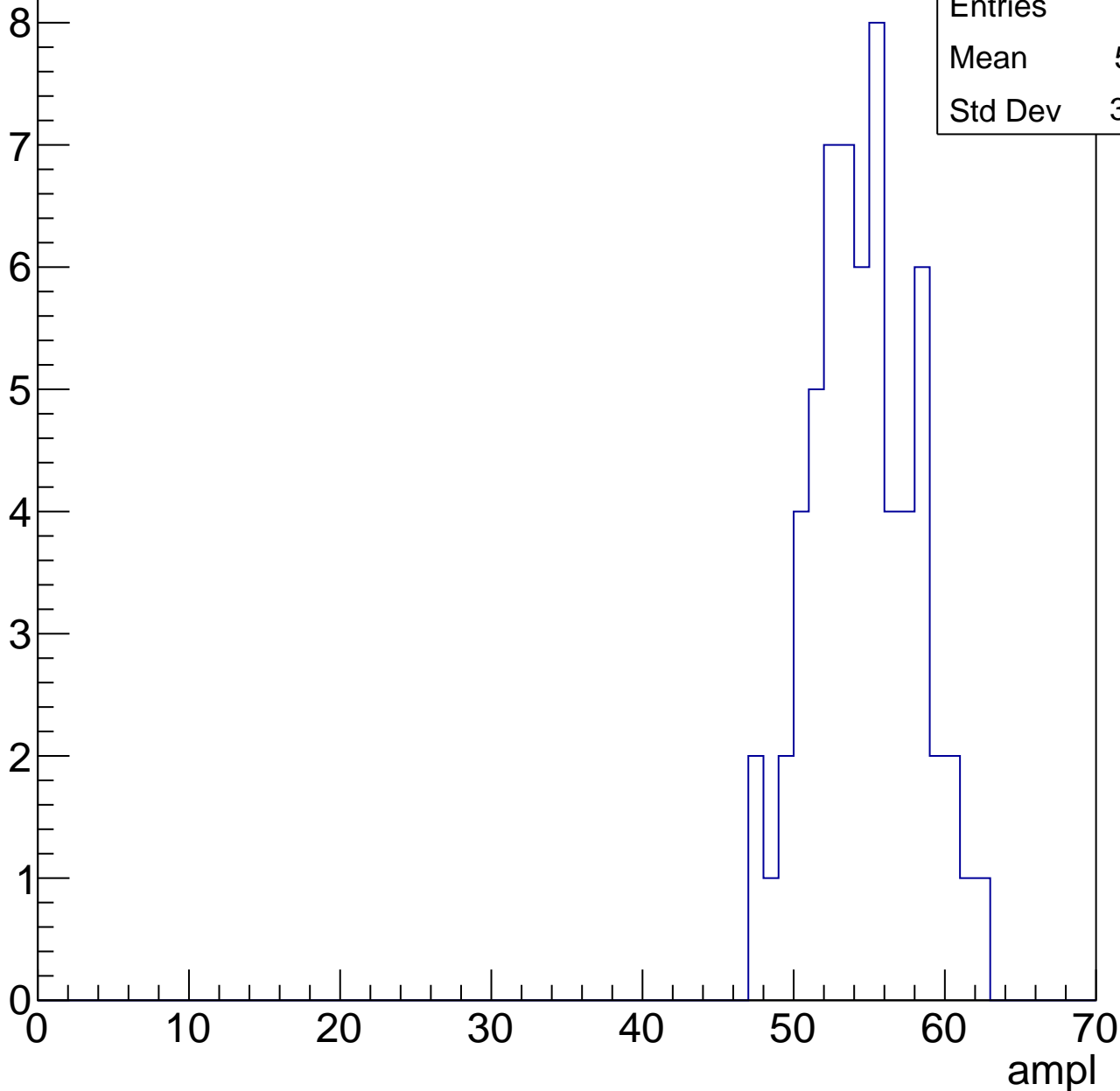


B1L103S, U6-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

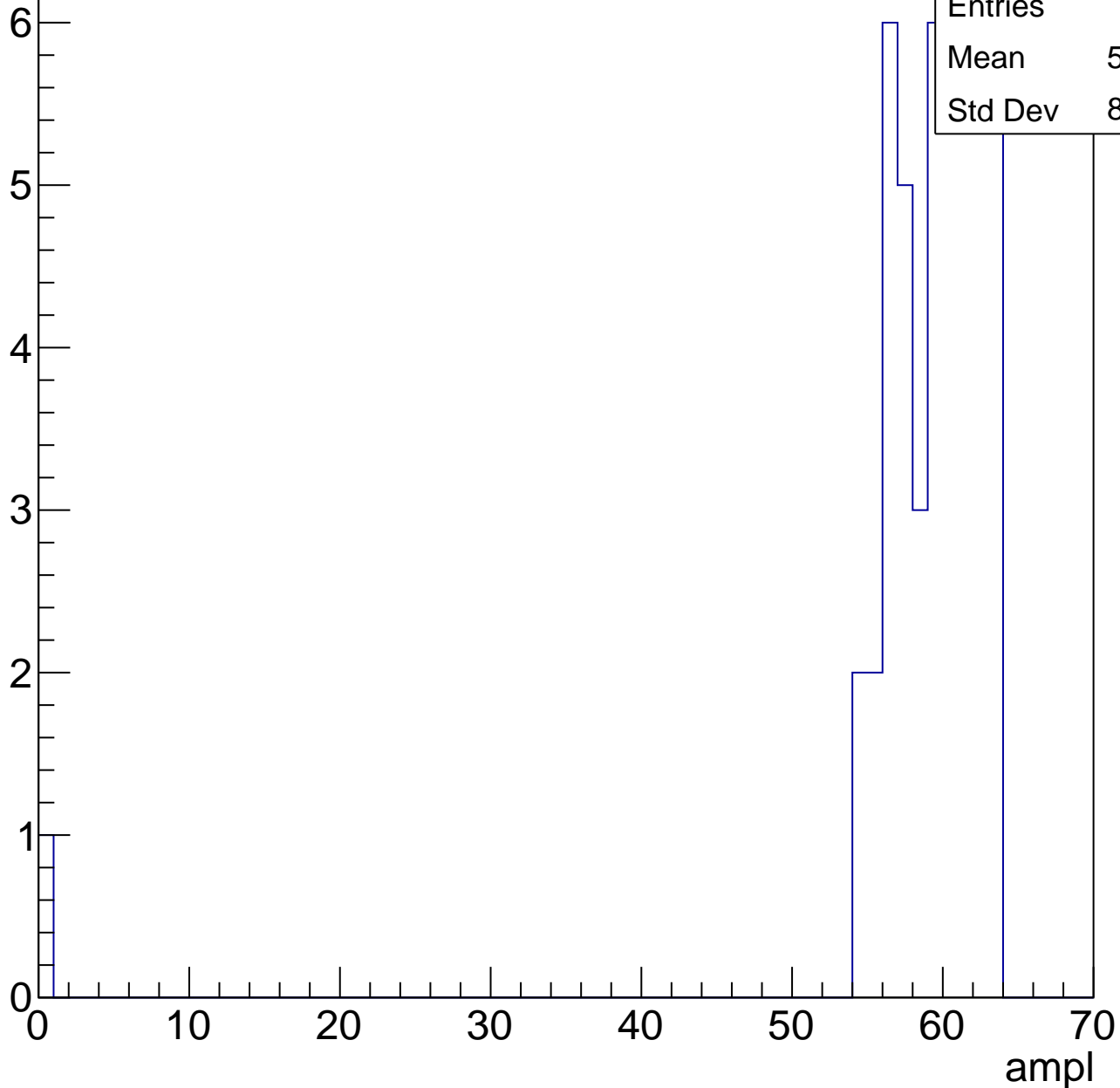
Entries	62
Mean	54.11
Std Dev	3.404



B1L103S, U6-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

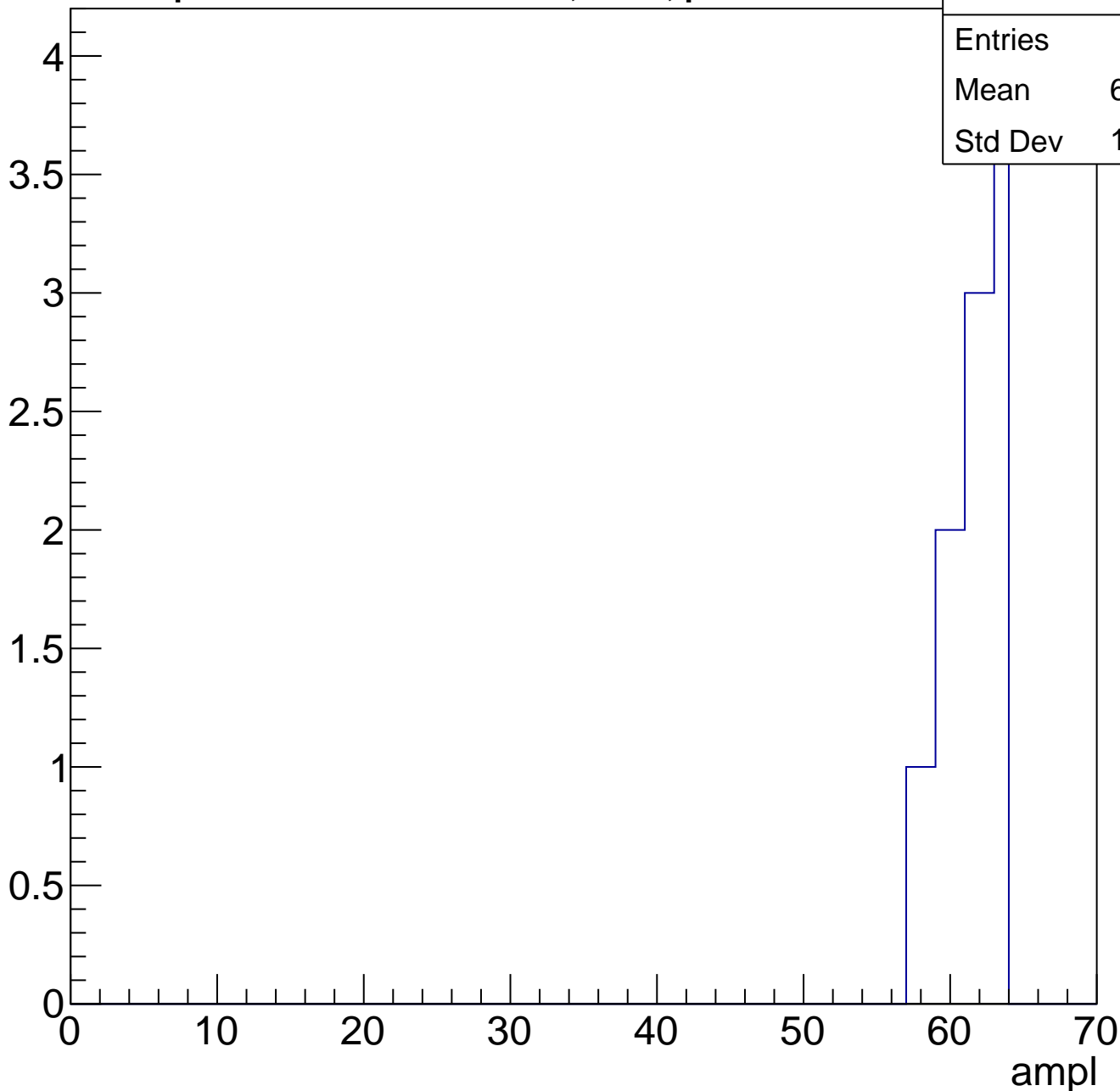
Entry



B1L103S, U6-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	16
Mean	60.88
Std Dev	1.833

B1L103S, U6-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch55, adc0

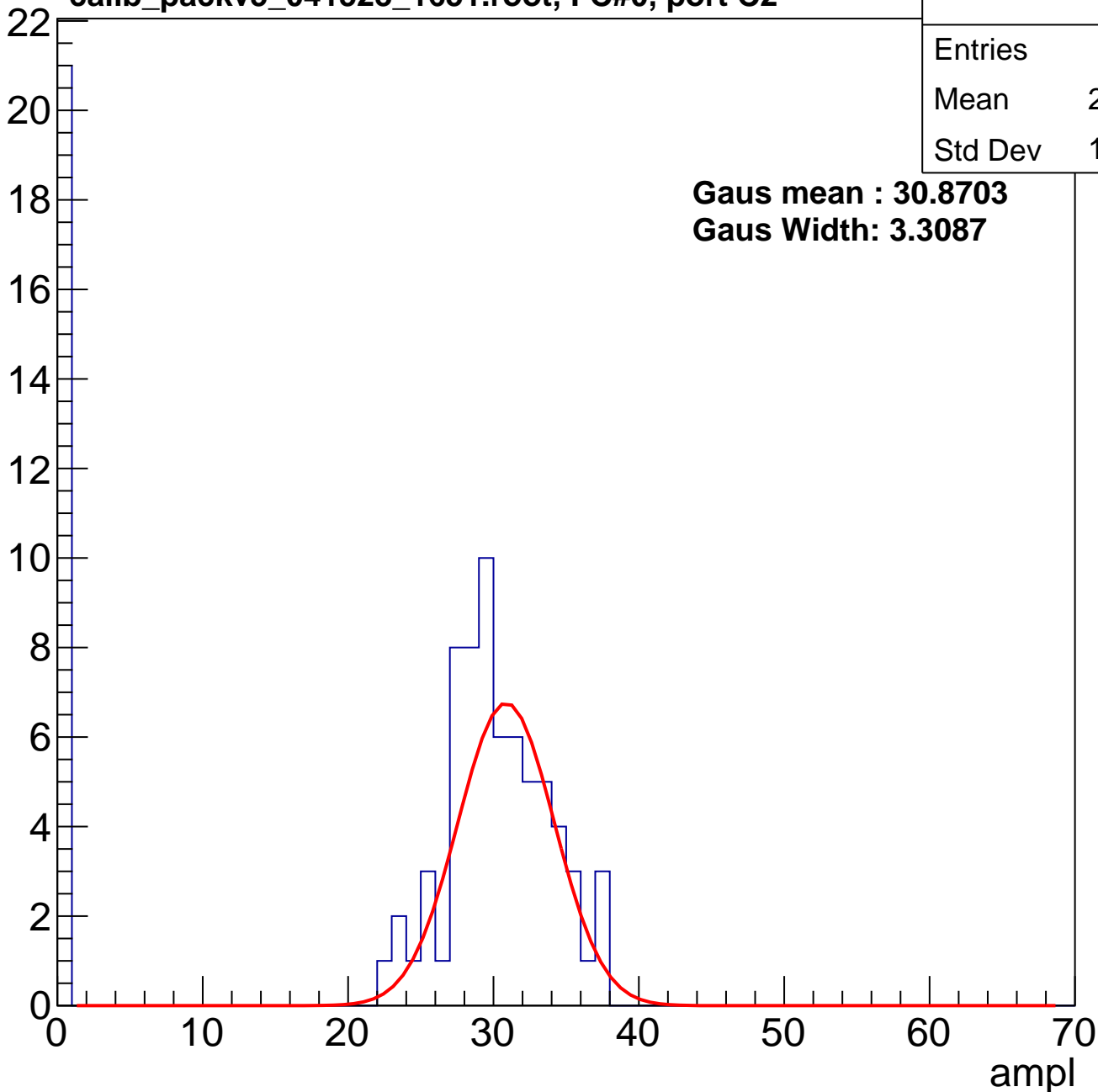
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	22.75
Std Dev	13.09

Gaus mean : 30.8703

Gaus Width: 3.3087

Entry



B1L103S, U6-ch55, adc1

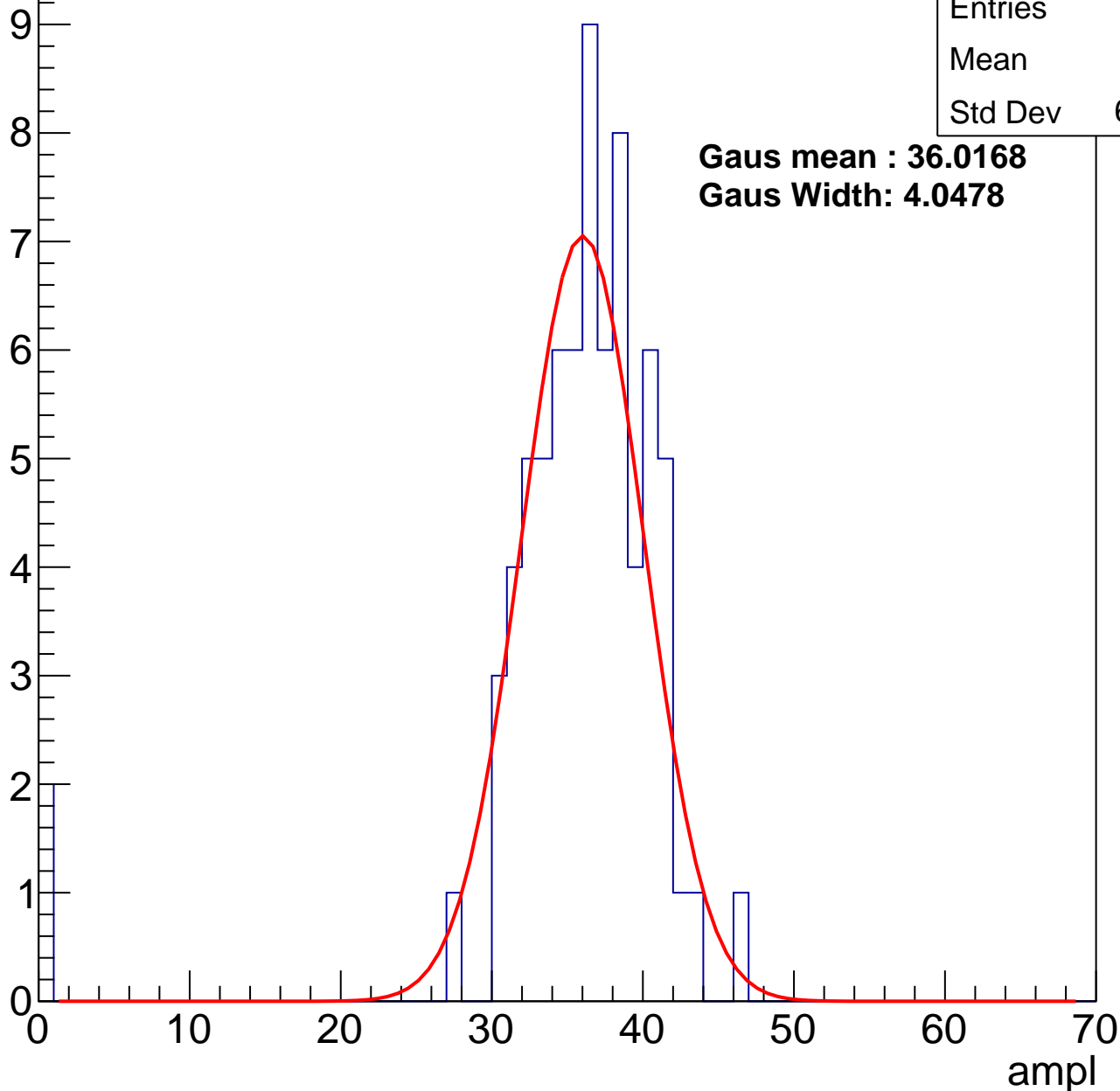
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.1
Std Dev	6.881

Gaus mean : 36.0168

Gaus Width: 4.0478



B1L103S, U6-ch55, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

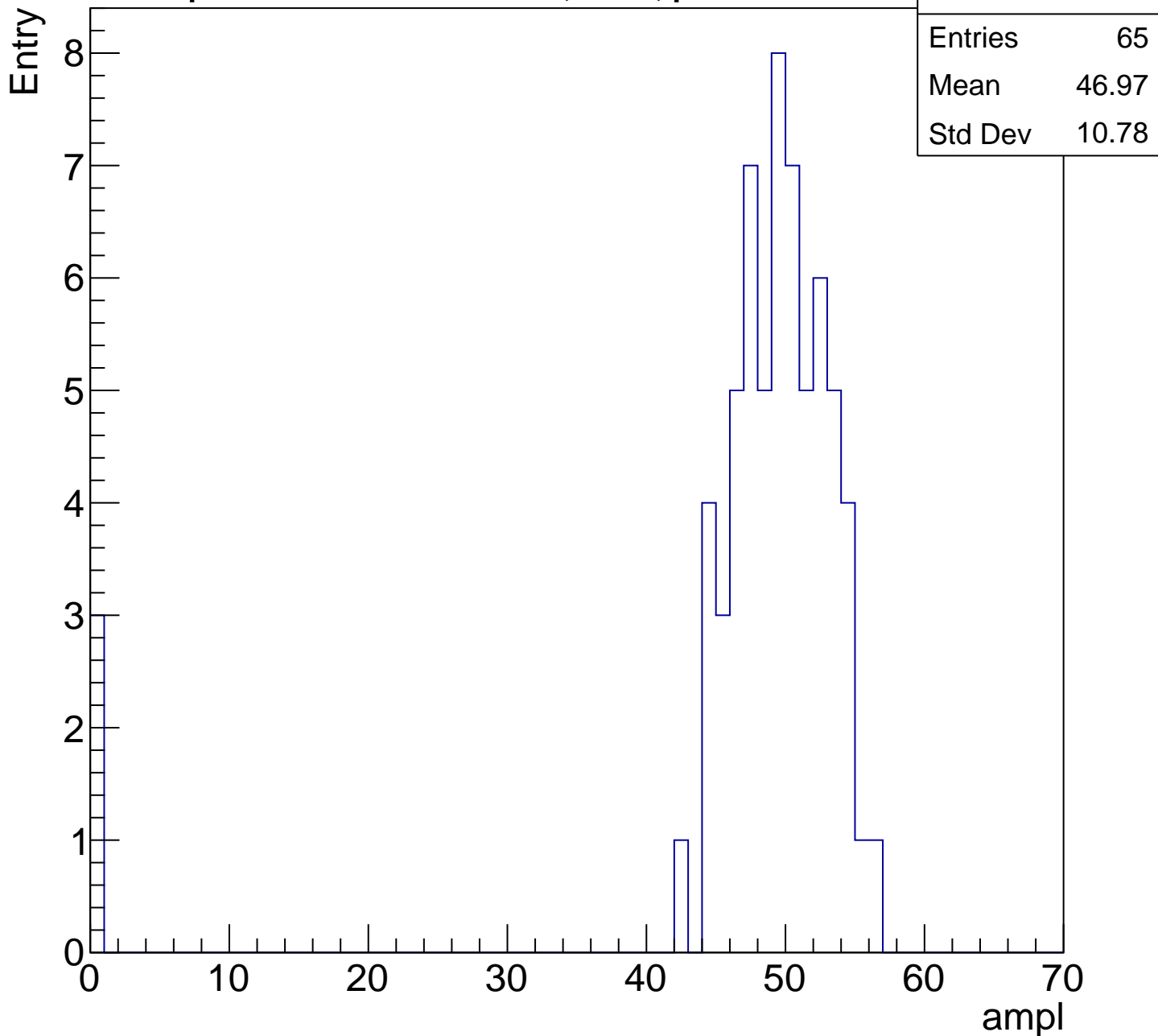
Entries	71
Mean	36.24
Std Dev	15.79

Gaus mean : 43.3554

Gaus Width: 3.2466

B1L103S, U6-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

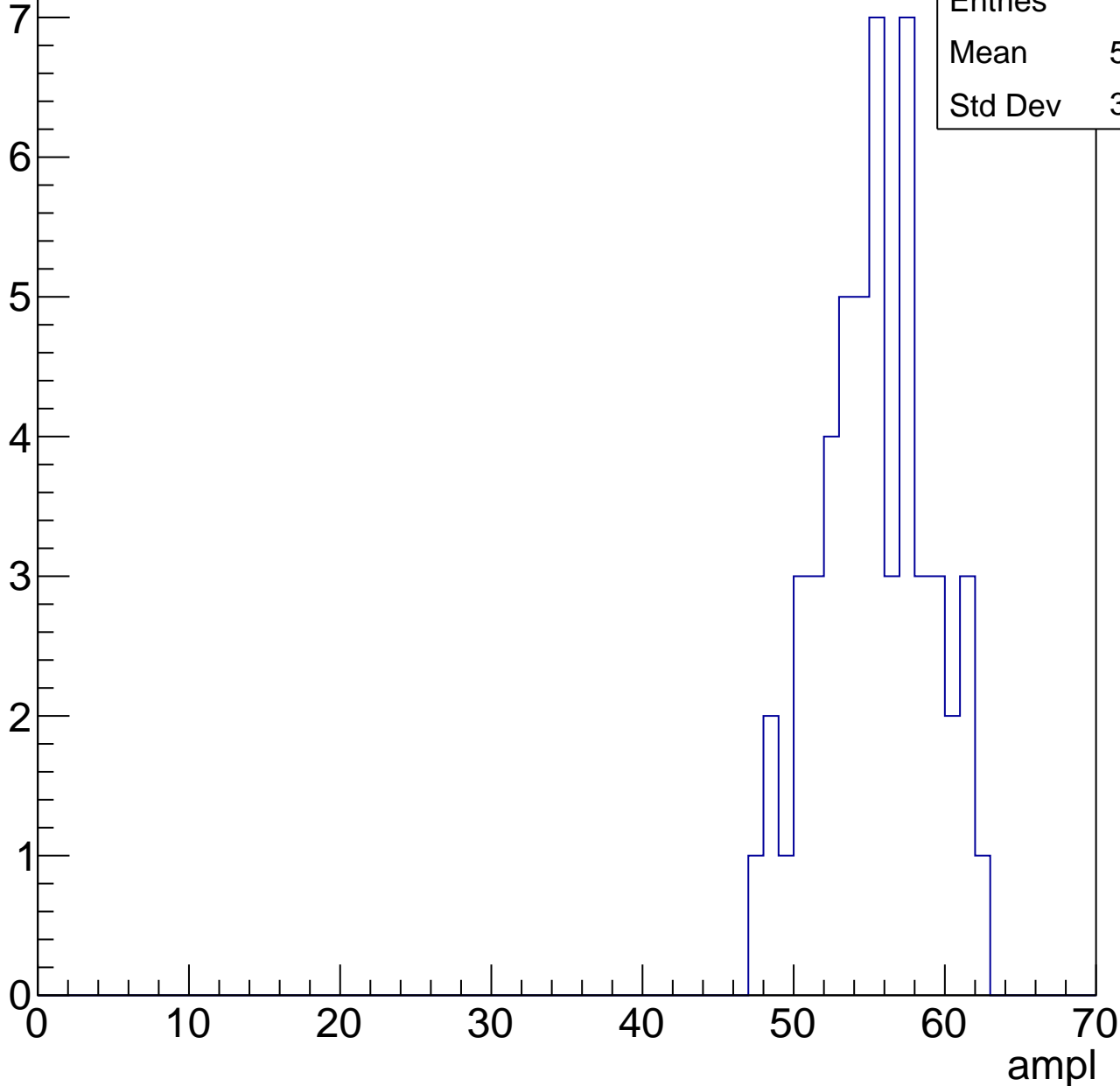


B1L103S, U6-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

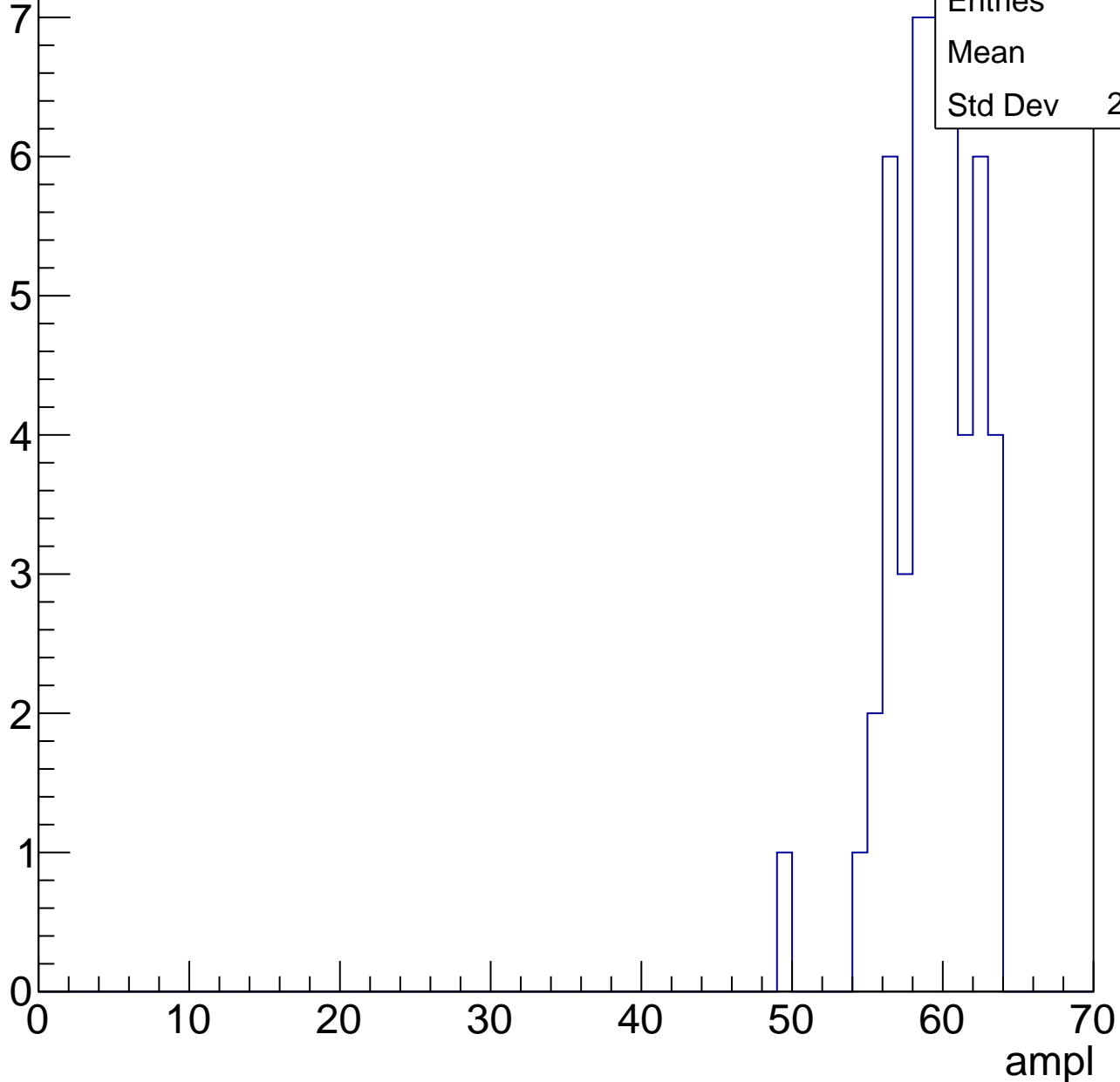
Entries	53
Mean	54.83
Std Dev	3.617



B1L103S, U6-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

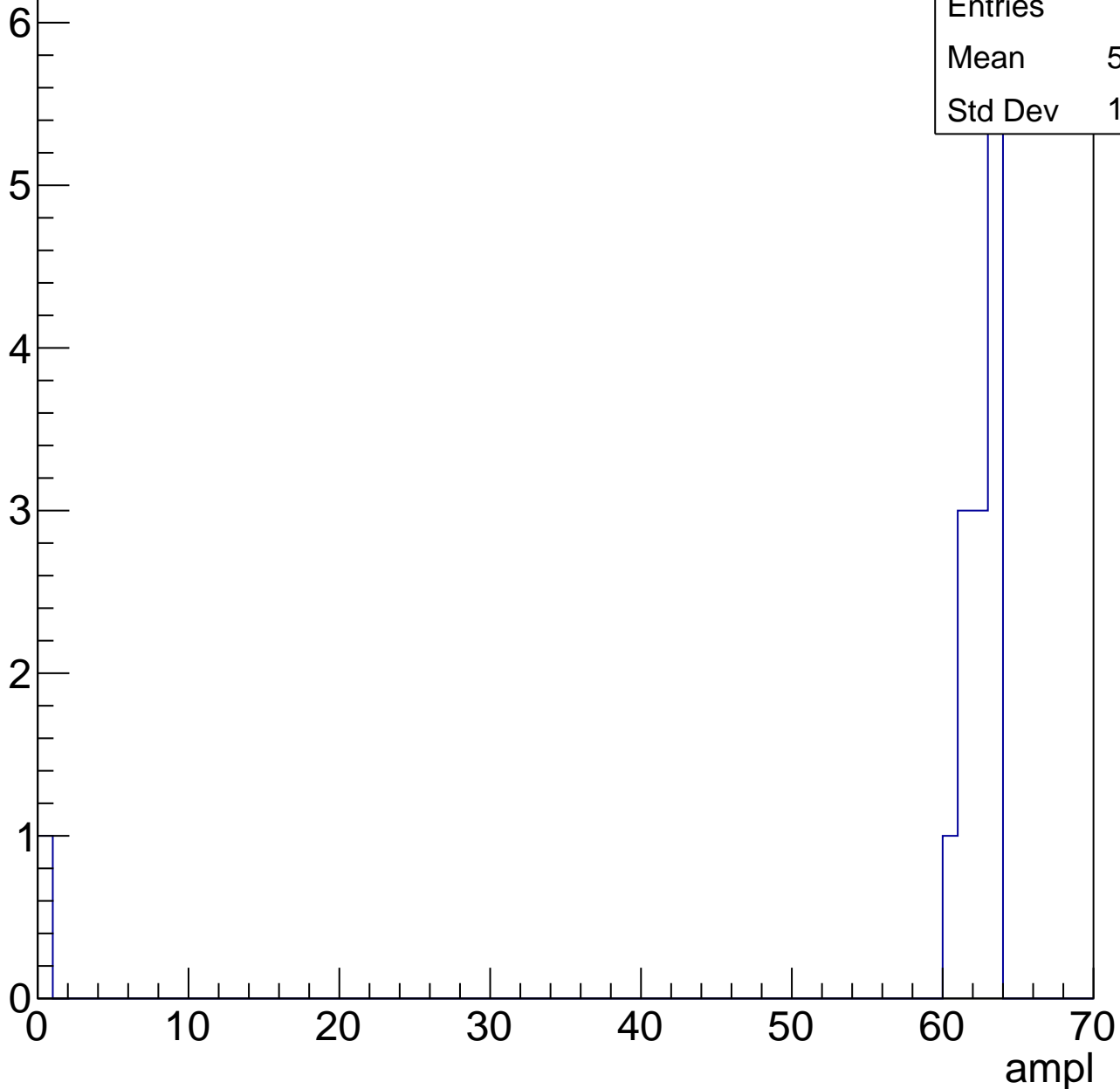


B1L103S, U6-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.02

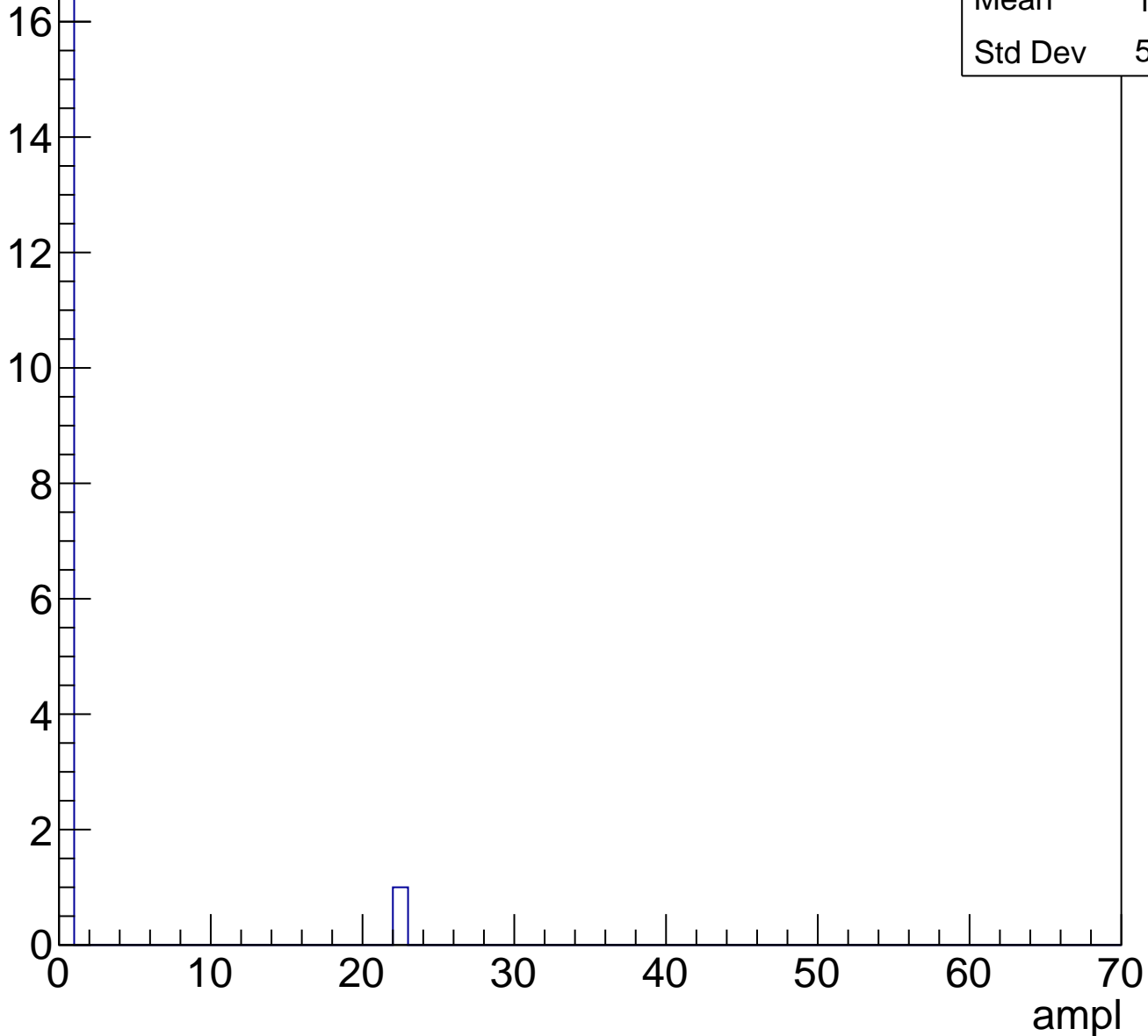


B1L103S, U6-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039

Entry



B1L103S, U6-ch56, adc0

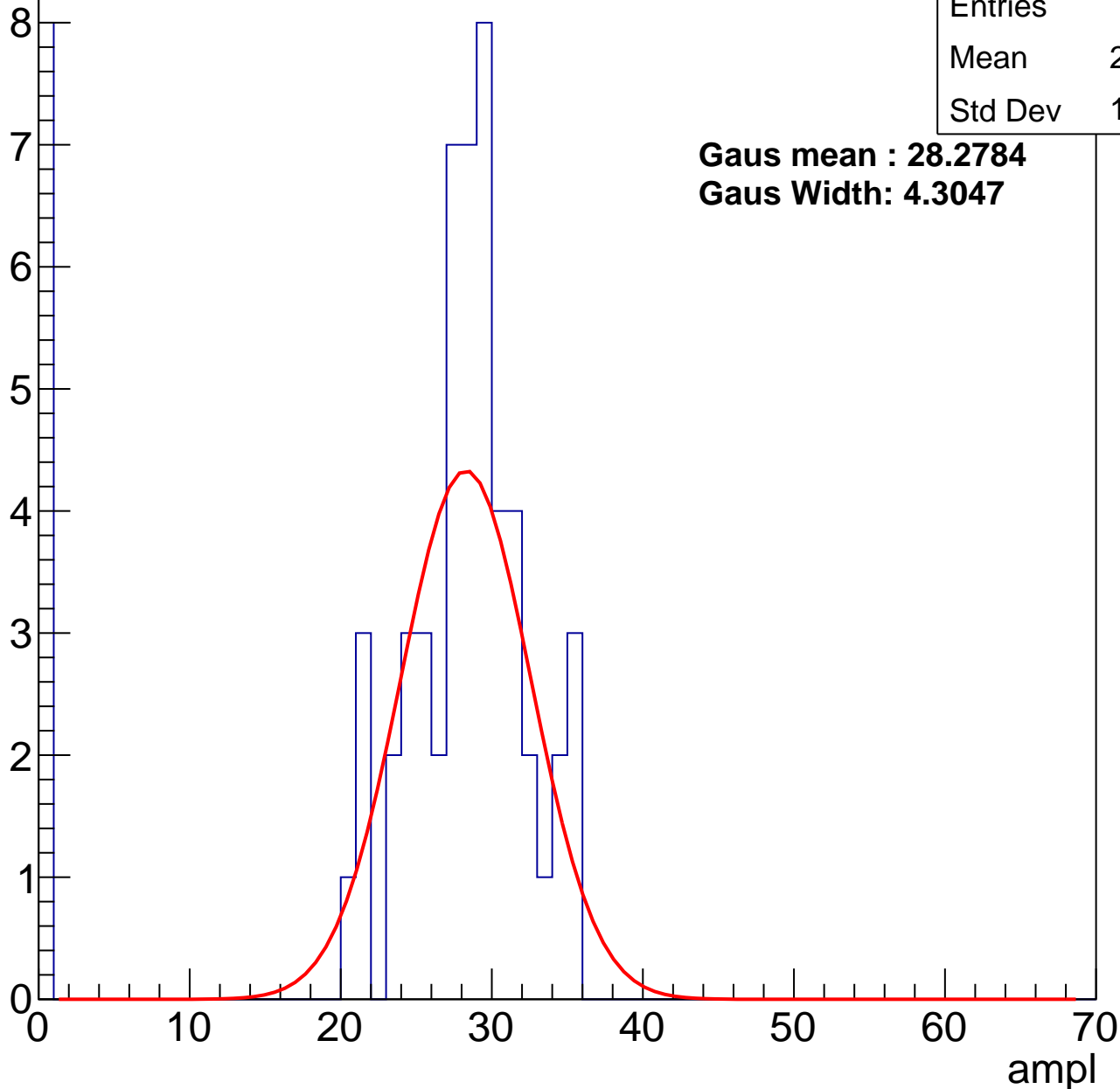
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	24.32
Std Dev	10.12

Gaus mean : 28.2784

Gaus Width: 4.3047



B1L103S, U6-ch56, adc1

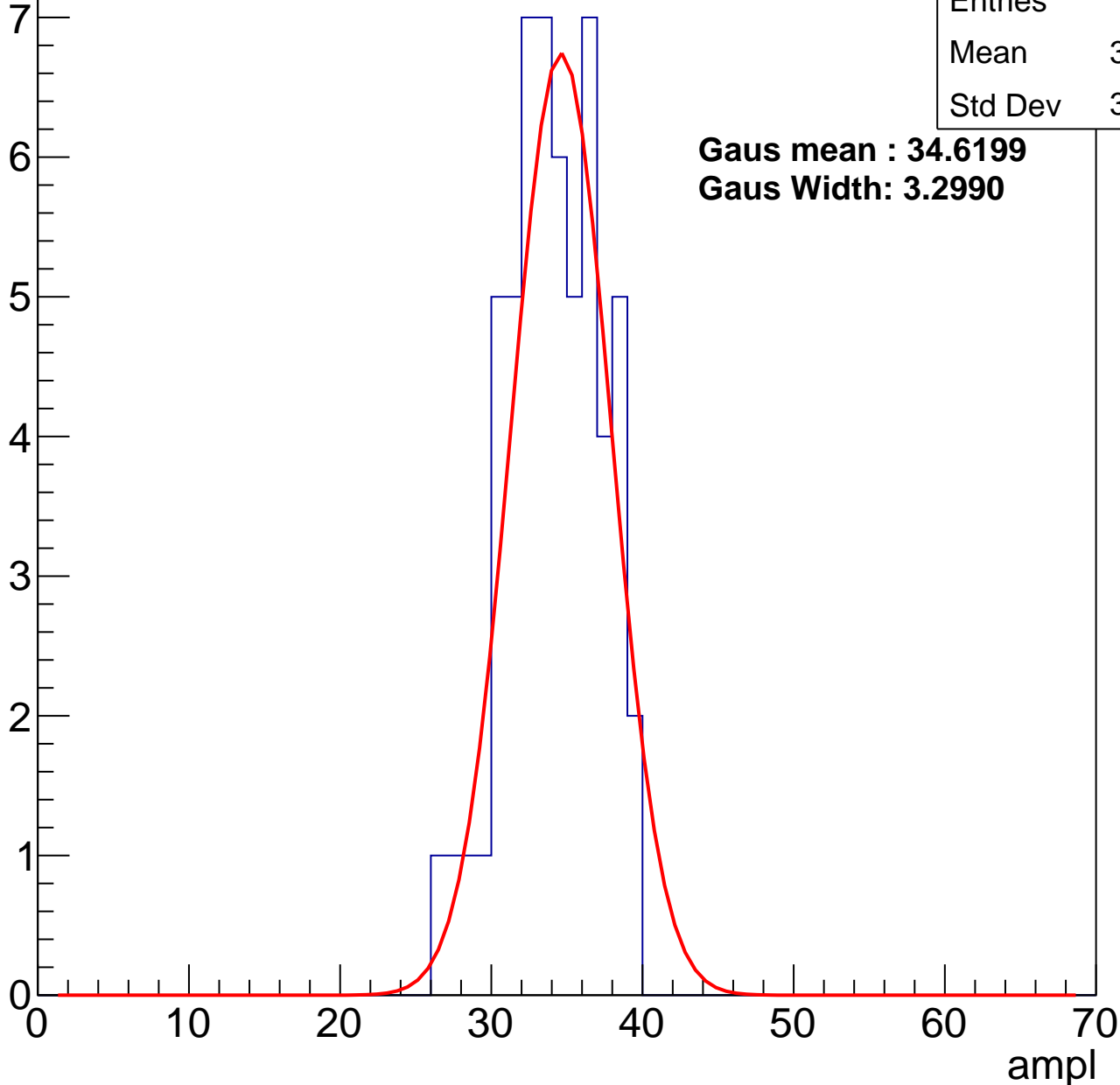
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	33.63
Std Dev	3.036

Gaus mean : 34.6199

Gaus Width: 3.2990



B1L103S, U6-ch56, adc2

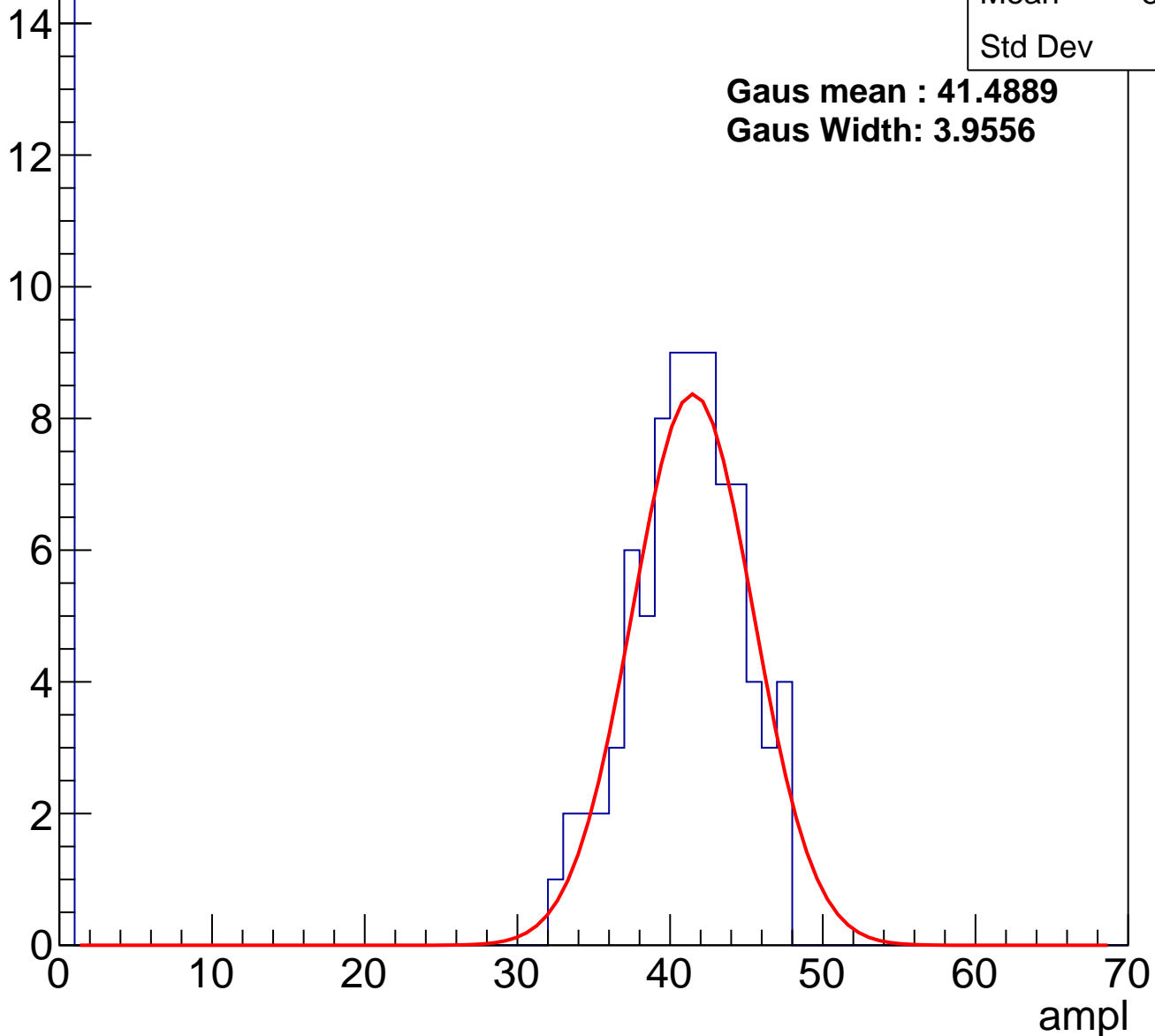
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	34.27
Std Dev	15.1

Gaus mean : 41.4889

Gaus Width: 3.9556

Entry

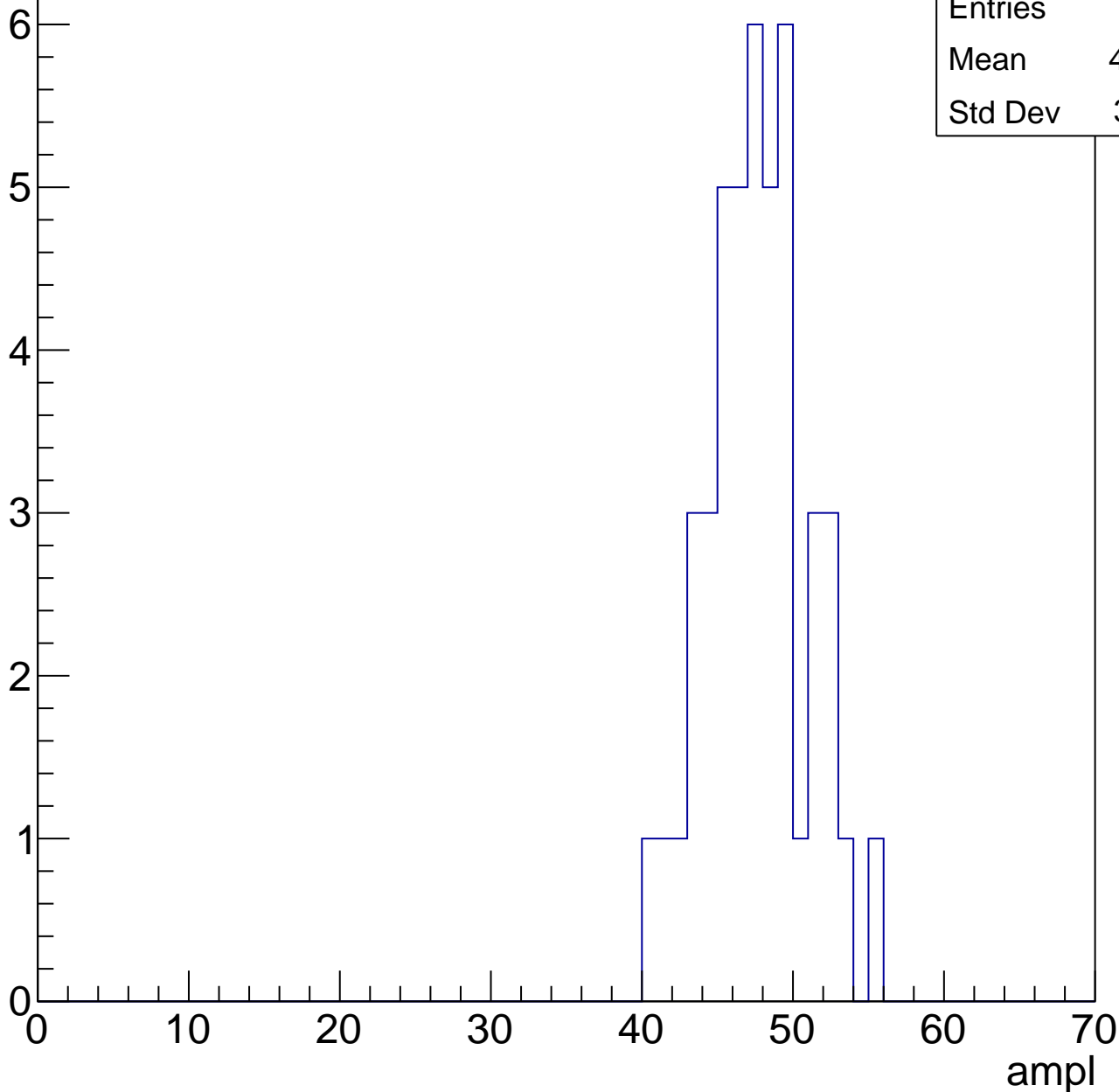


B1L103S, U6-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	47.16
Std Dev	3.231

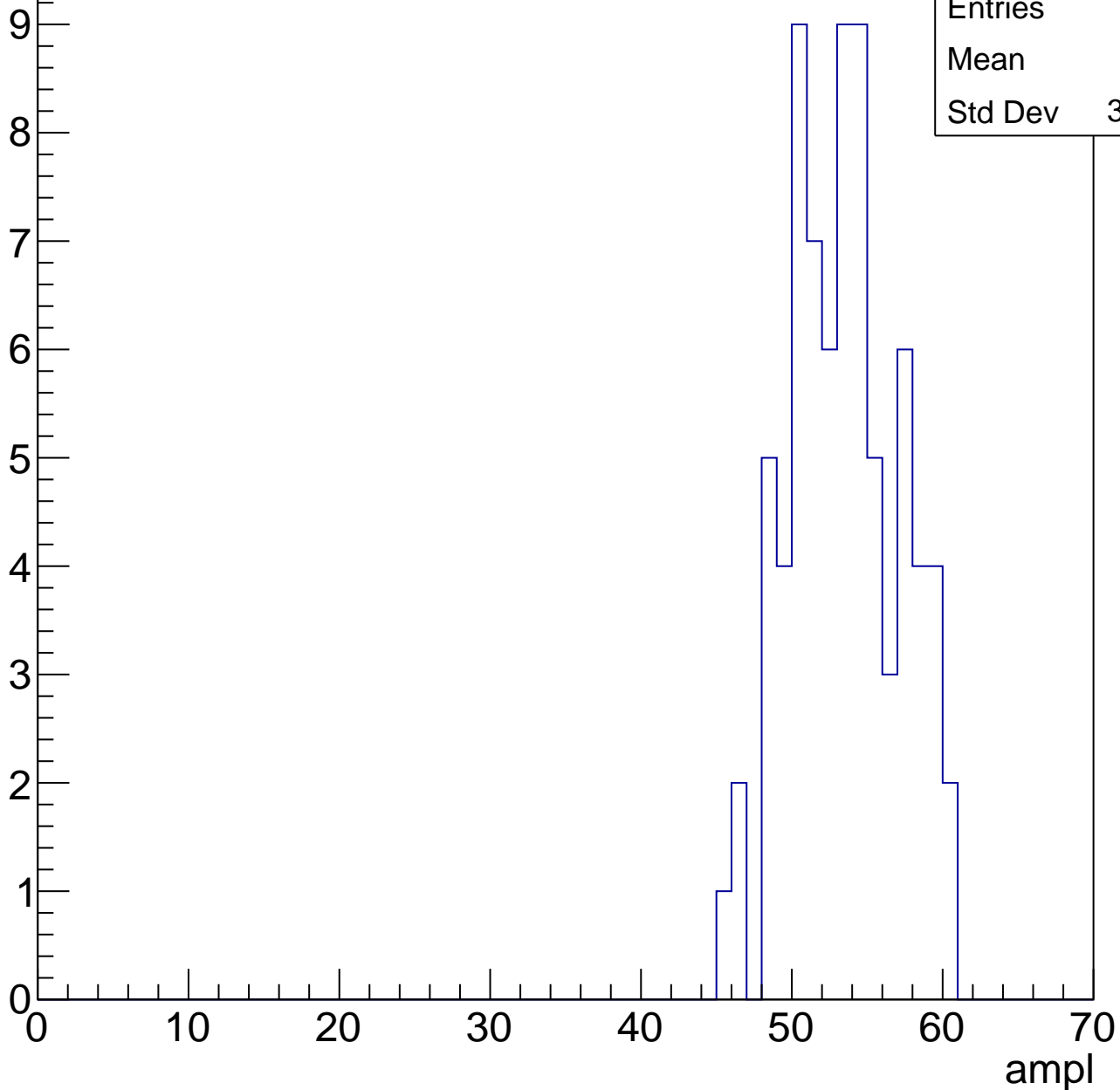


B1L103S, U6-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

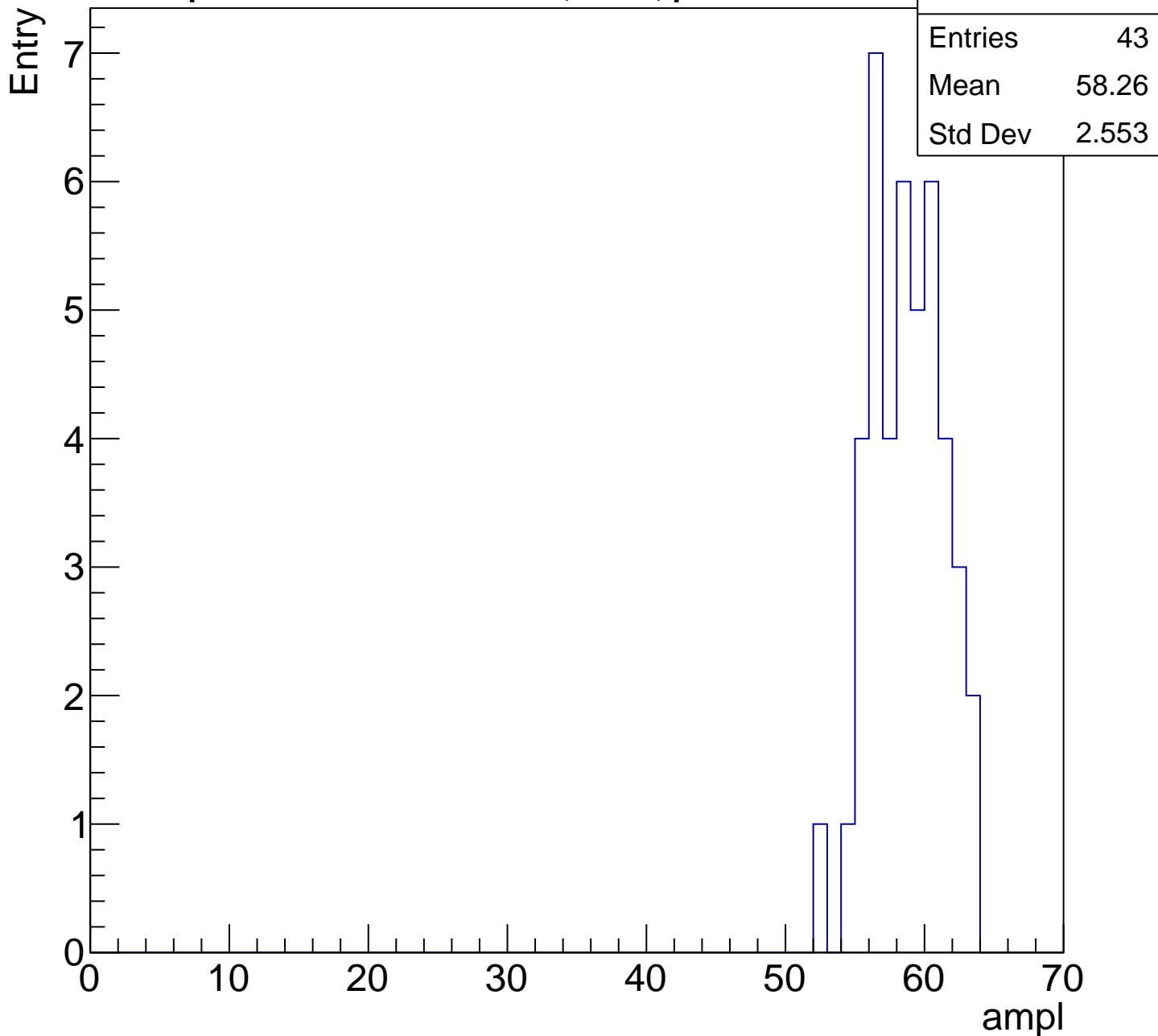
Entry

Entries	76
Mean	53
Std Dev	3.554



B1L103S, U6-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

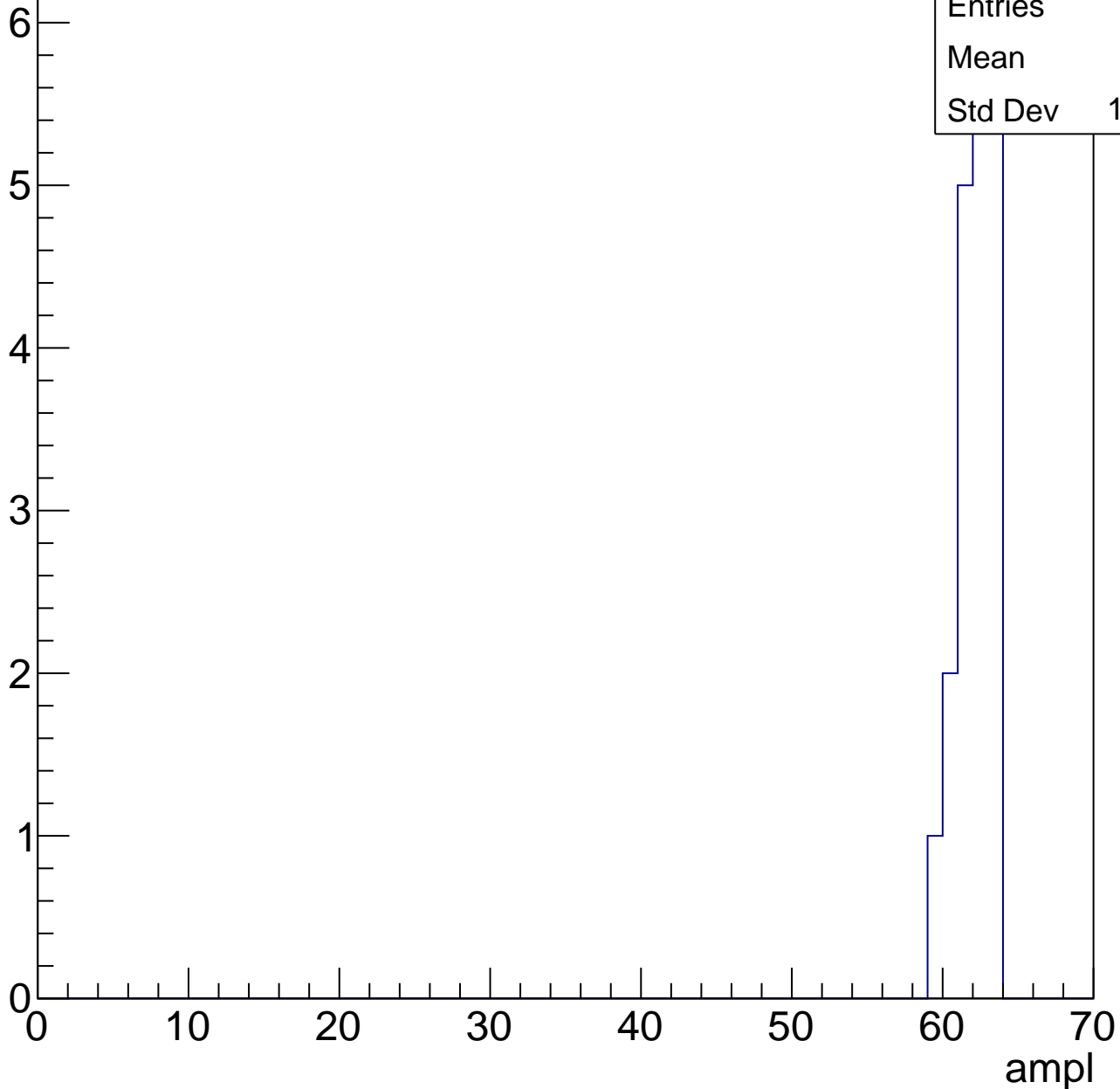


B1L103S, U6-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

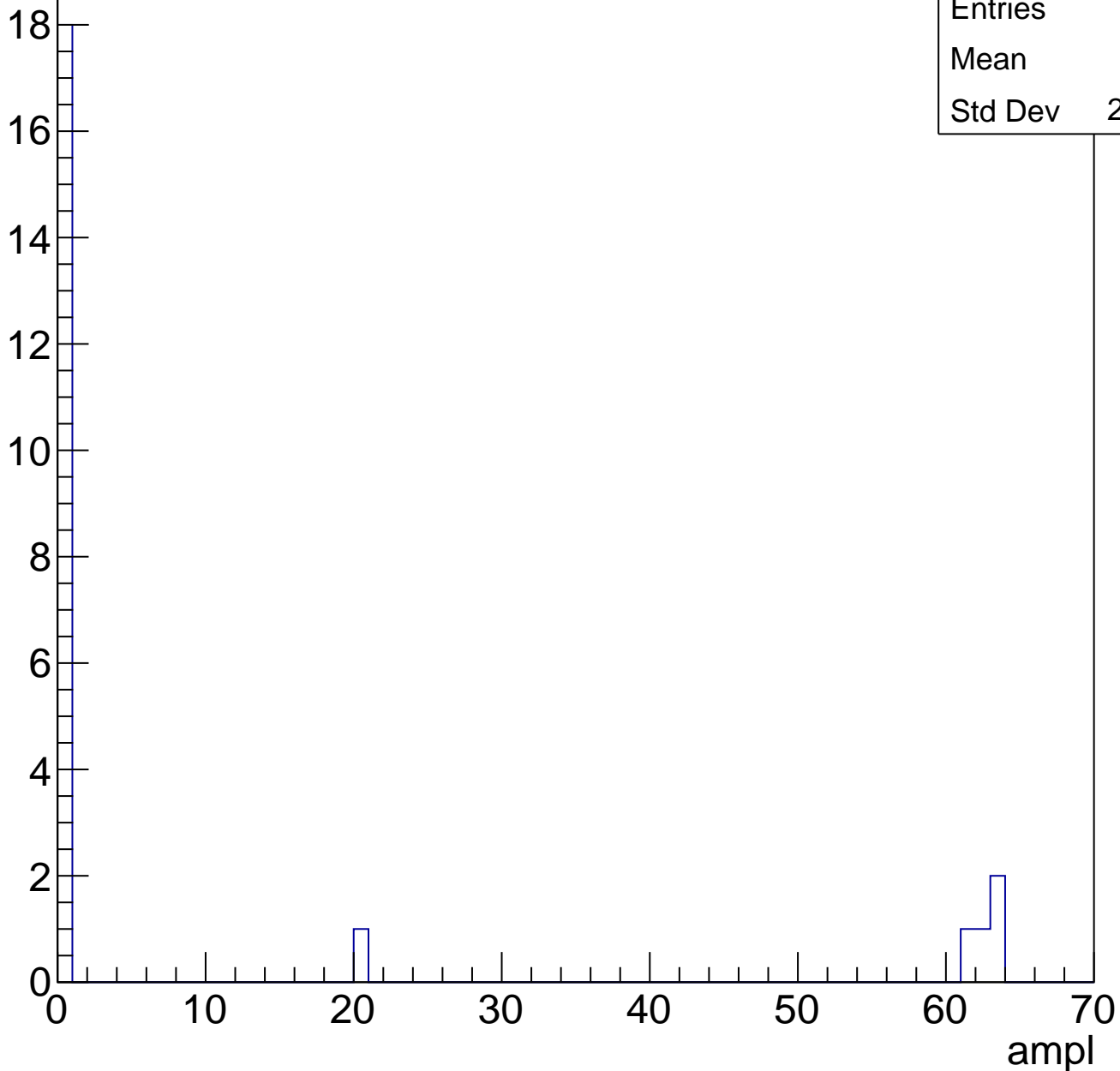
Entries	20
Mean	61.7
Std Dev	1.145



B1L103S, U6-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	23
Mean	11.7
Std Dev	23.55

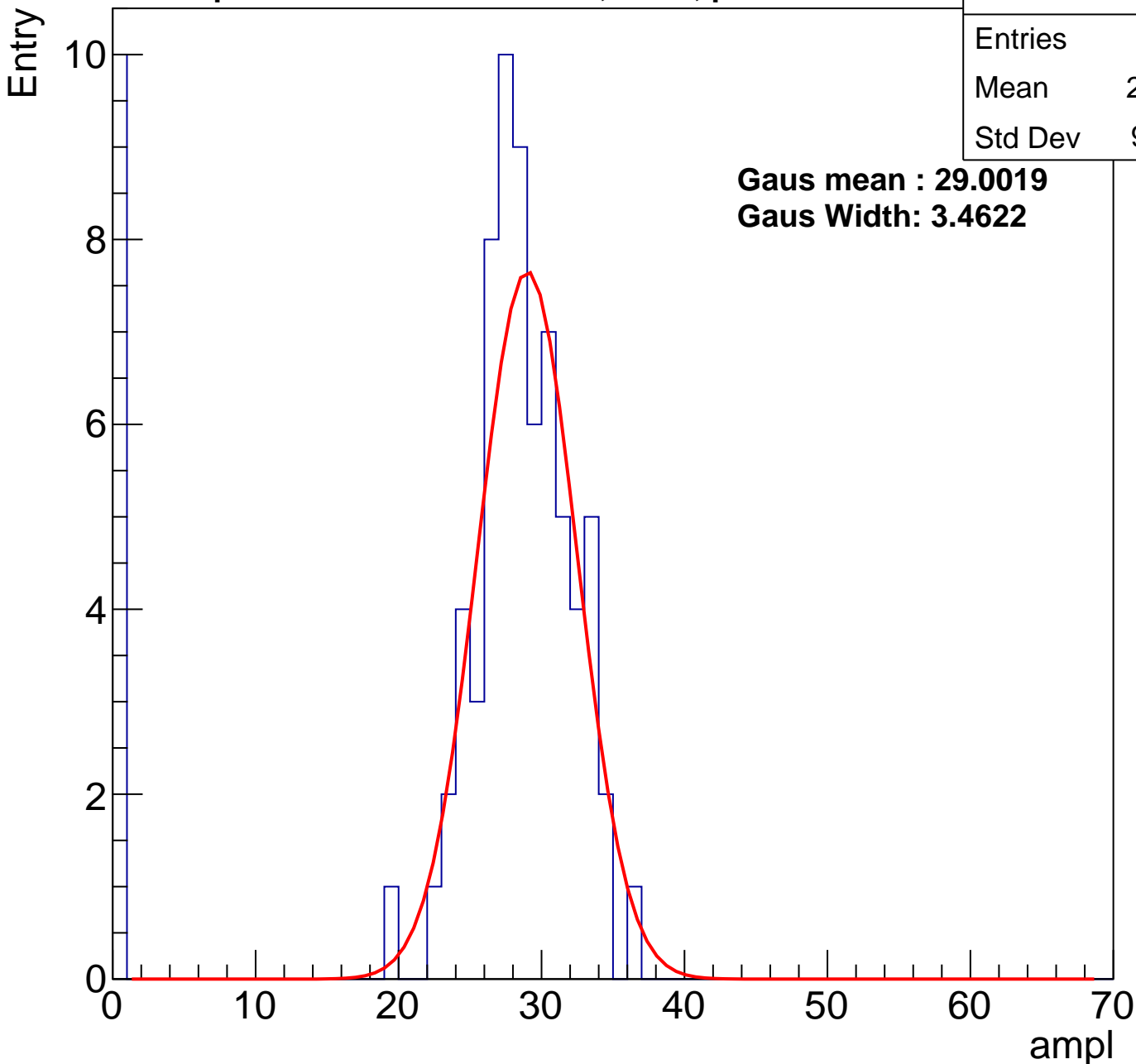
B1L103S, U6-ch57, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	24.67
Std Dev	9.921

Gaus mean : 29.0019

Gaus Width: 3.4622



B1L103S, U6-ch57, adc1

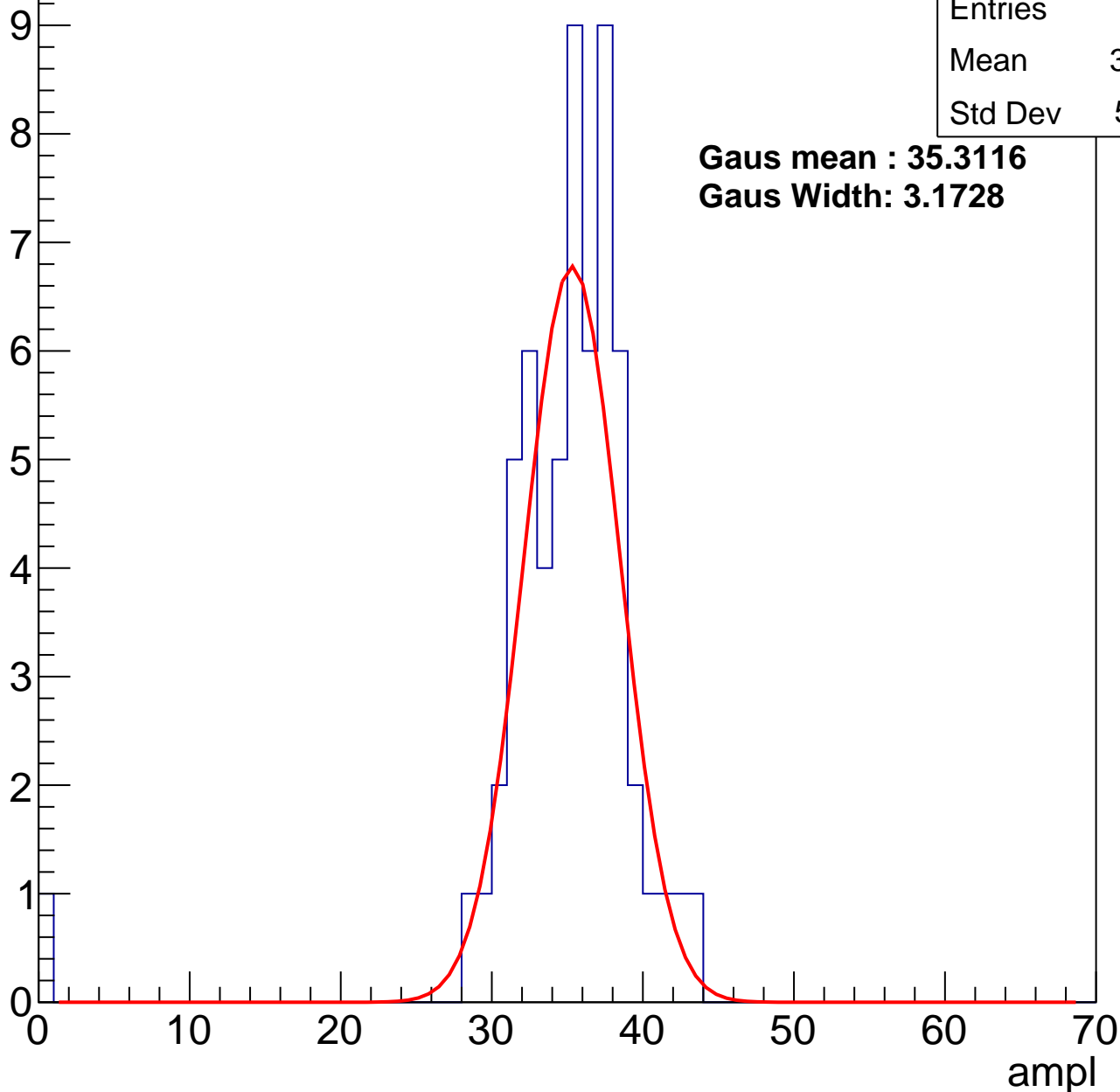
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.46
Std Dev	5.431

Gaus mean : 35.3116

Gaus Width: 3.1728



B1L103S, U6-ch57, adc2

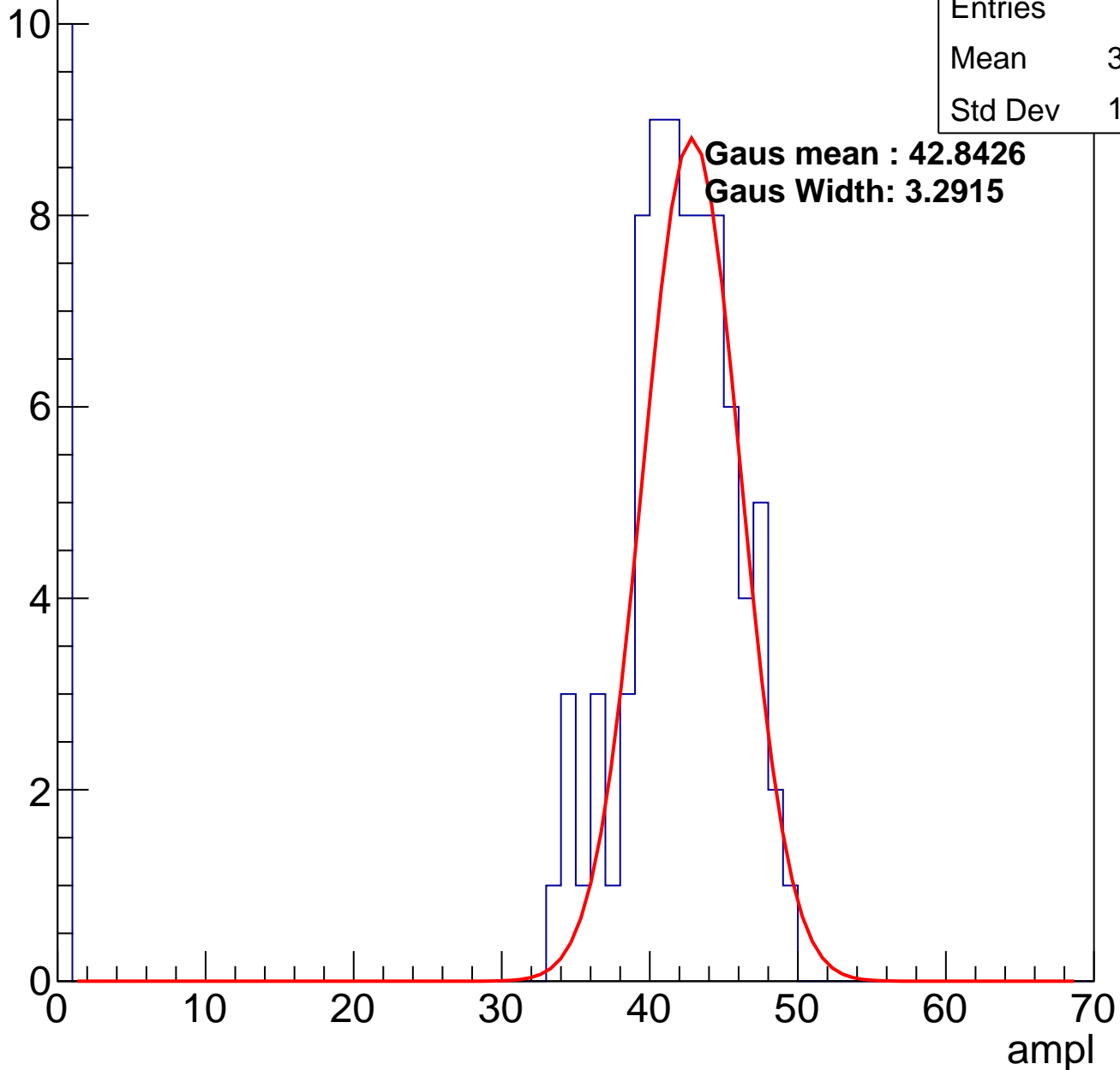
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	37.07
Std Dev	13.53

Gaus mean : 42.8426

Gaus Width: 3.2915

Entry

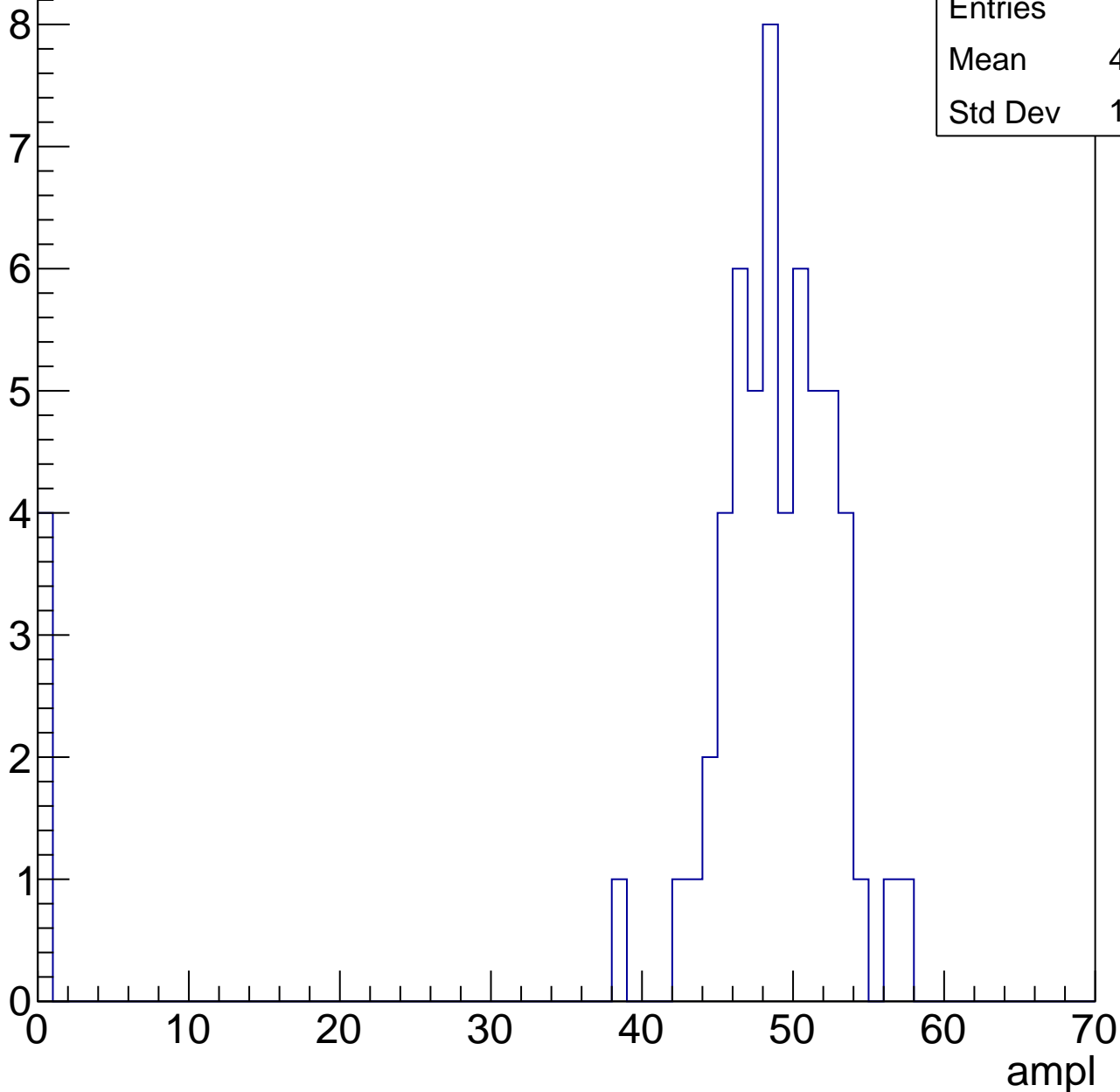


B1L103S, U6-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	45.36
Std Dev	12.69

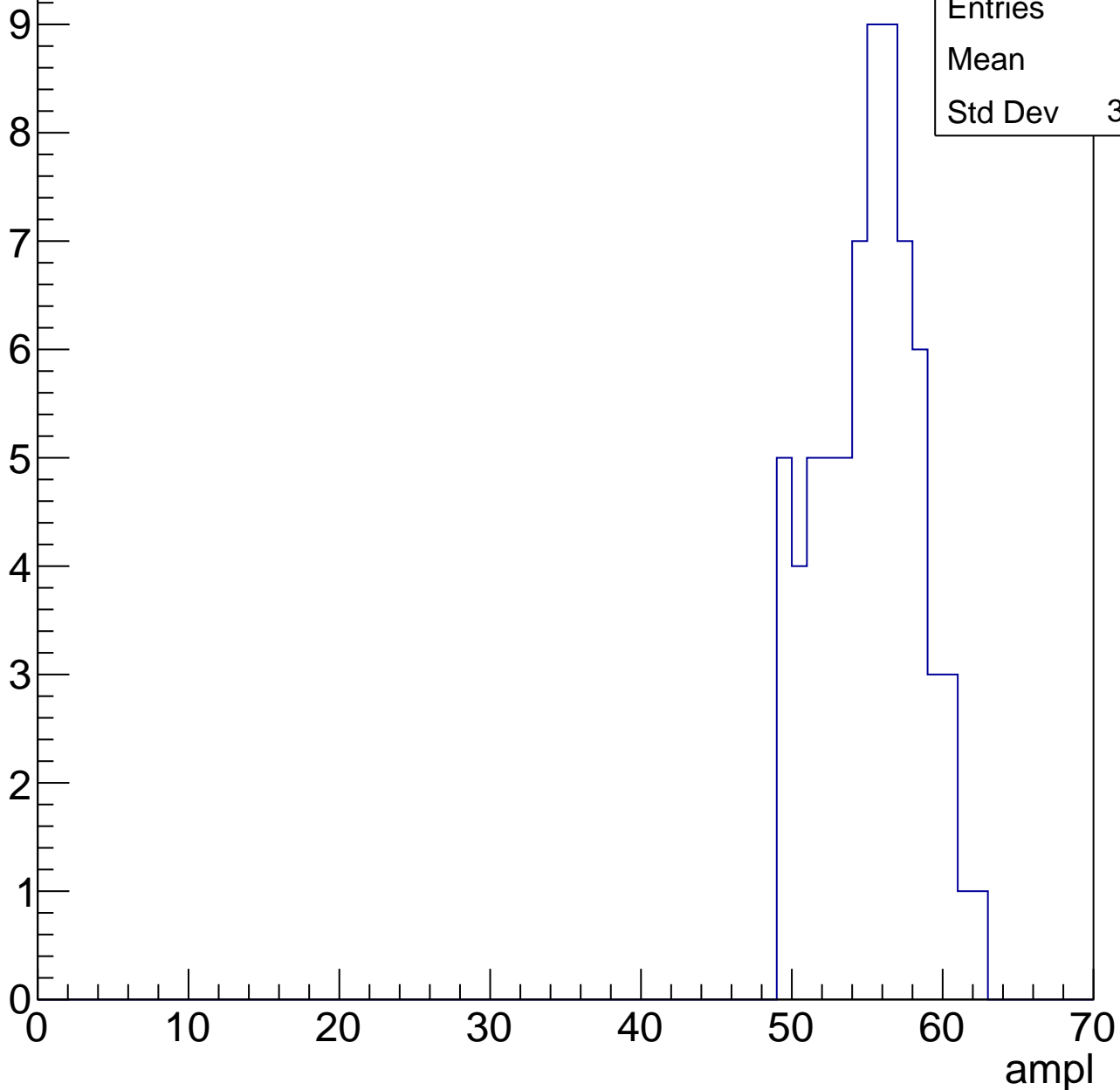


B1L103S, U6-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

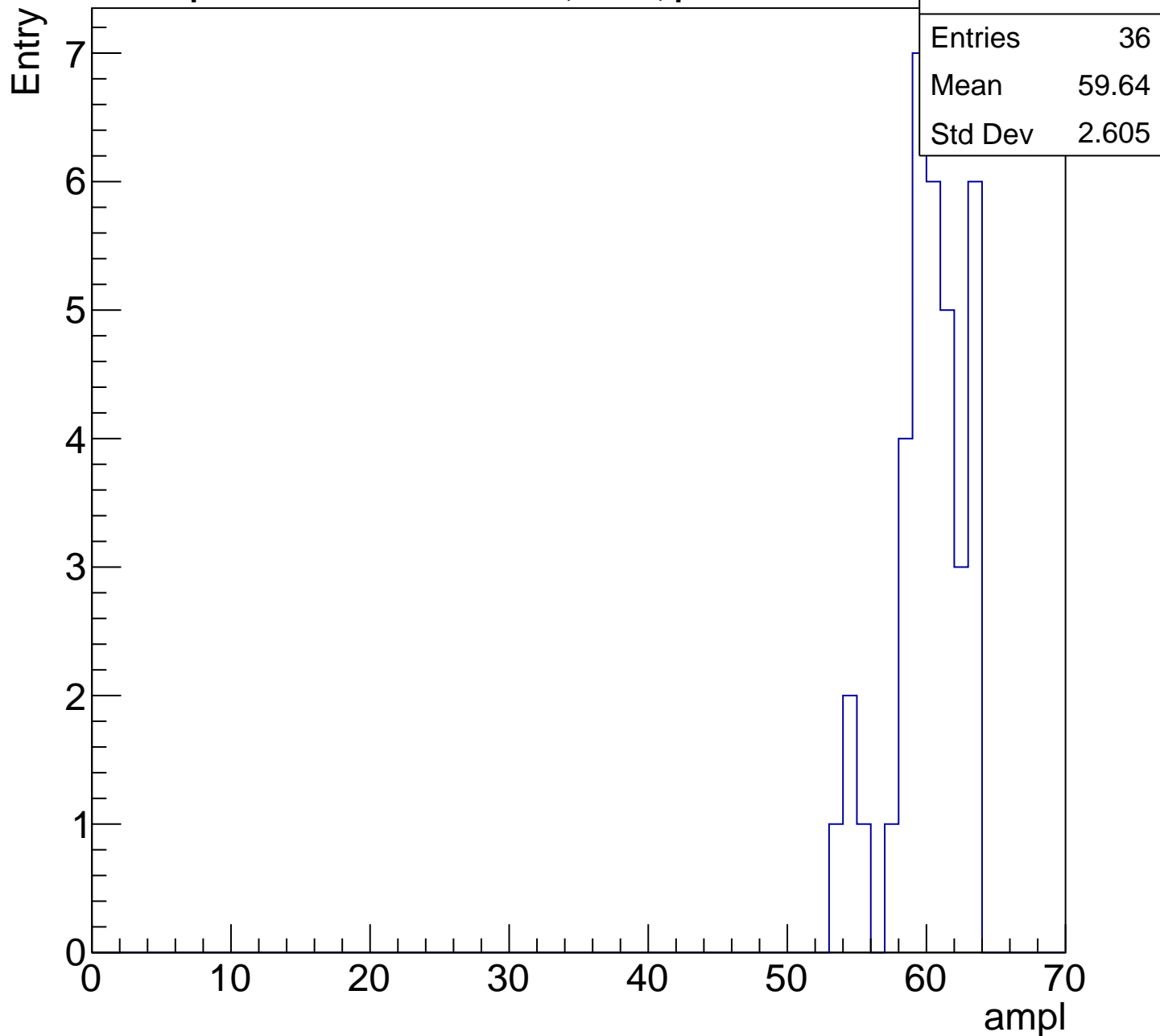
Entry

Entries	70
Mean	54.7
Std Dev	3.218



B1L103S, U6-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

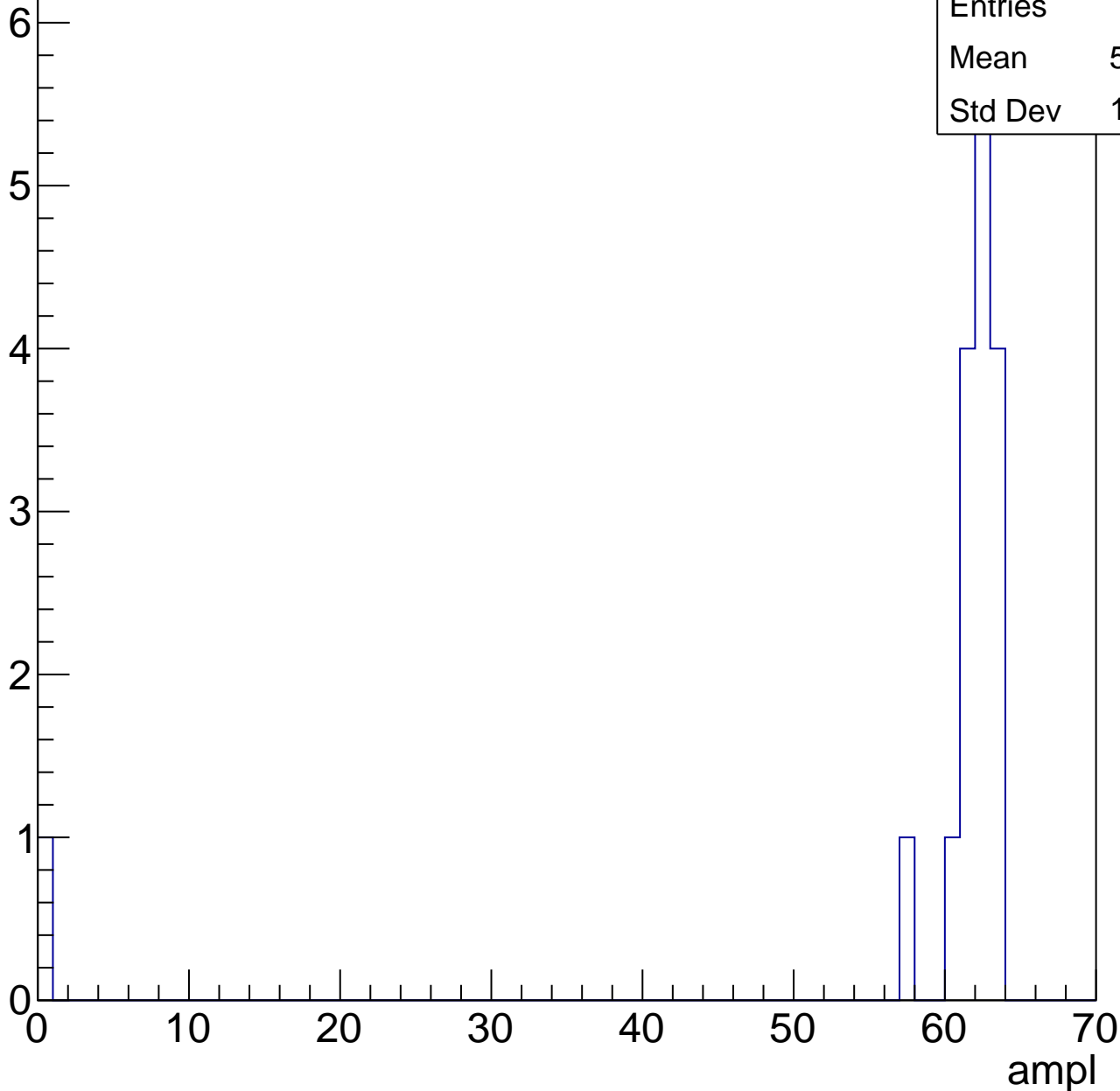


B1L103S, U6-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.94
Std Dev	14.55



B1L103S, U6-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch58, adc0

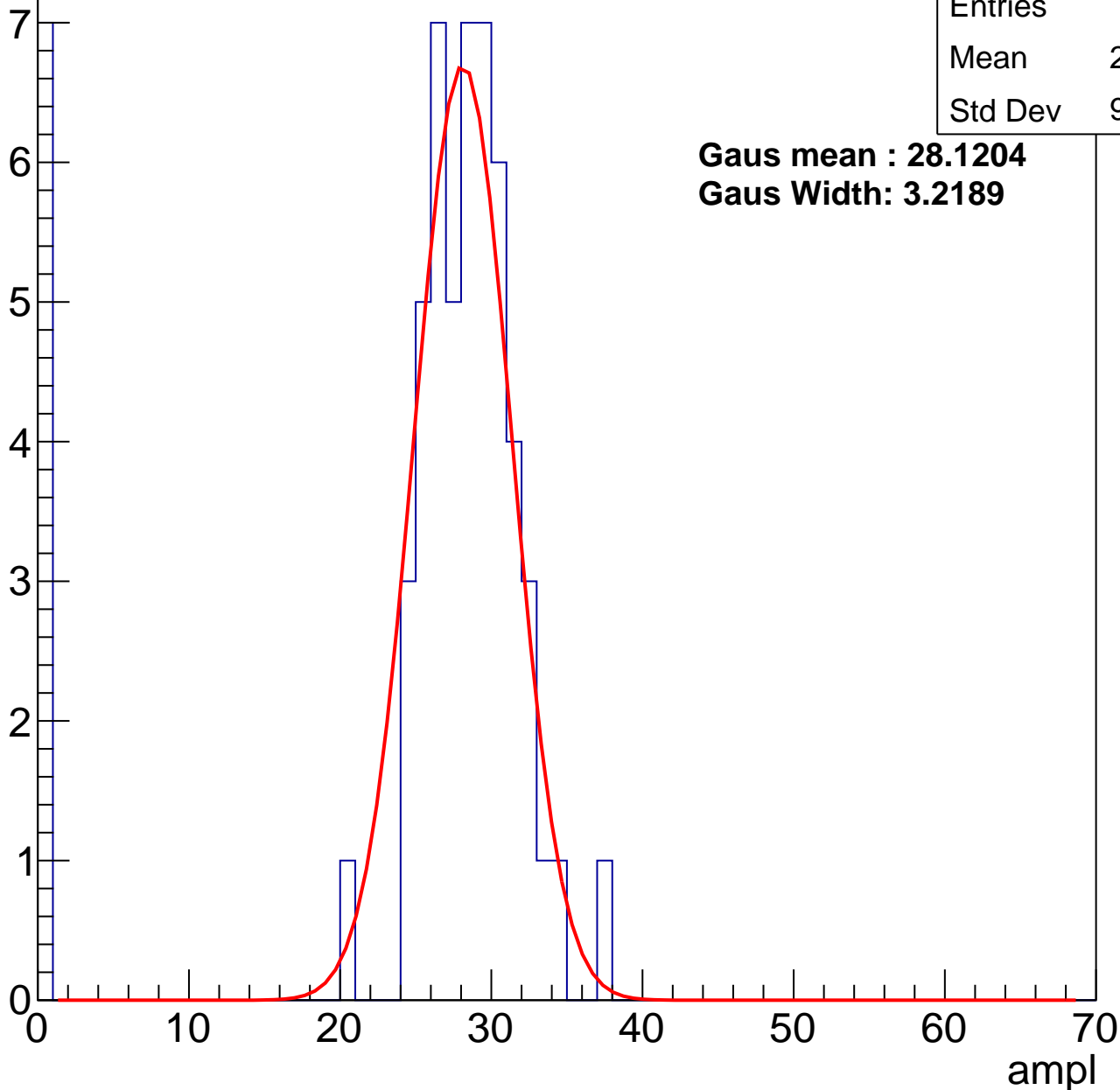
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	24.78
Std Dev	9.588

Gaus mean : 28.1204

Gaus Width: 3.2189



B1L103S, U6-ch58, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	33.28
Std Dev	6.48

Gaus mean : 34.6351

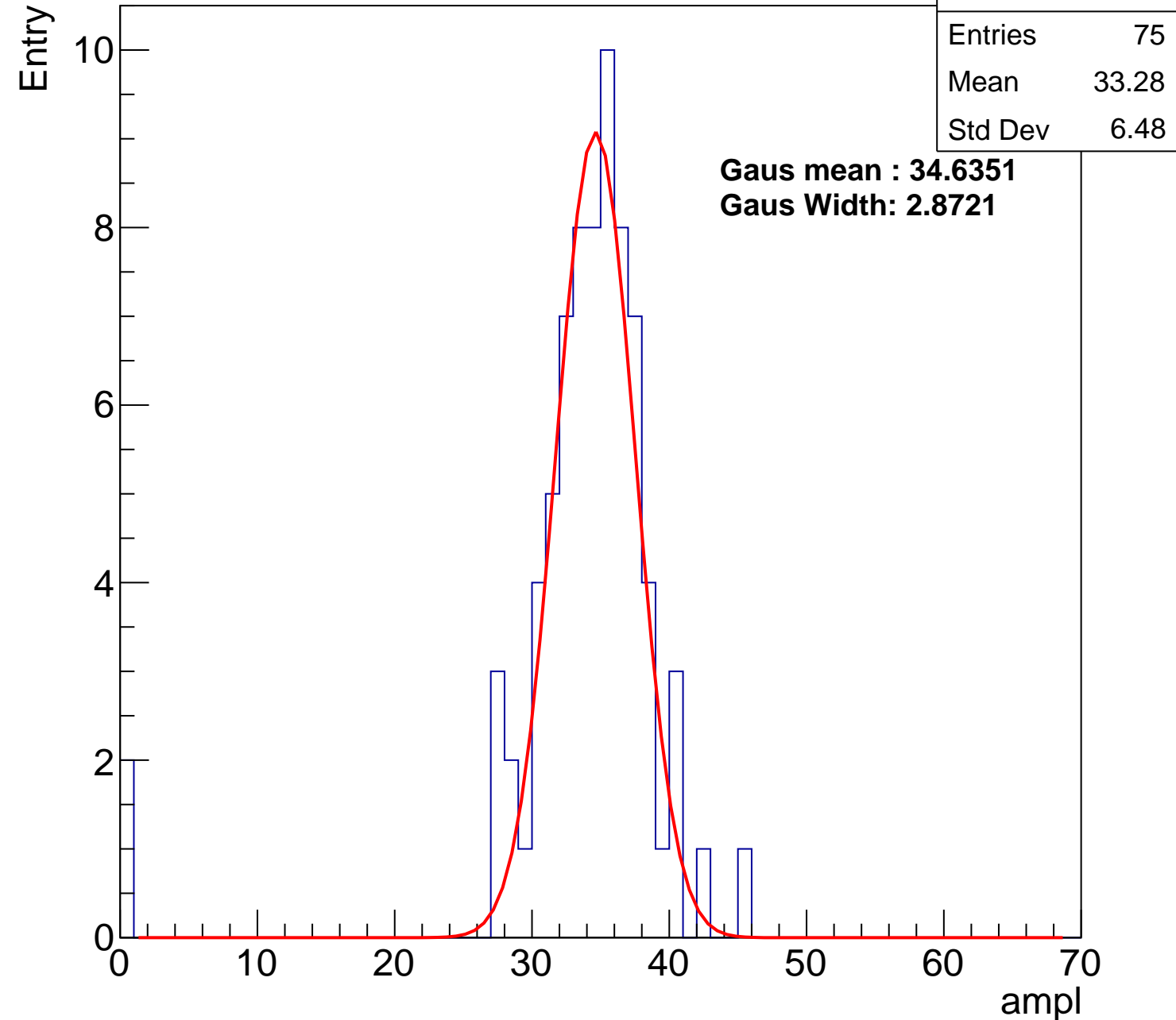
Gaus Width: 2.8721

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch58, adc2

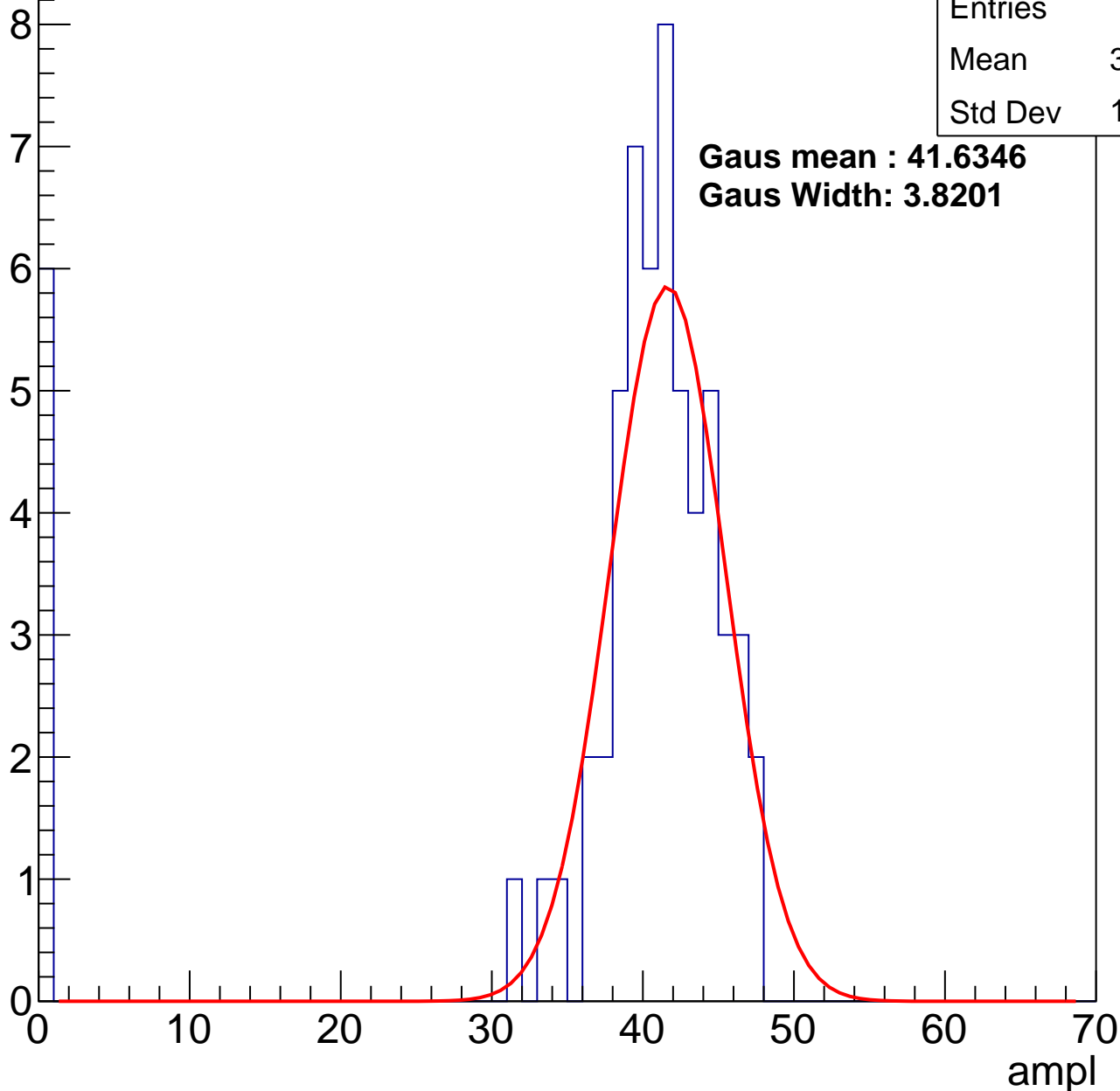
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	36.79
Std Dev	12.57

Gaus mean : 41.6346

Gaus Width: 3.8201

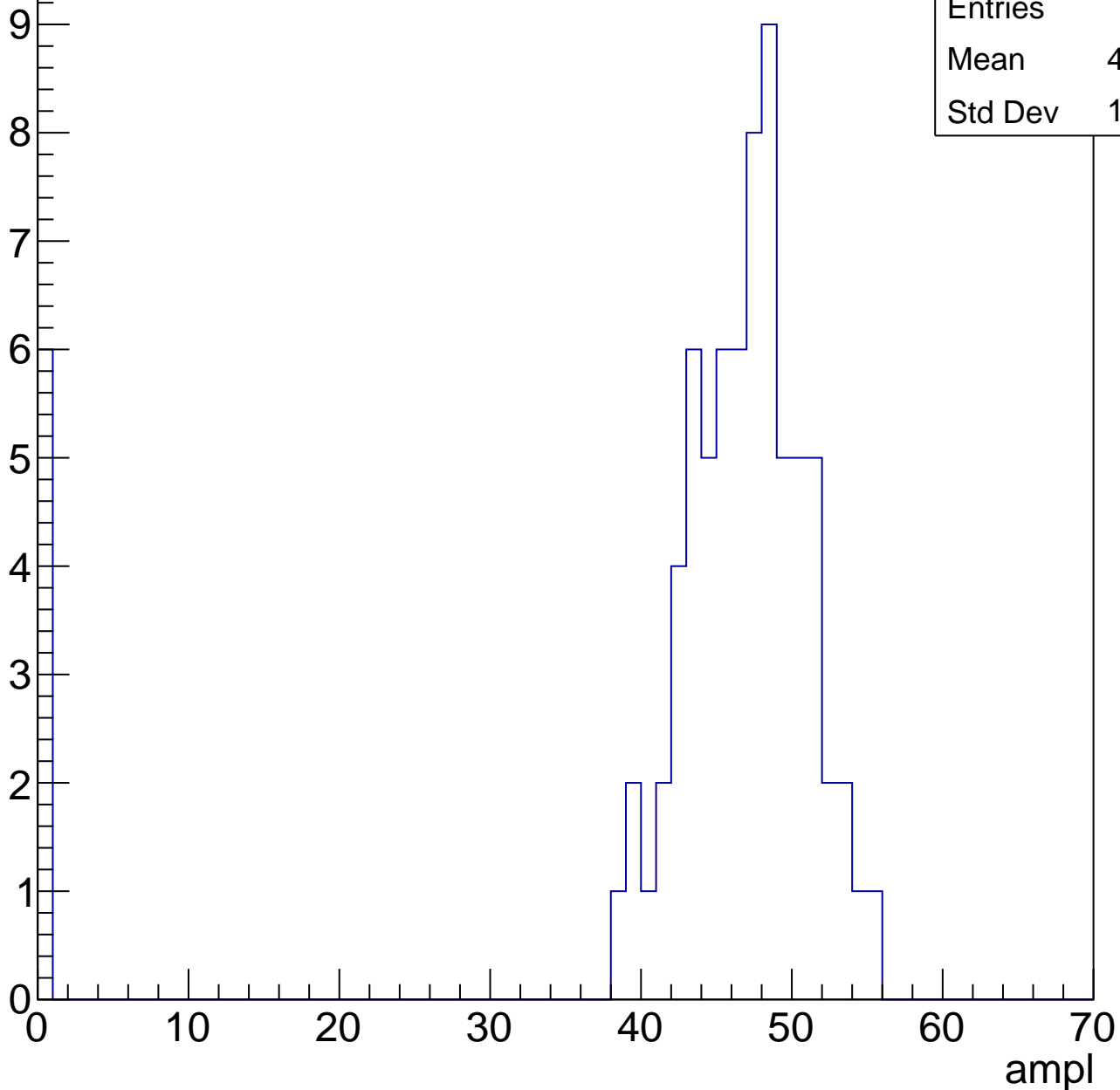


B1L103S, U6-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

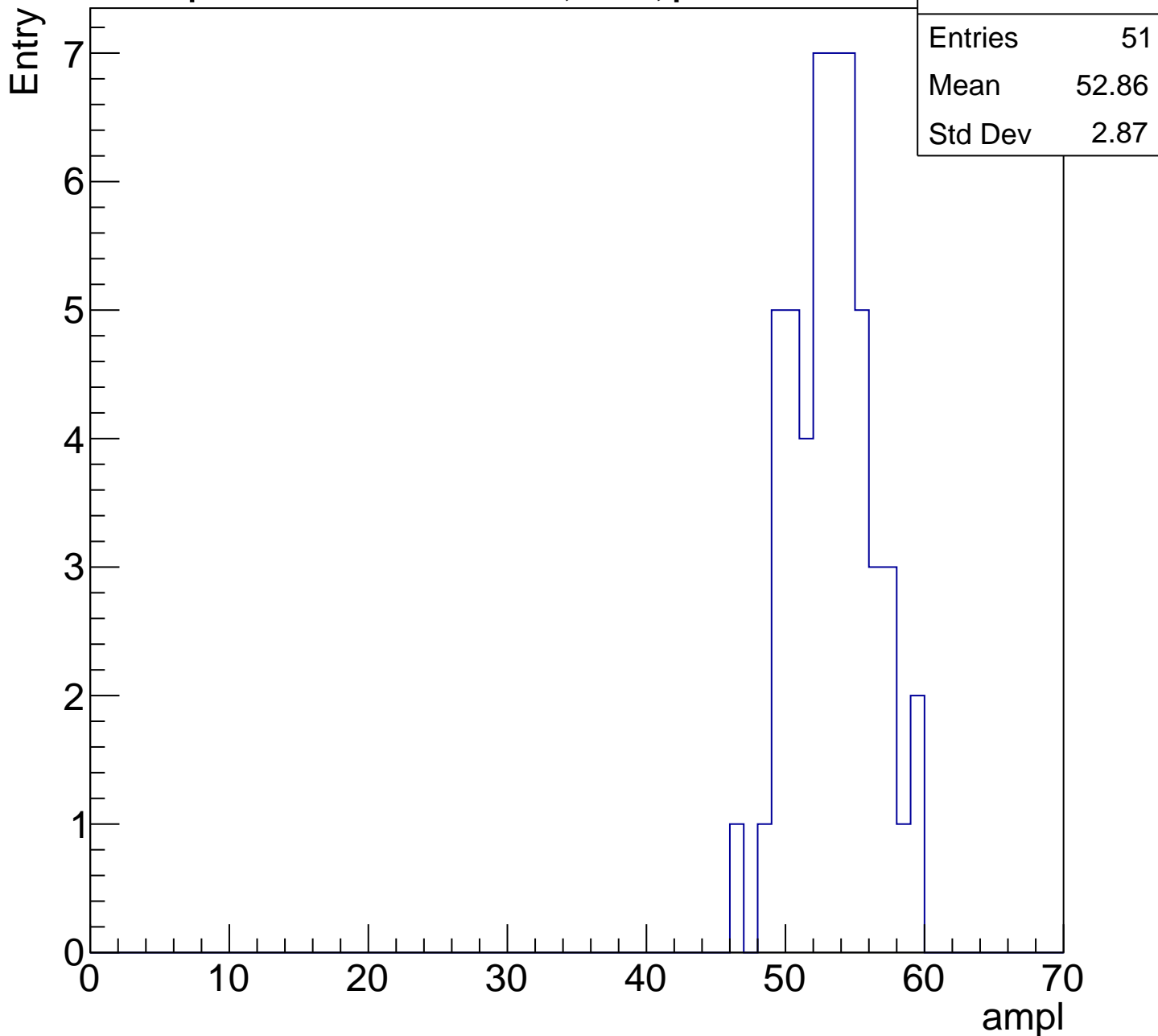
Entry

Entries	77
Mean	42.95
Std Dev	12.98



B1L103S, U6-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

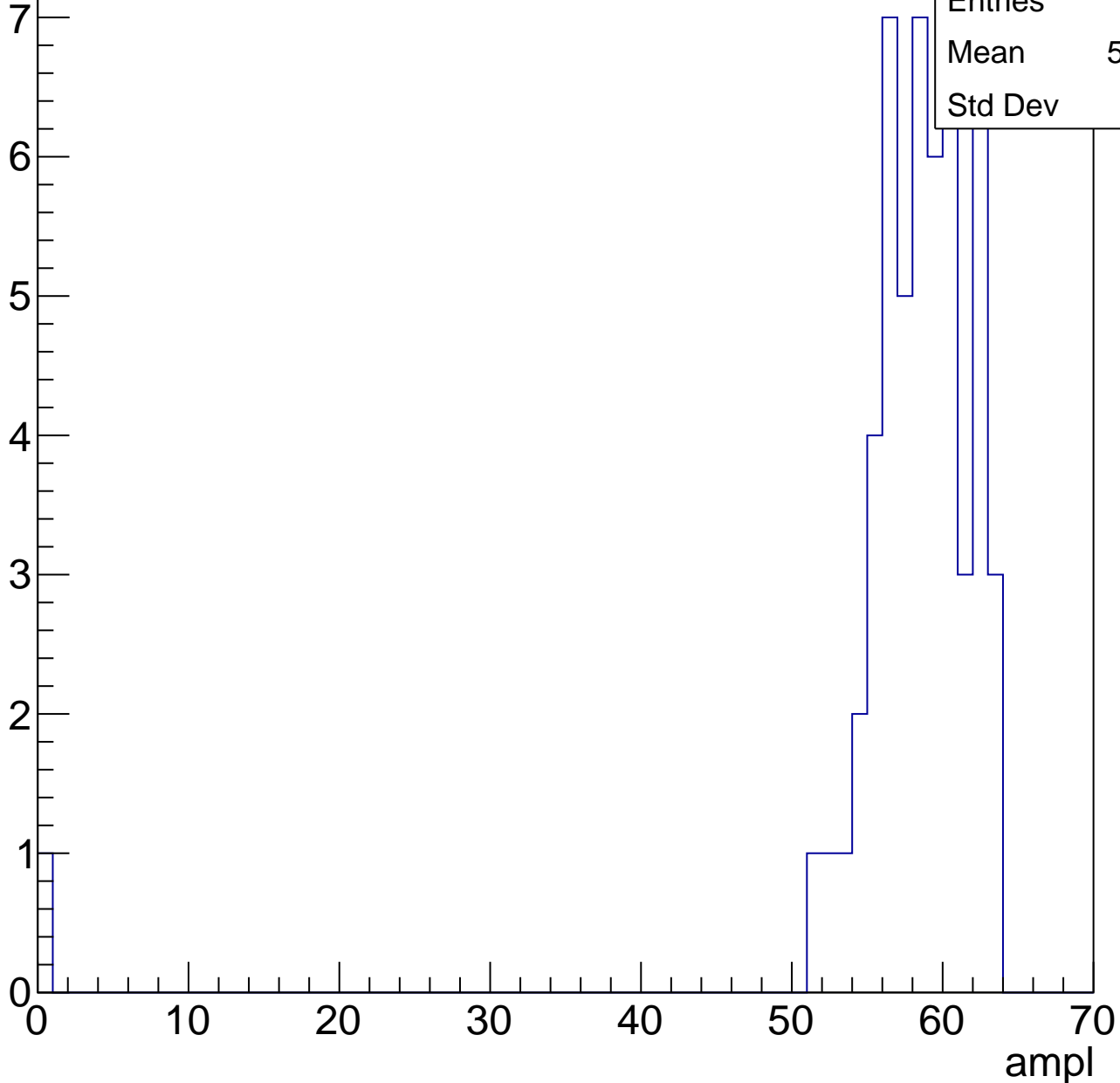


B1L103S, U6-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.22
Std Dev	8.3

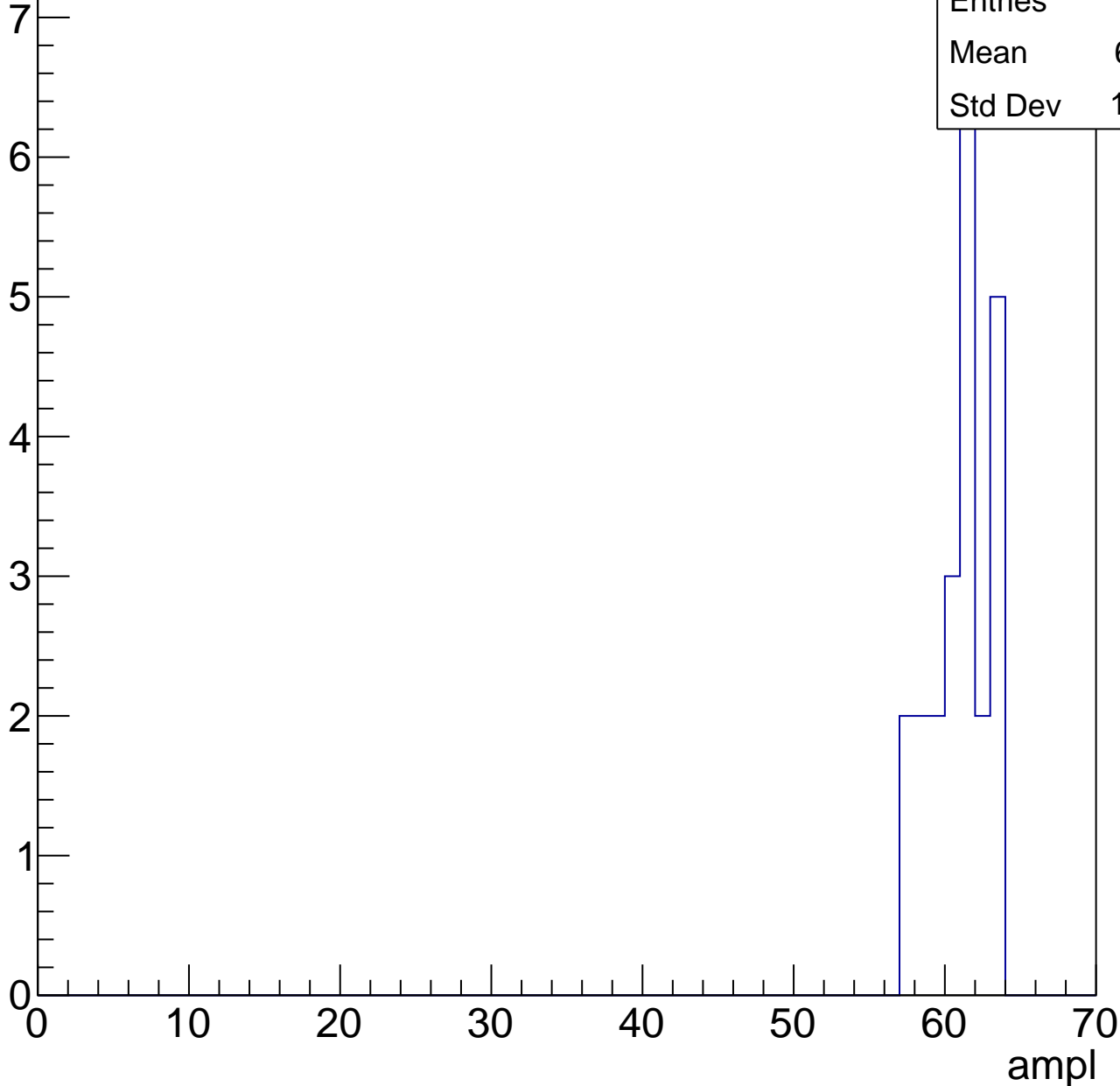


B1L103S, U6-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	60.61
Std Dev	1.859



B1L103S, U6-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

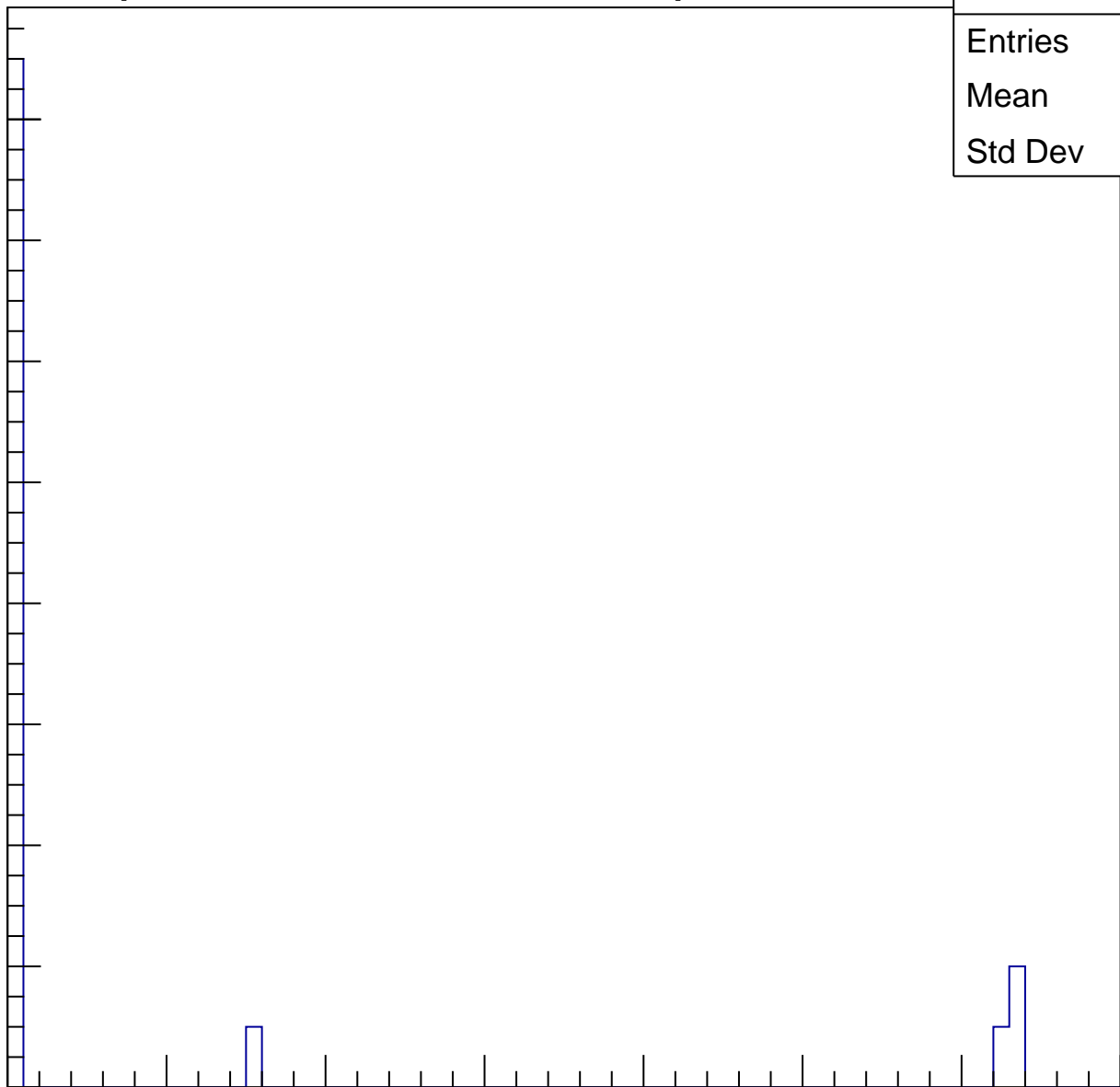
Entries	21
Mean	9.667
Std Dev	21.87

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	24.18
Std Dev	10.87

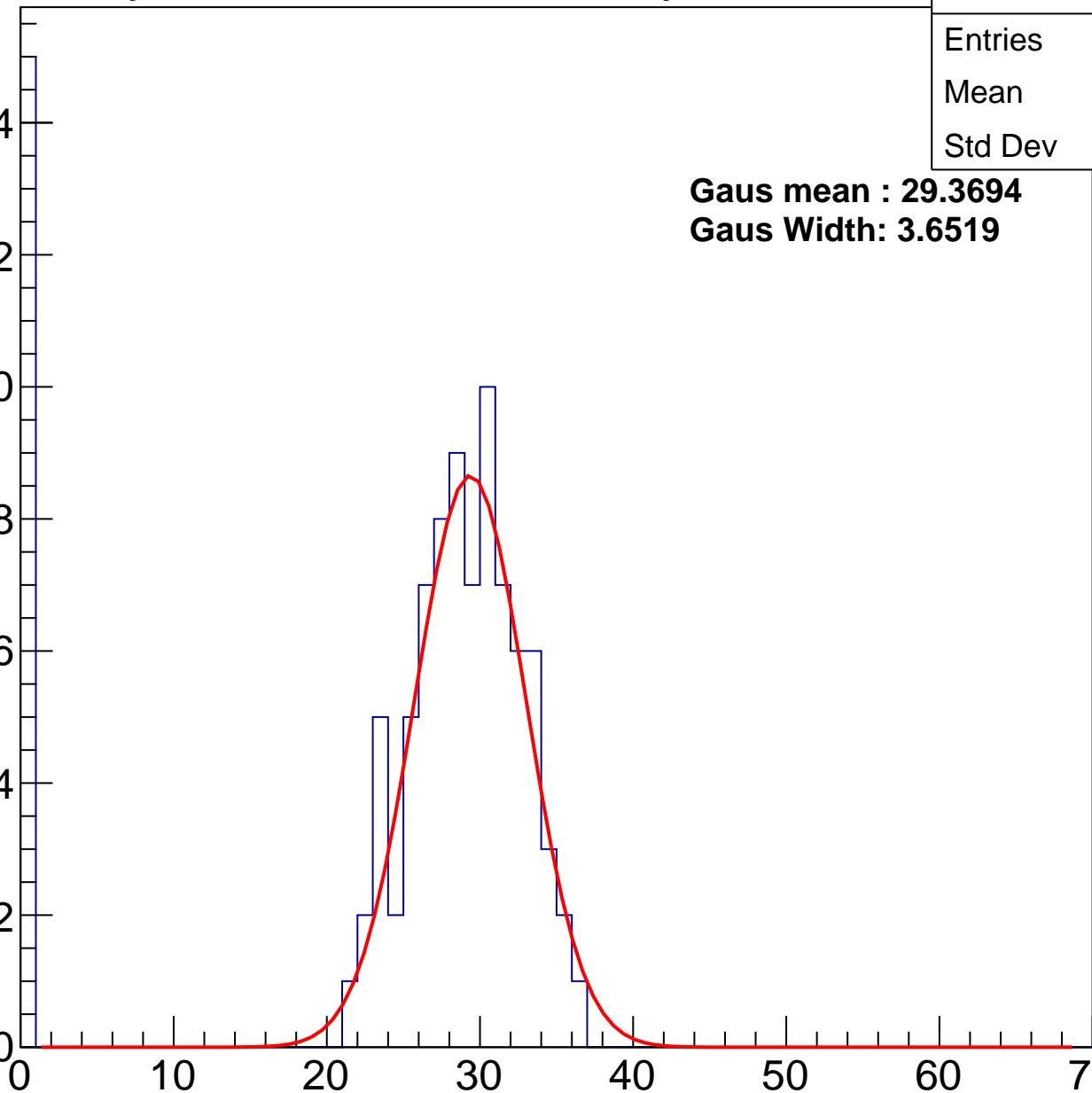
Gaus mean : 29.3694

Gaus Width: 3.6519

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch59, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	56
Mean	35.73
Std Dev	2.979

Gaus mean : 36.0764

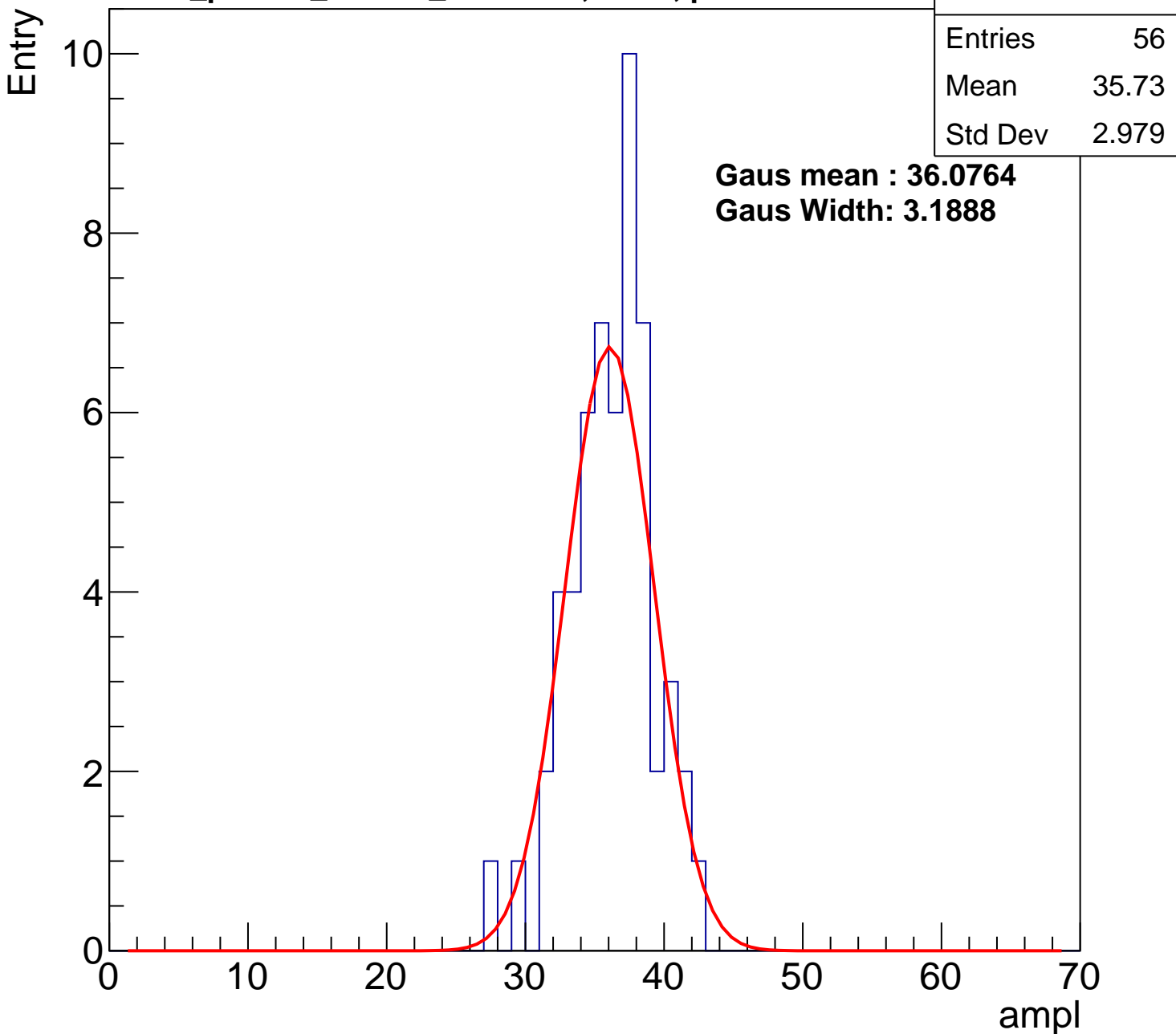
Gaus Width: 3.1888

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch59, adc2

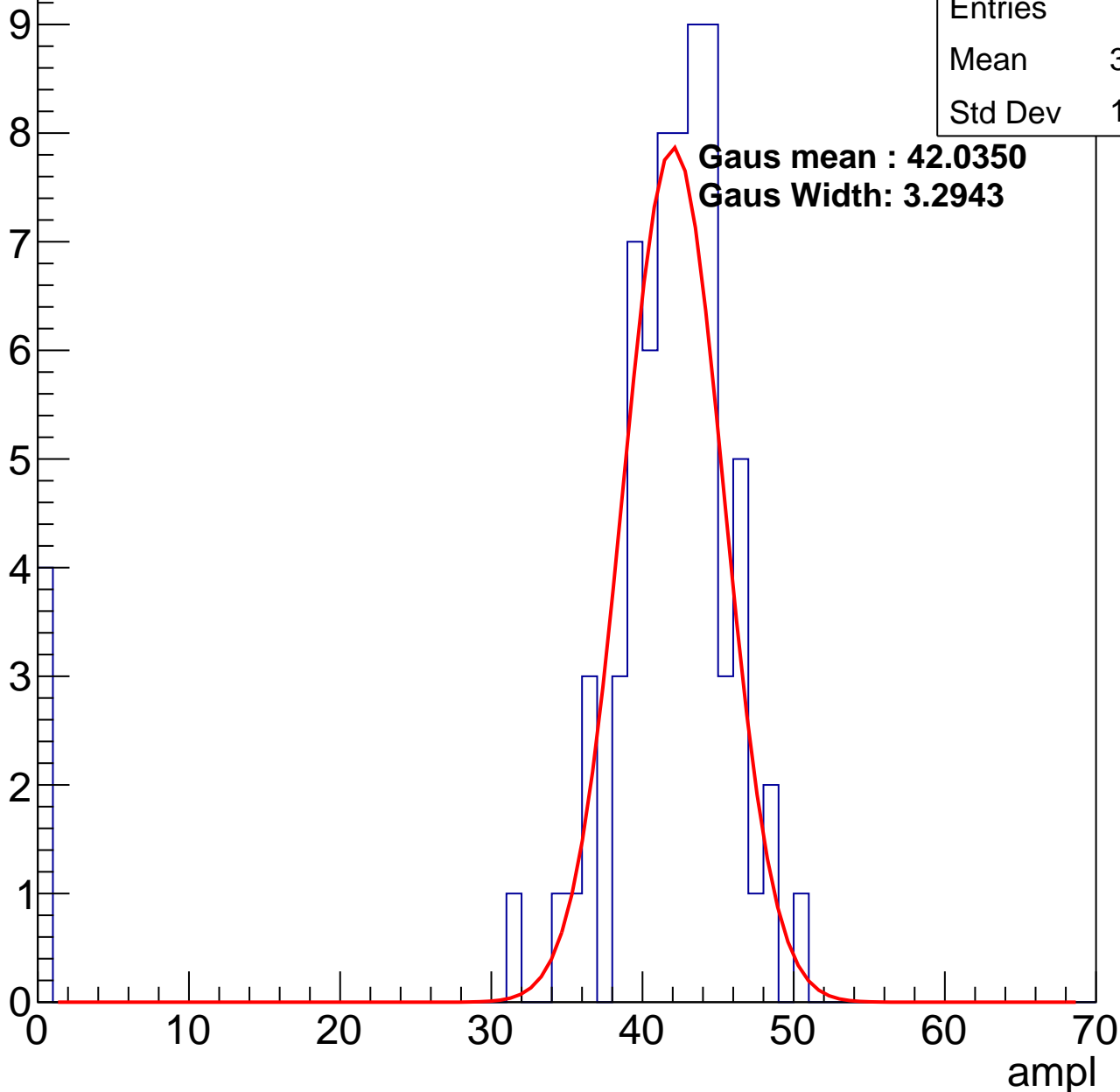
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	39.44
Std Dev	10.13

Gaus mean : 42.0350

Gaus Width: 3.2943



B1L103S, U6-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	41.24
Std Dev	17.51

Entry

10

8

6

4

2

0

0

10

20

30

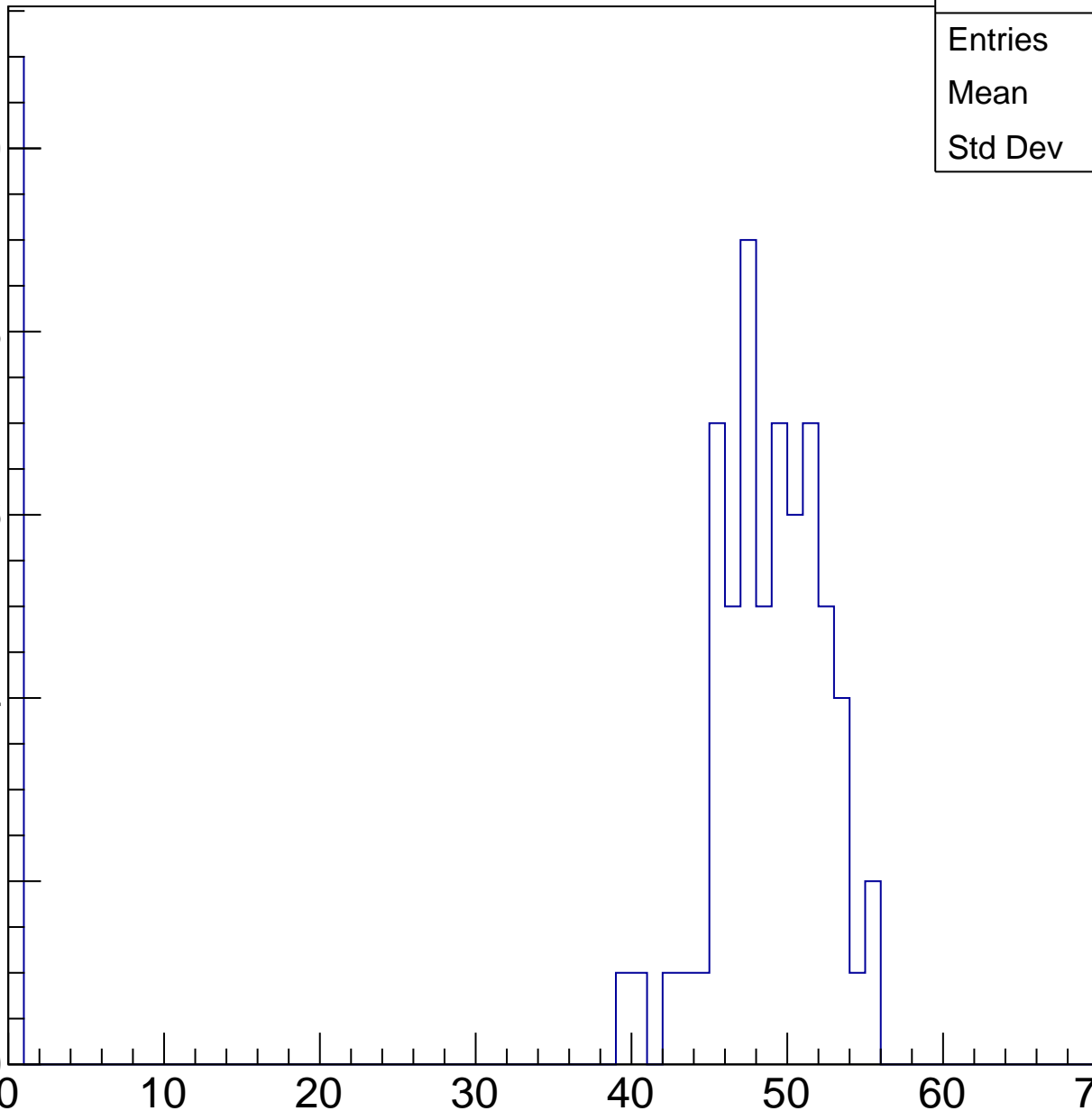
40

50

60

70

ampl

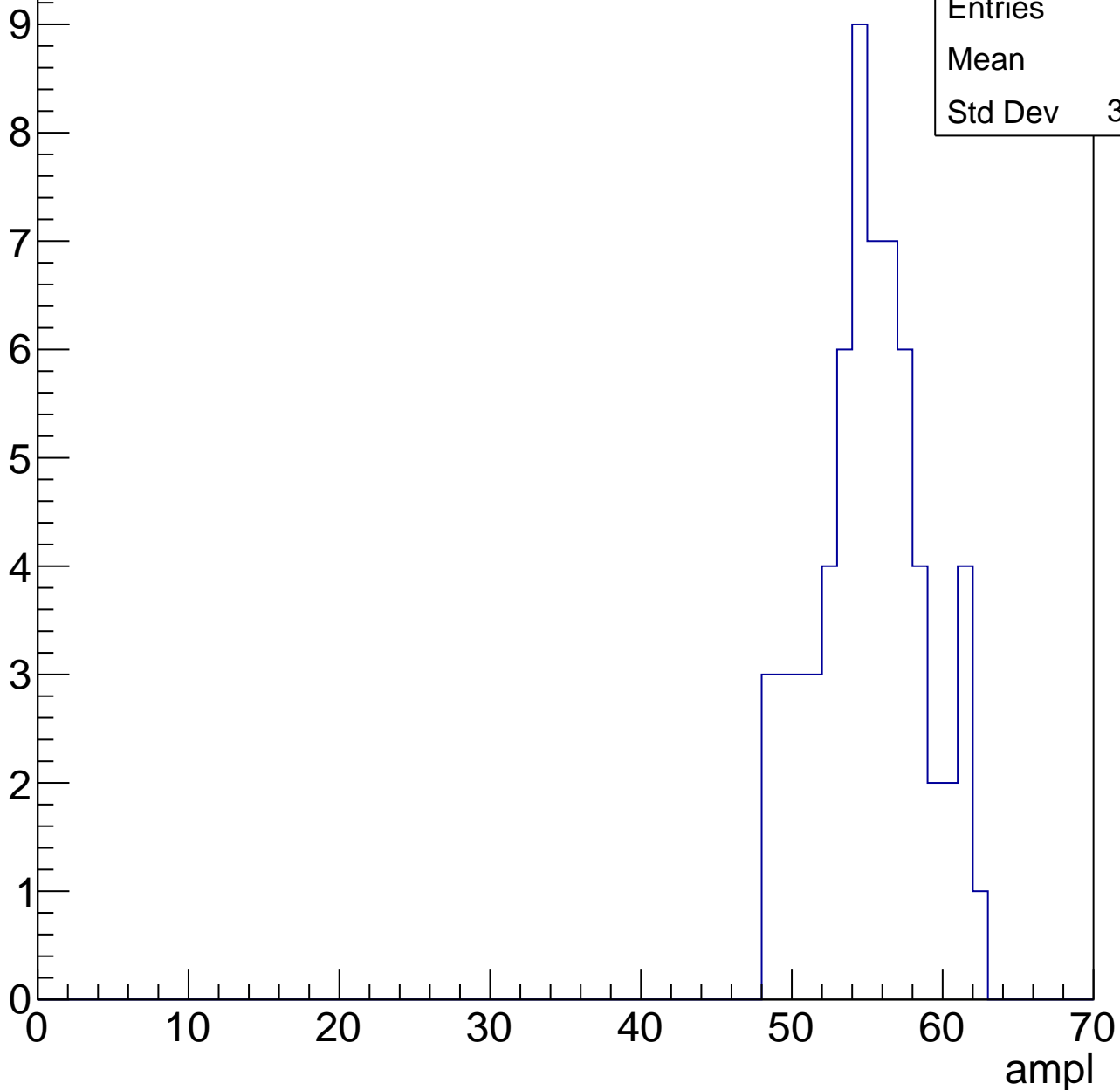


B1L103S, U6-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.7
Std Dev	3.499

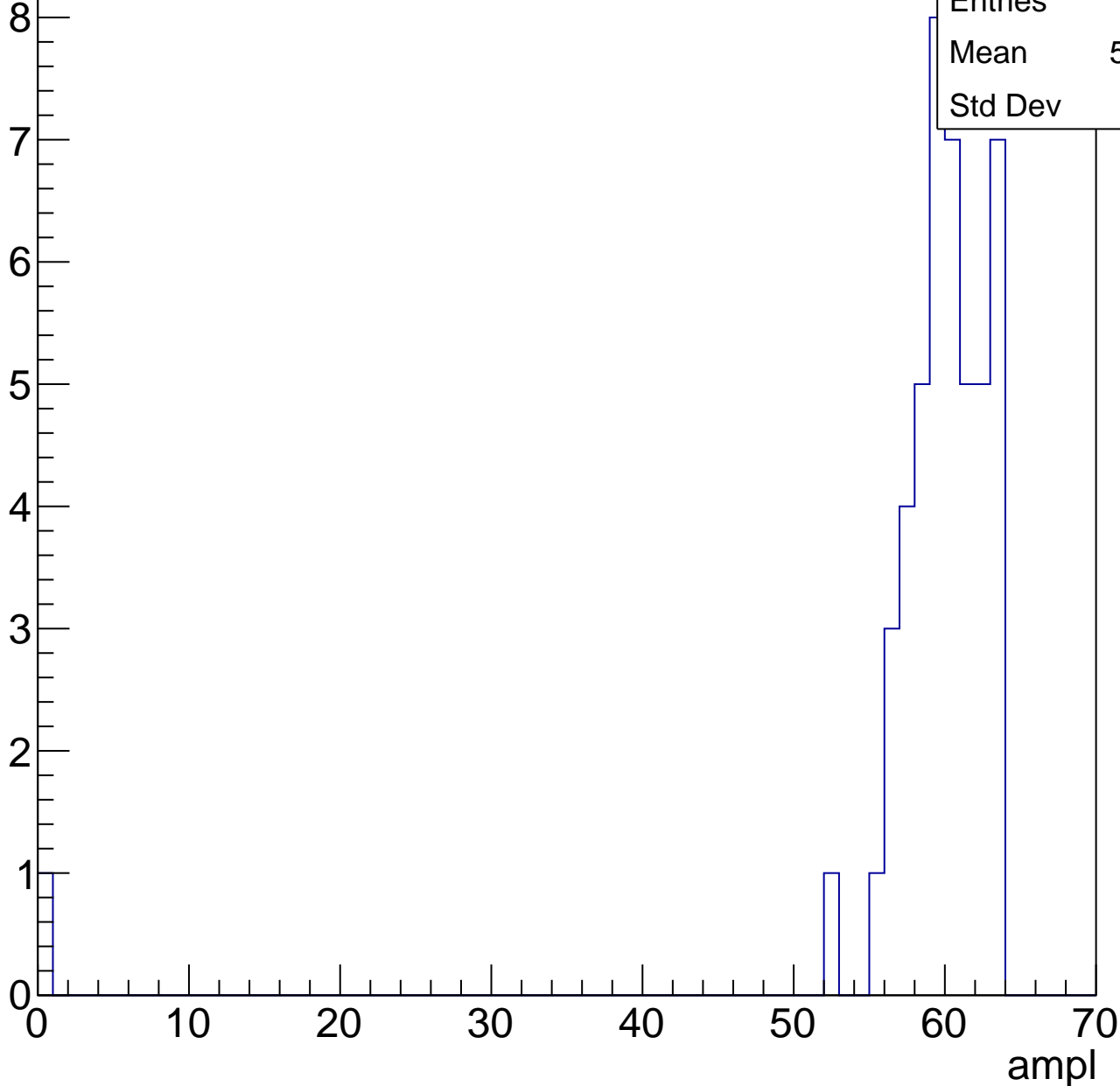


B1L103S, U6-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

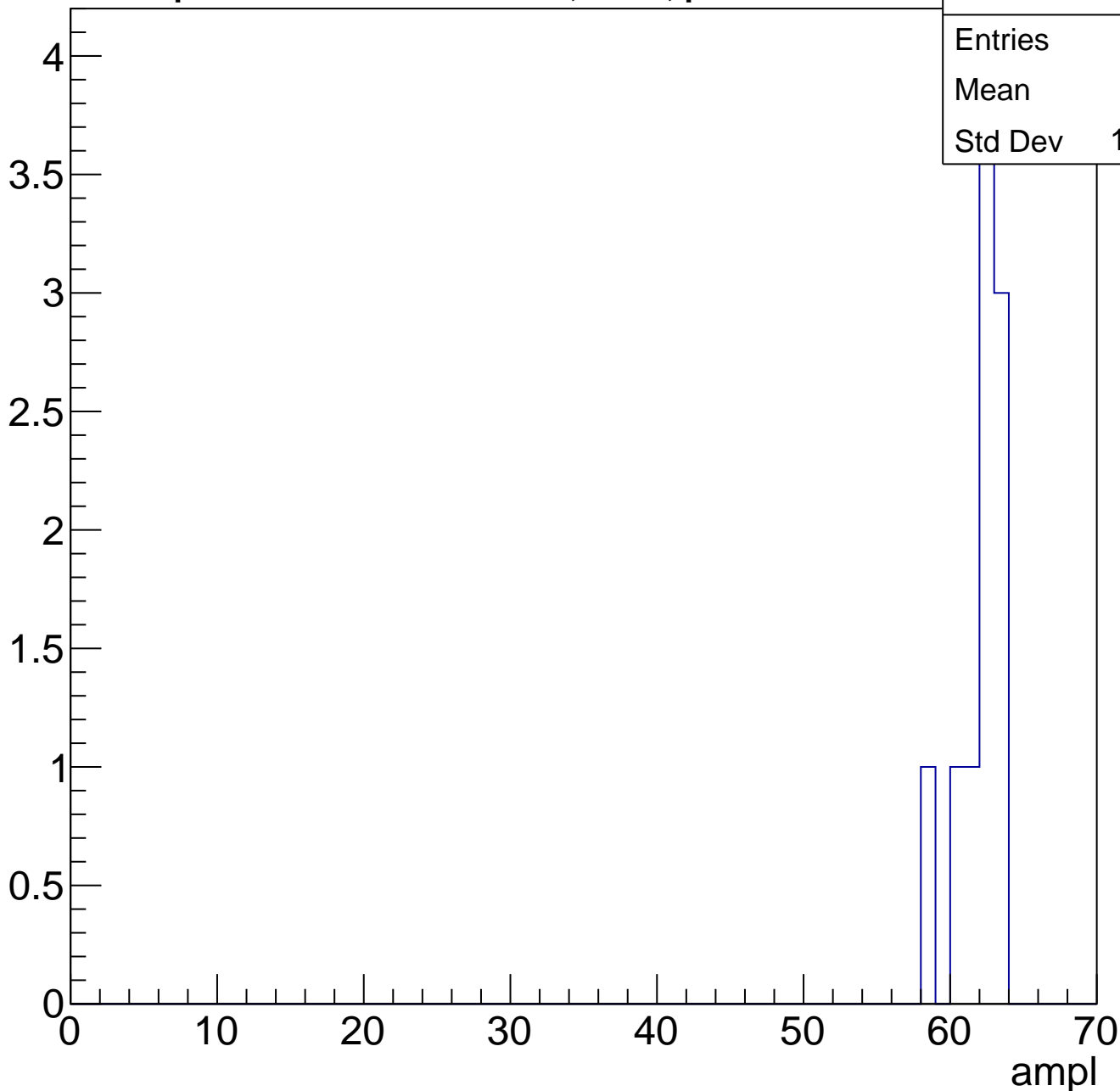
Entries	47
Mean	58.32
Std Dev	8.94



B1L103S, U6-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



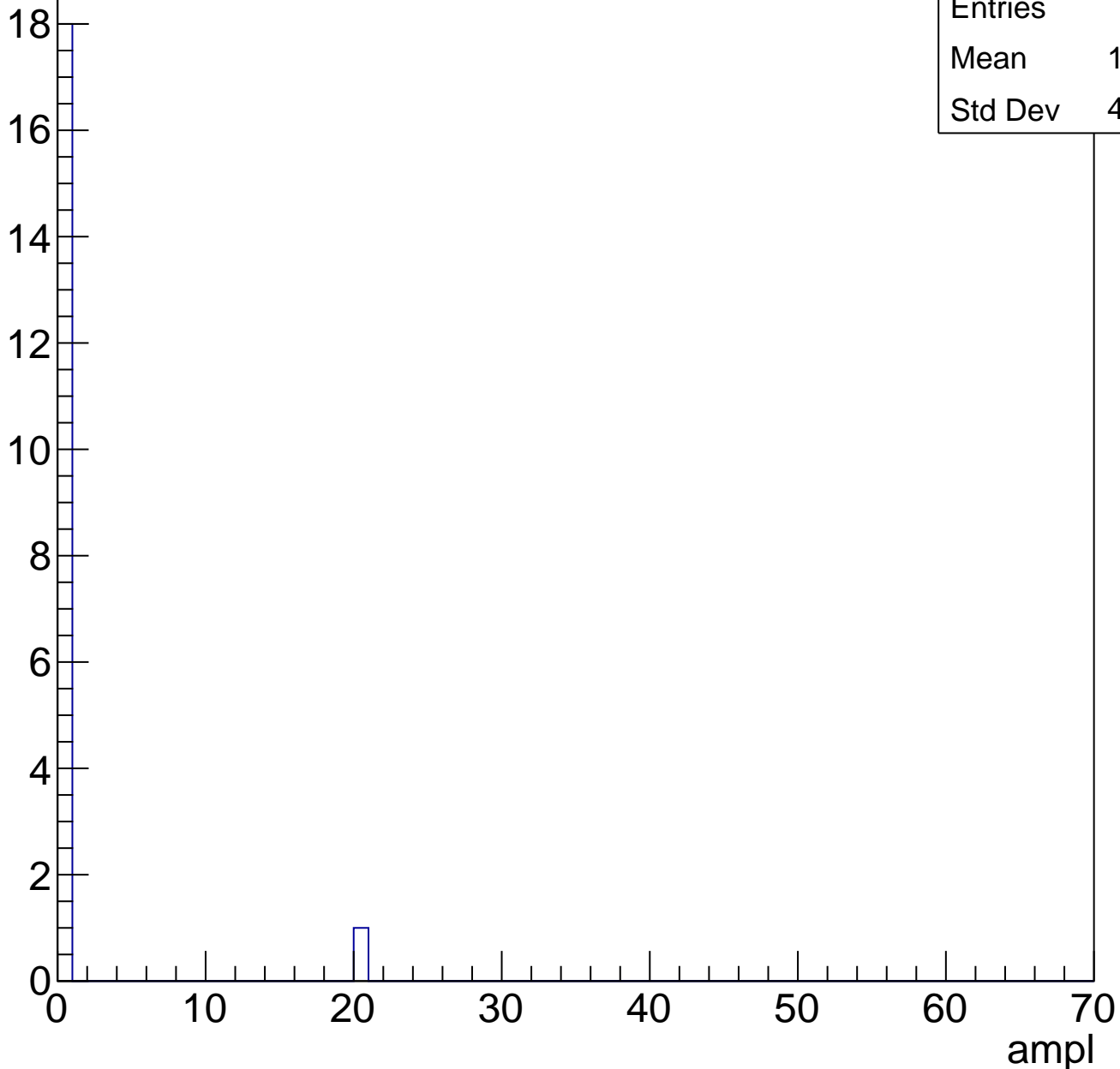
Entries	10
Mean	61.6
Std Dev	1.497

B1L103S, U6-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U6-ch60, adc0

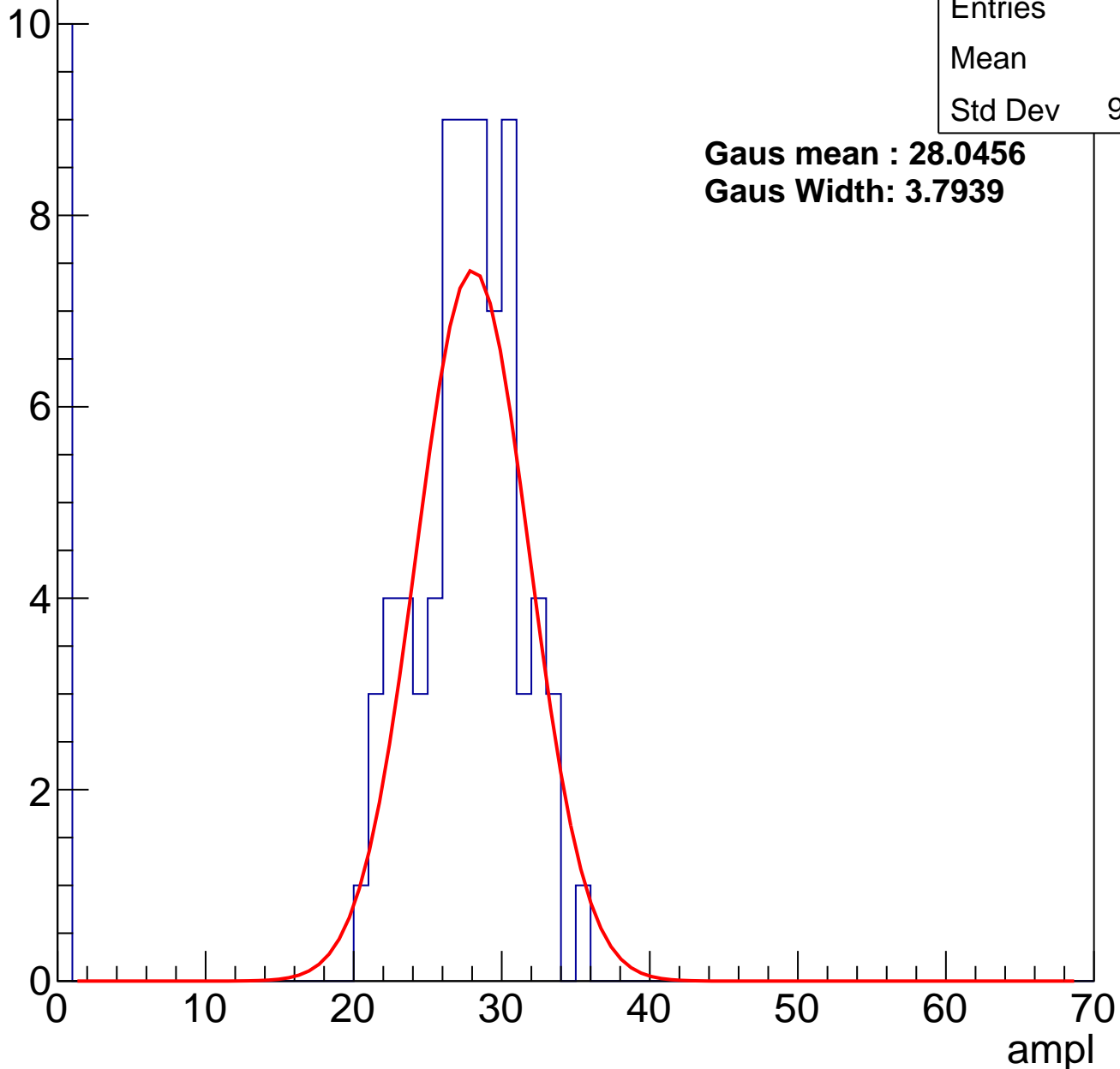
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24
Std Dev	9.413

Gaus mean : 28.0456

Gaus Width: 3.7939

Entry



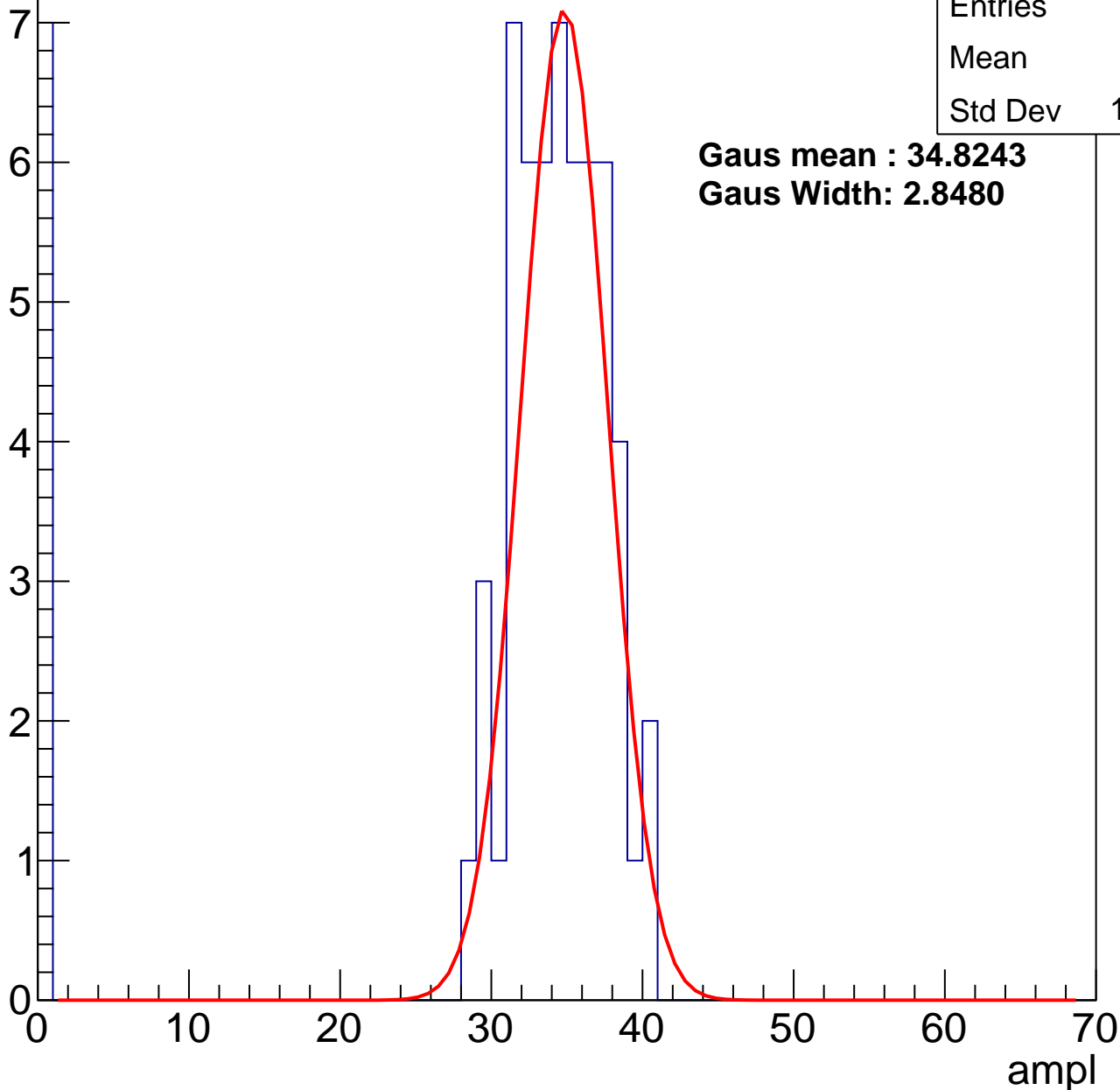
B1L103S, U6-ch60, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	30.3
Std Dev	11.05

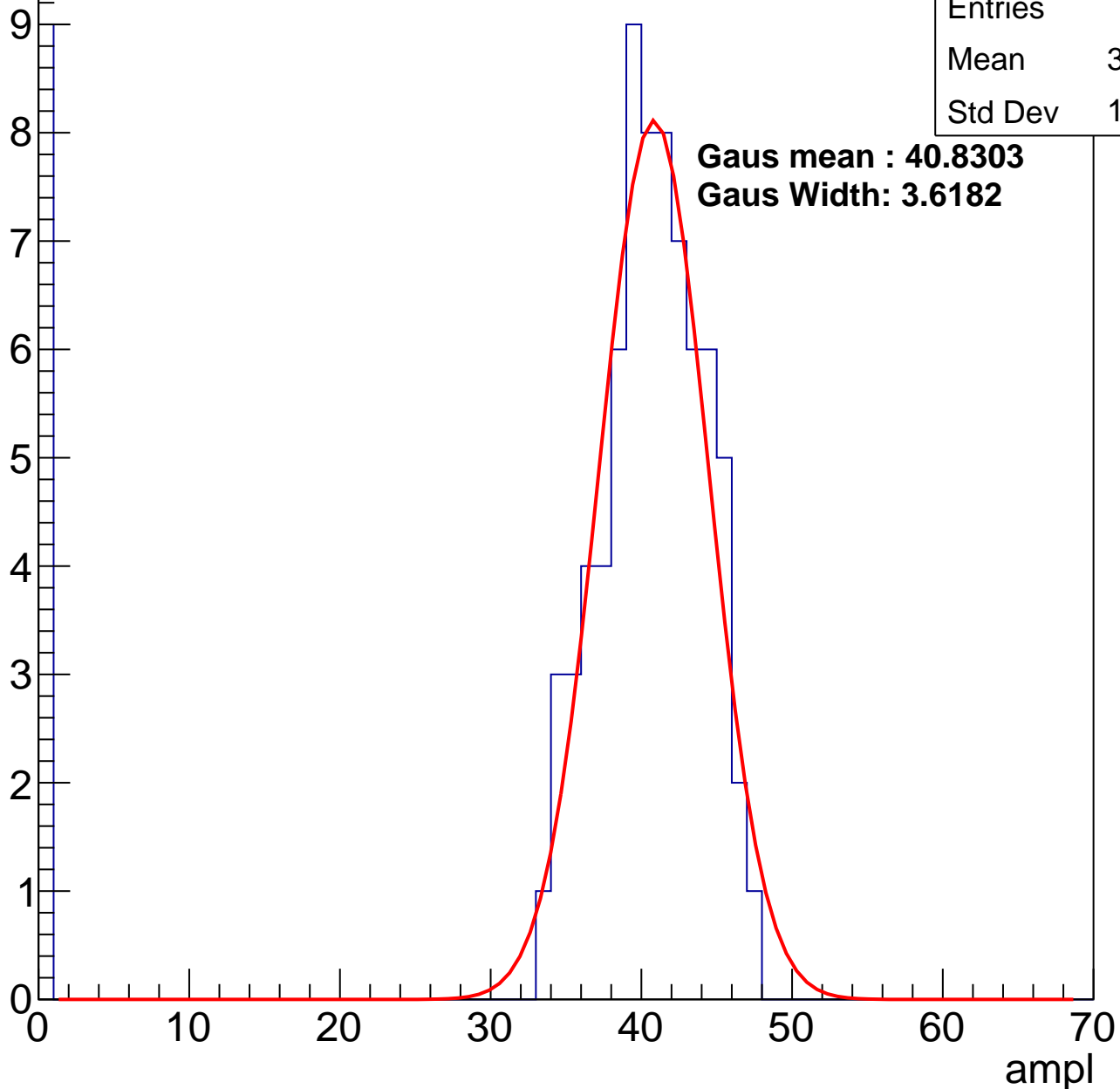
Gaus mean : 34.8243
Gaus Width: 2.8480



B1L103S, U6-ch60, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry



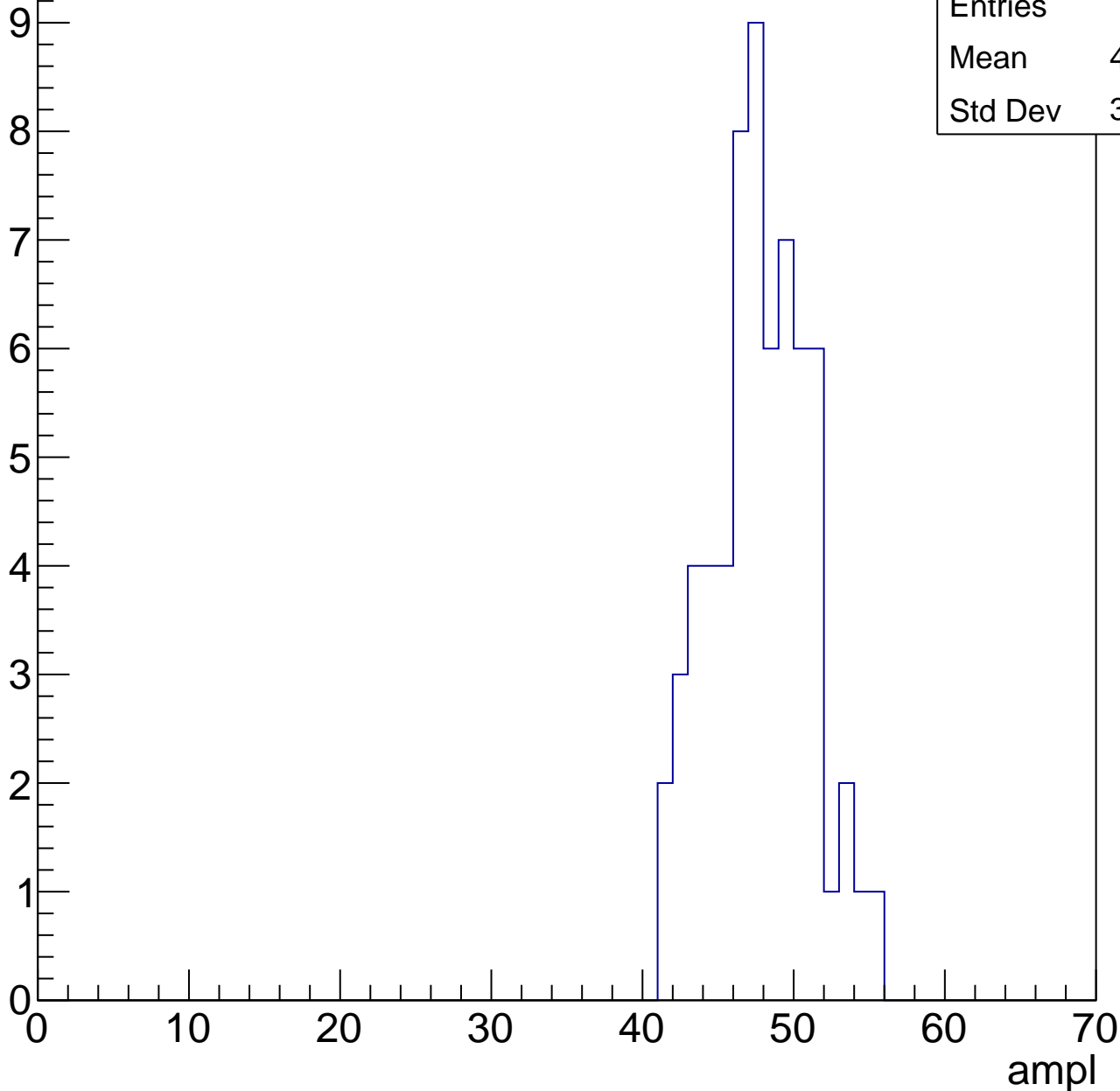
Entries	82
Mean	35.84
Std Dev	12.96

B1L103S, U6-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

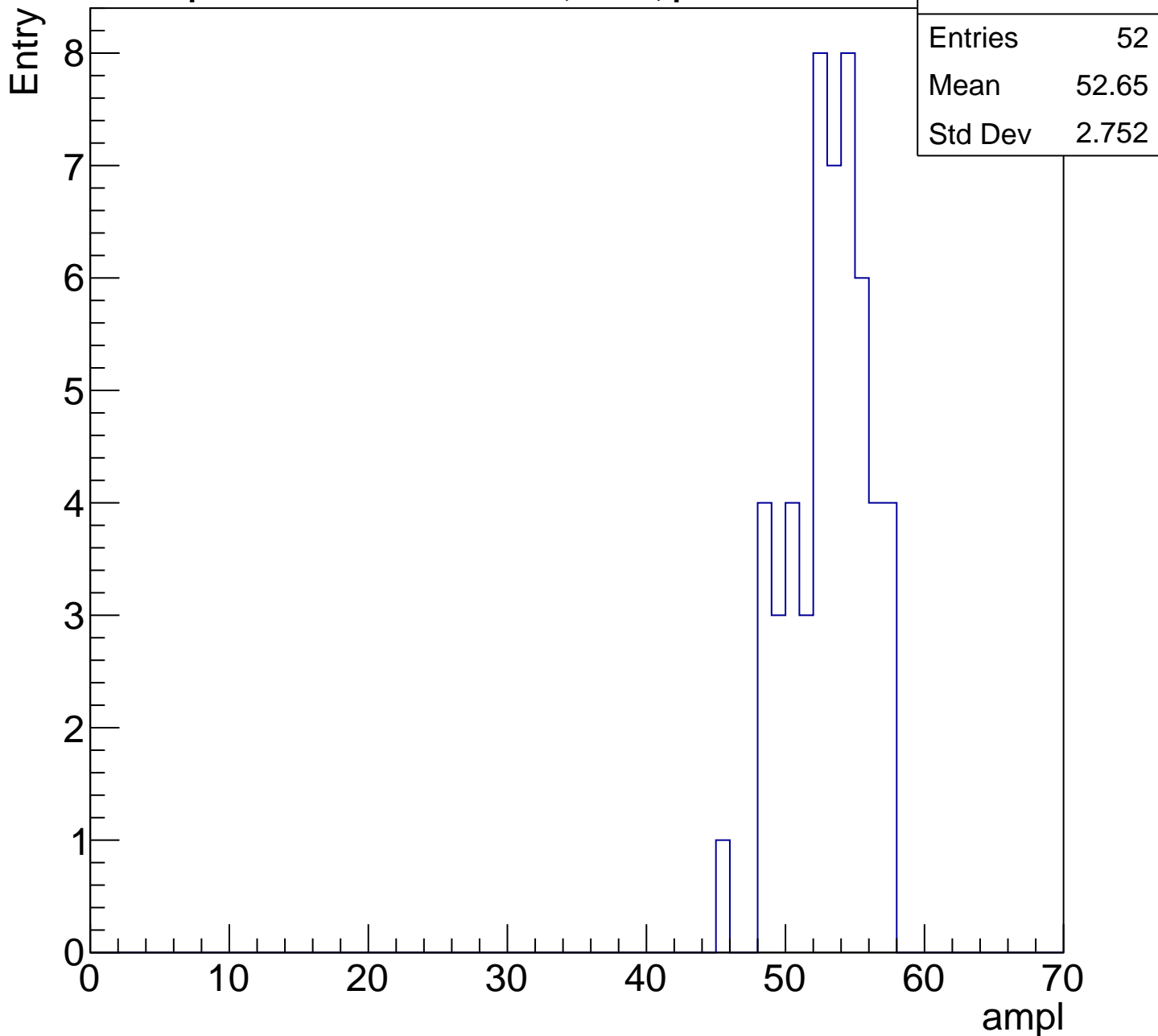
Entry

Entries	64
Mean	47.36
Std Dev	3.203



B1L103S, U6-ch60, adc4

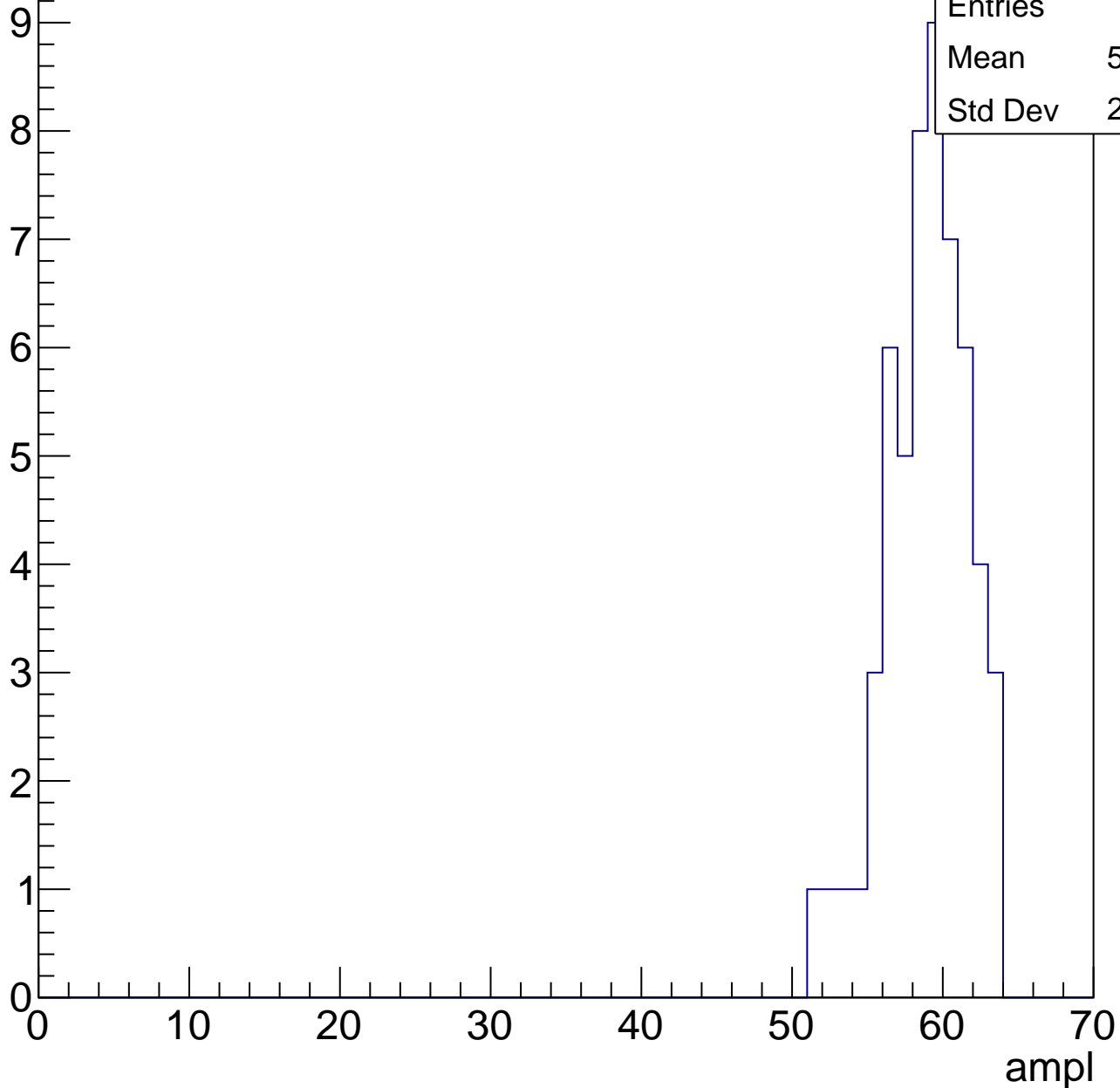
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

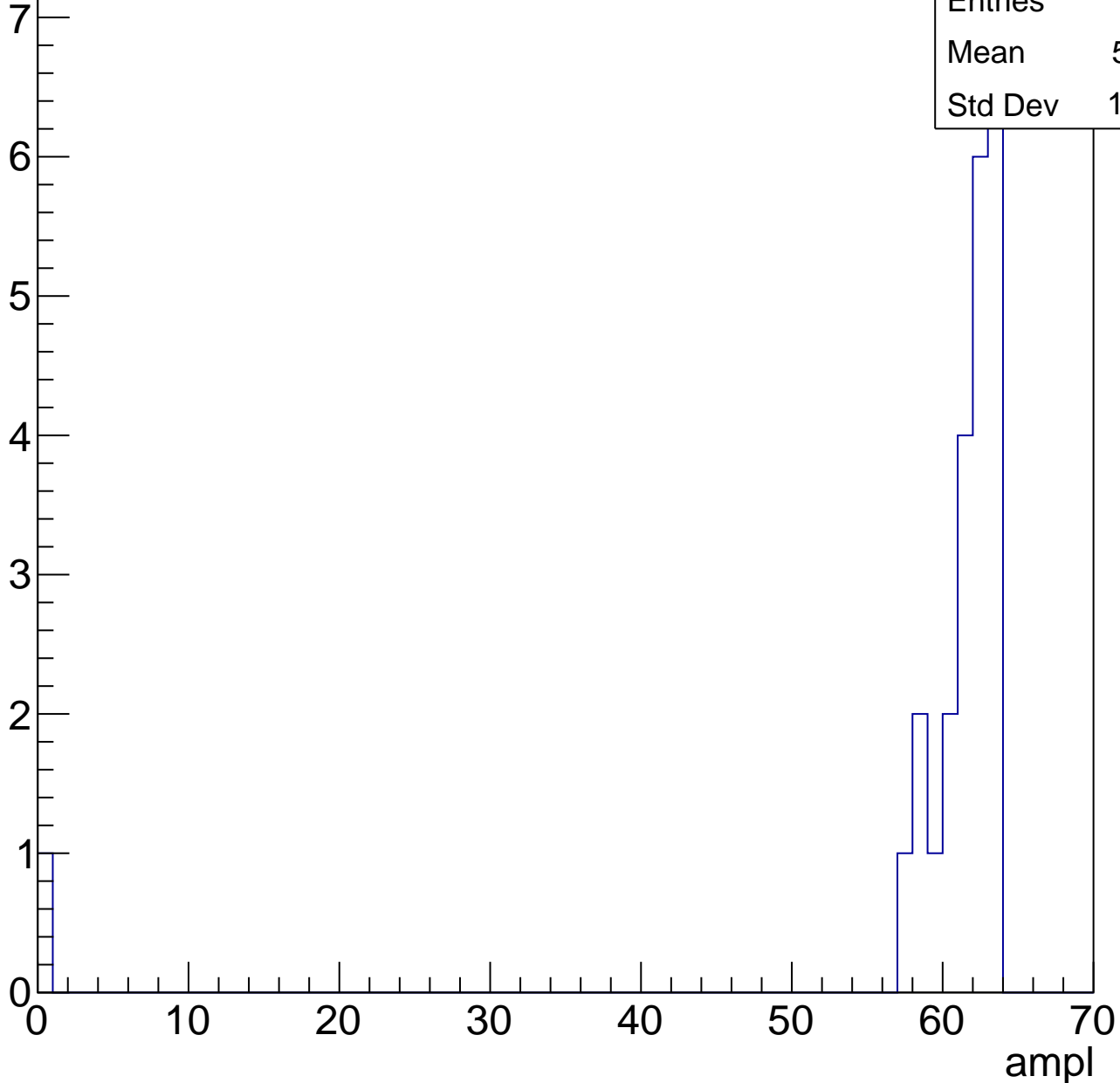


B1L103S, U6-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.71
Std Dev	12.36



B1L103S, U6-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



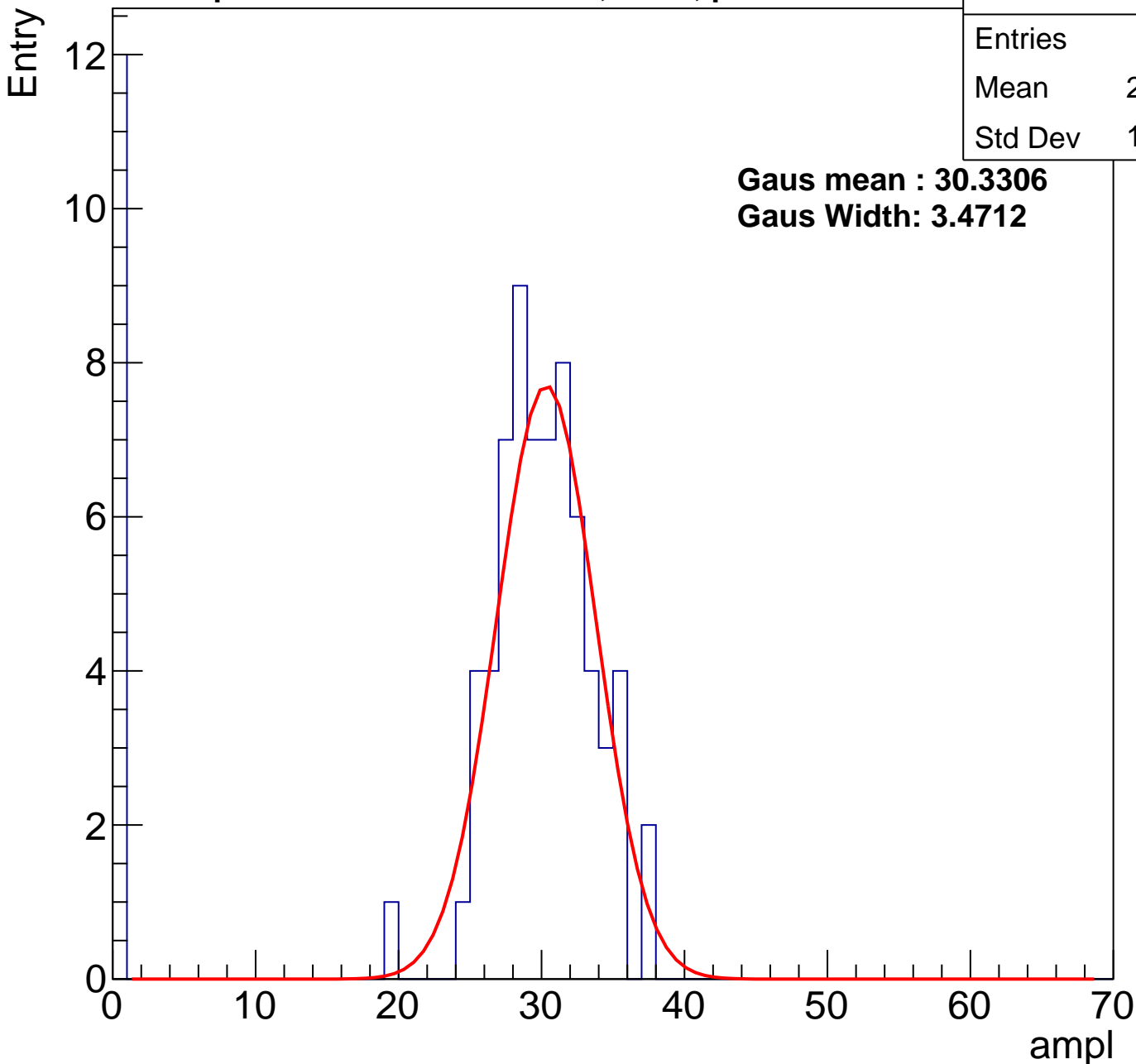
B1L103S, U6-ch61, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	25.18
Std Dev	11.08

Gaus mean : 30.3306

Gaus Width: 3.4712



B1L103S, U6-ch61, adc1

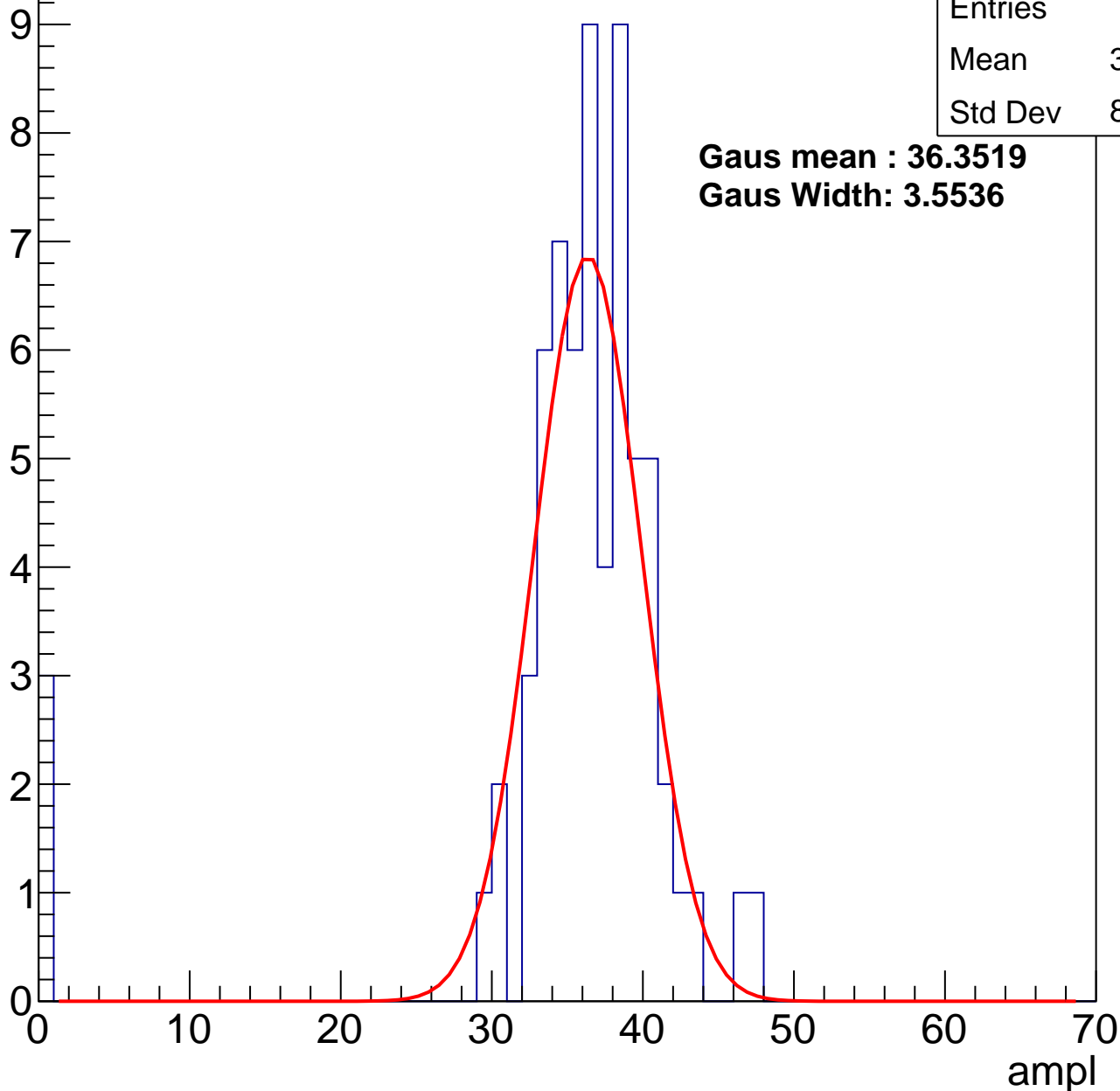
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.85
Std Dev	8.329

Gaus mean : 36.3519

Gaus Width: 3.5536



B1L103S, U6-ch61, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

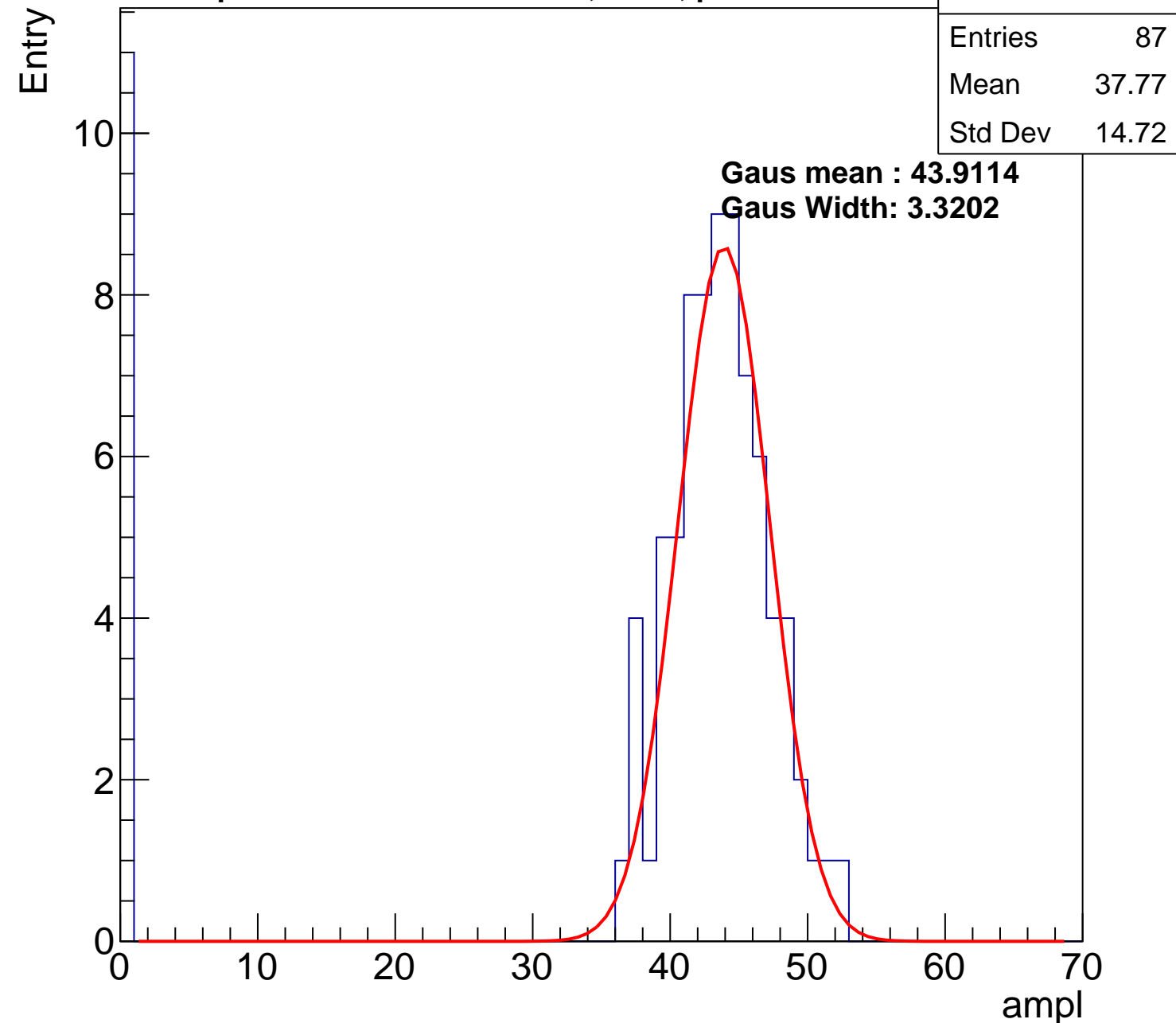
70

ampl

Entries	87
Mean	37.77
Std Dev	14.72

Gaus mean : 43.9114

Gaus Width: 3.3202

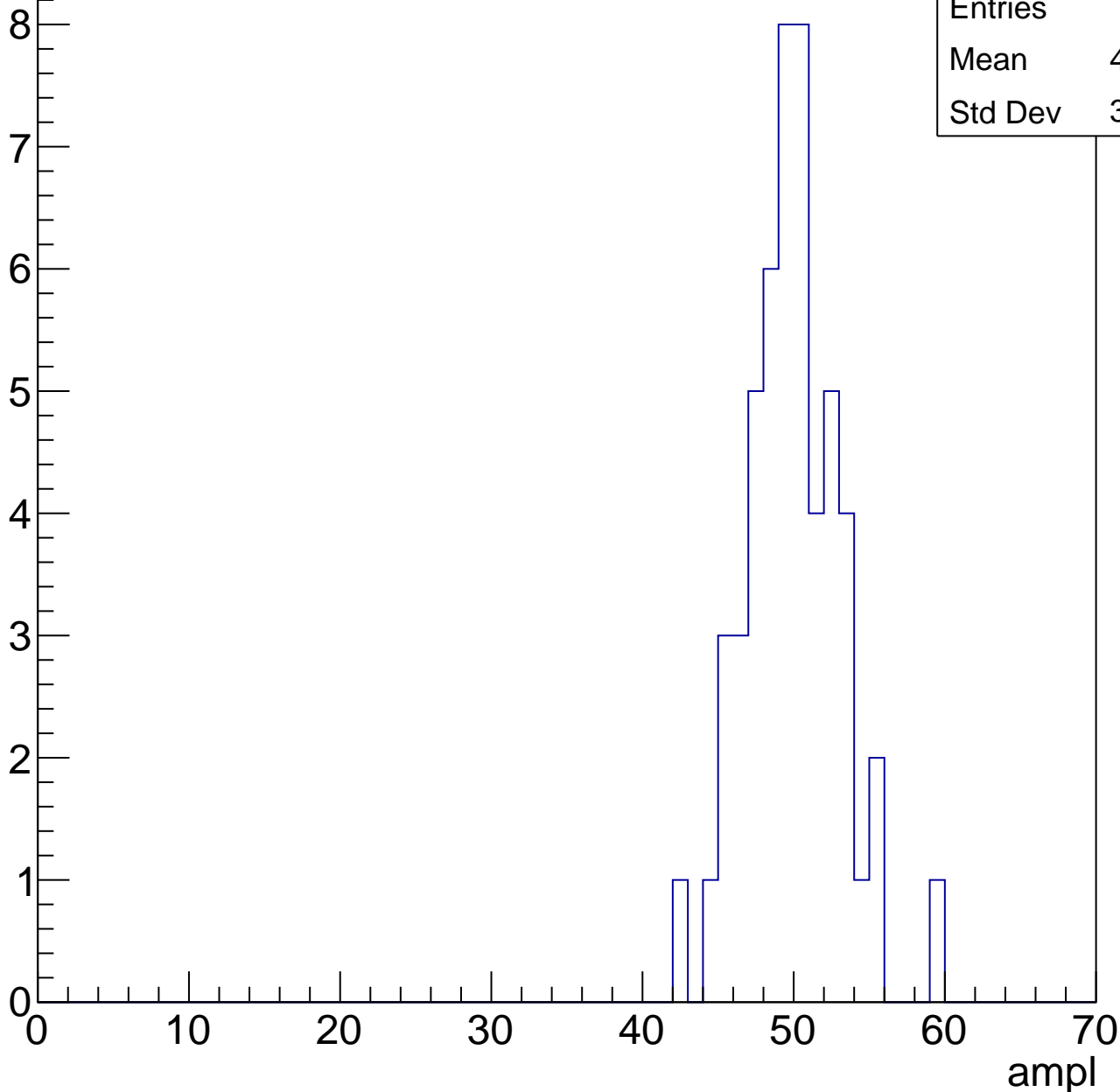


B1L103S, U6-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	49.48
Std Dev	3.079

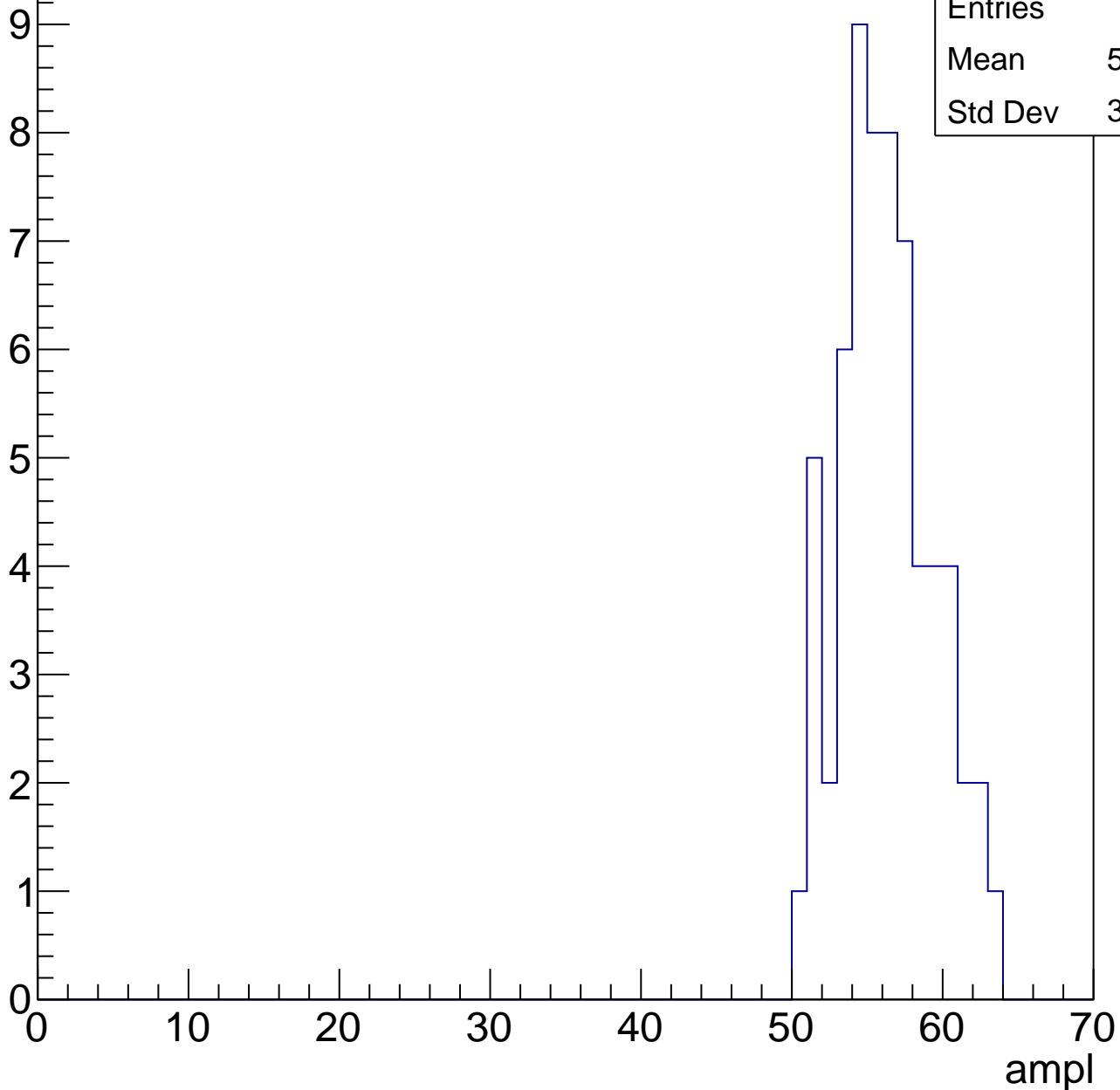


B1L103S, U6-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	55.83
Std Dev	3.042

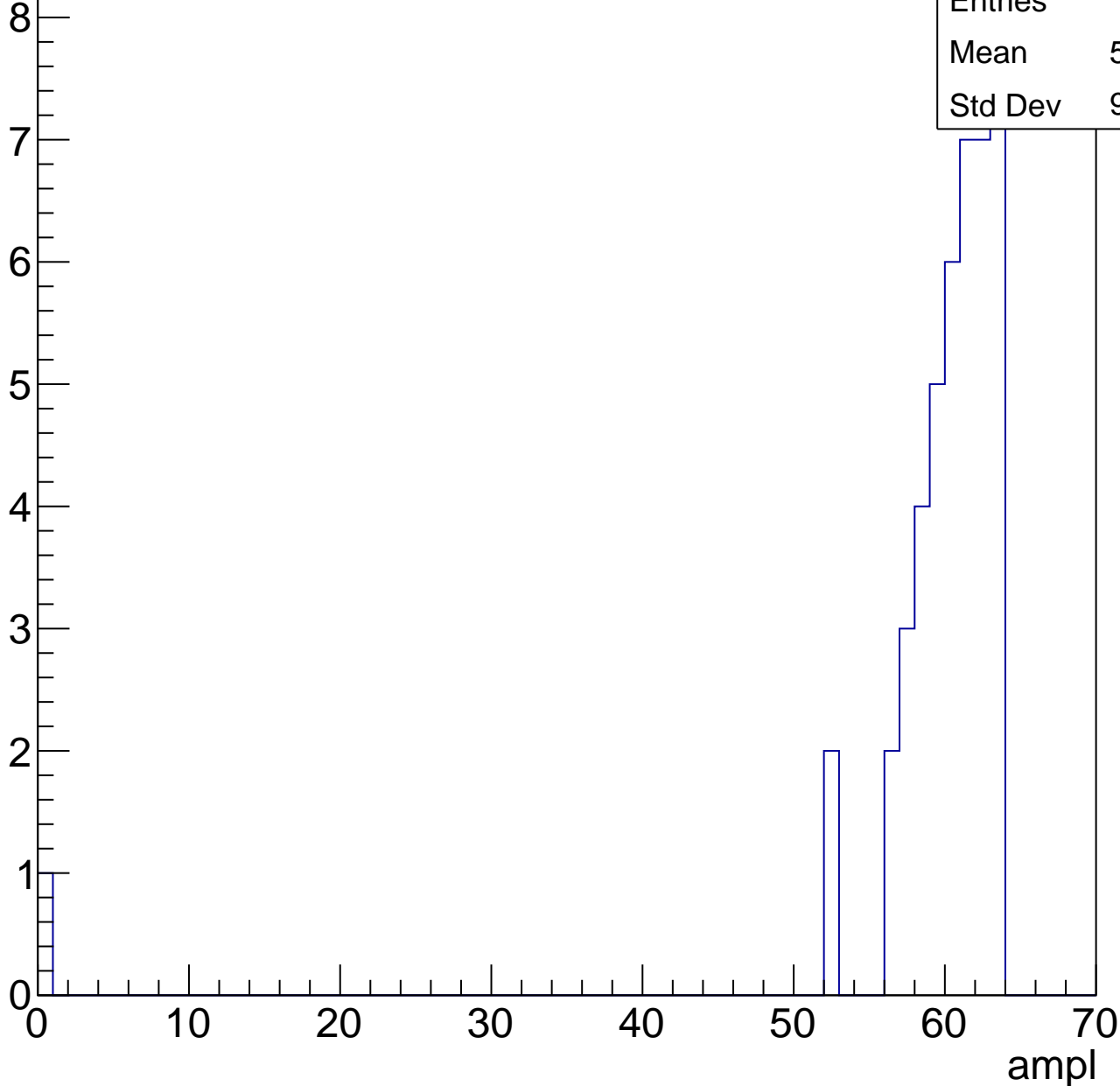


B1L103S, U6-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.64
Std Dev	9.228



B1L103S, U6-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

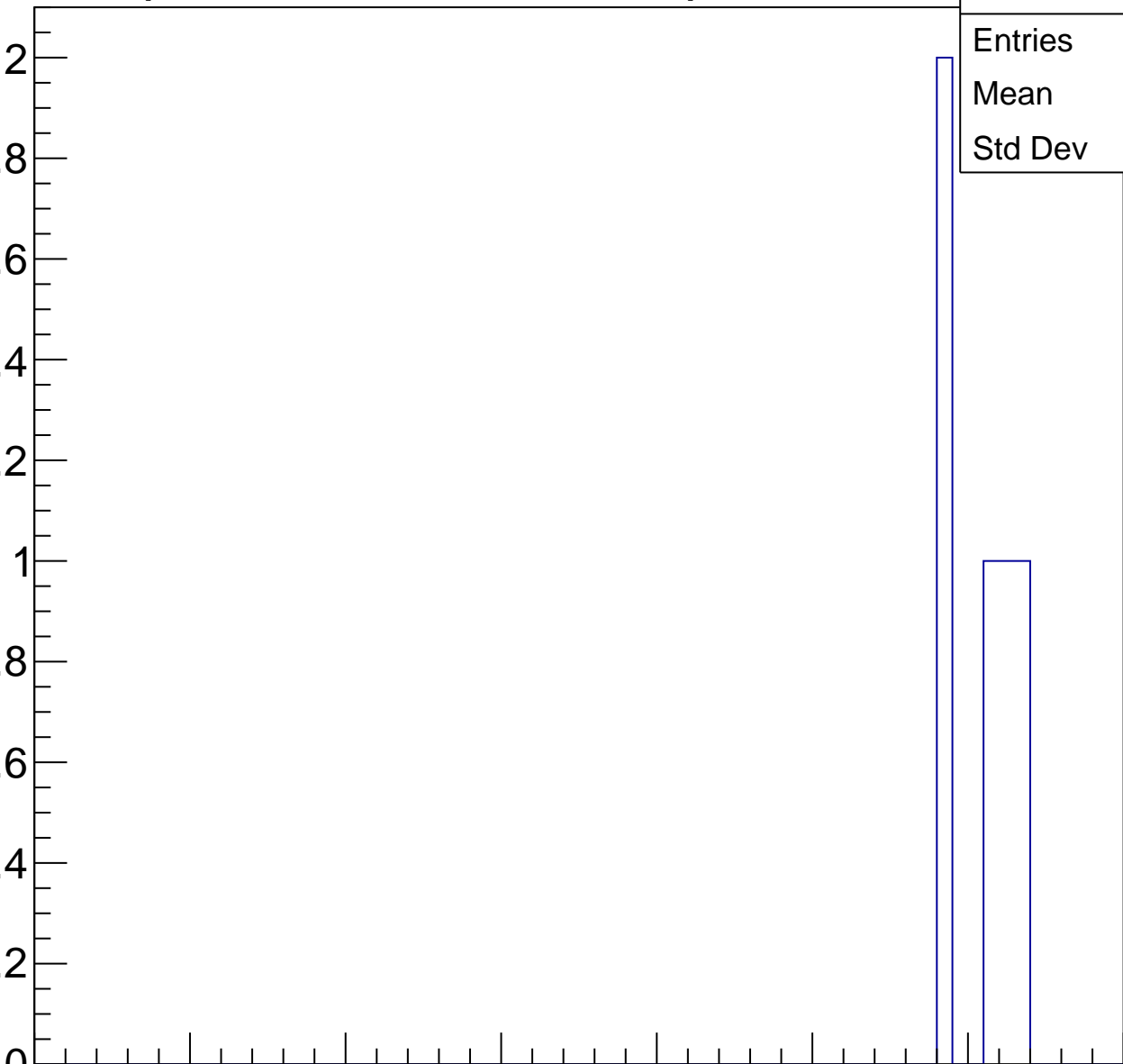
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.4
Std Dev	2.059

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch62, adc0

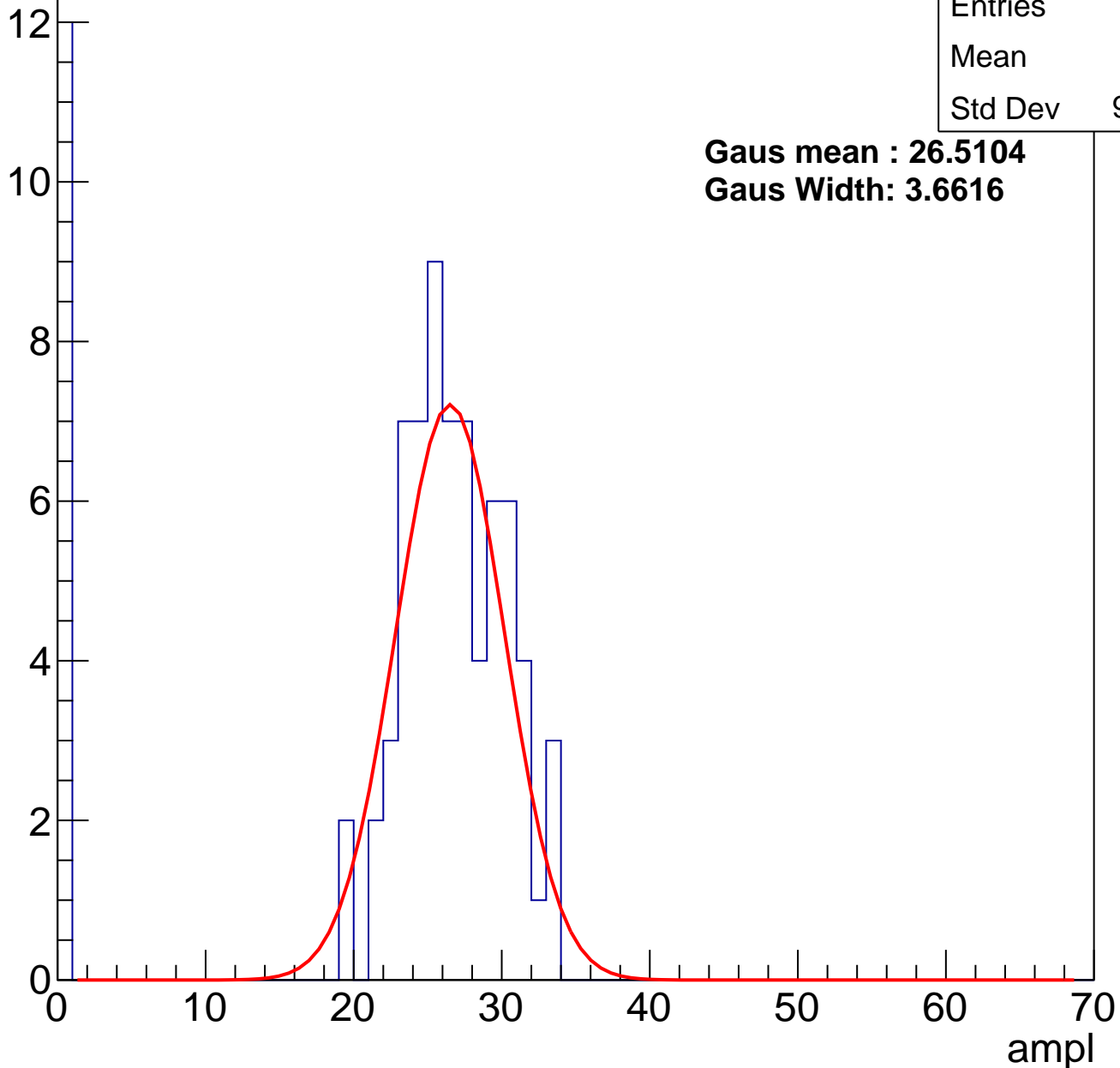
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	22.4
Std Dev	9.891

Gaus mean : 26.5104

Gaus Width: 3.6616

Entry



B1L103S, U6-ch62, adc1

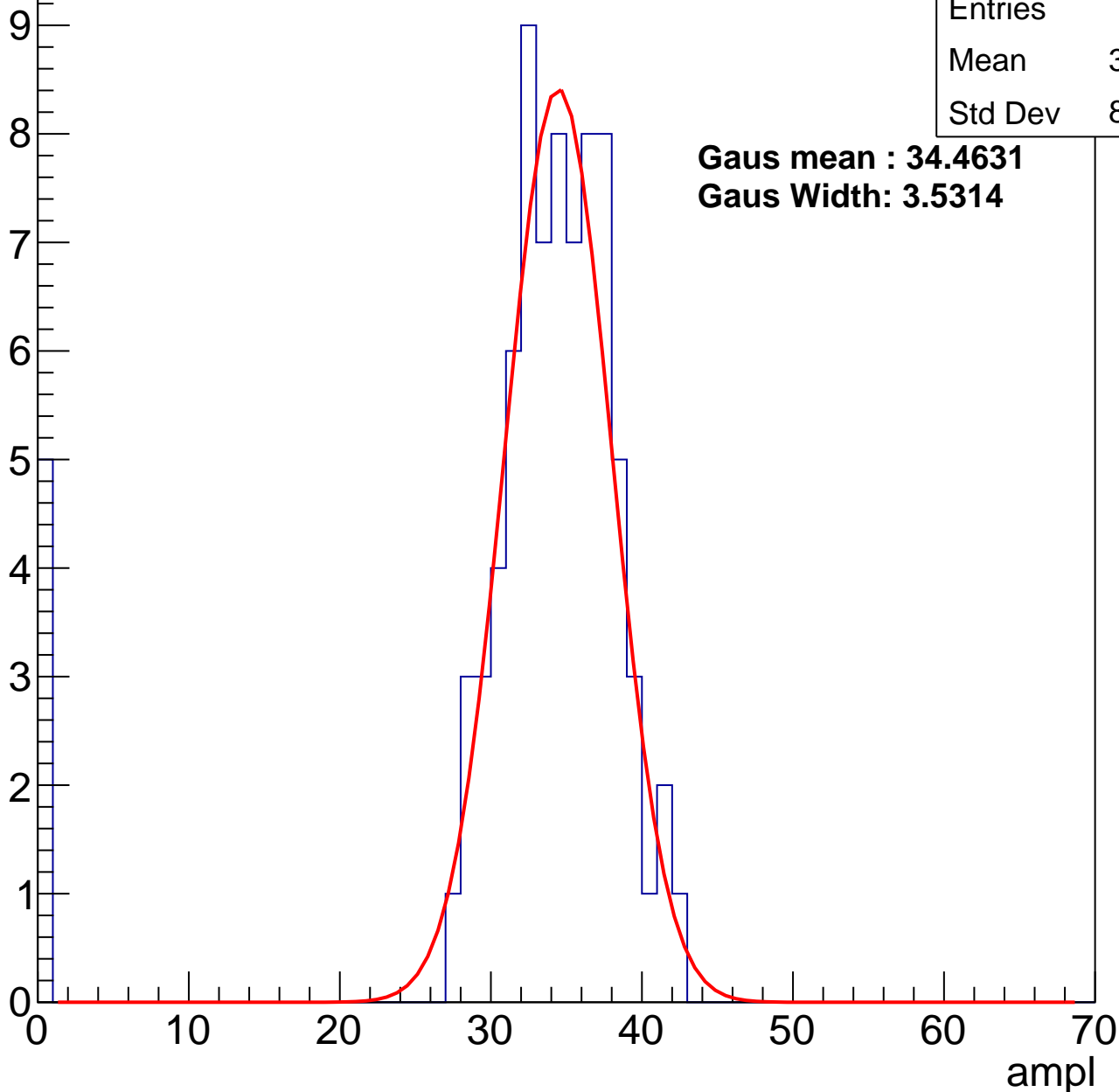
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	32.04
Std Dev	8.837

Gaus mean : 34.4631

Gaus Width: 3.5314



B1L103S, U6-ch62, adc2

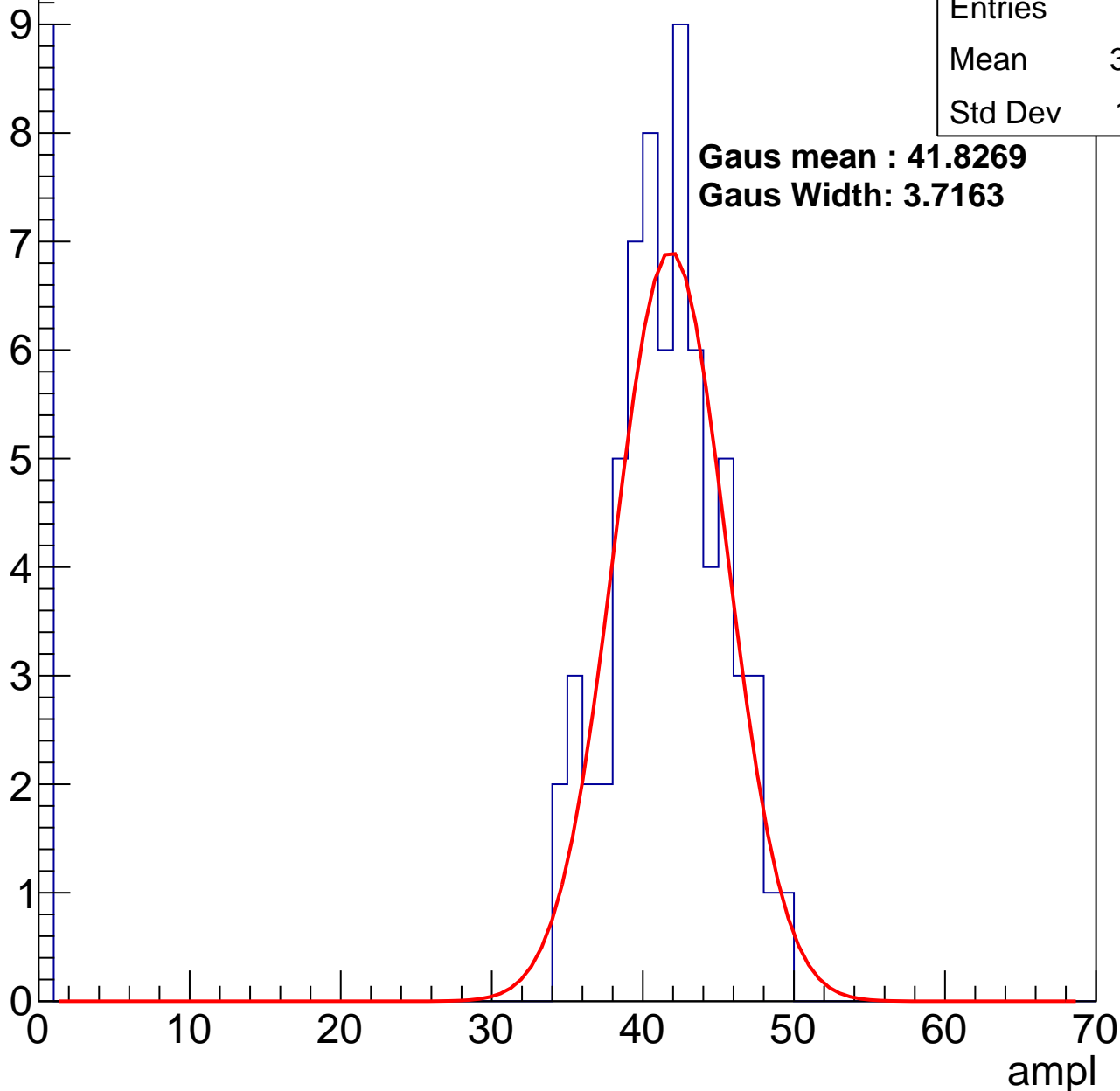
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.33
Std Dev	13.71

Gaus mean : 41.8269

Gaus Width: 3.7163

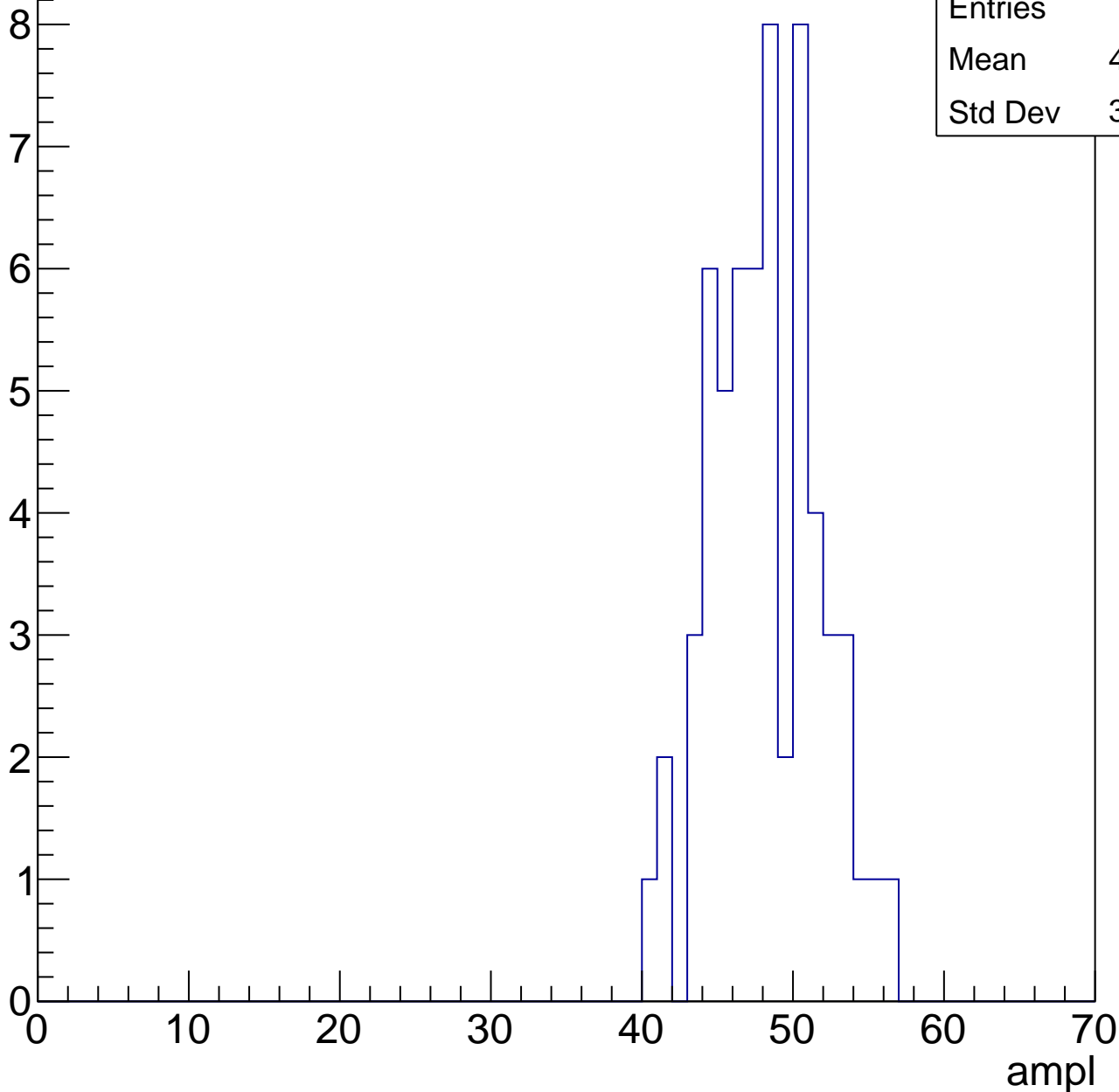


B1L103S, U6-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

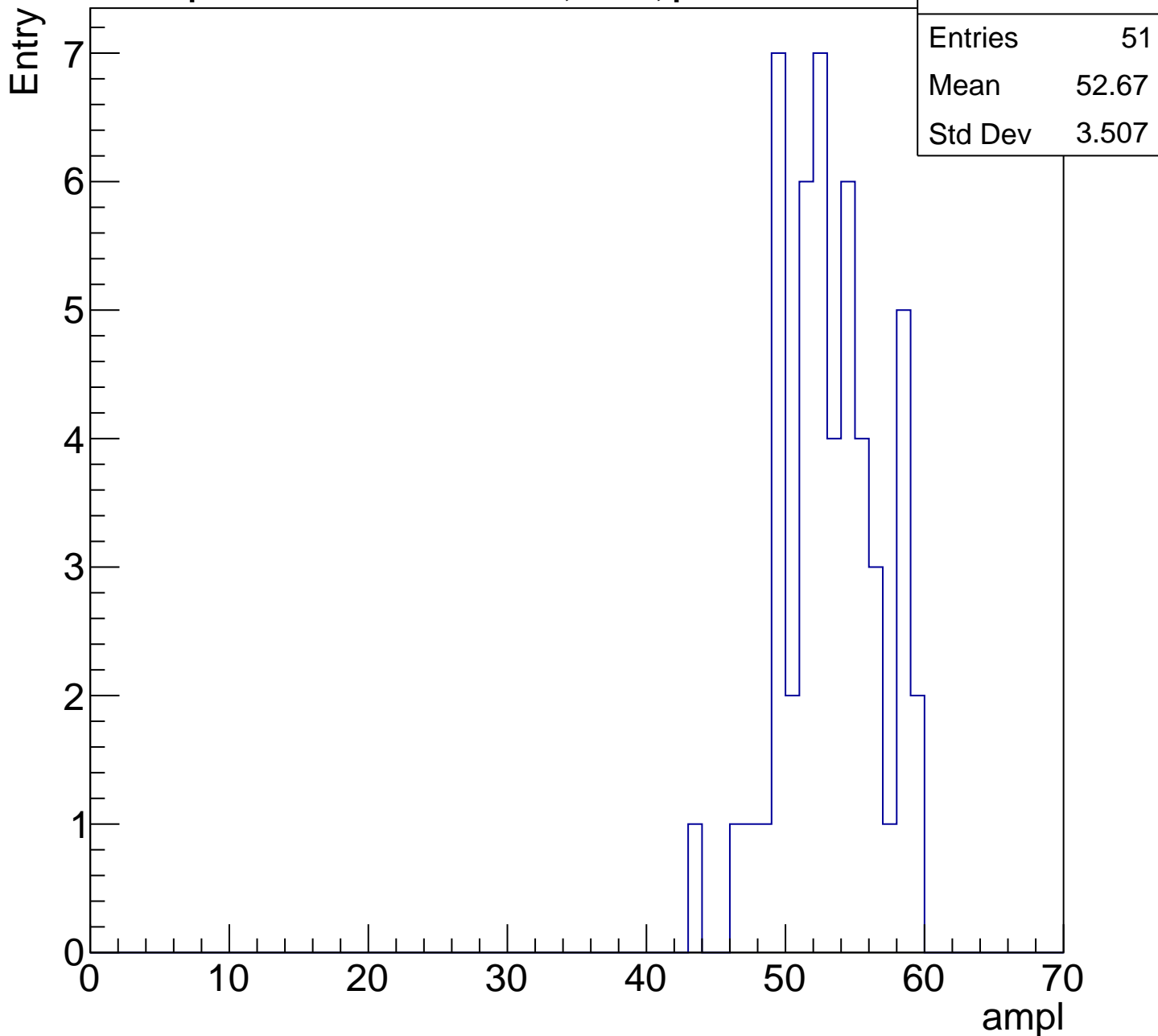
Entry

Entries	60
Mean	47.73
Std Dev	3.525



B1L103S, U6-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	57.64
Std Dev	7.617

Entry

10

8

6

4

2

0

0

10

20

30

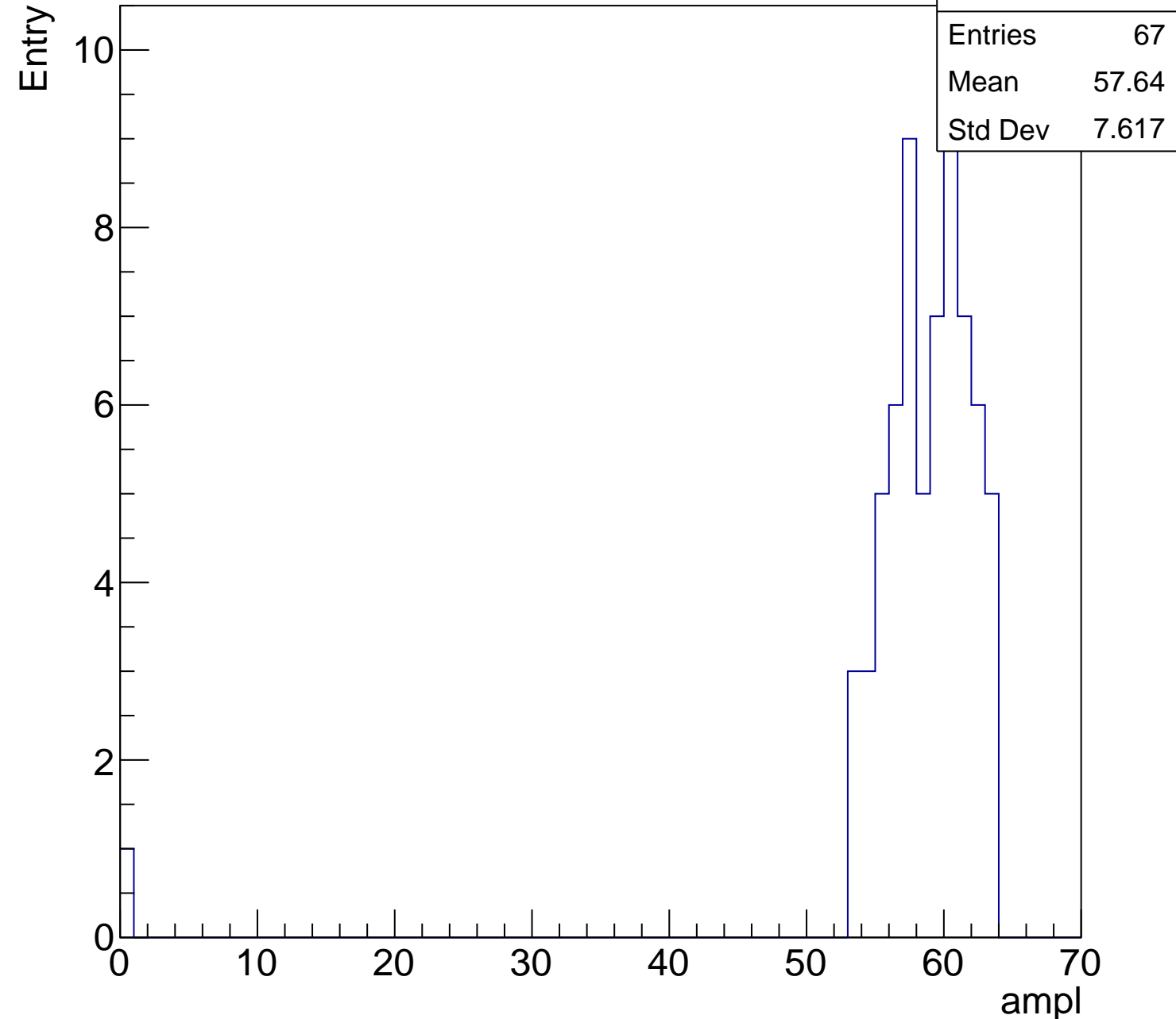
40

50

60

ampl

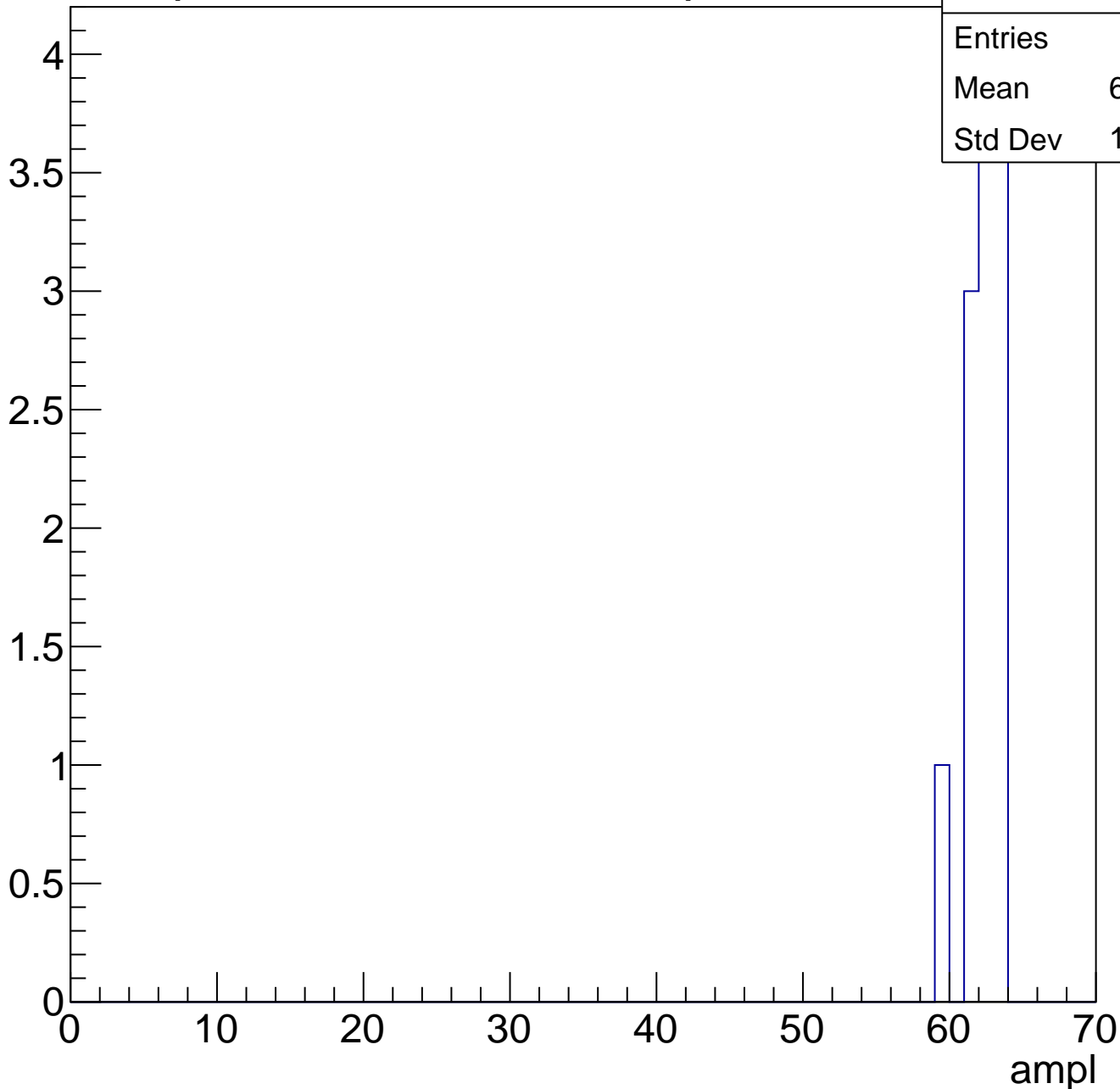
70



B1L103S, U6-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	3.15
Std Dev	13.73

ampl

0 10 20 30 40 50 60 70

B1L103S, U6-ch63, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	26.39
Std Dev	10.96

Gaus mean : 31.0015

Gaus Width: 3.8509

Entry

12

10

8

6

4

2

0

0

10

20

30

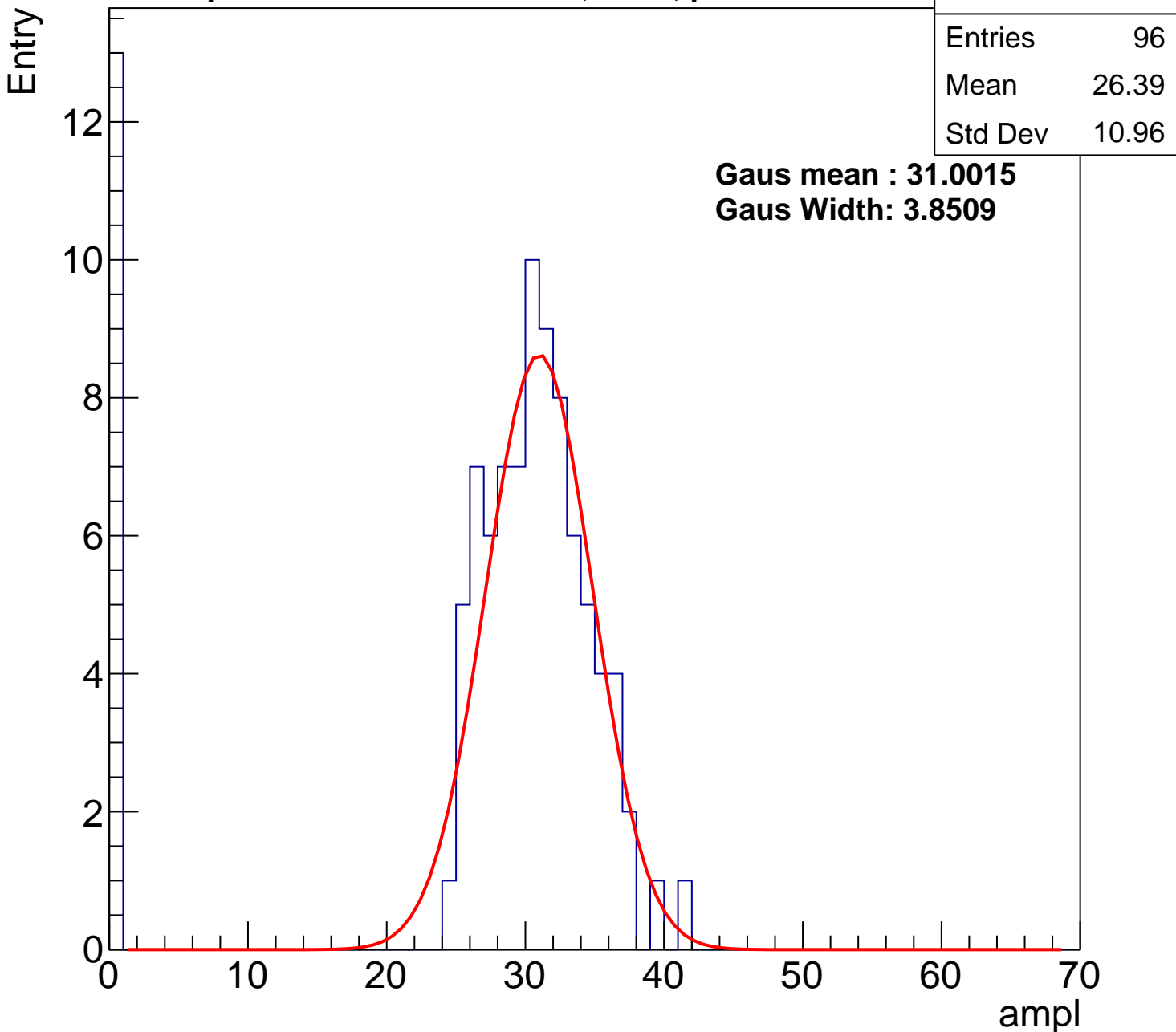
40

50

60

70

ampl



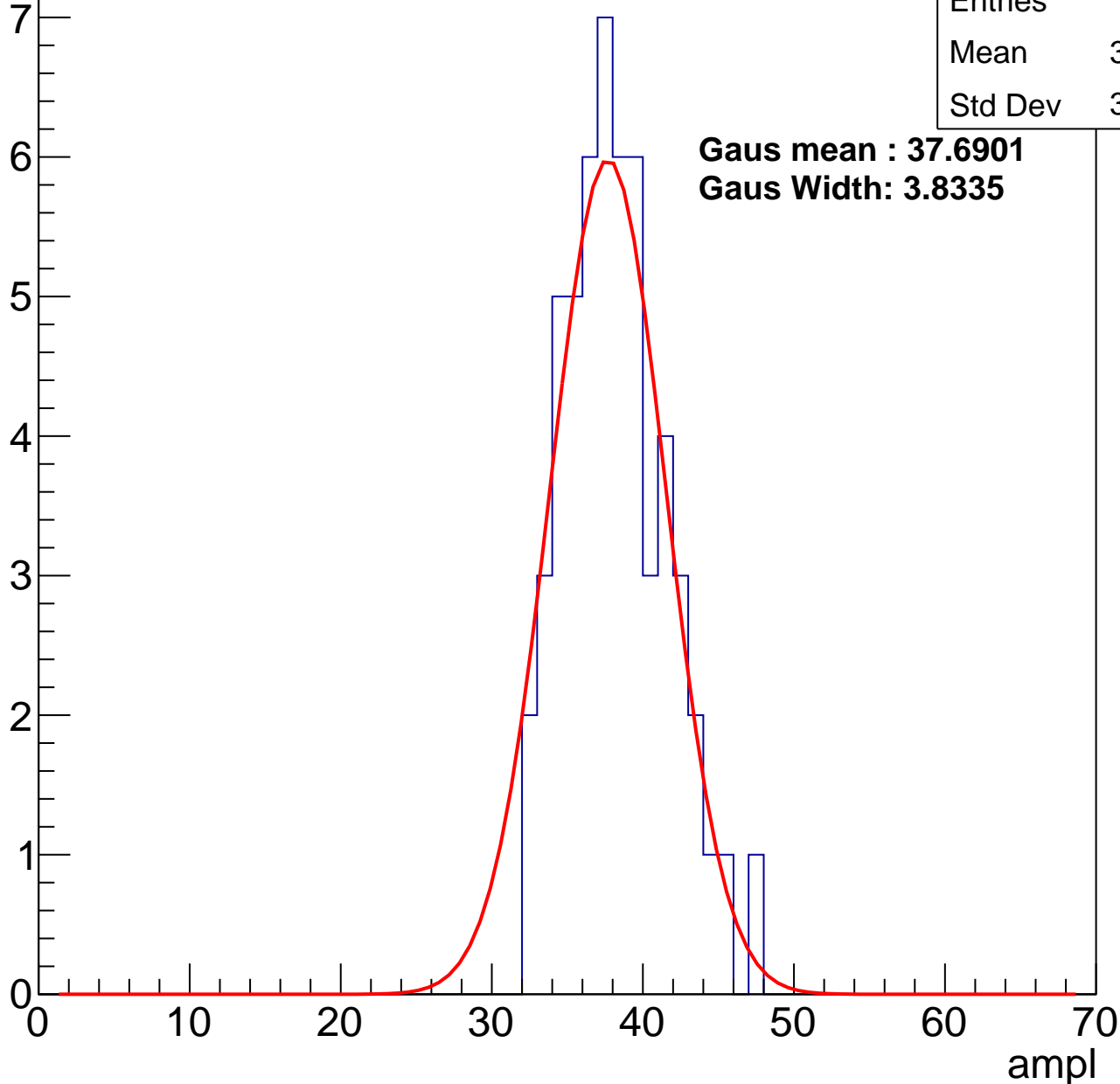
B1L103S, U6-ch63, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	37.76
Std Dev	3.352

Gaus mean : 37.6901
Gaus Width: 3.8335



B1L103S, U6-ch63, adc2

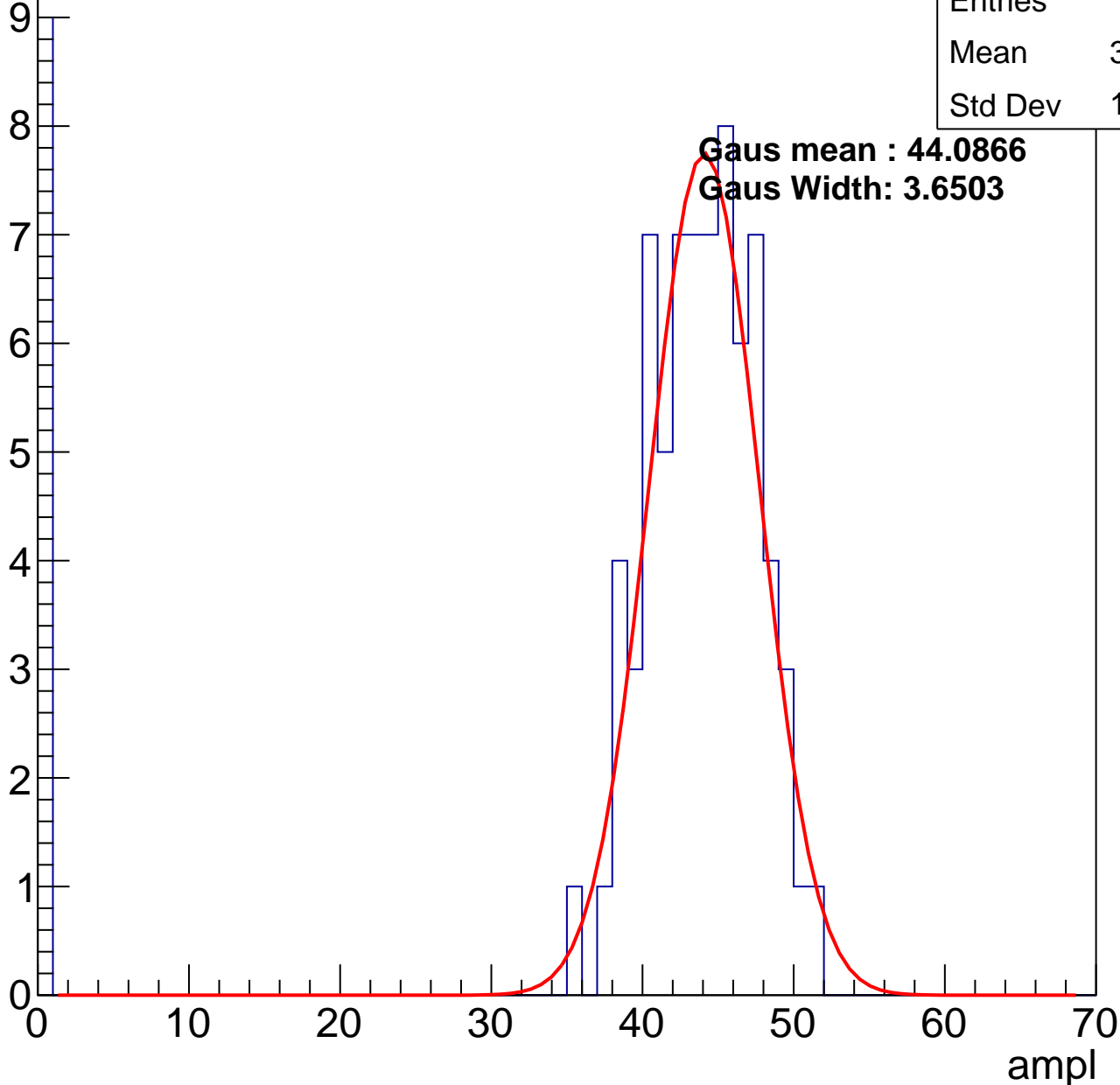
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	38.69
Std Dev	14.06

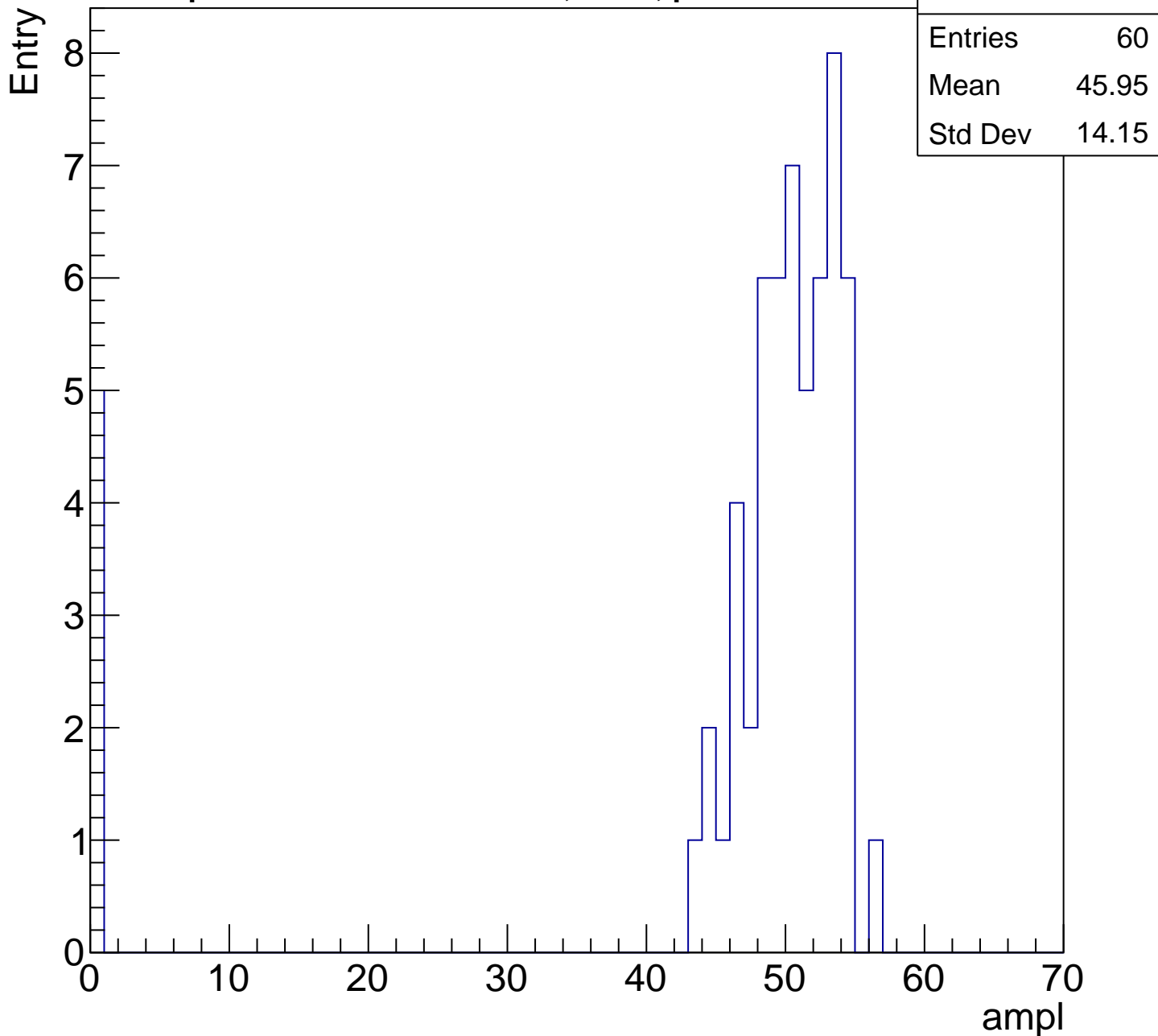
Gaus mean : 44.0866

Gaus Width: 3.6503



B1L103S, U6-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

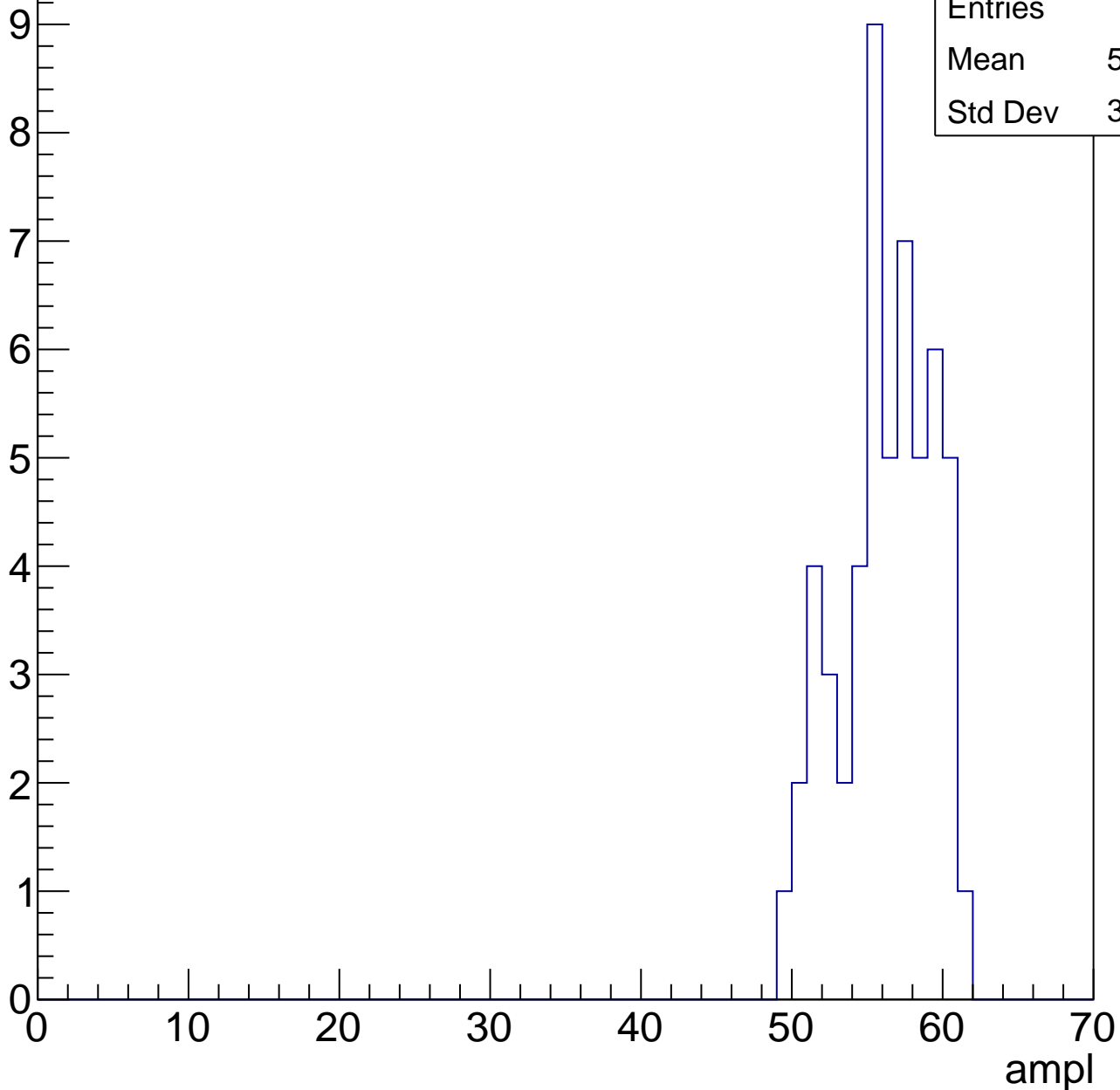


B1L103S, U6-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.74
Std Dev	3.026

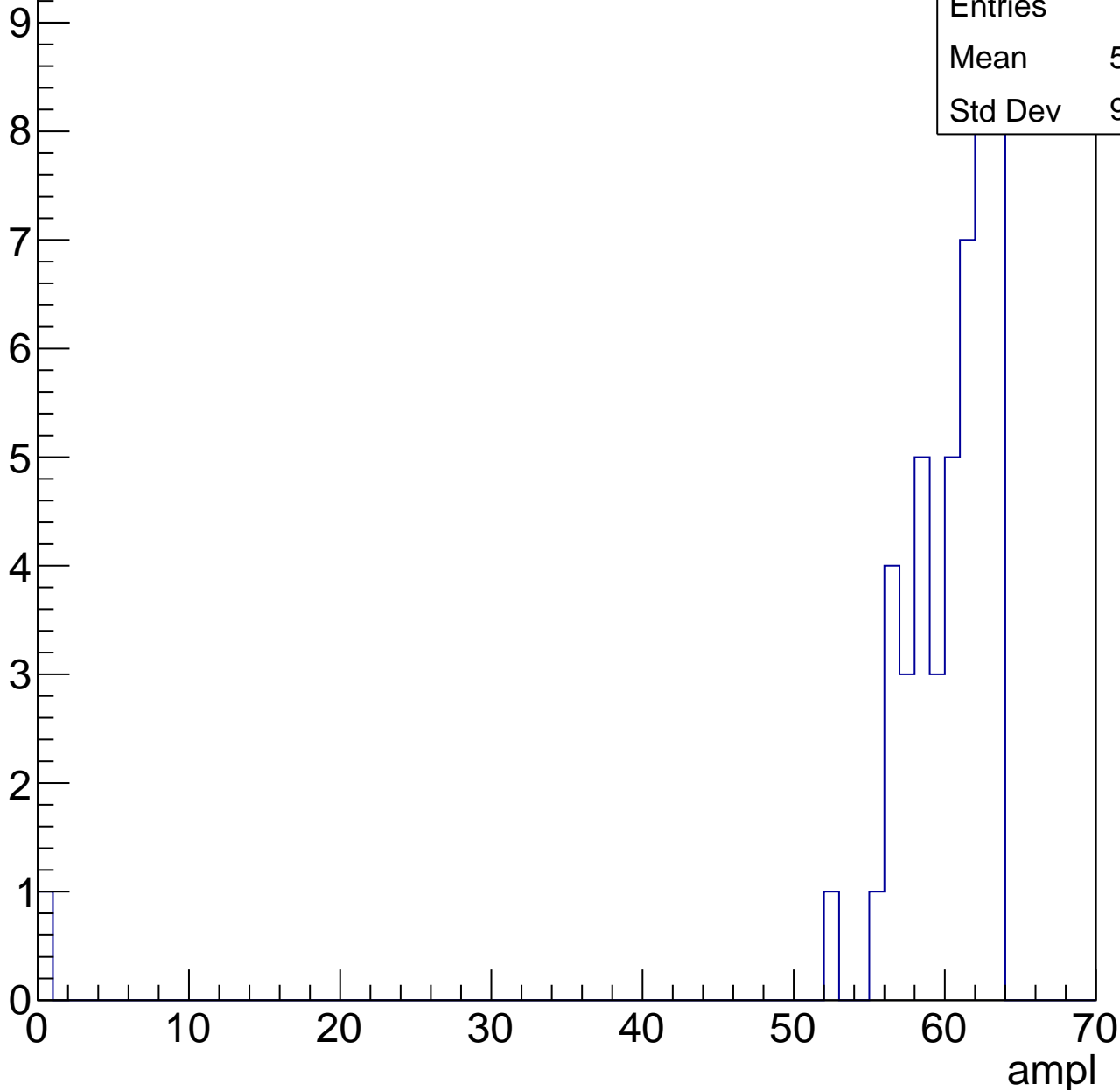


B1L103S, U6-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

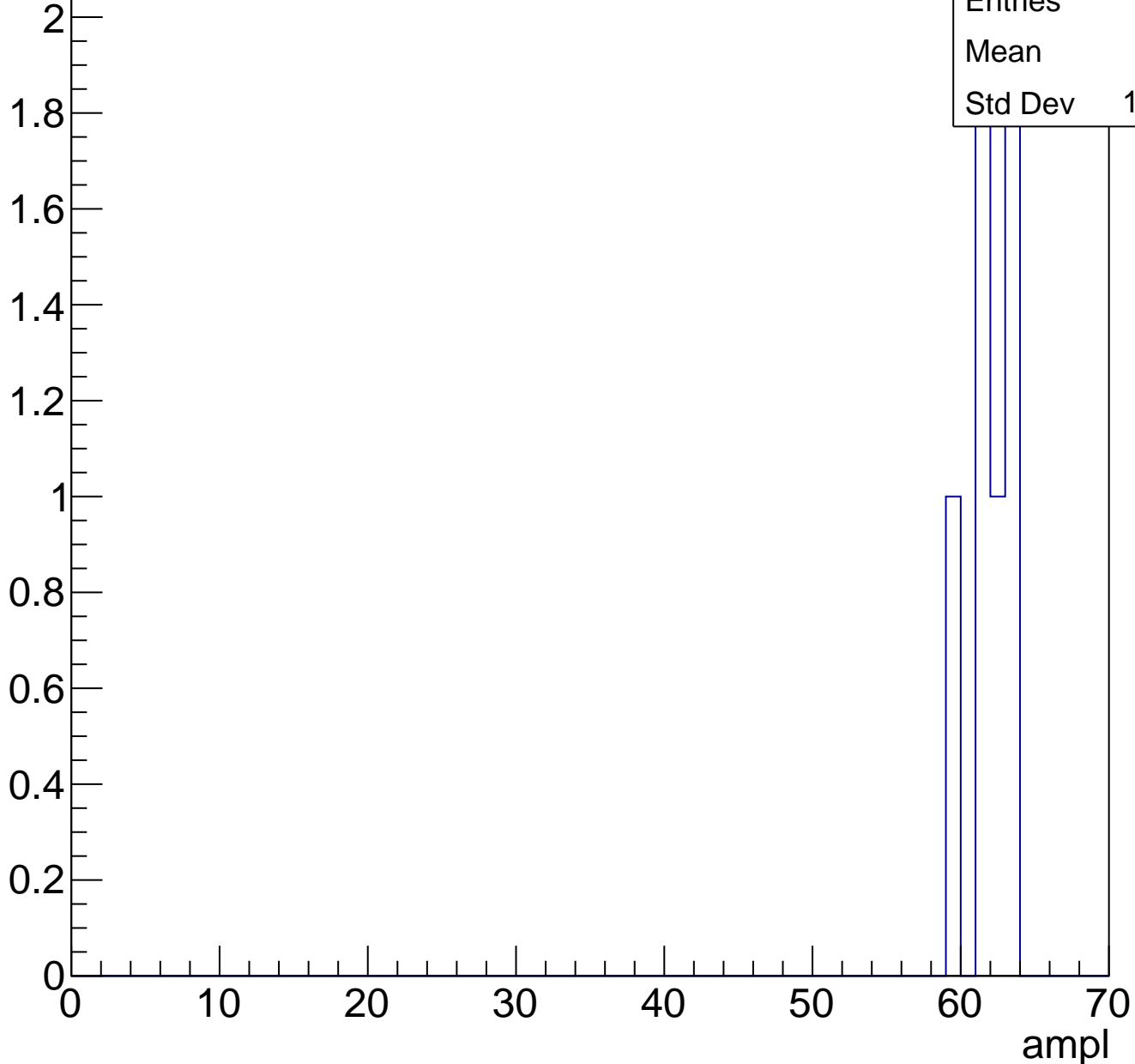
Entries	47
Mean	58.68
Std Dev	9.034



B1L103S, U6-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

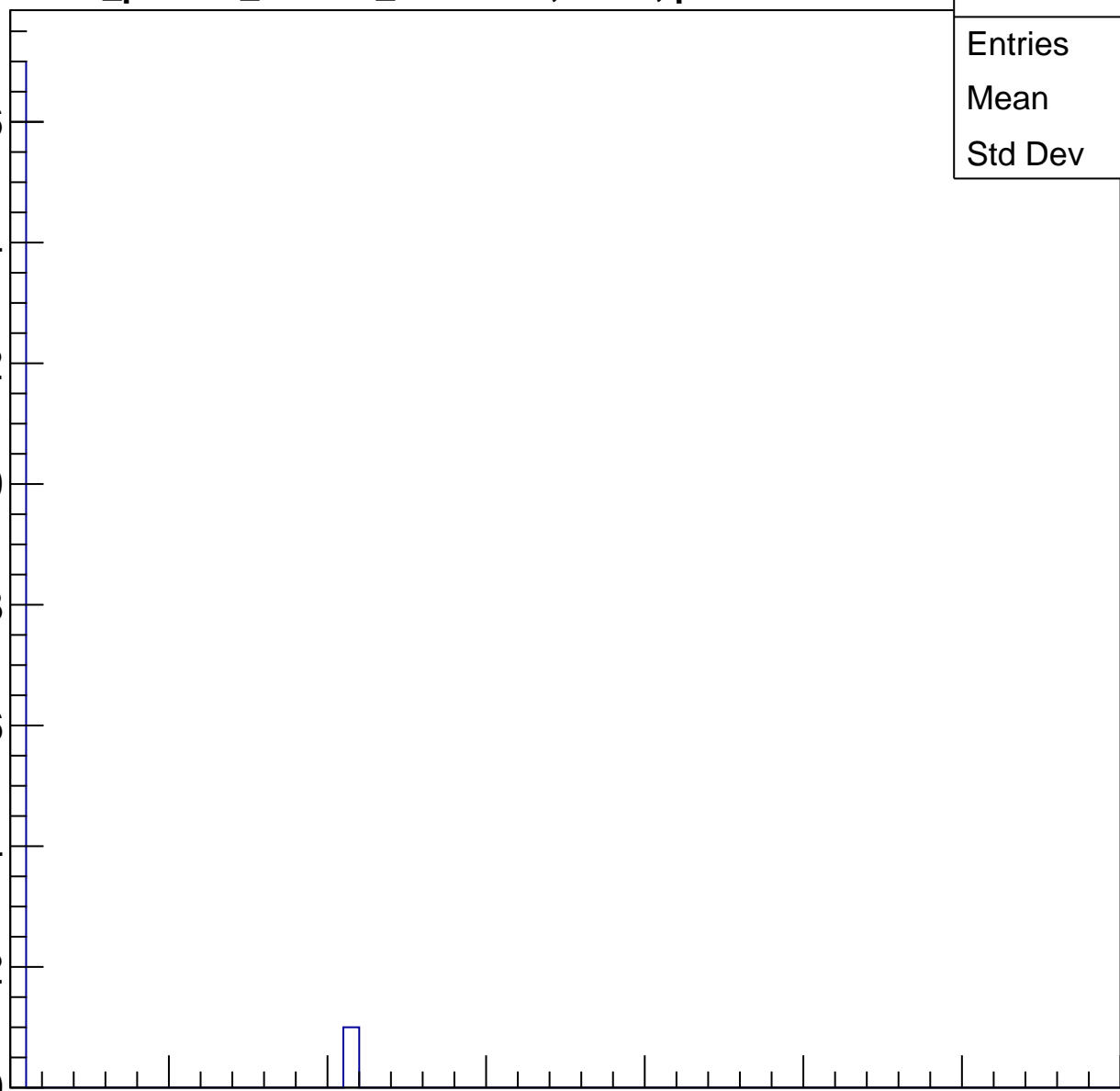
Entries	18
Mean	1.167
Std Dev	4.81

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch64, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	23.76
Std Dev	11.08

Gaus mean : 29.0132

Gaus Width: 4.2083

Entry

10

8

6

4

2

0

0

10

20

30

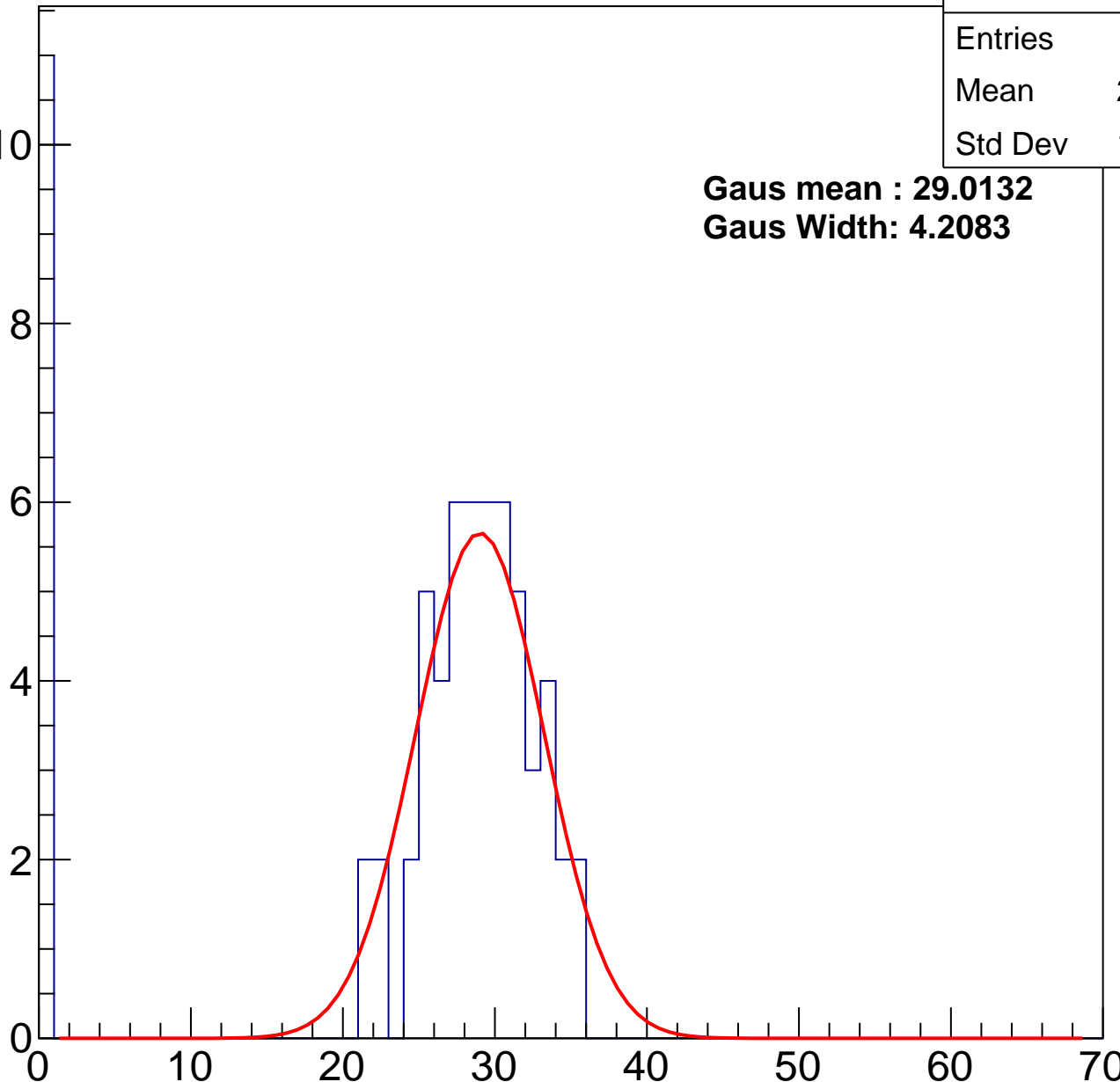
40

50

60

70

ampl



B1L103S, U6-ch64, adc1

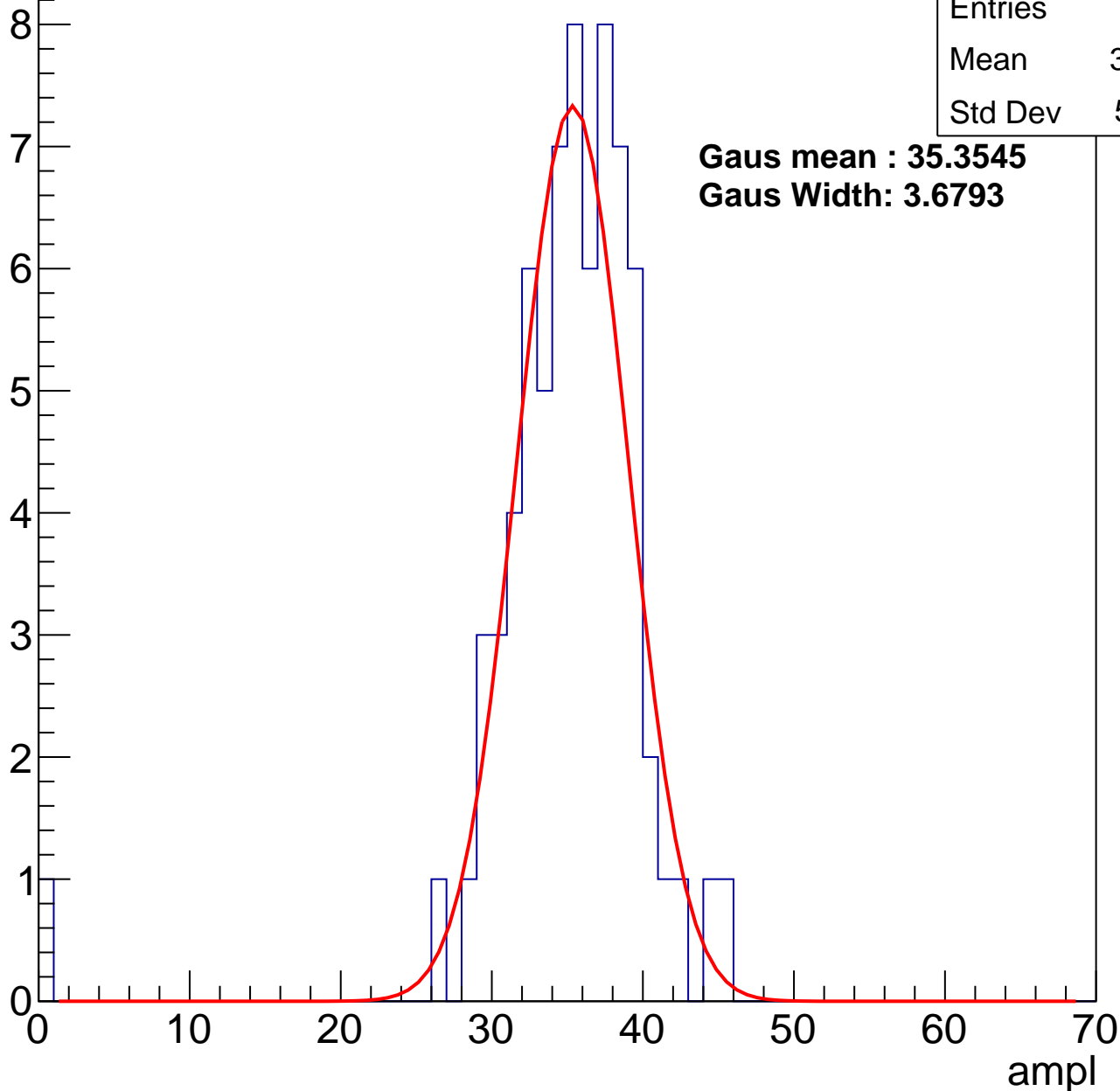
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	34.64
Std Dev	5.511

Gaus mean : 35.3545

Gaus Width: 3.6793



B1L103S, U6-ch64, adc2

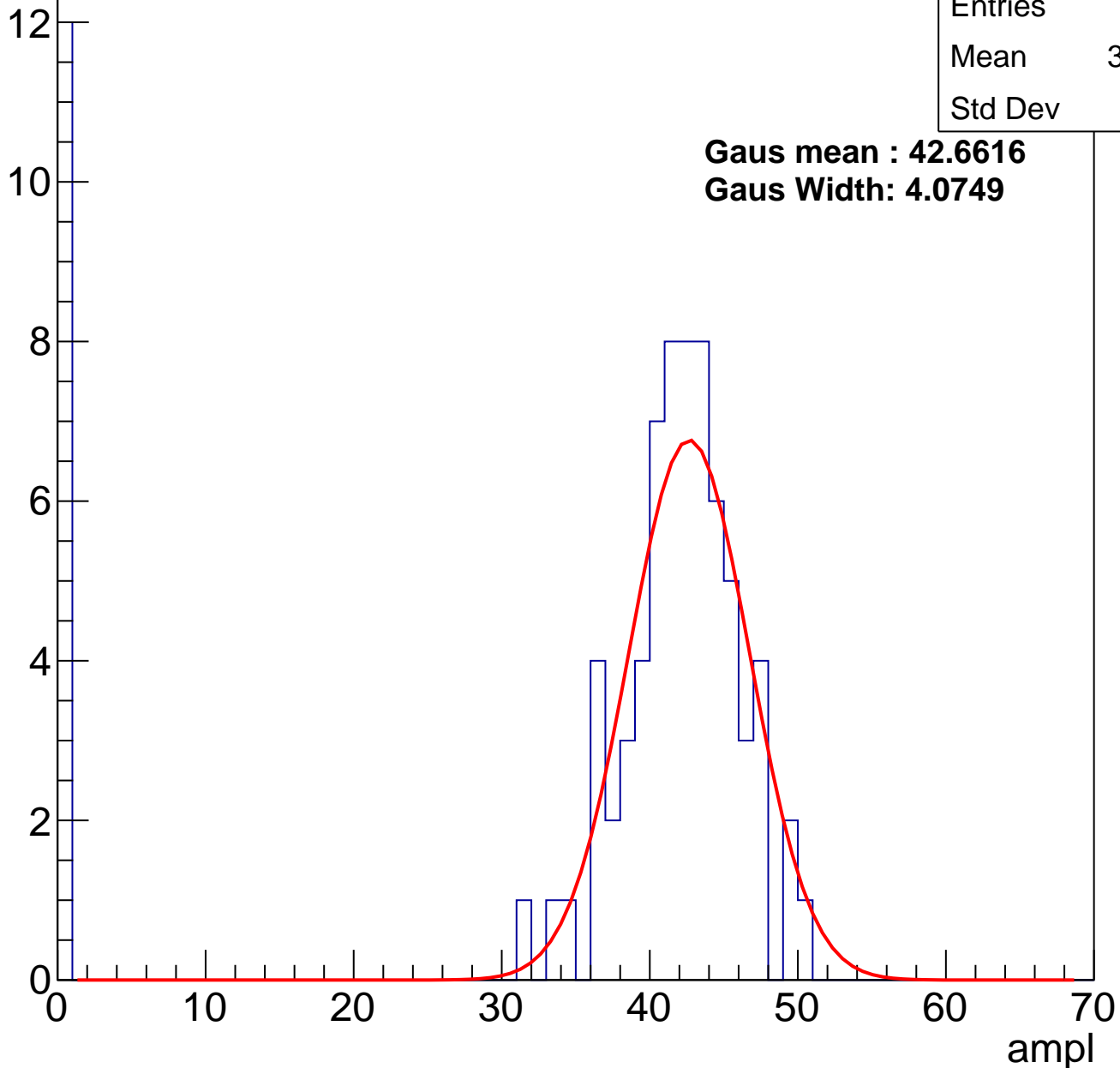
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	35.46
Std Dev	15.3

Gaus mean : 42.6616

Gaus Width: 4.0749

Entry

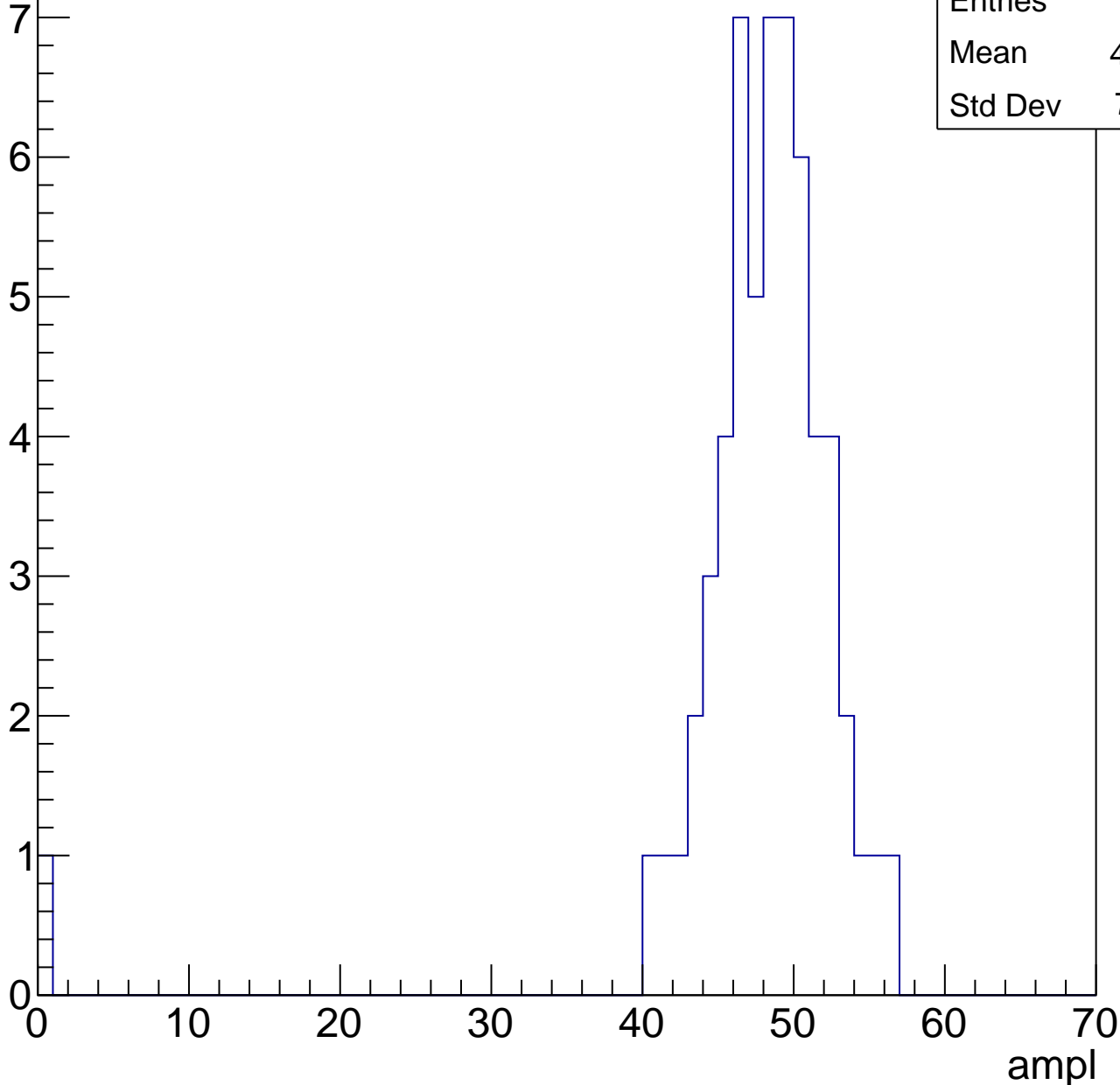


B1L103S, U6-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

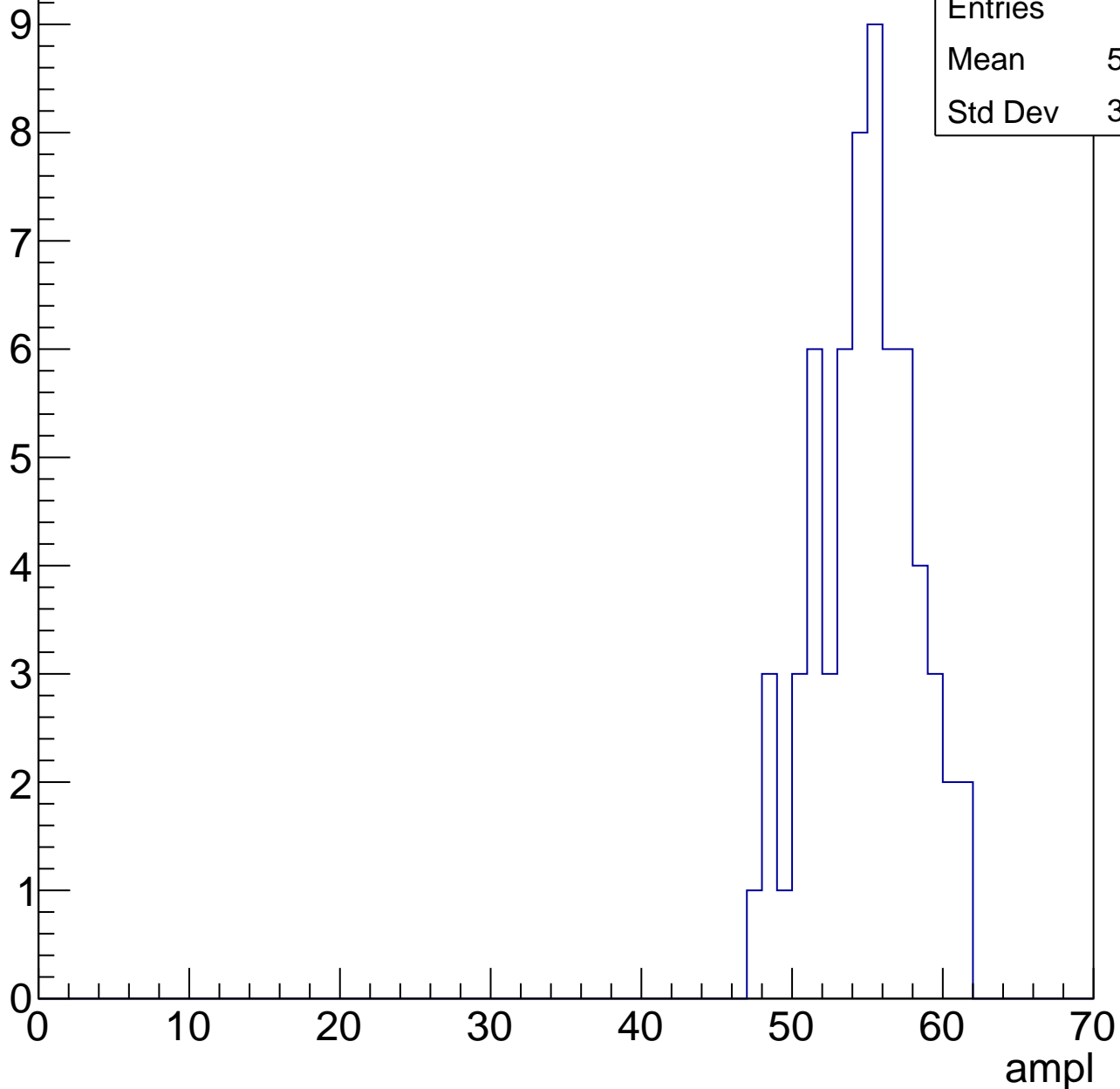
Entries	58
Mean	47.24
Std Dev	7.091



B1L103S, U6-ch64, adc4

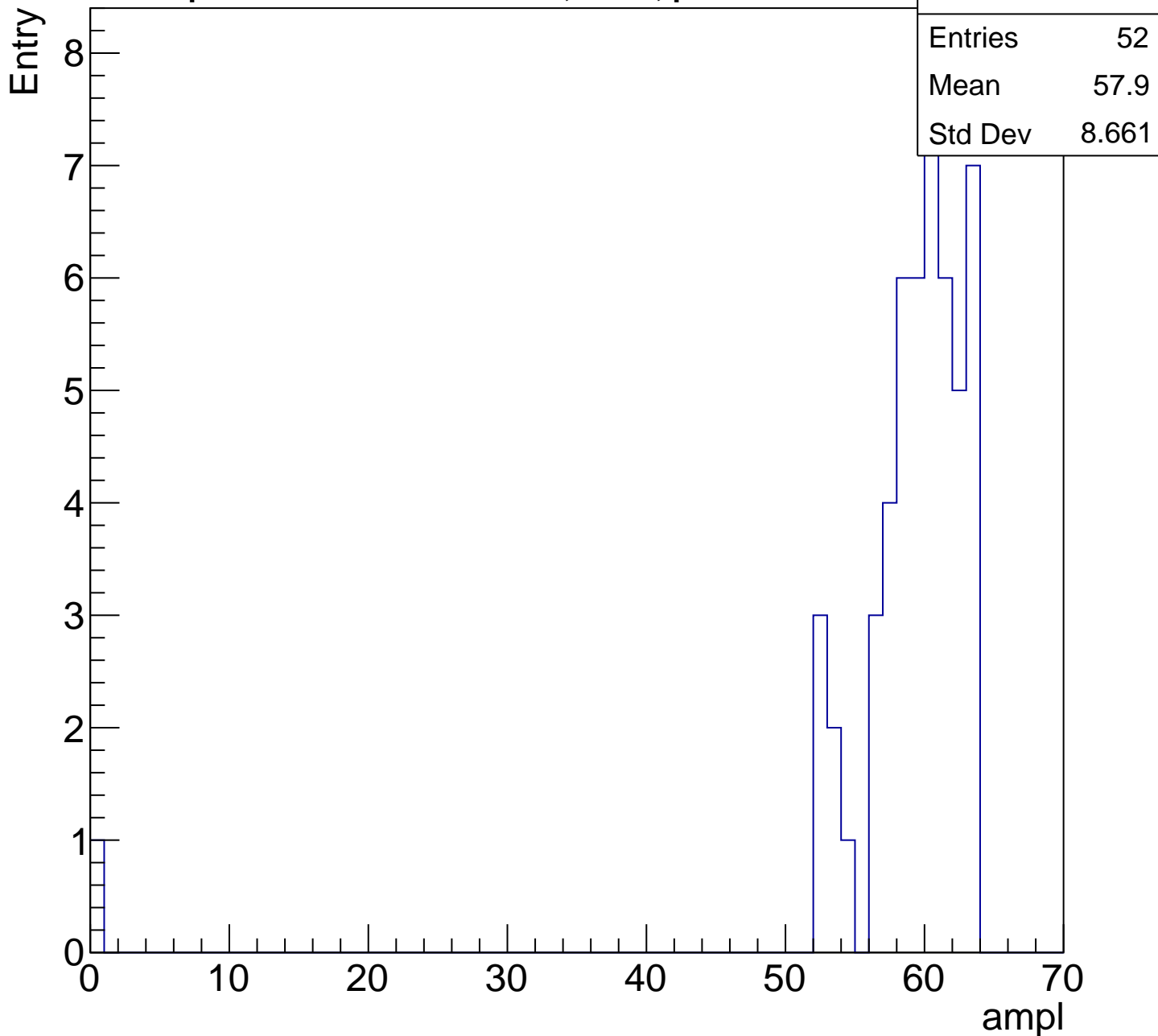
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

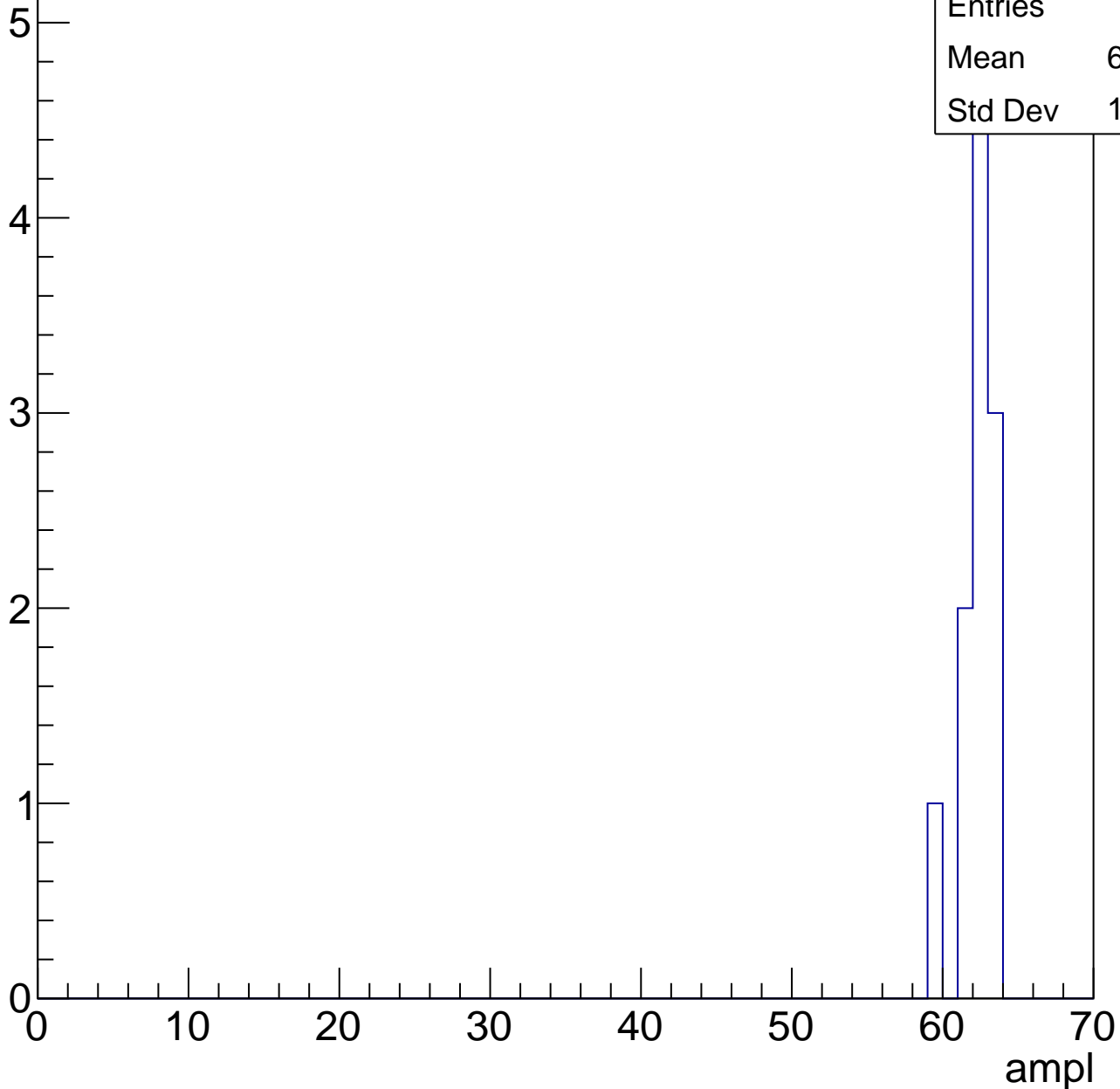


B1L103S, U6-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

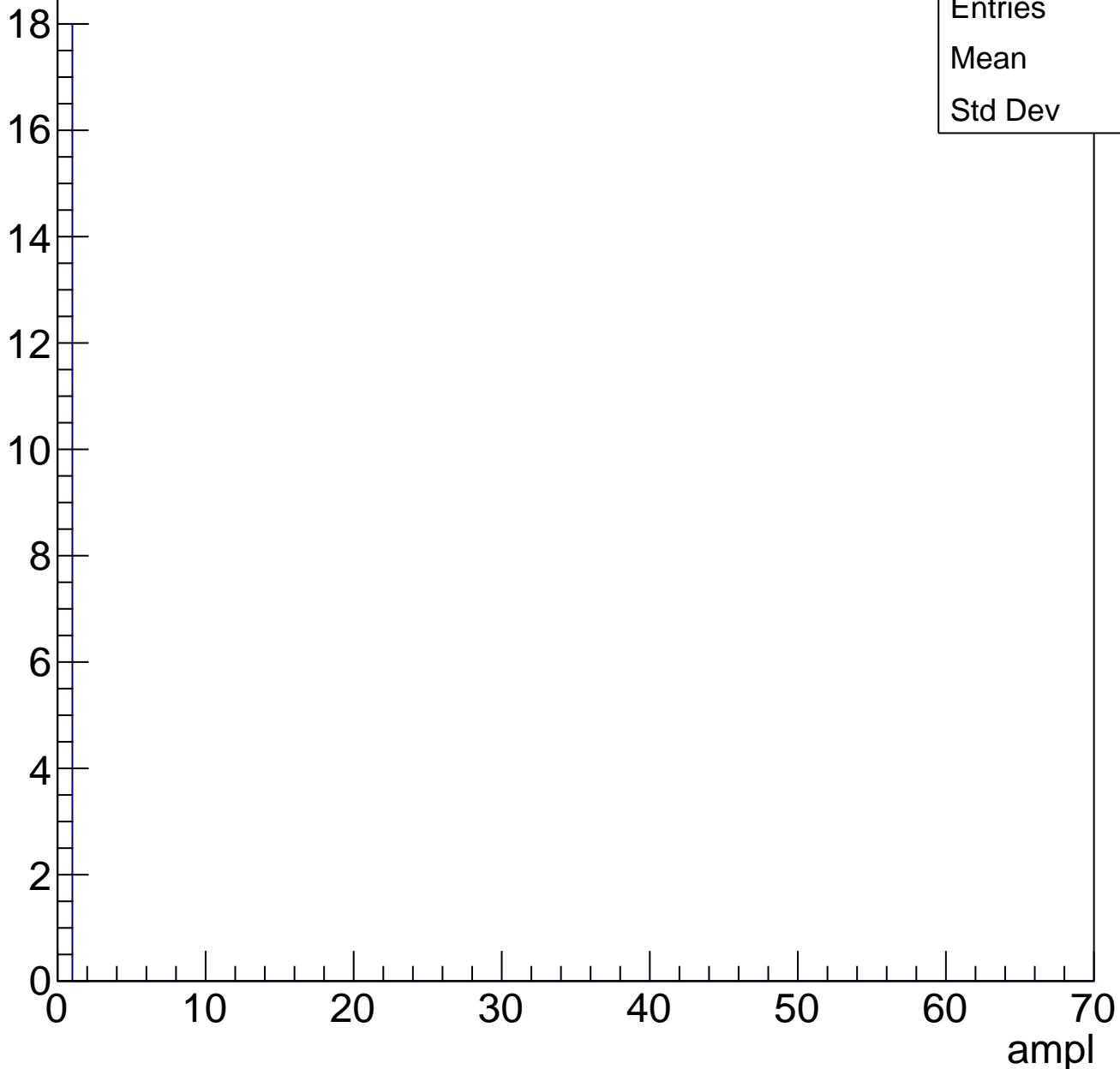
Entries	11
Mean	61.82
Std Dev	1.113



B1L103S, U6-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U6-ch65, adc0

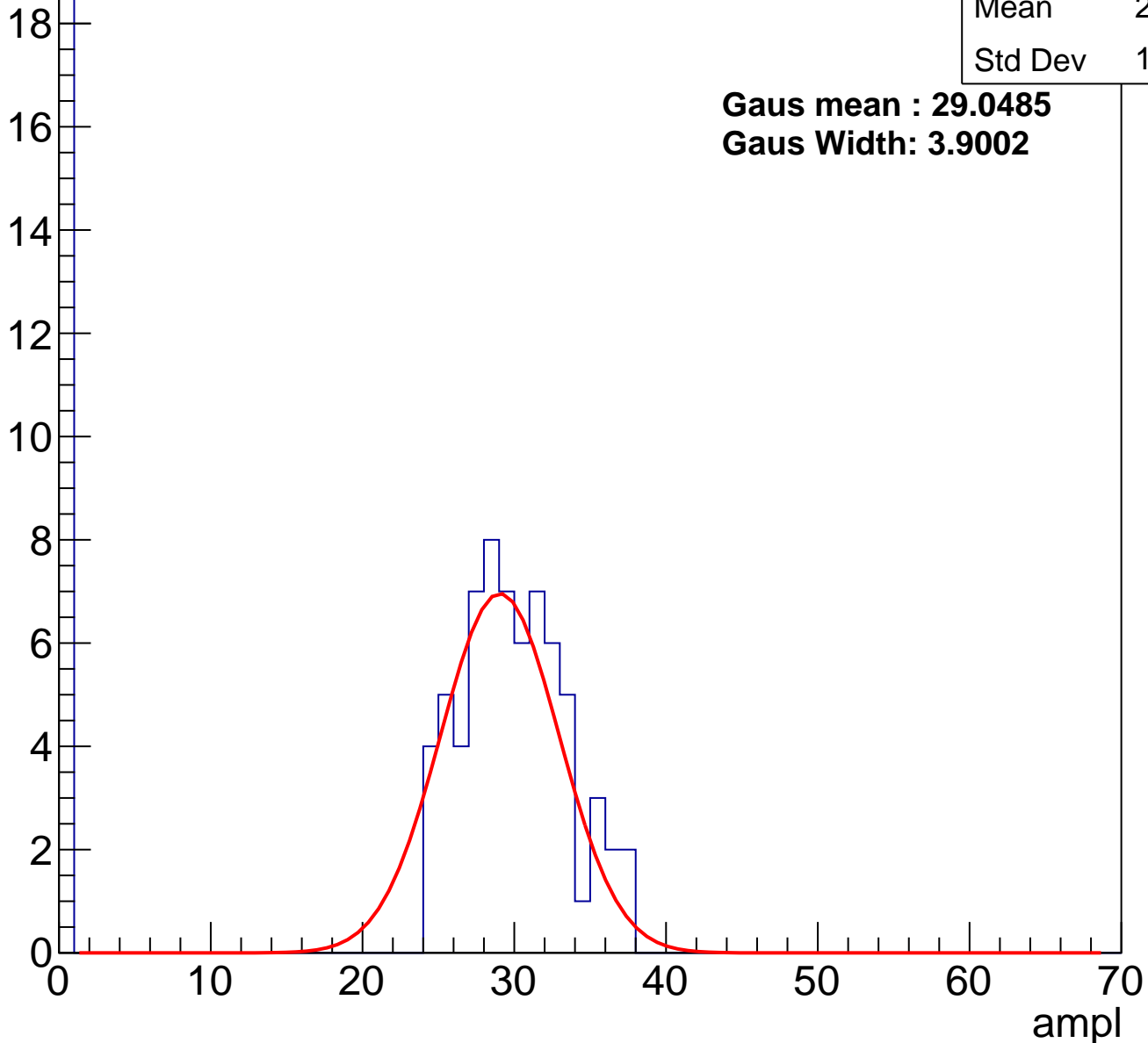
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.02
Std Dev	12.62

Gaus mean : 29.0485

Gaus Width: 3.9002

Entry



B1L103S, U6-ch65, adc1

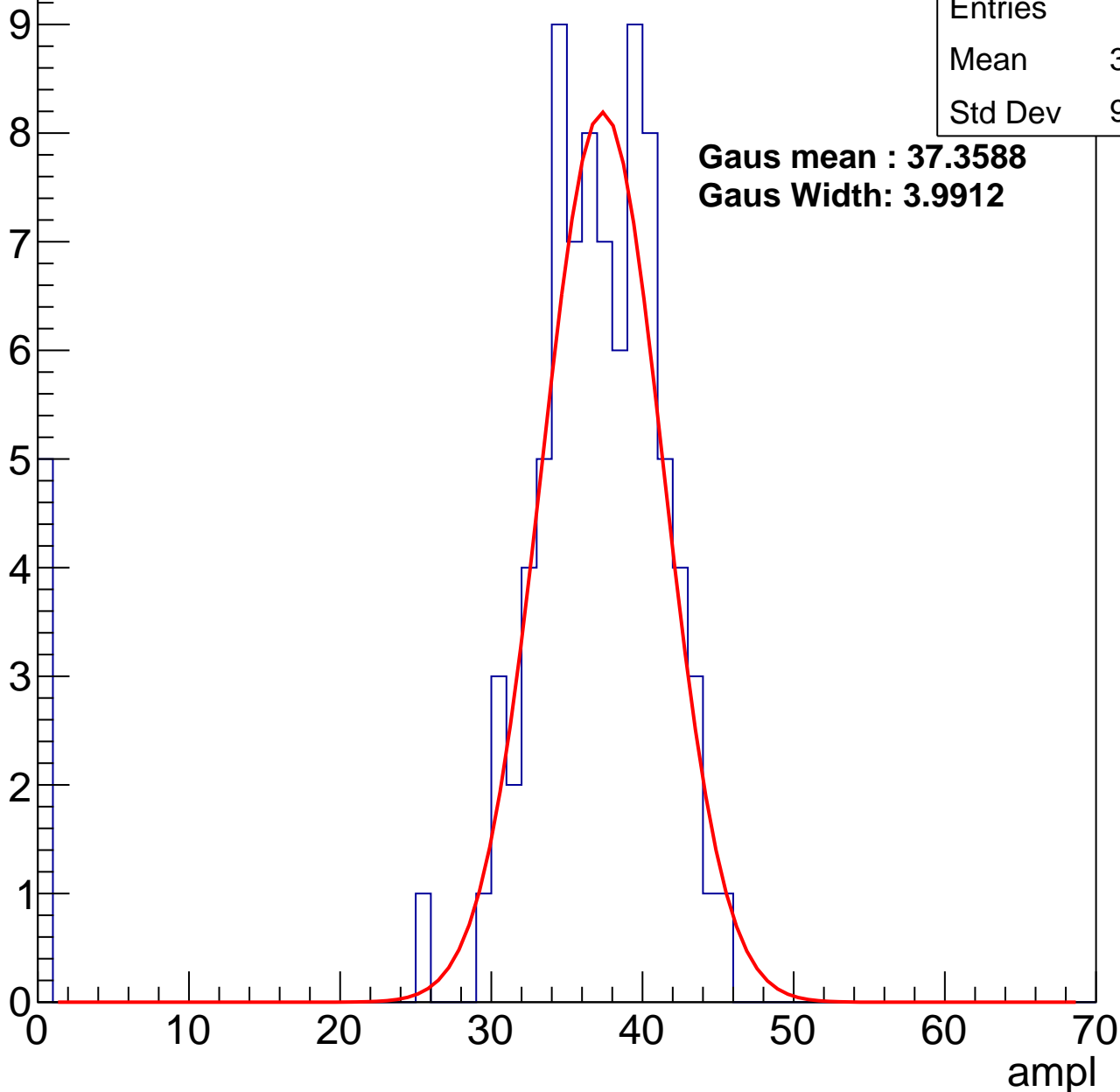
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	34.69
Std Dev	9.245

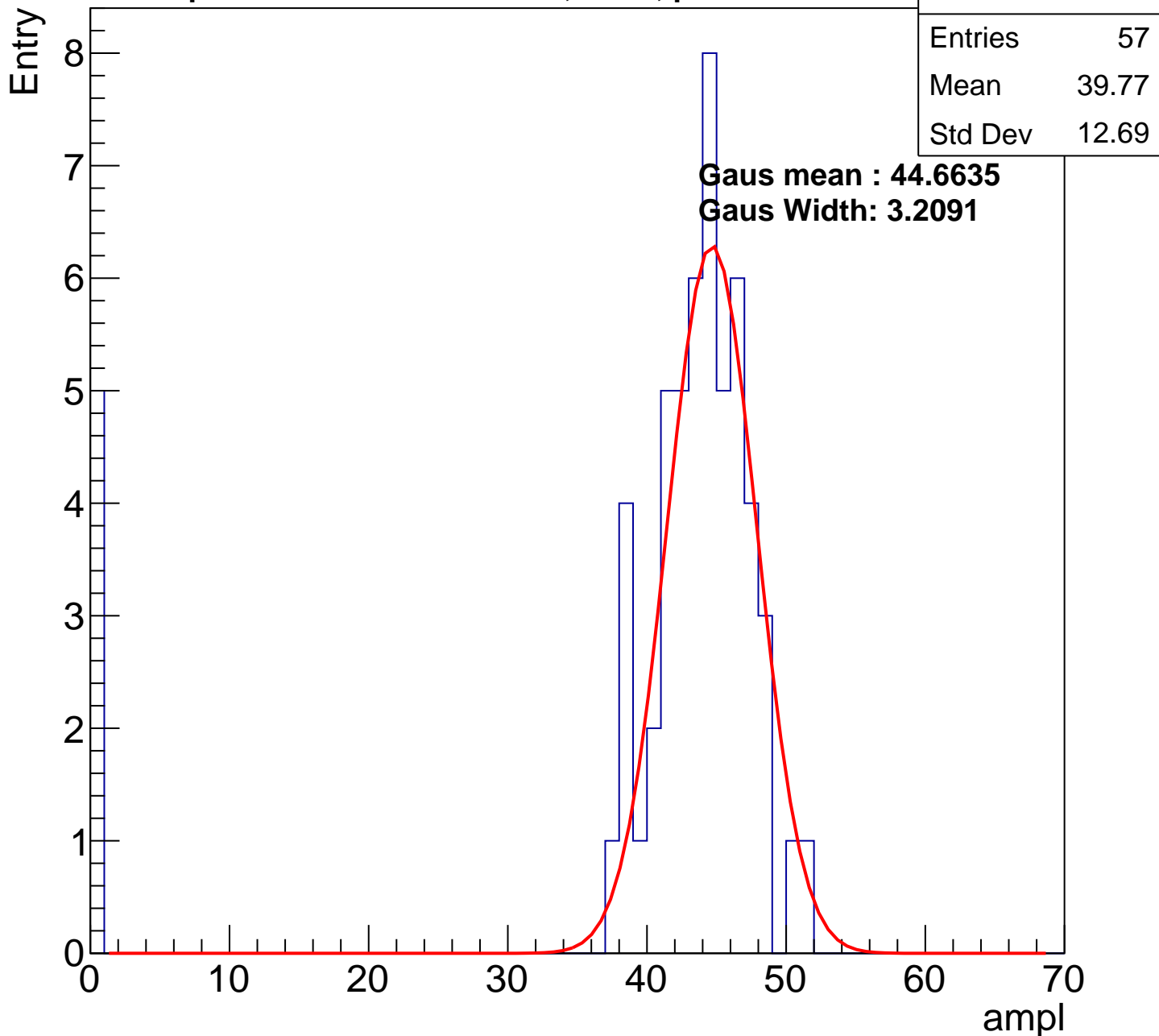
Gaus mean : 37.3588

Gaus Width: 3.9912



B1L103S, U6-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

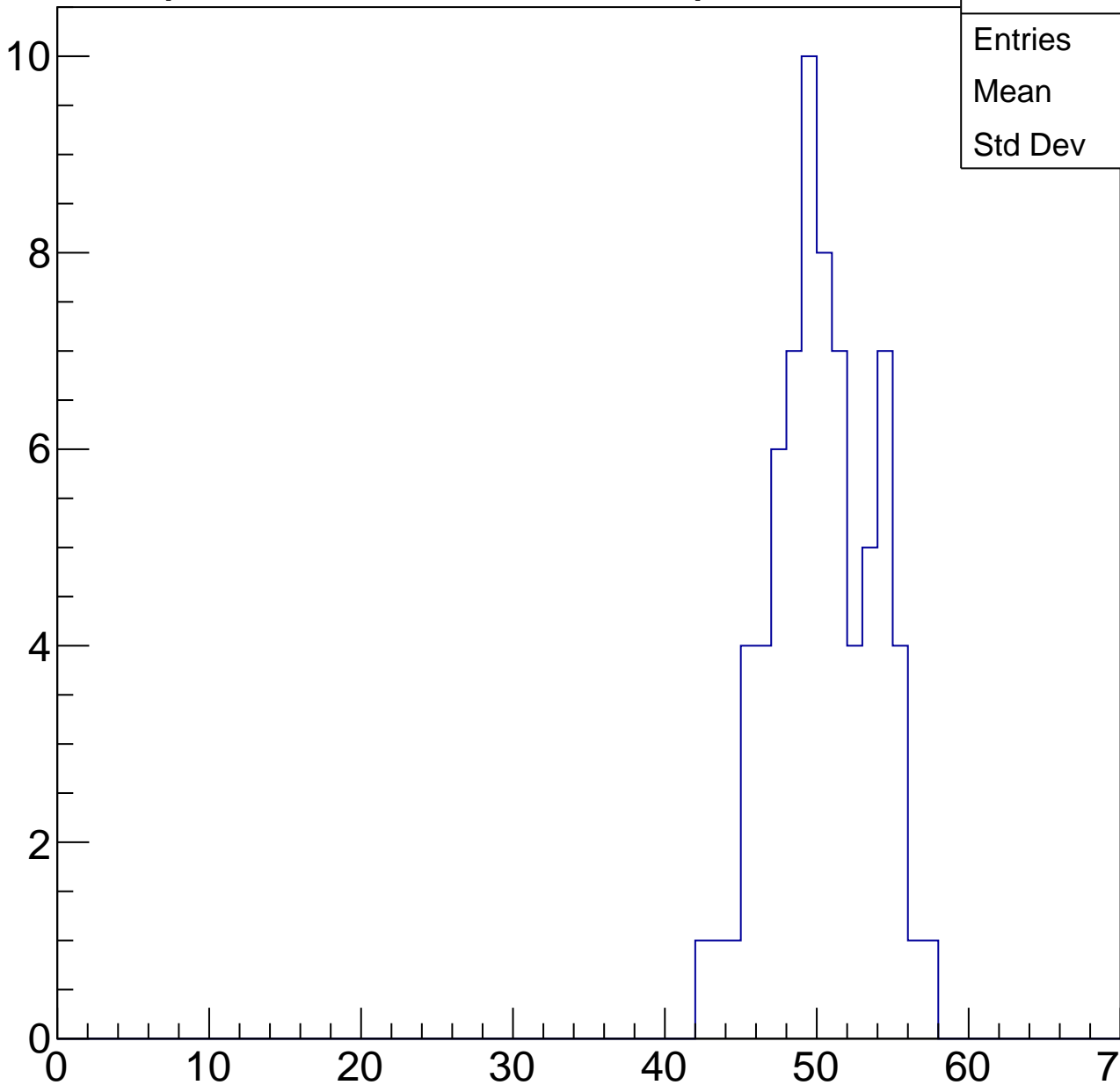
Entries	71
Mean	49.89
Std Dev	3.291

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

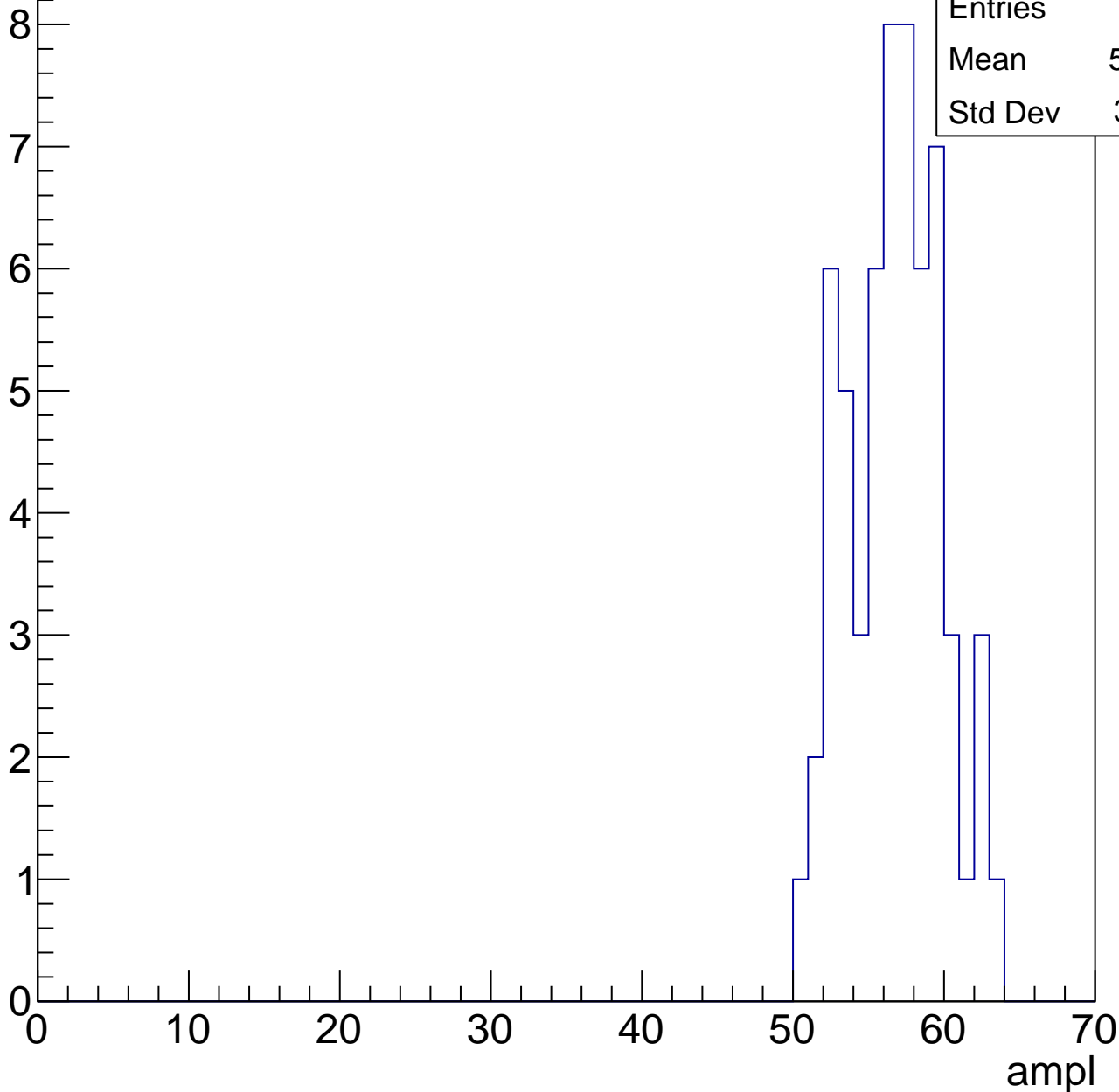


B1L103S, U6-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.27
Std Dev	3.071

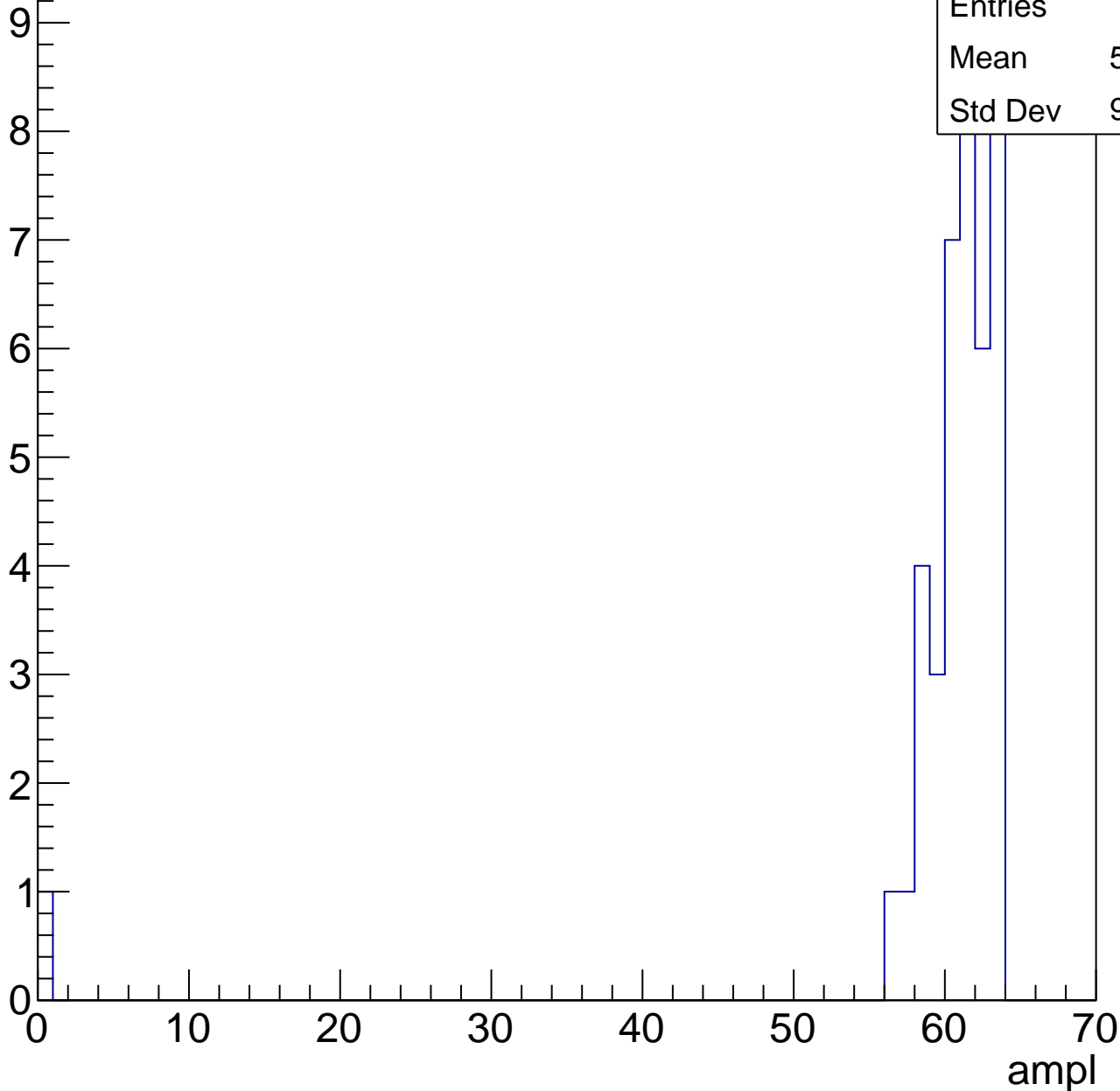


B1L103S, U6-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.17
Std Dev	9.646



B1L103S, U6-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

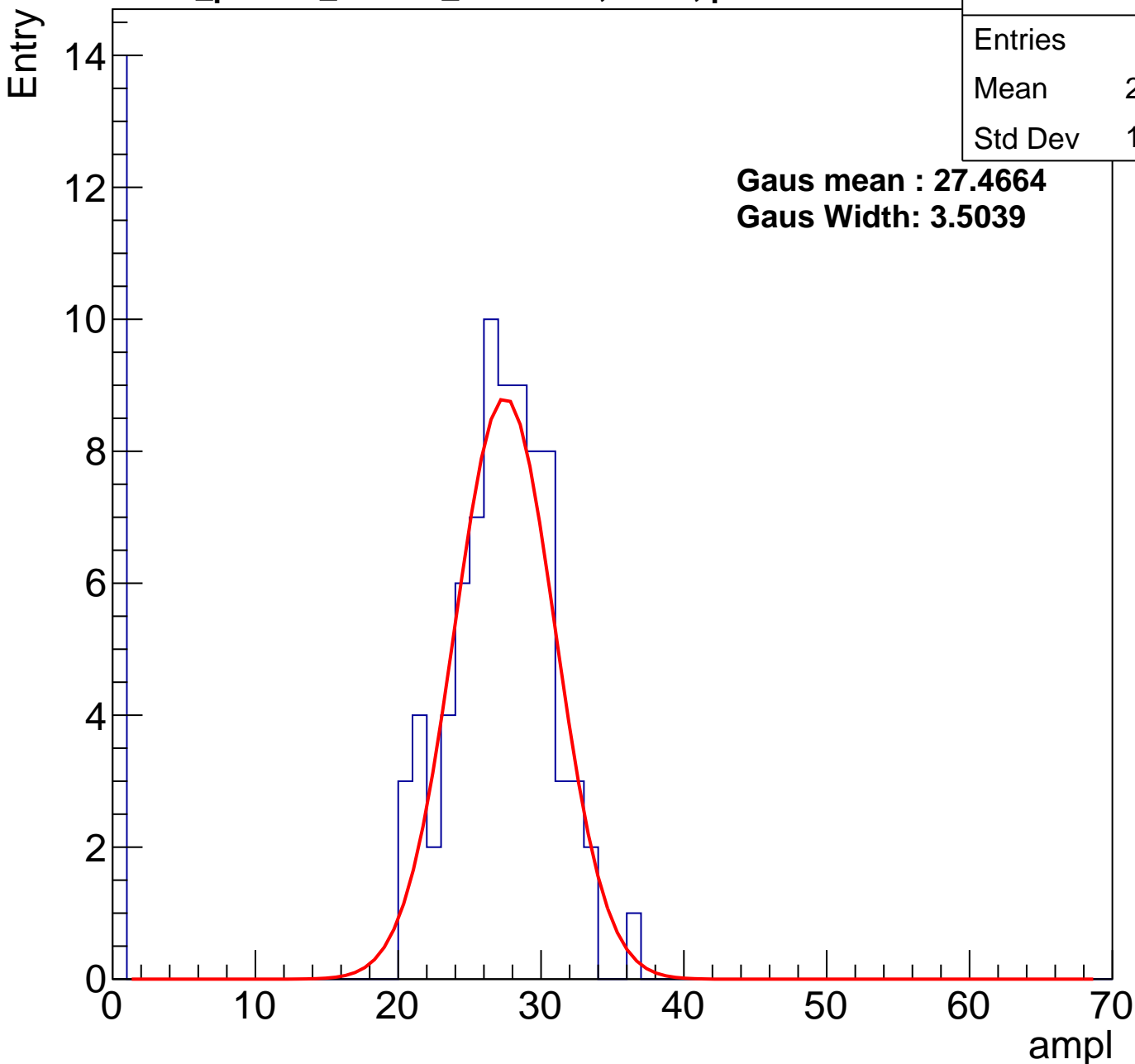
B1L103S, U6-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	22.76
Std Dev	10.07

Gaus mean : 27.4664

Gaus Width: 3.5039



B1L103S, U6-ch66, adc1

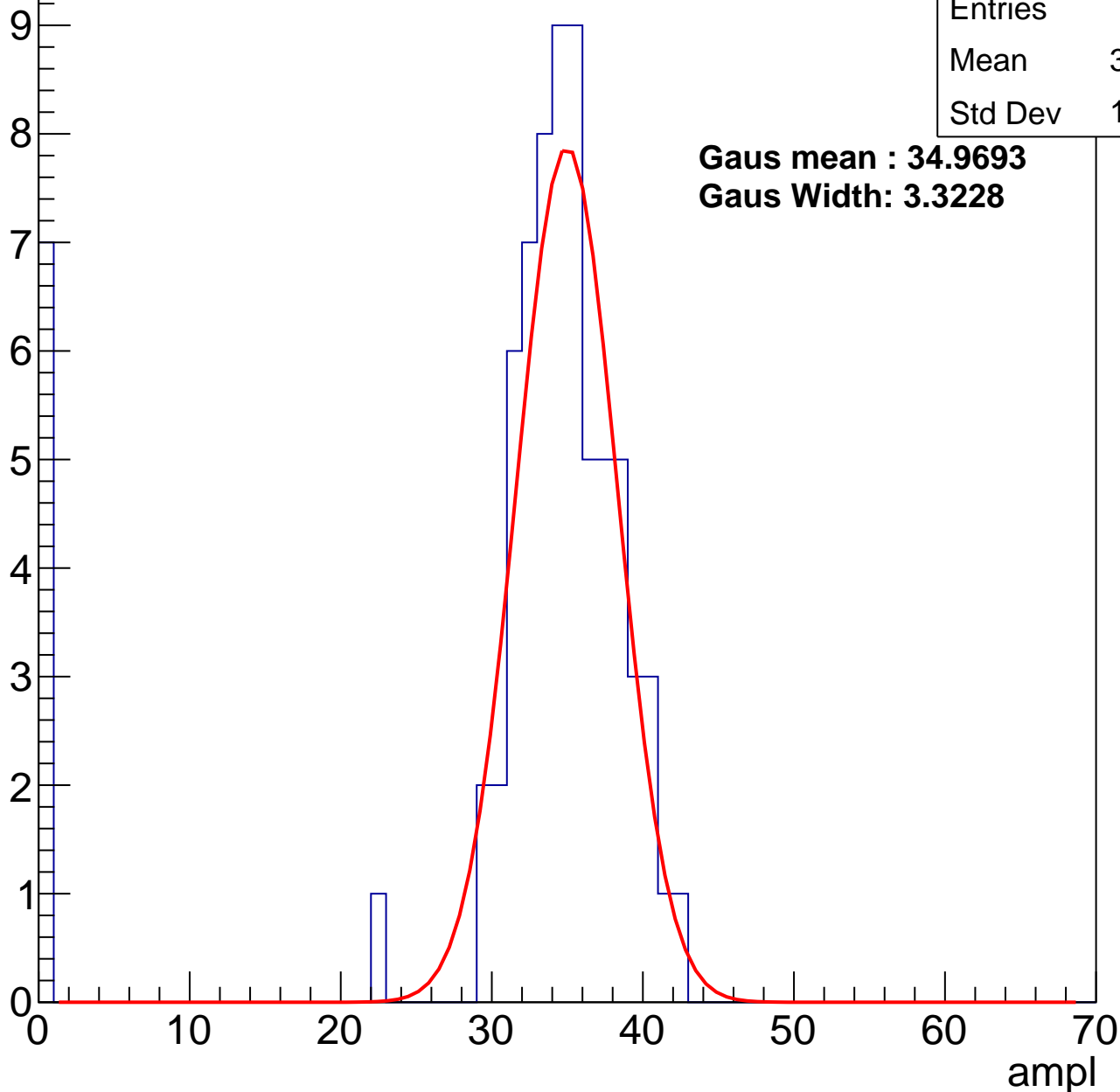
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	31.22
Std Dev	10.58

Gaus mean : 34.9693

Gaus Width: 3.3228



B1L103S, U6-ch66, adc2

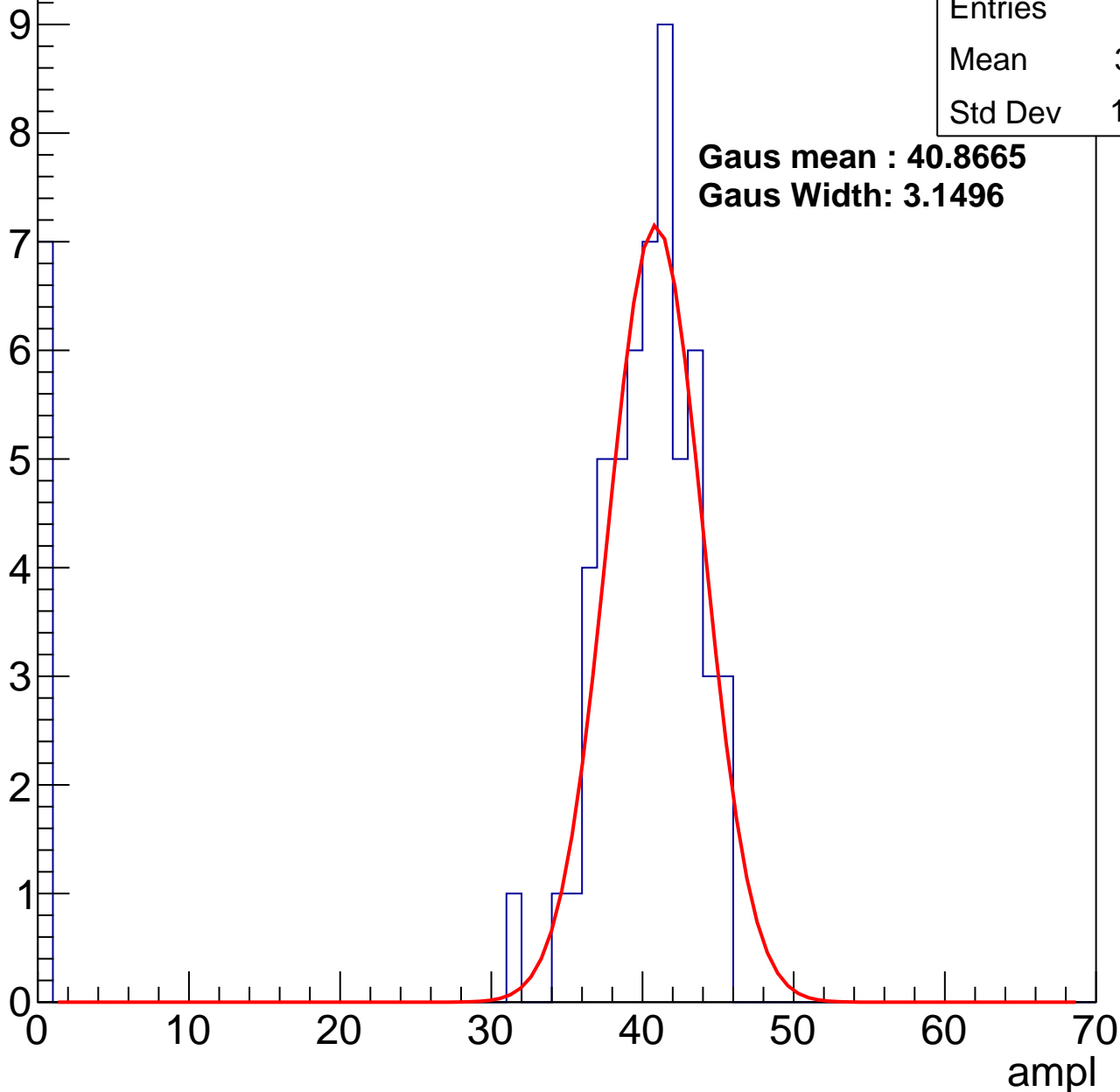
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	35.51
Std Dev	12.86

Gaus mean : 40.8665

Gaus Width: 3.1496



B1L103S, U6-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

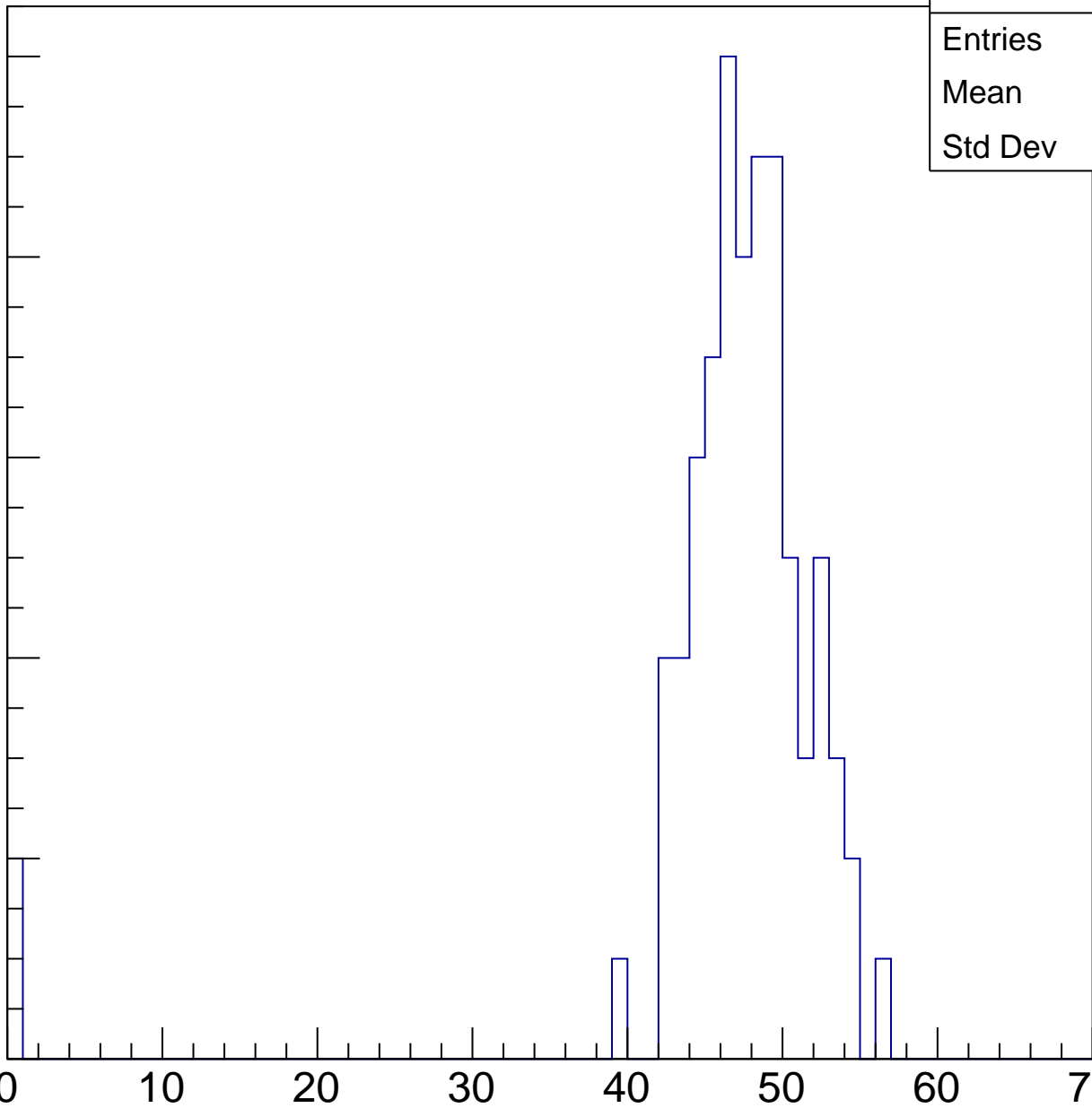
Entries	79
Mean	46.24
Std Dev	8.146

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

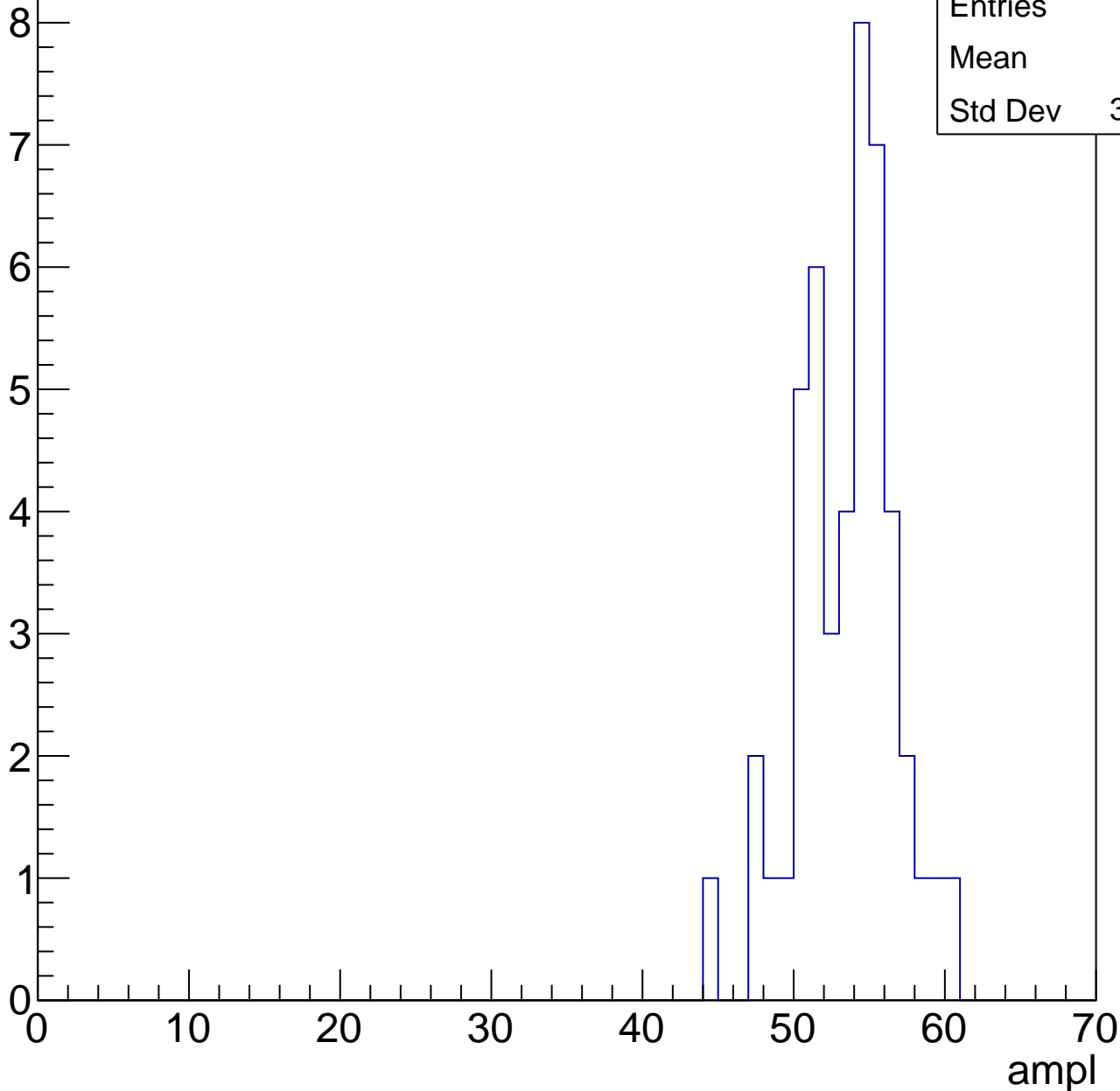


B1L103S, U6-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

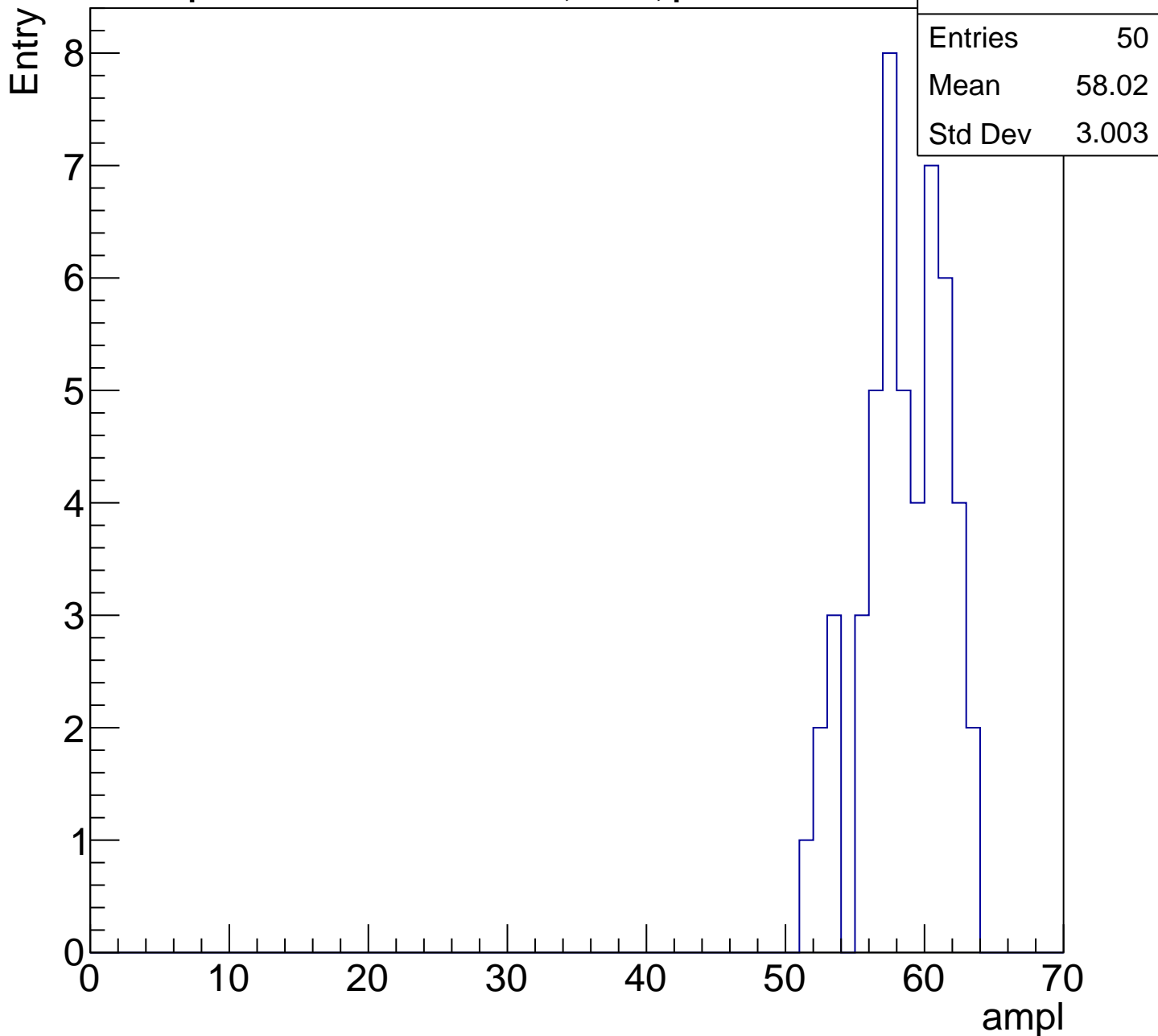
Entry

Entries	47
Mean	53
Std Dev	3.196



B1L103S, U6-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

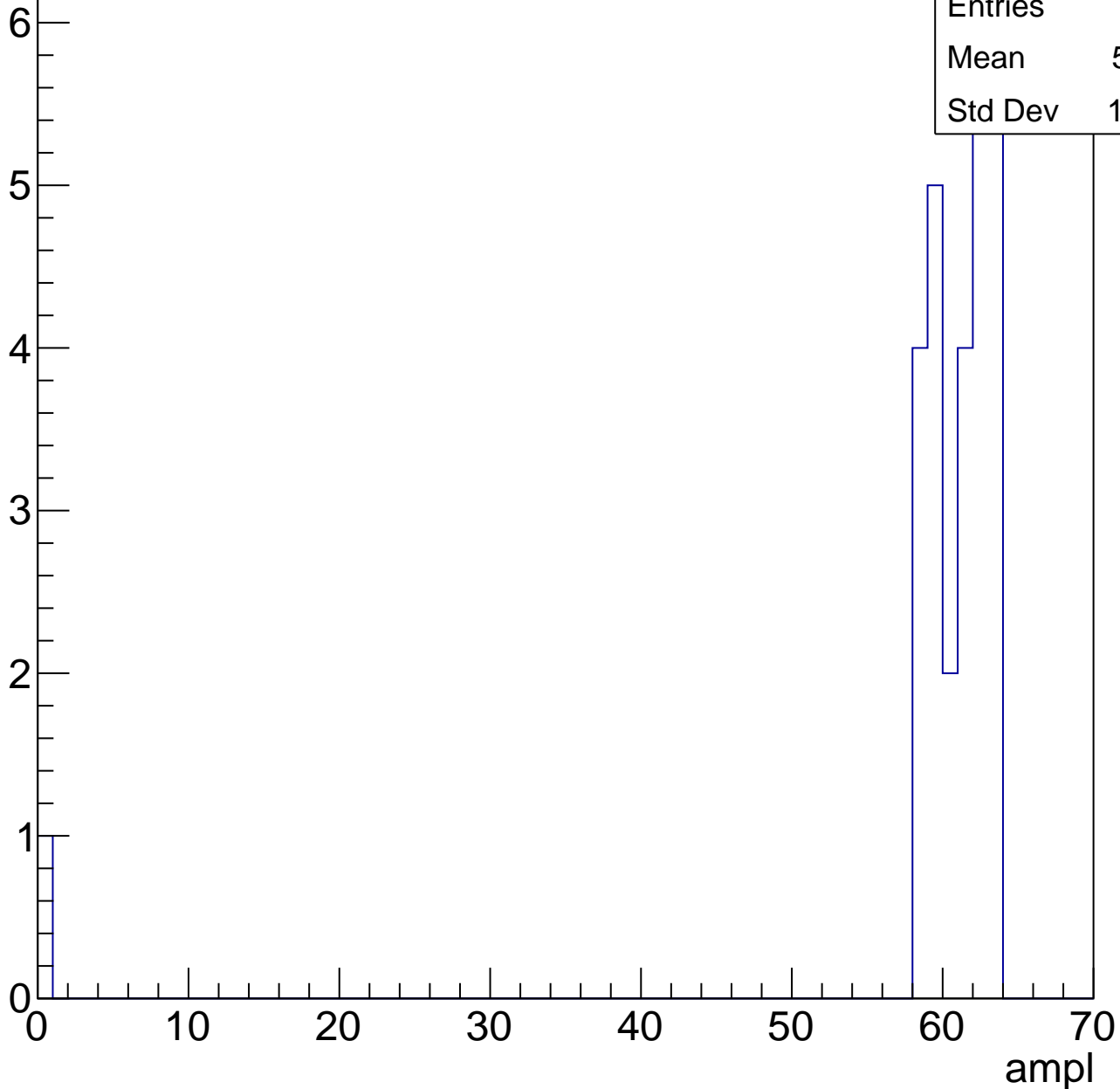


B1L103S, U6-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.61
Std Dev	11.42

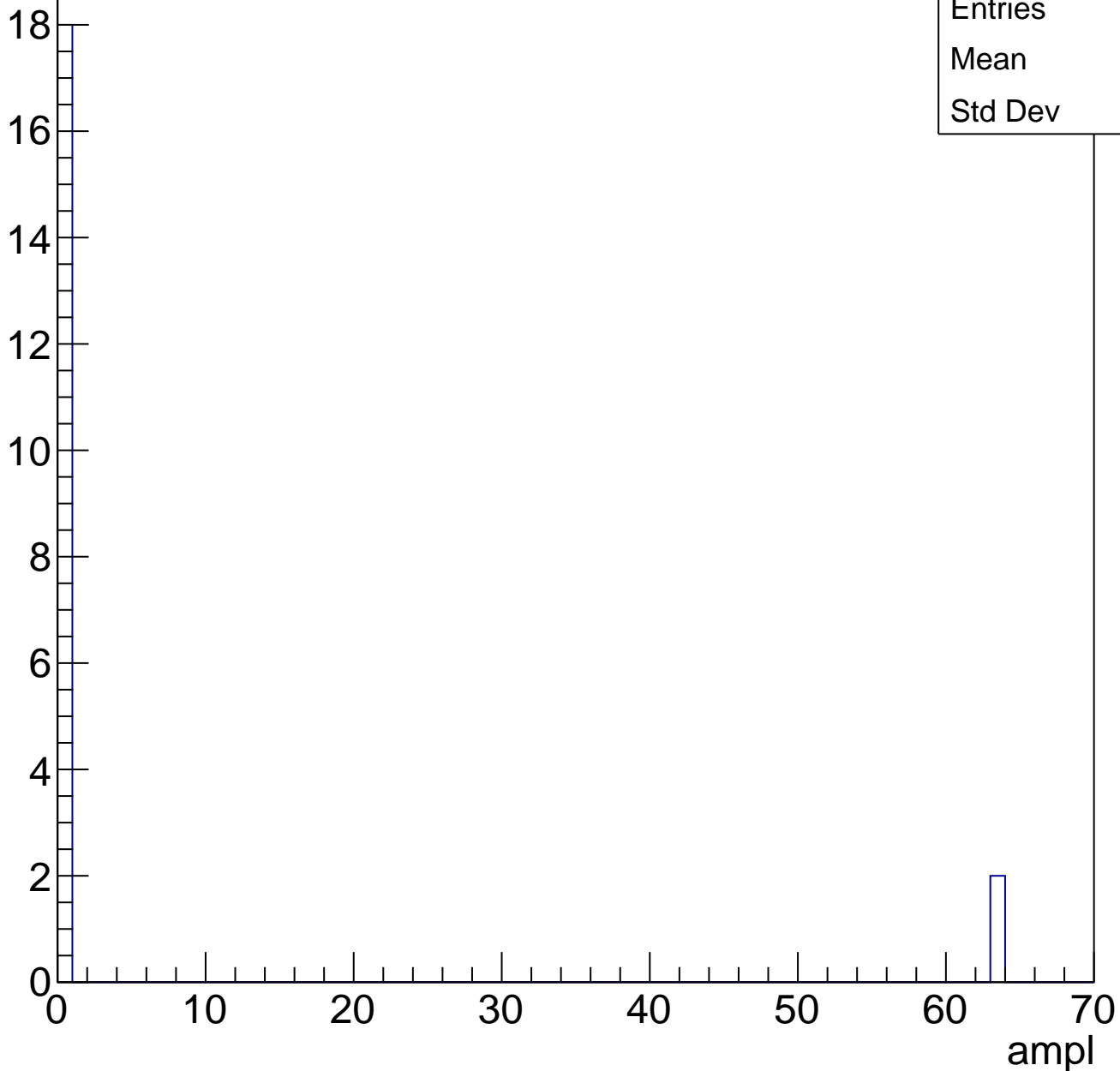


B1L103S, U6-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.3
Std Dev	18.9

Entry



B1L103S, U6-ch67, adc0

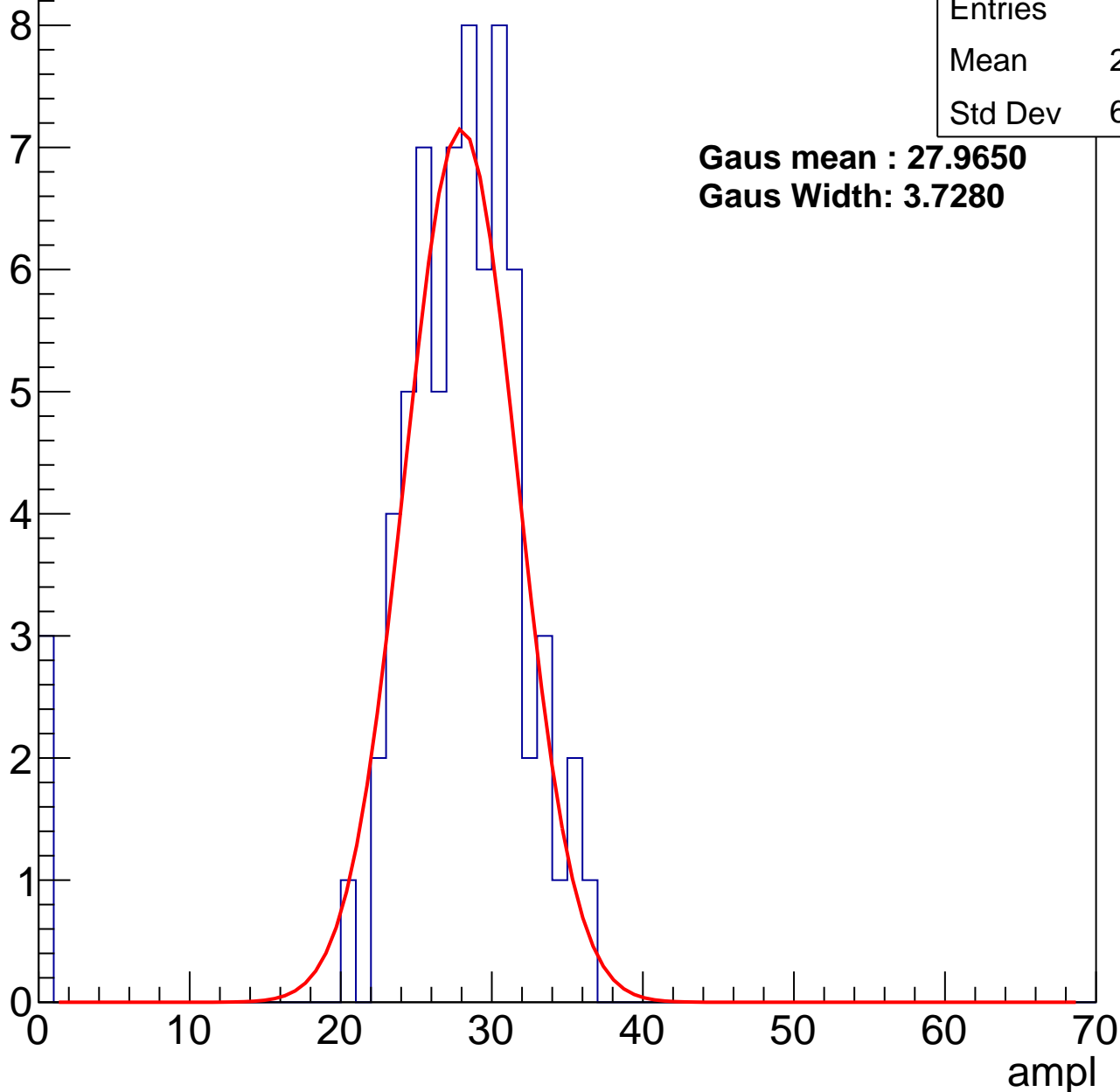
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	26.72
Std Dev	6.542

Gaus mean : 27.9650

Gaus Width: 3.7280



B1L103S, U6-ch67, adc1

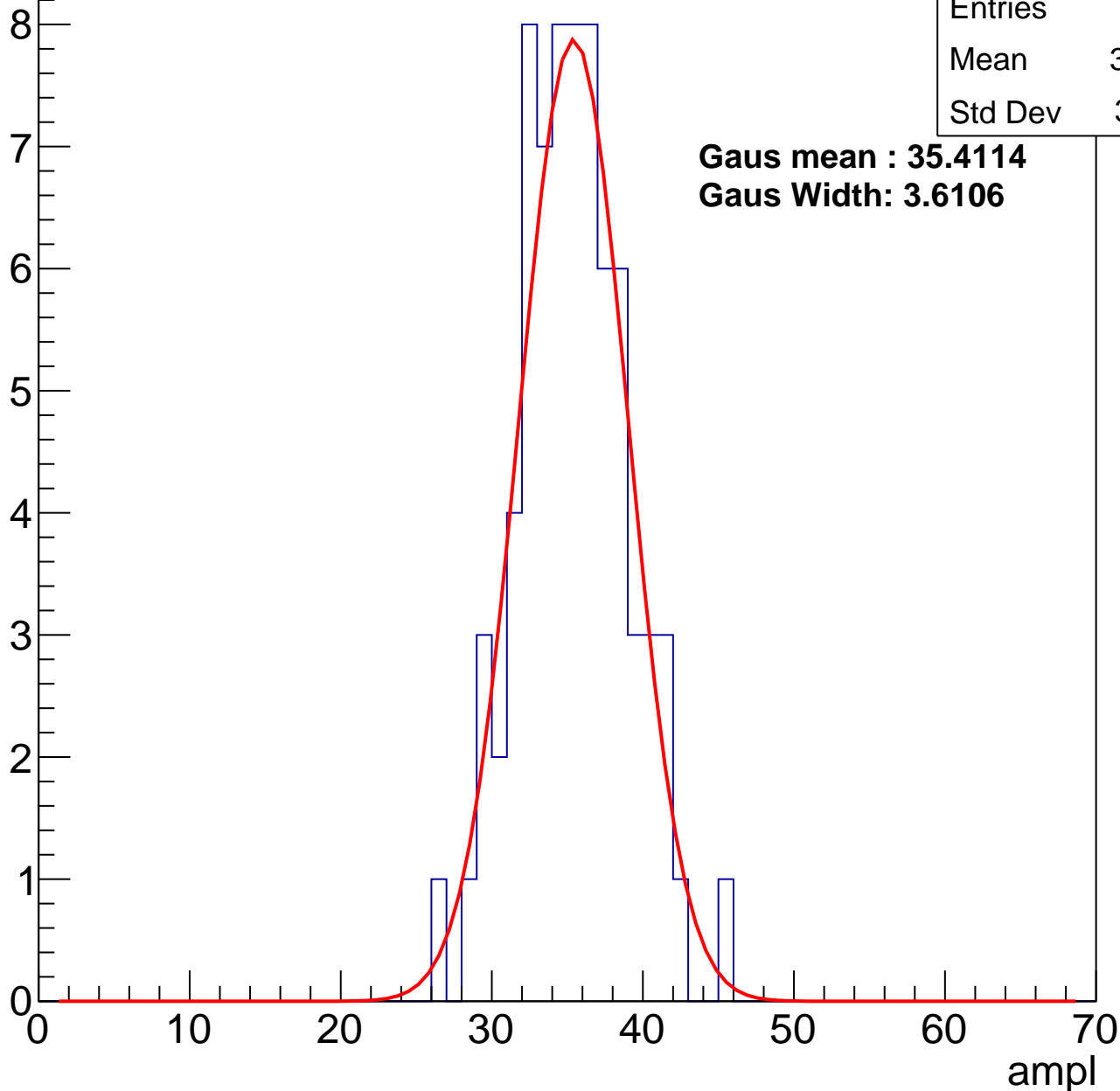
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.92
Std Dev	3.561

Gaus mean : 35.4114

Gaus Width: 3.6106



B1L103S, U6-ch67, adc2

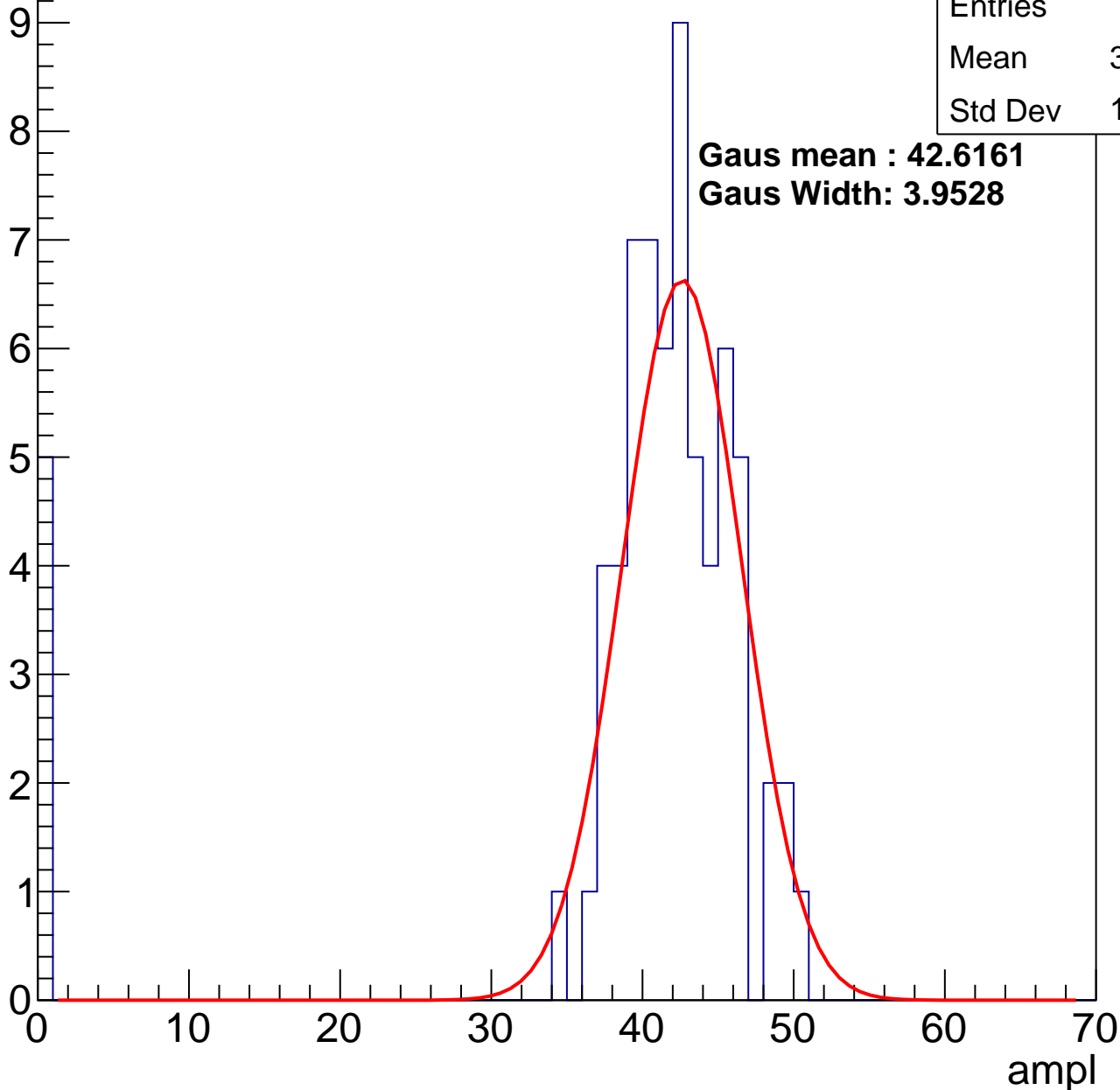
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	38.87
Std Dev	11.35

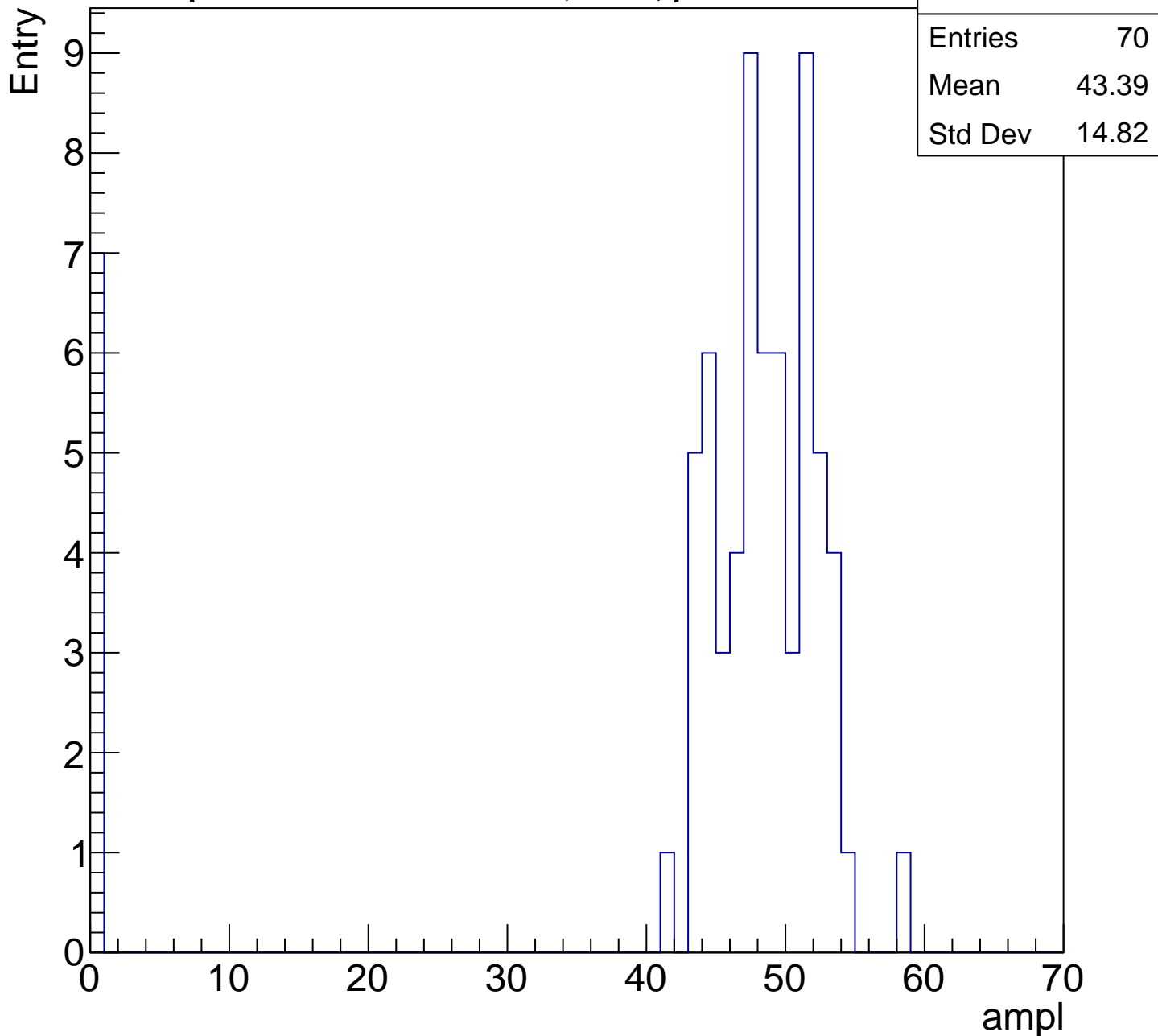
Gaus mean : 42.6161

Gaus Width: 3.9528



B1L103S, U6-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

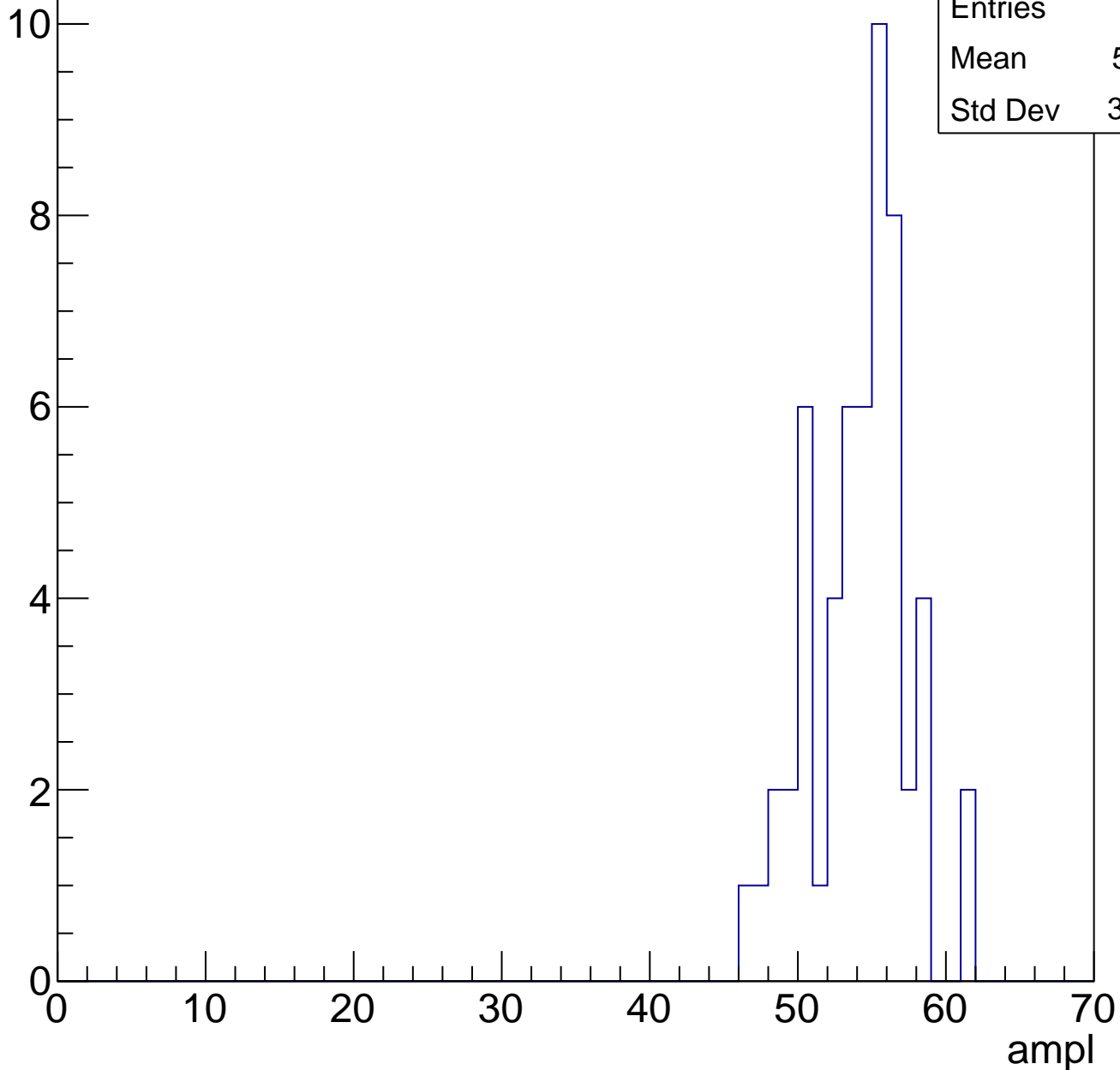


B1L103S, U6-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

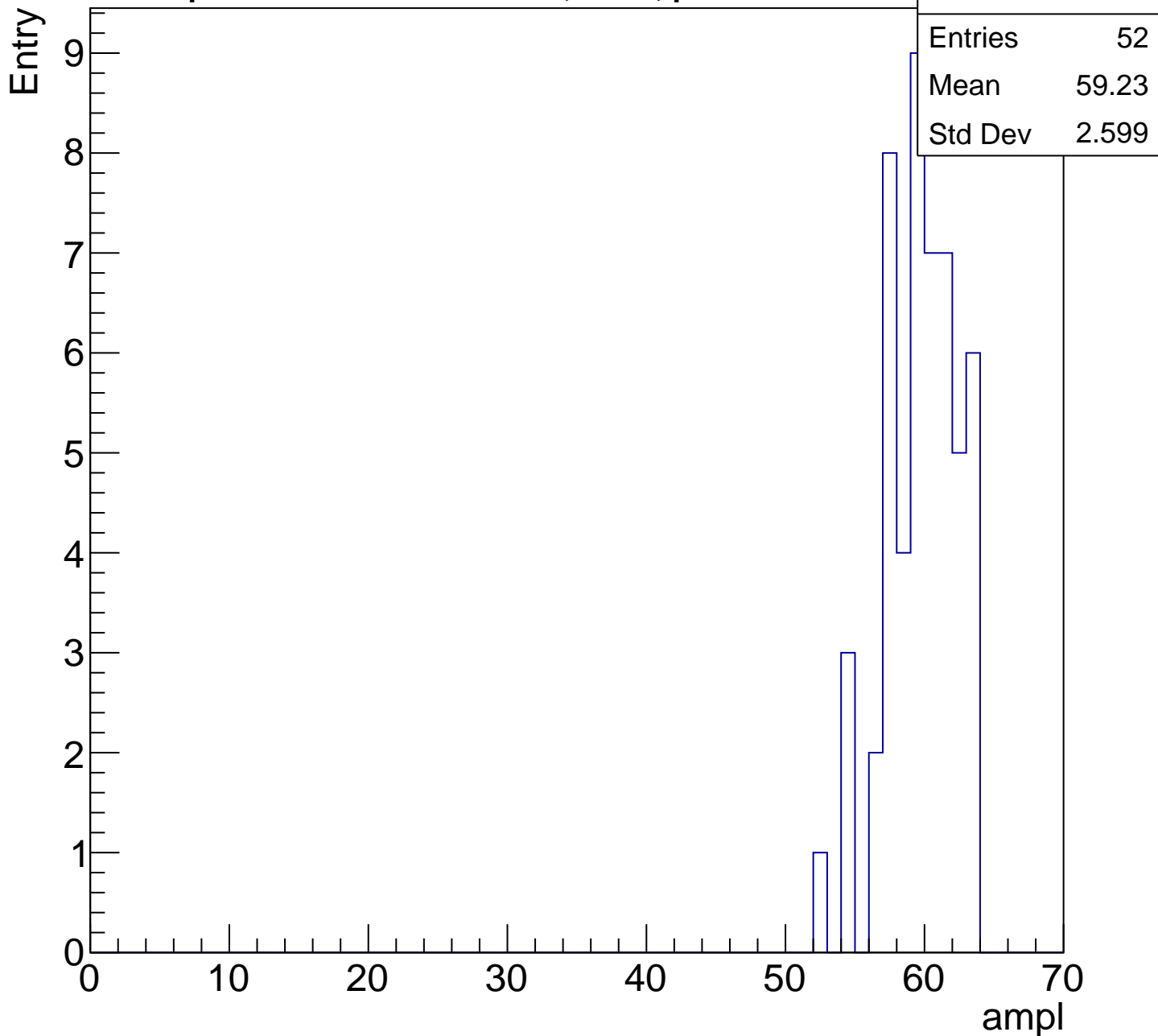
Entries	55
Mean	53.71
Std Dev	3.246

Entry



B1L103S, U6-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

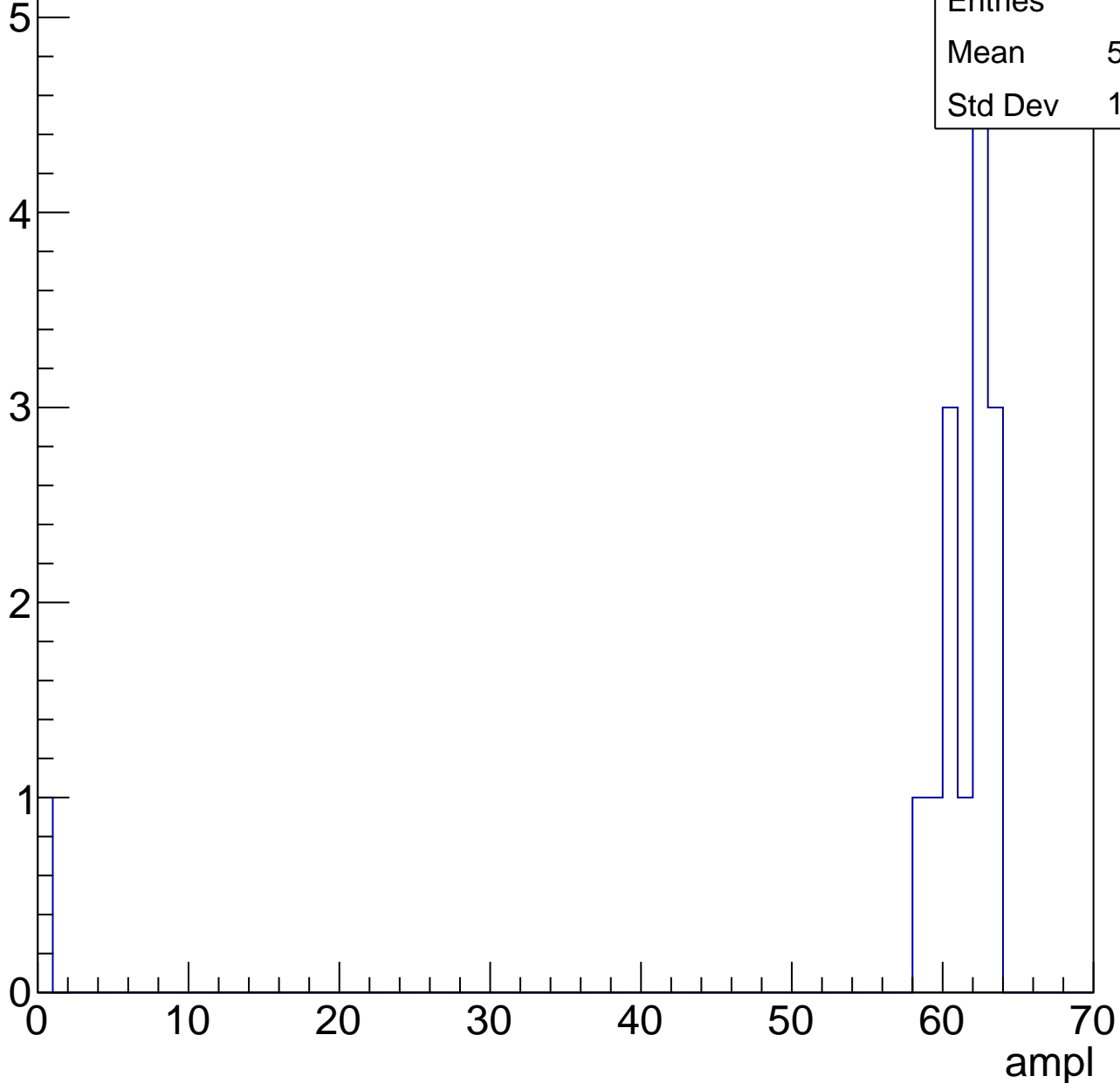


B1L103S, U6-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.13
Std Dev	15.34



B1L103S, U6-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

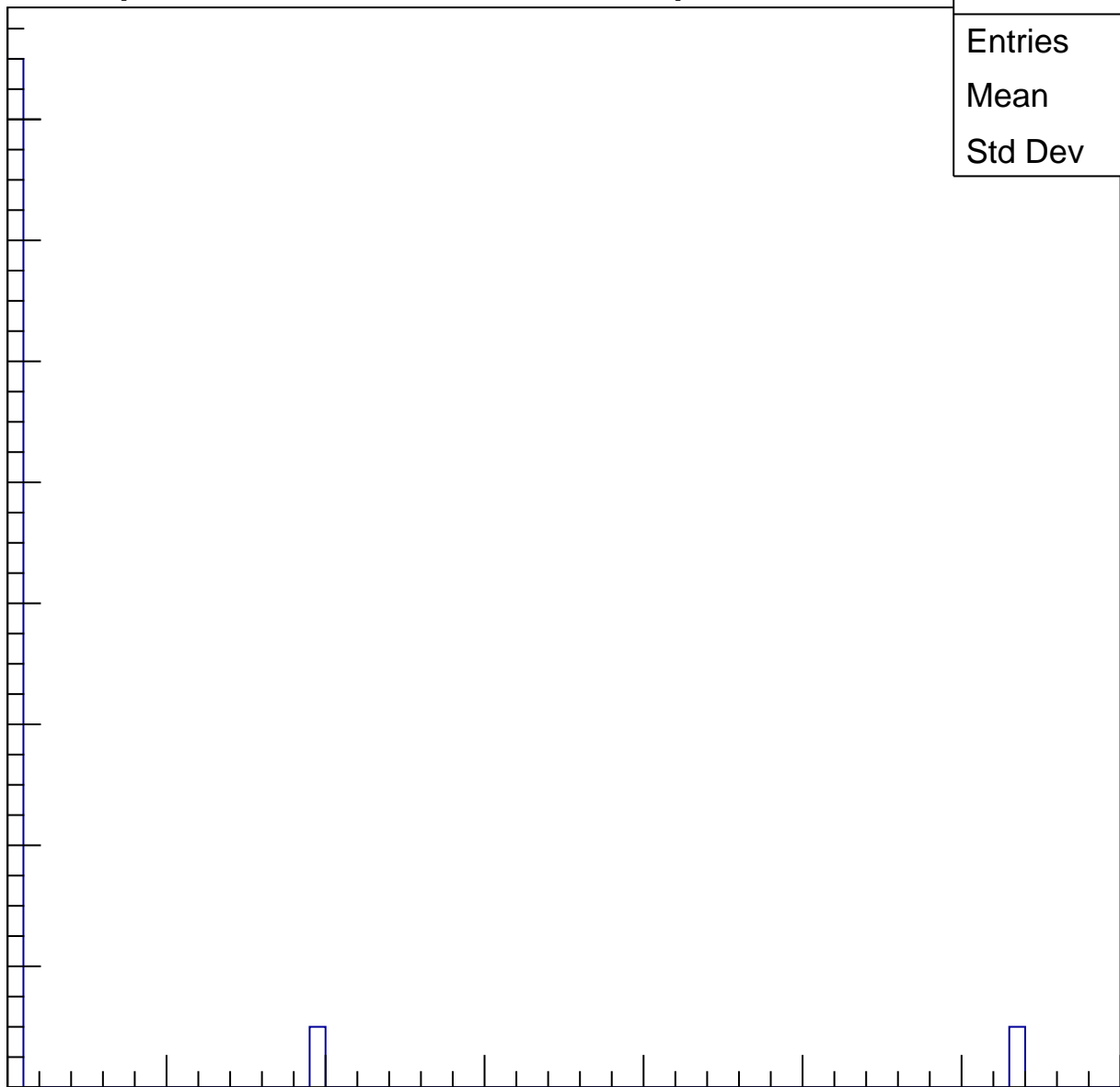
Entries	19
Mean	4.316
Std Dev	14.47

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch68, adc0

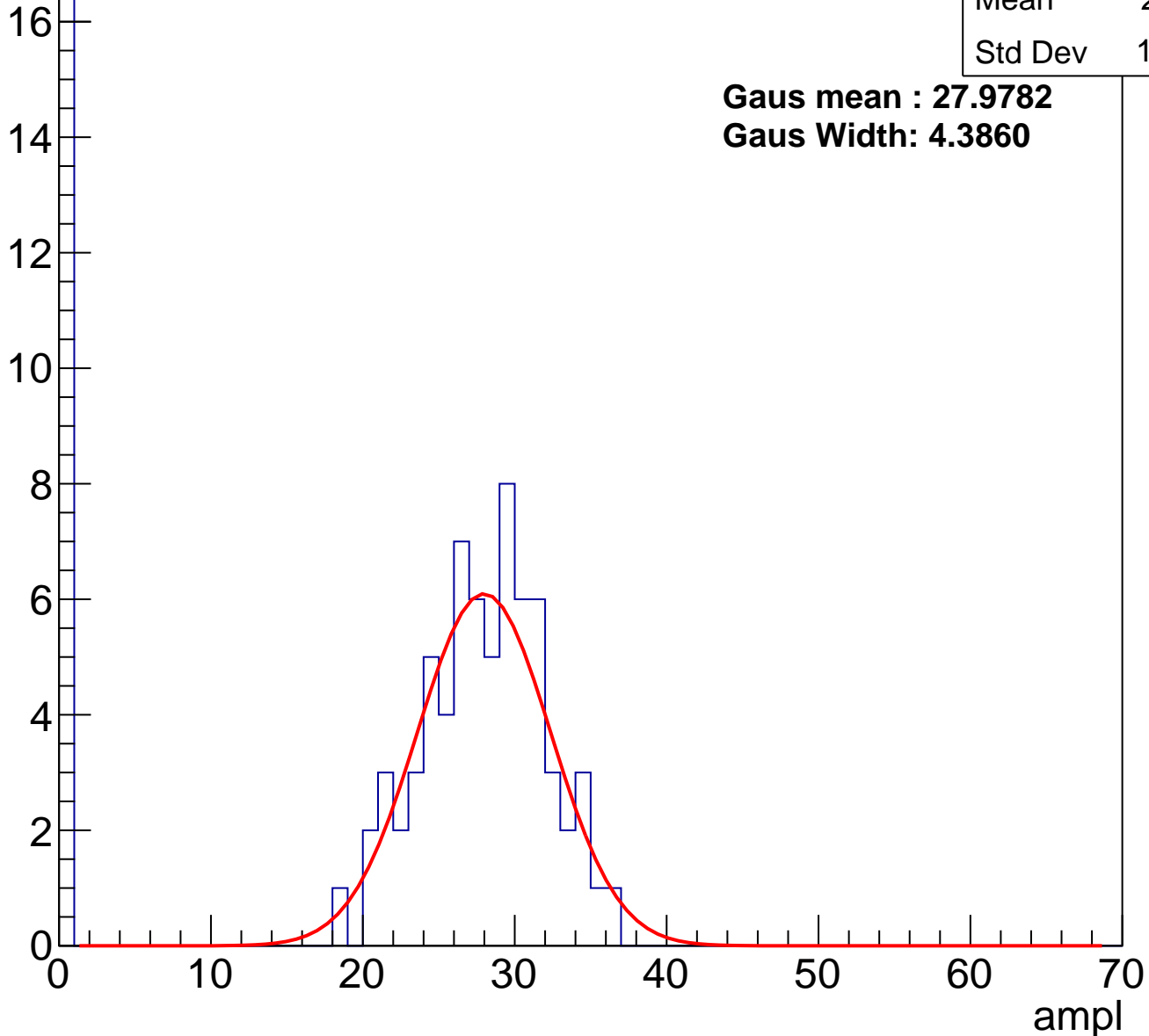
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.01
Std Dev	11.56

Gaus mean : 27.9782

Gaus Width: 4.3860

Entry



B1L103S, U6-ch68, adc1

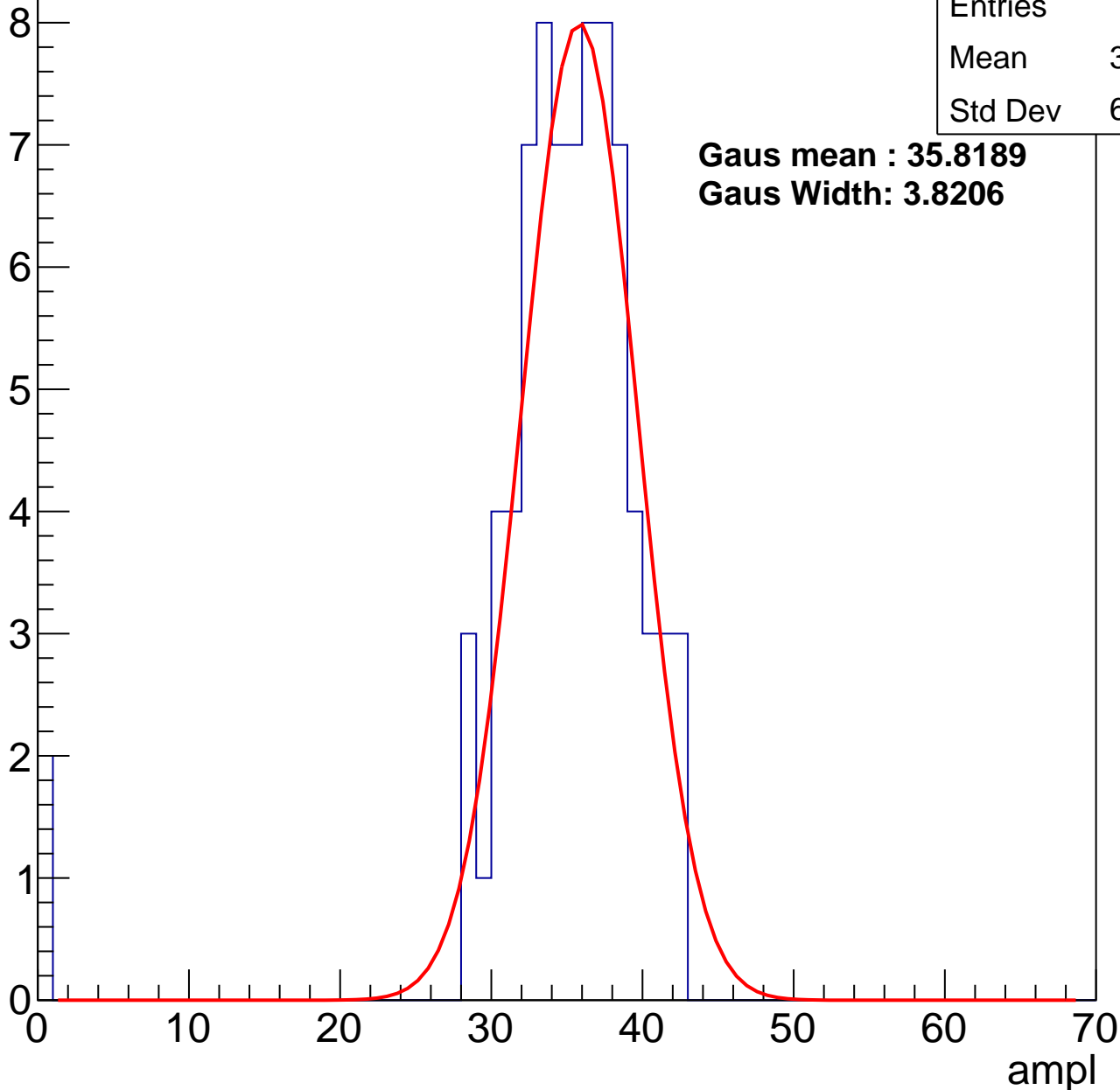
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	34.22
Std Dev	6.509

Gaus mean : 35.8189

Gaus Width: 3.8206



B1L103S, U6-ch68, adc2

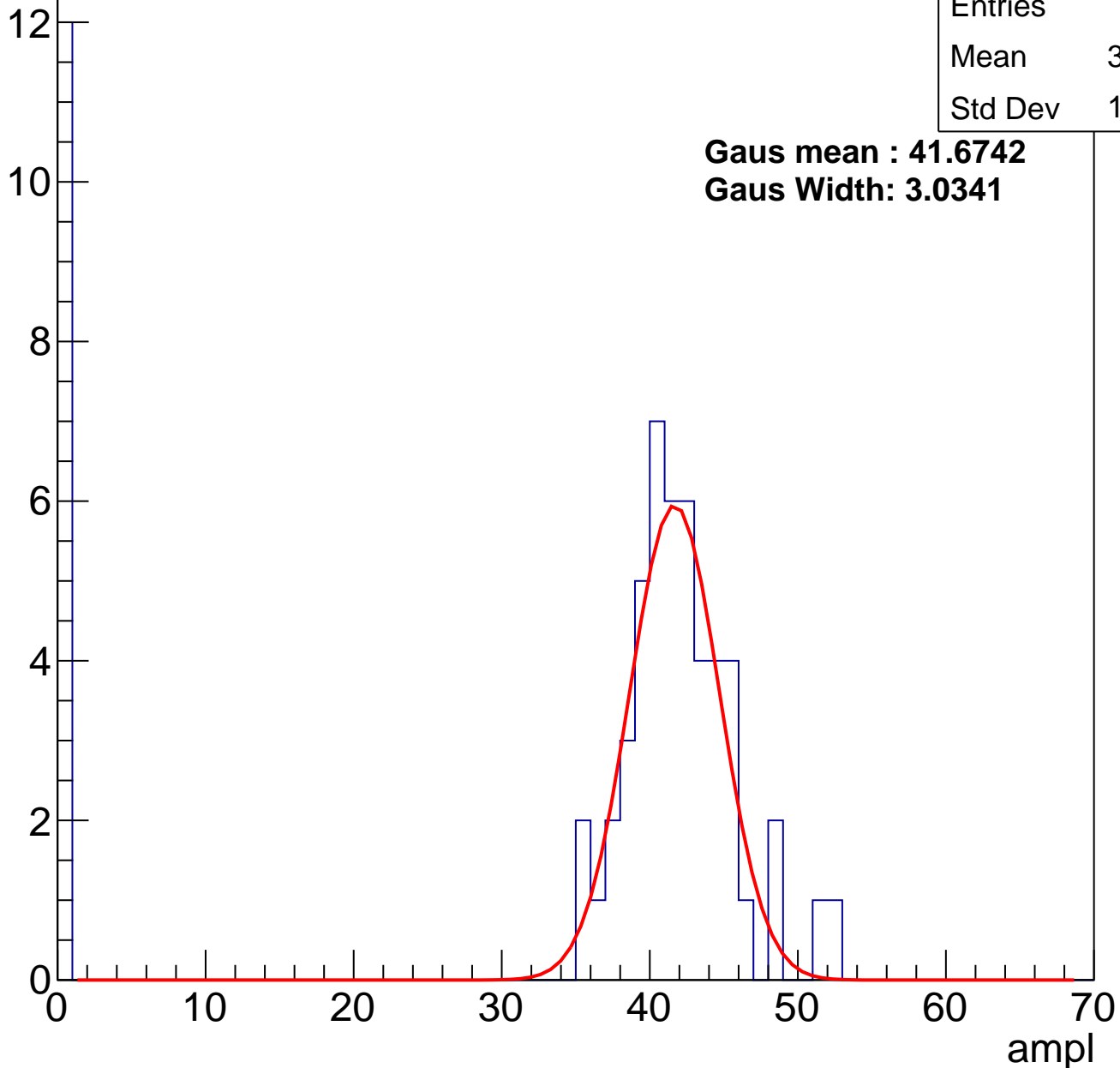
calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	33.44
Std Dev	16.86

Gaus mean : 41.6742

Gaus Width: 3.0341

Entry

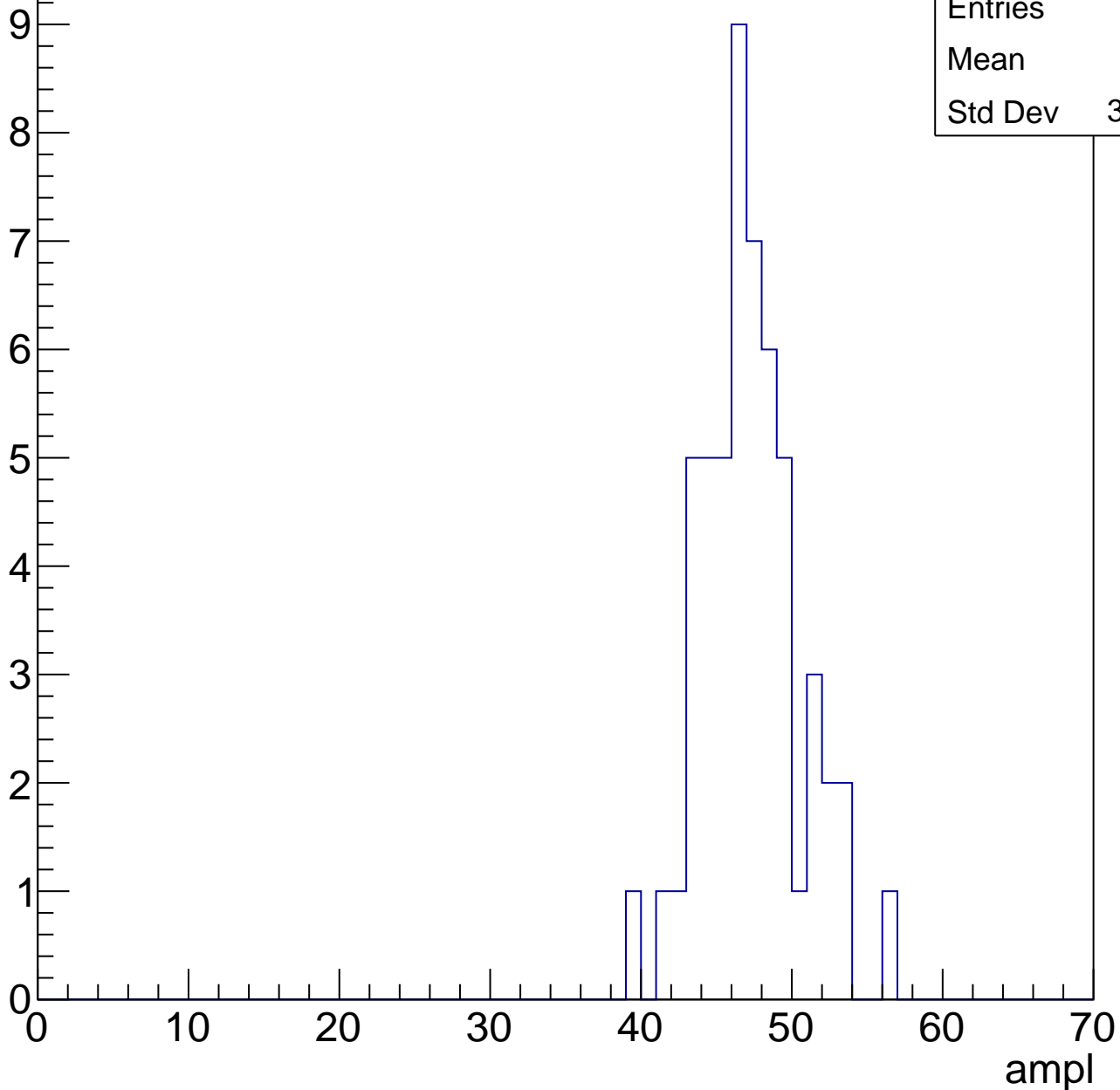


B1L103S, U6-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.8
Std Dev	3.228

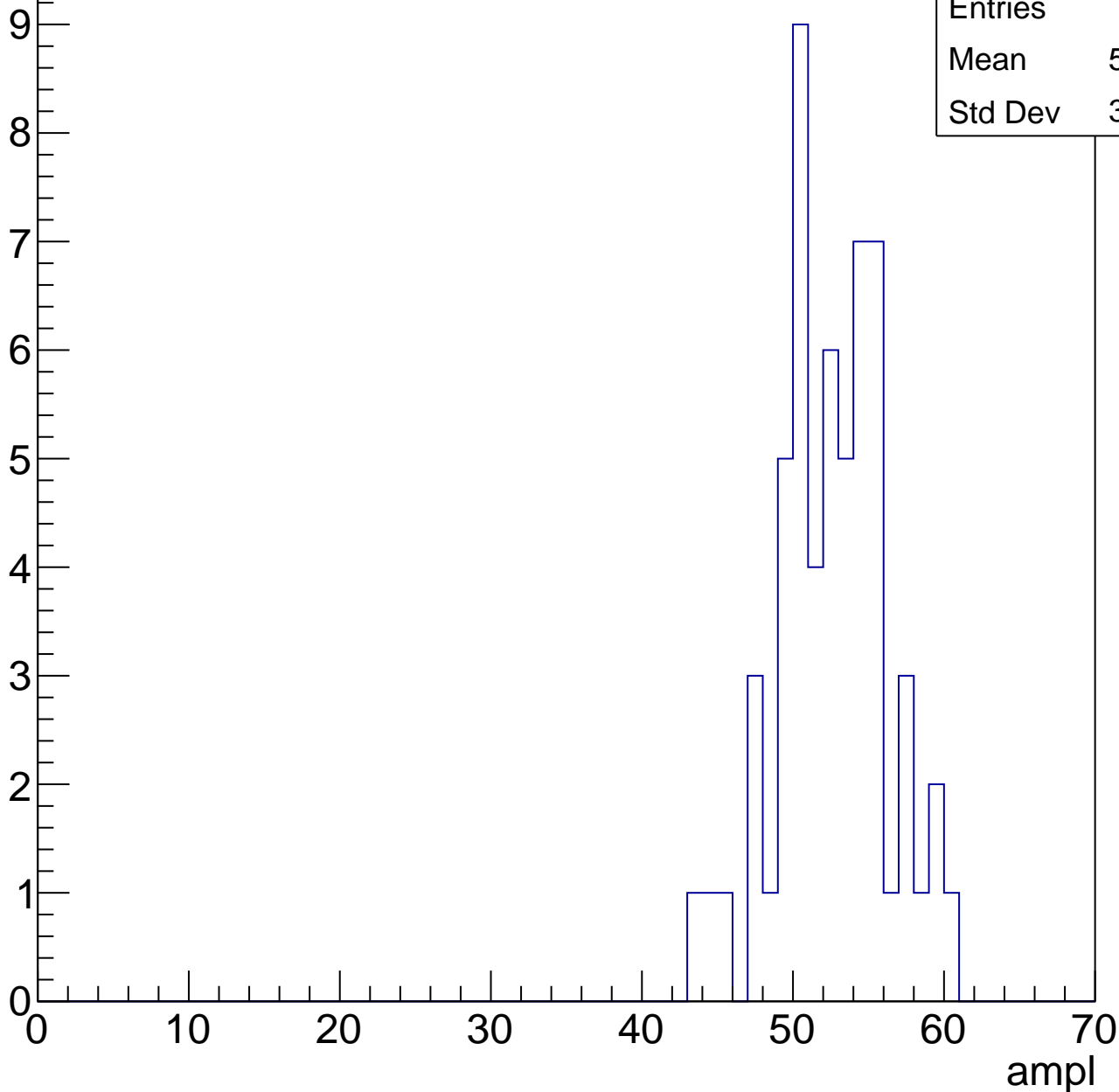


B1L103S, U6-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.12
Std Dev	3.606

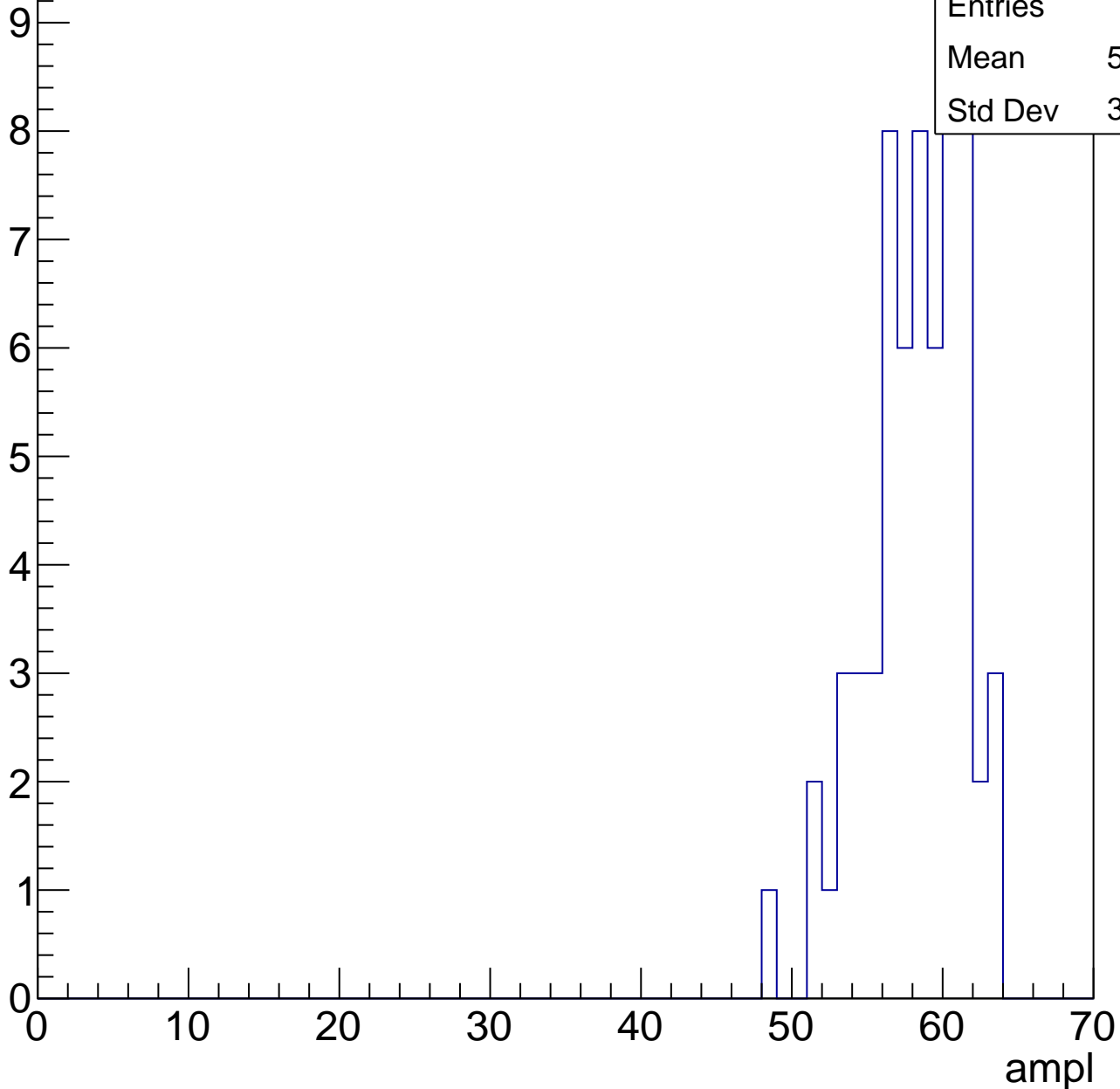


B1L103S, U6-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	57.73
Std Dev	3.183

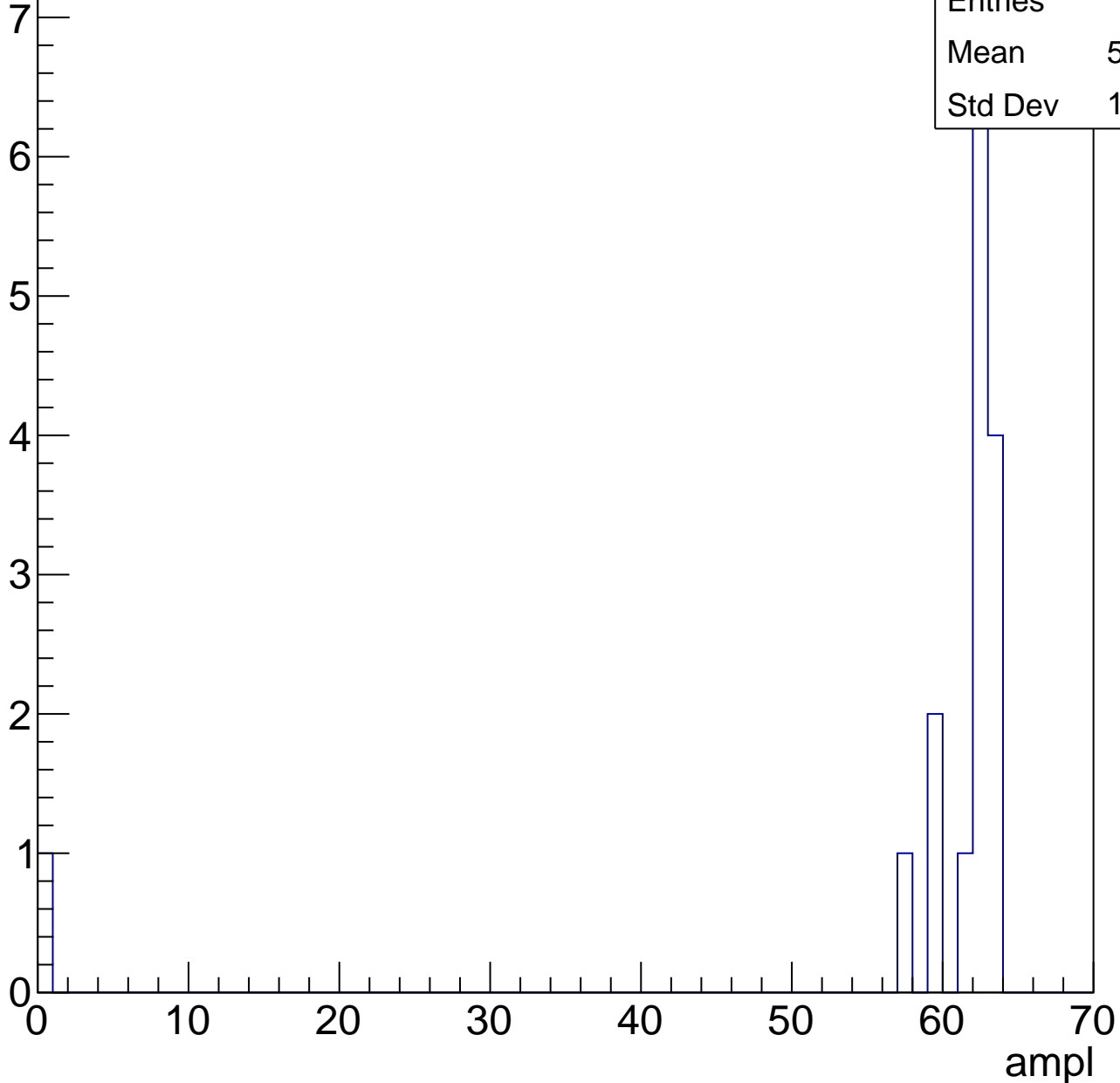


B1L103S, U6-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.62
Std Dev	14.97

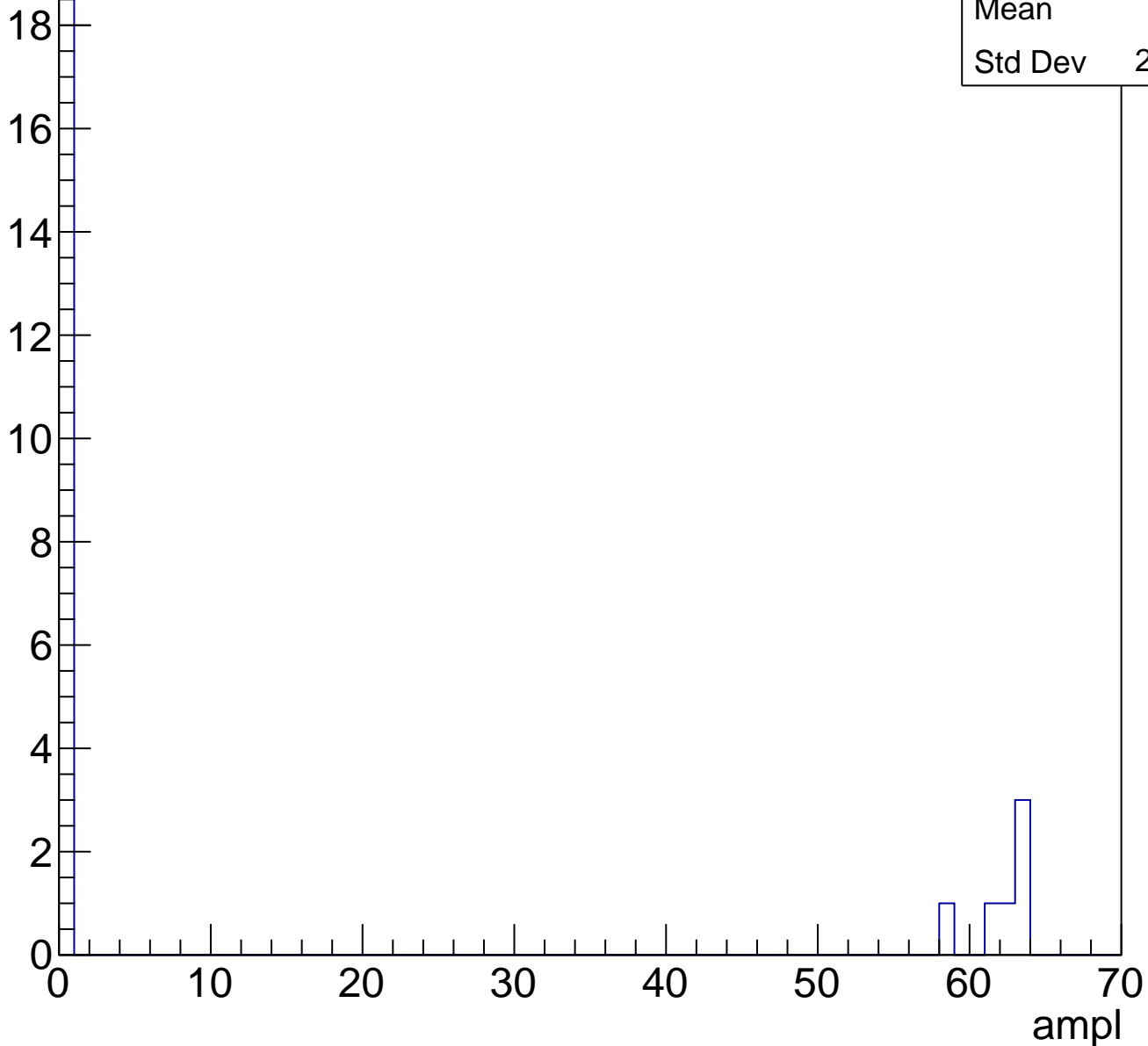


B1L103S, U6-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	25
Mean	14.8
Std Dev	26.35

Entry



B1L103S, U6-ch69, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	22.67
Std Dev	11.96

Gaus mean : 29.5443

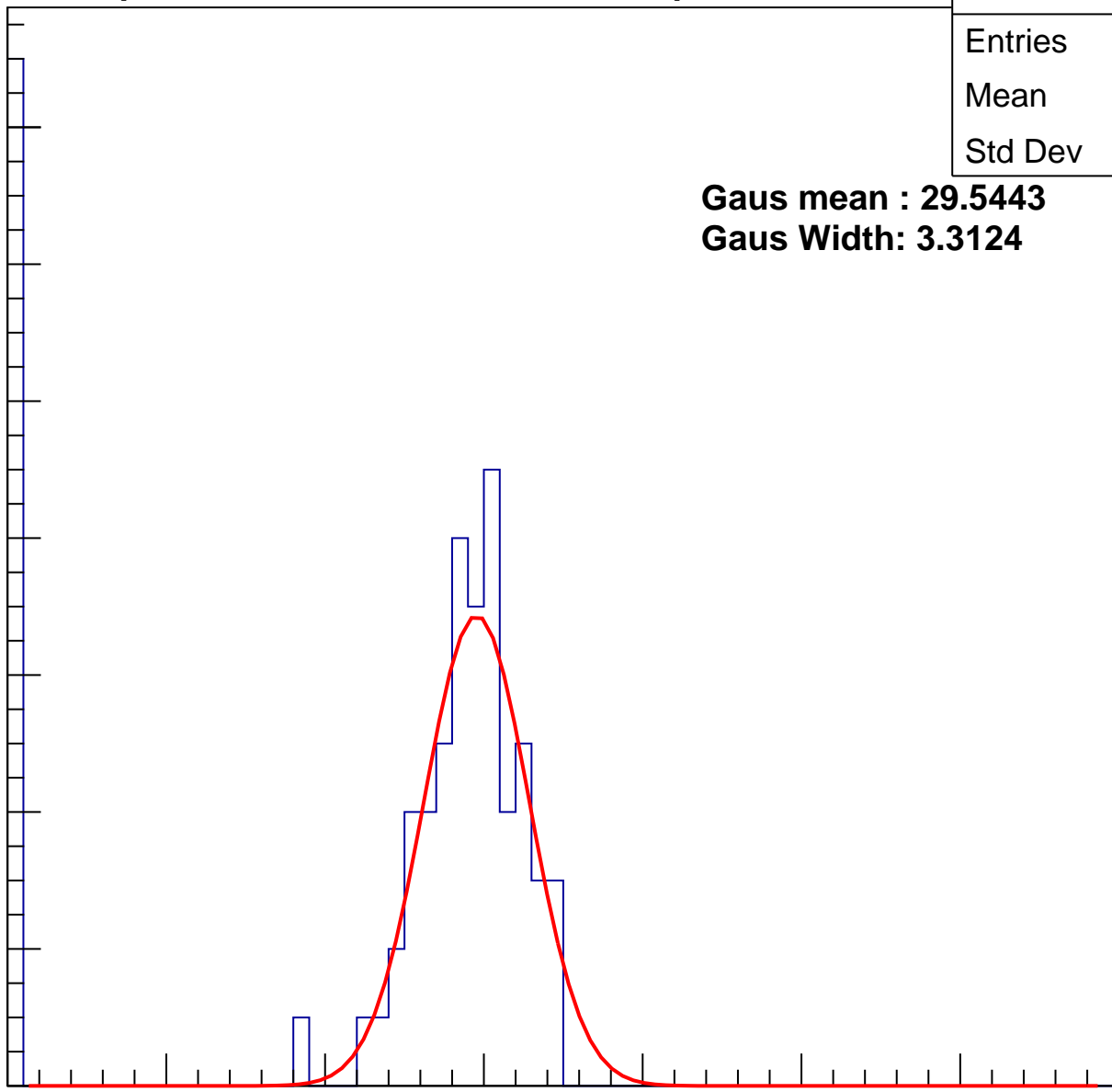
Gaus Width: 3.3124

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch69, adc1

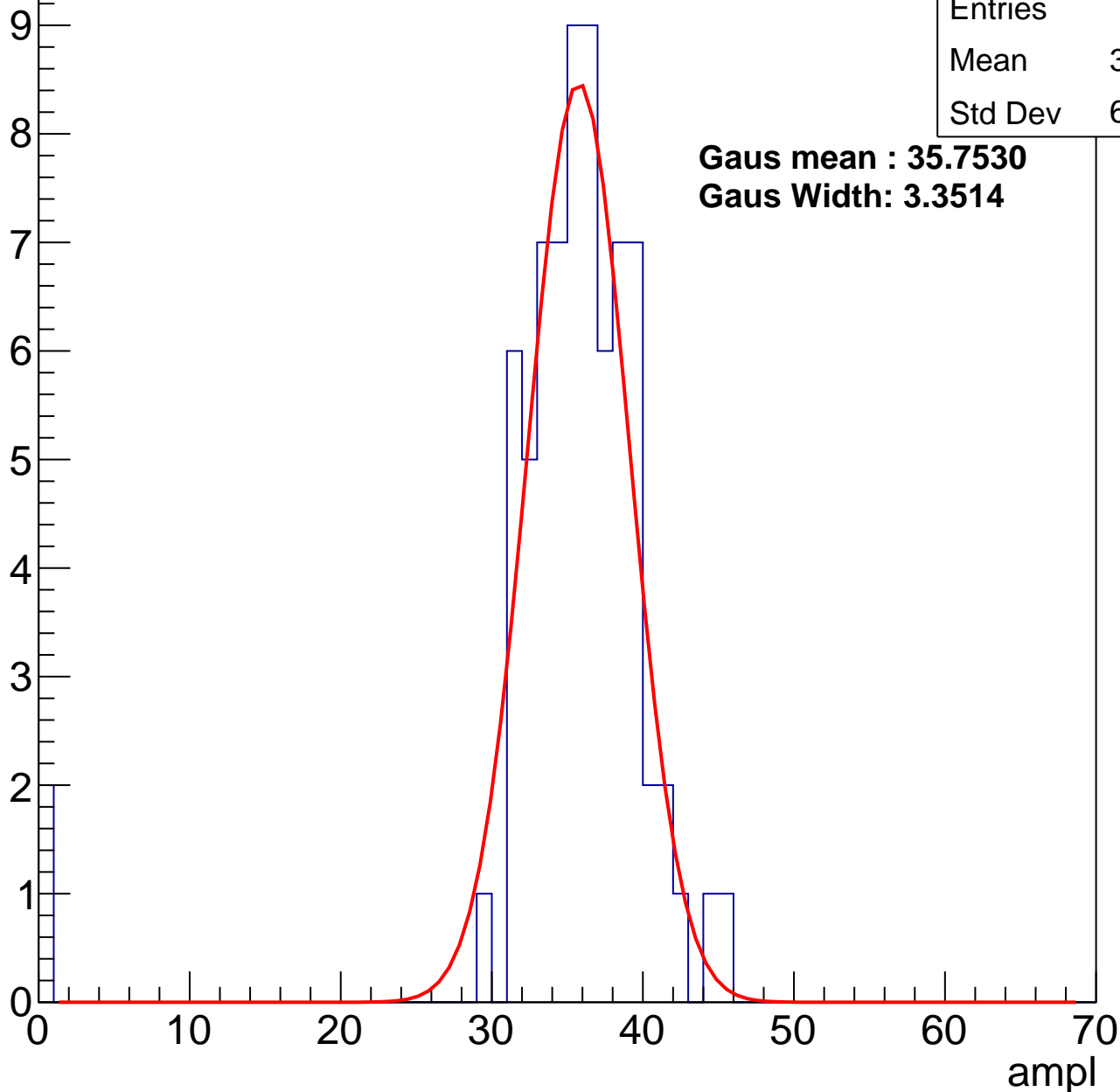
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.75
Std Dev	6.639

Gaus mean : 35.7530

Gaus Width: 3.3514

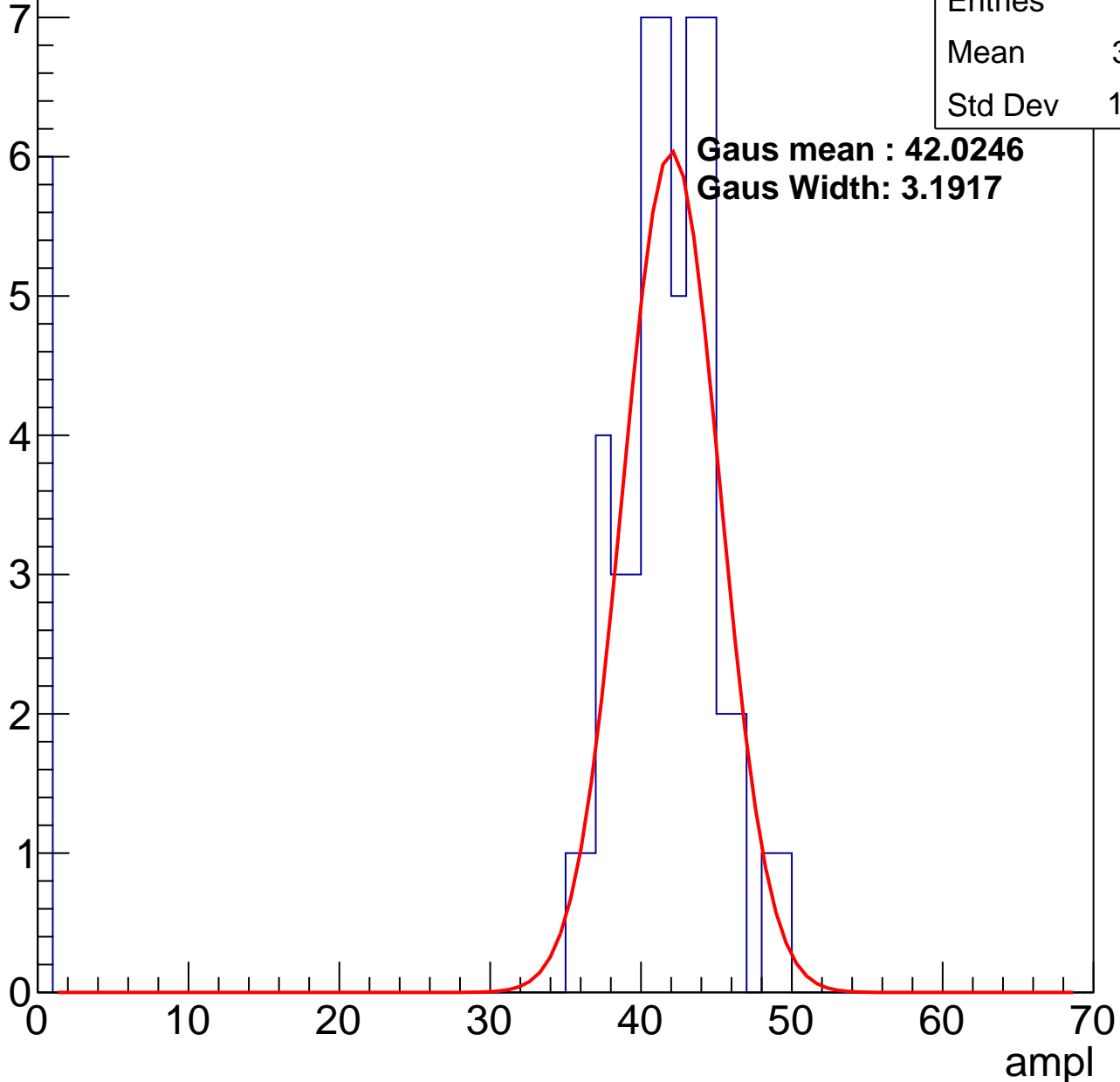


B1L103S, U6-ch69, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	37.11
Std Dev	13.03



B1L103S, U6-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	41.95
Std Dev	16.45

Entry

10

8

6

4

2

0

0

10

20

30

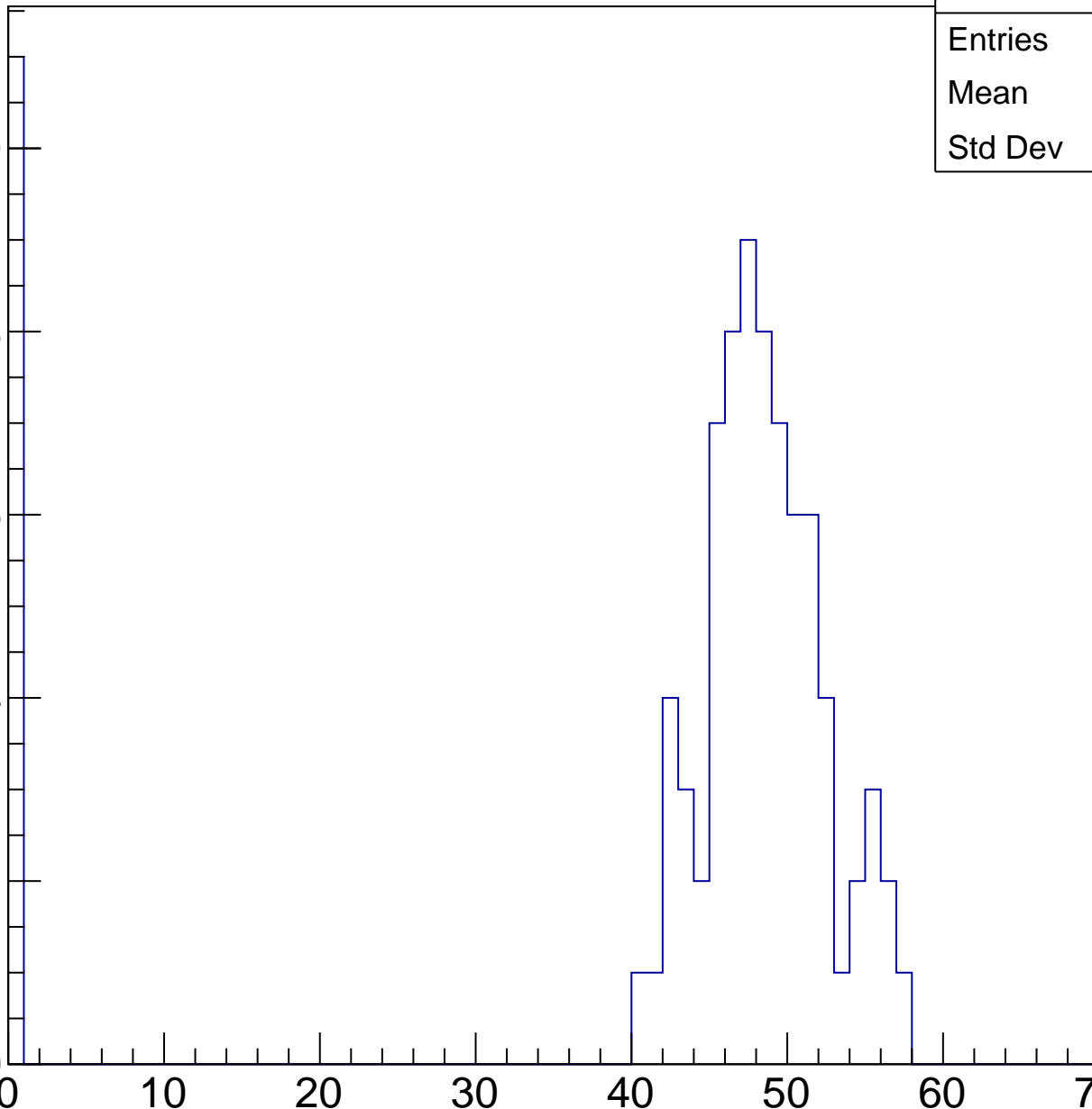
40

50

60

70

ampl

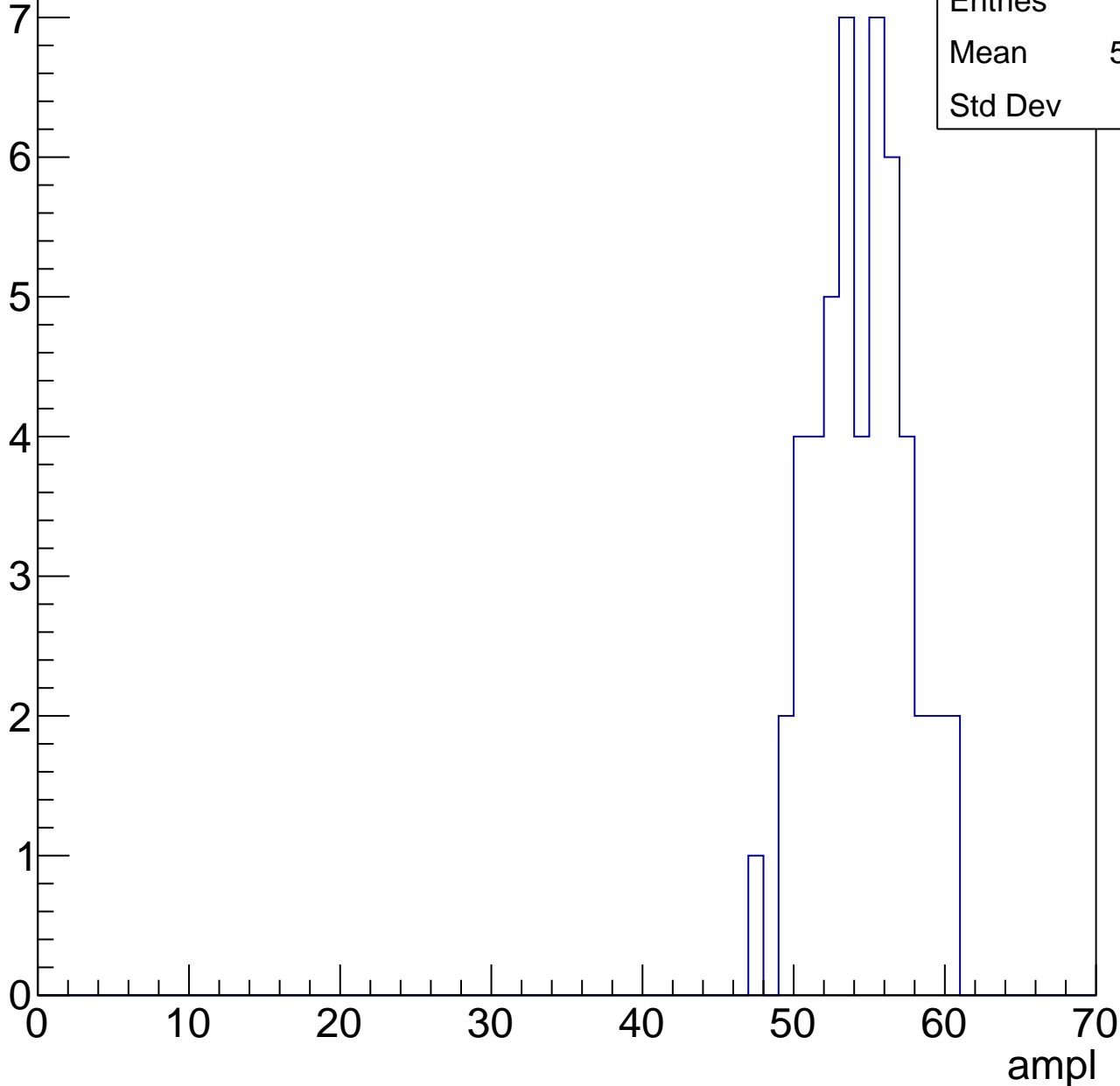


B1L103S, U6-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	53.98
Std Dev	2.99

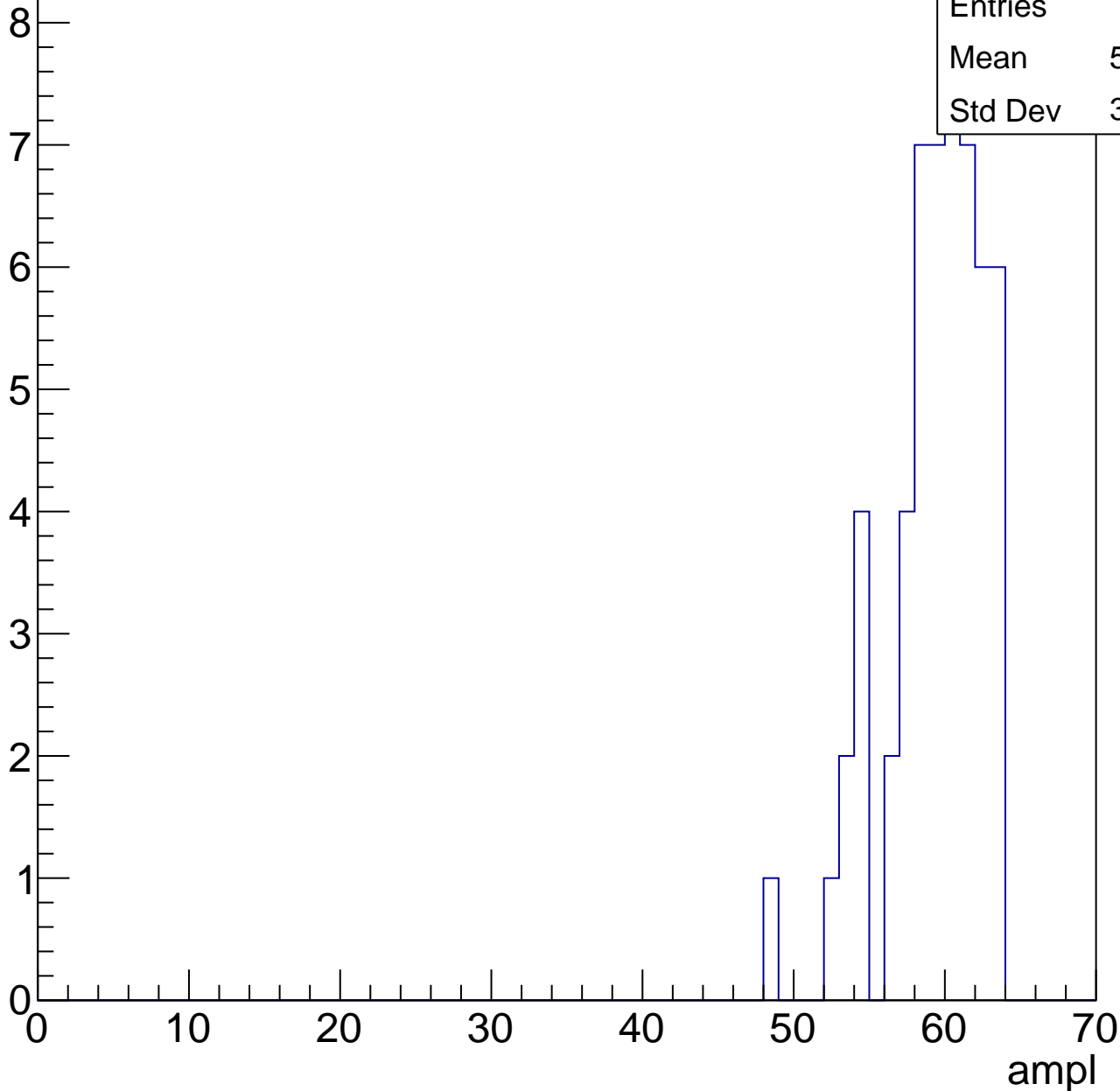


B1L103S, U6-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

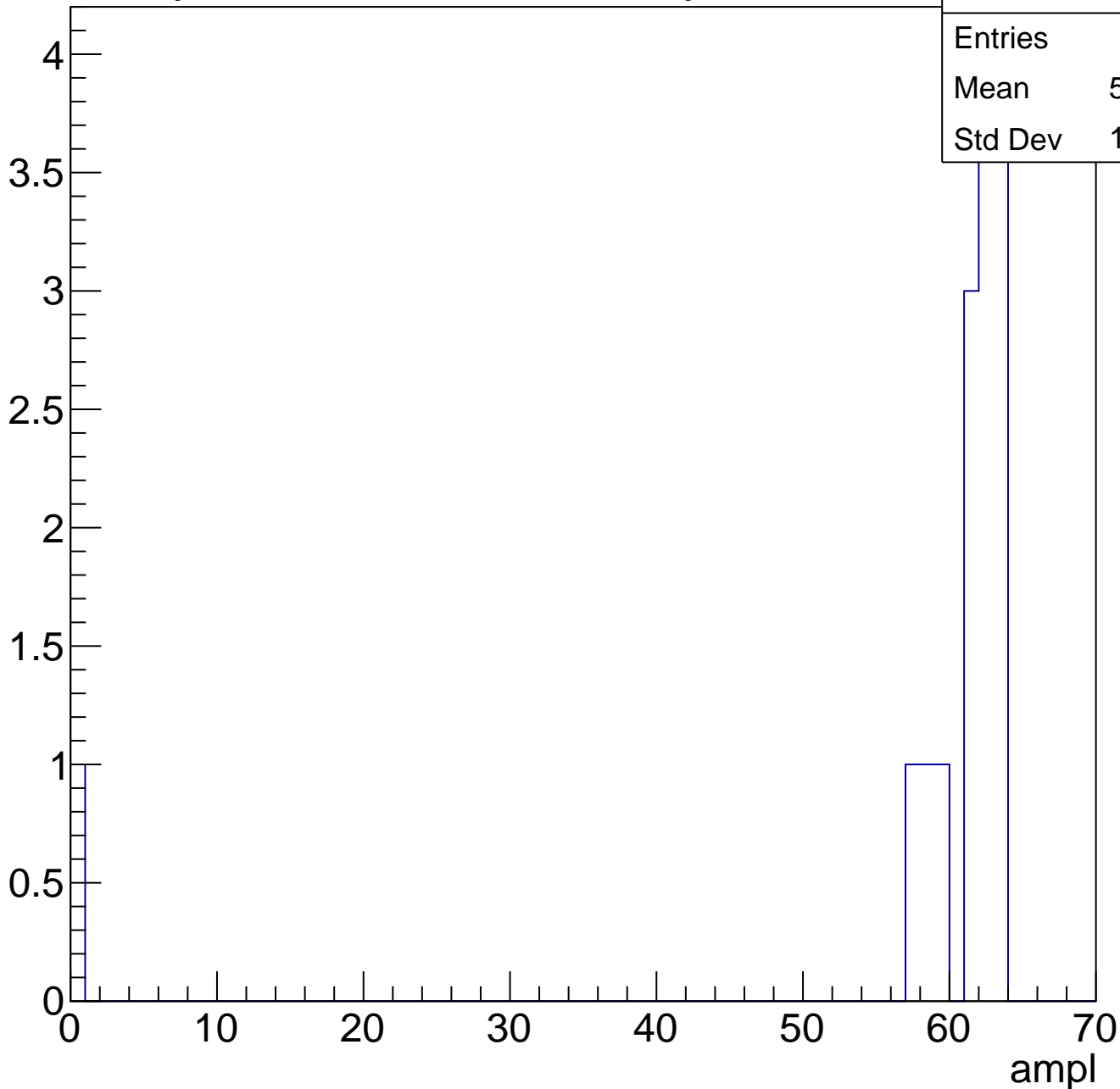
Entries	55
Mean	58.87
Std Dev	3.214



B1L103S, U6-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch70, adc0

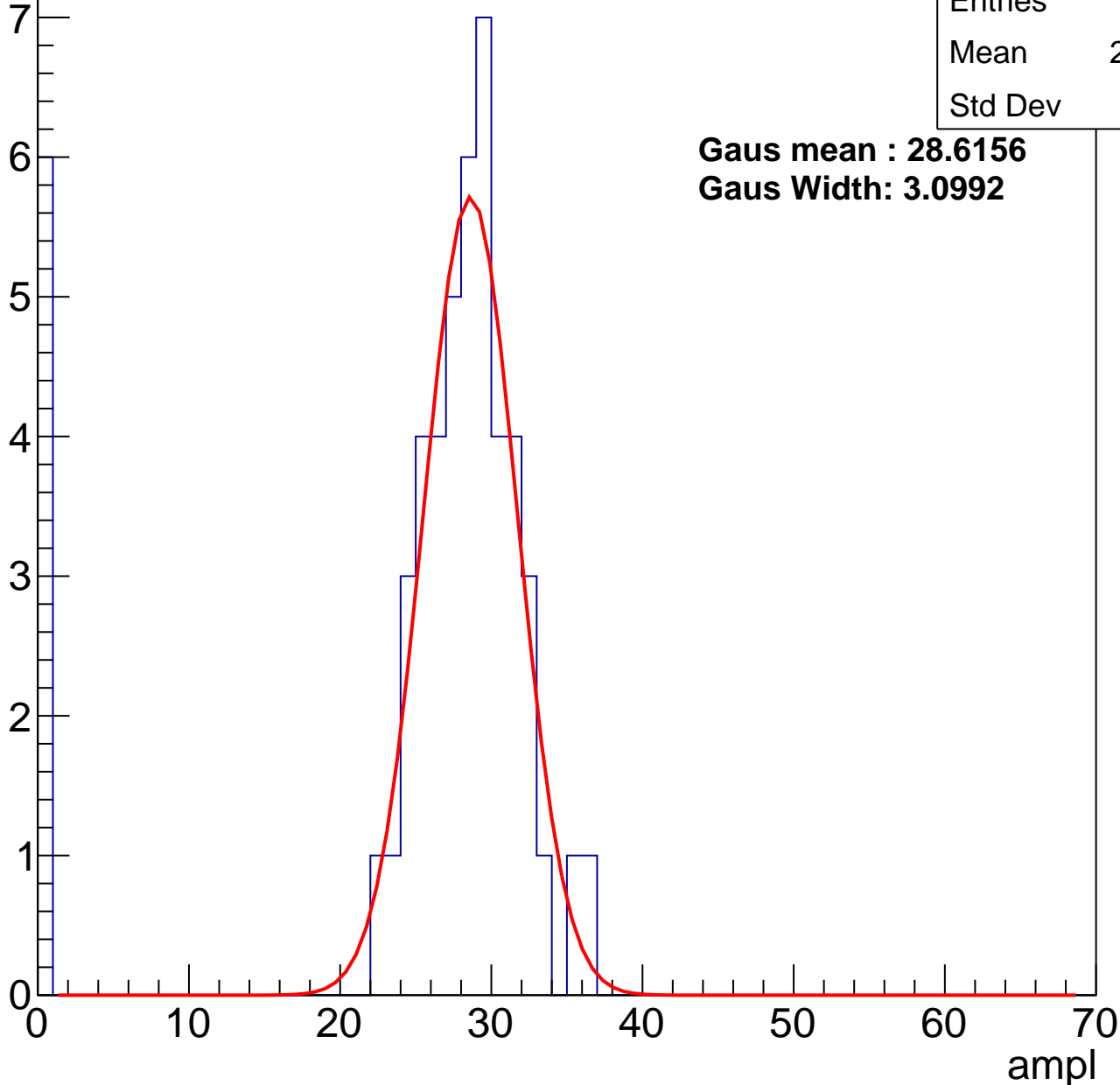
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	24.92
Std Dev	9.53

Gaus mean : 28.6156

Gaus Width: 3.0992



B1L103S, U6-ch70, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	34.77
Std Dev	3.256

Gaus mean : 35.3166

Gaus Width: 3.3123

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U6-ch70, adc2

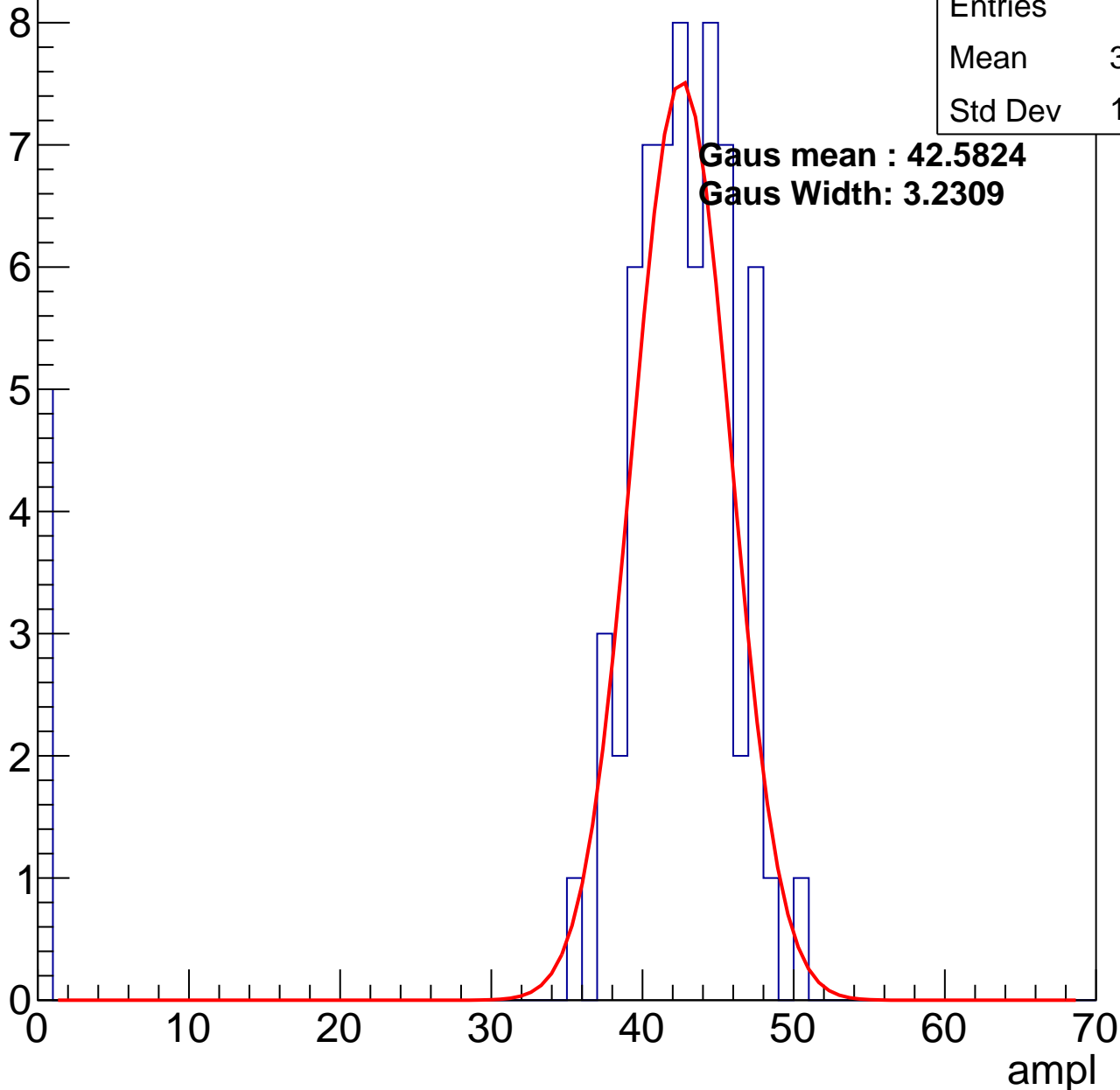
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	39.37
Std Dev	11.32

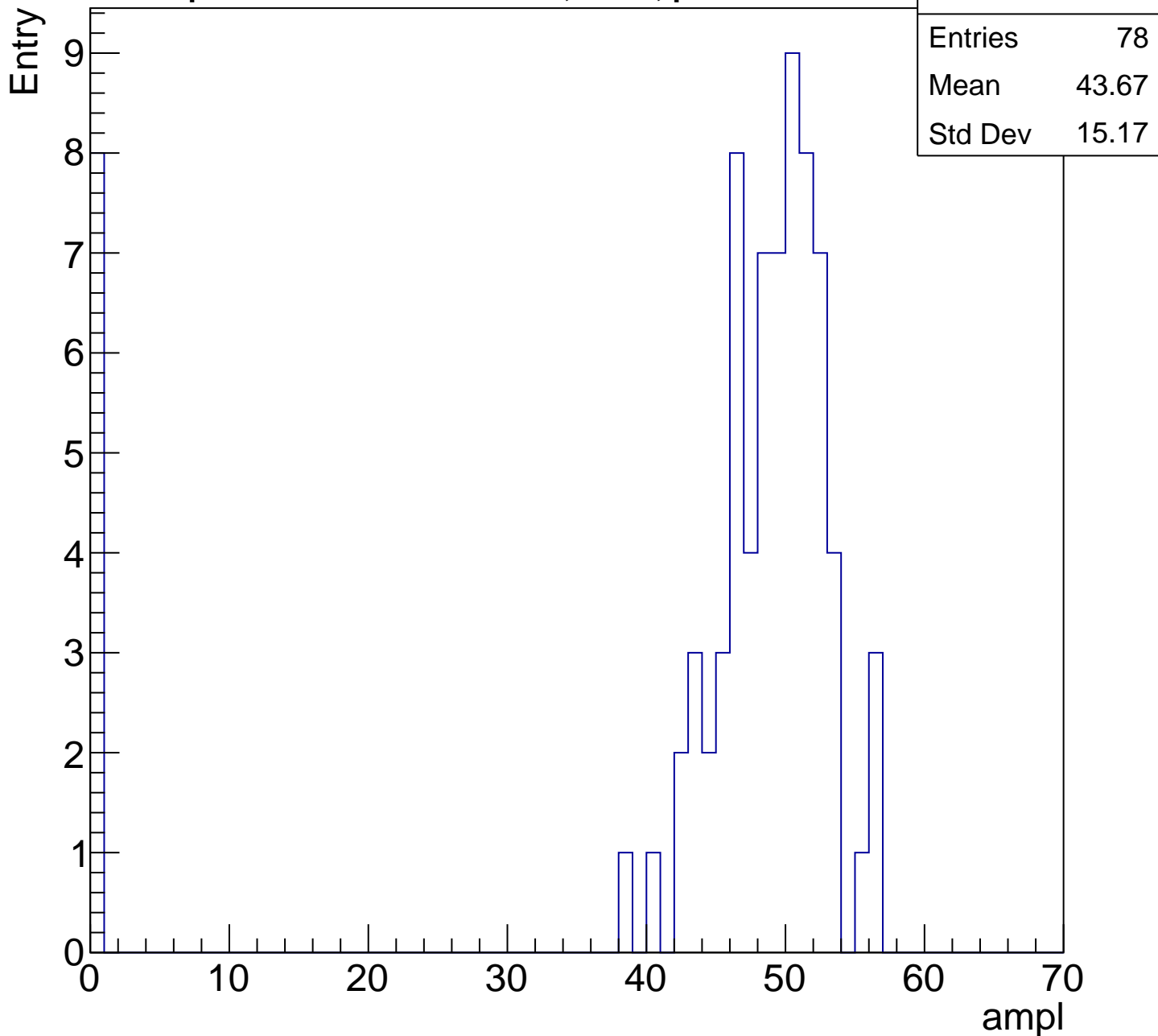
Gaus mean : 42.5824

Gaus Width: 3.2309



B1L103S, U6-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

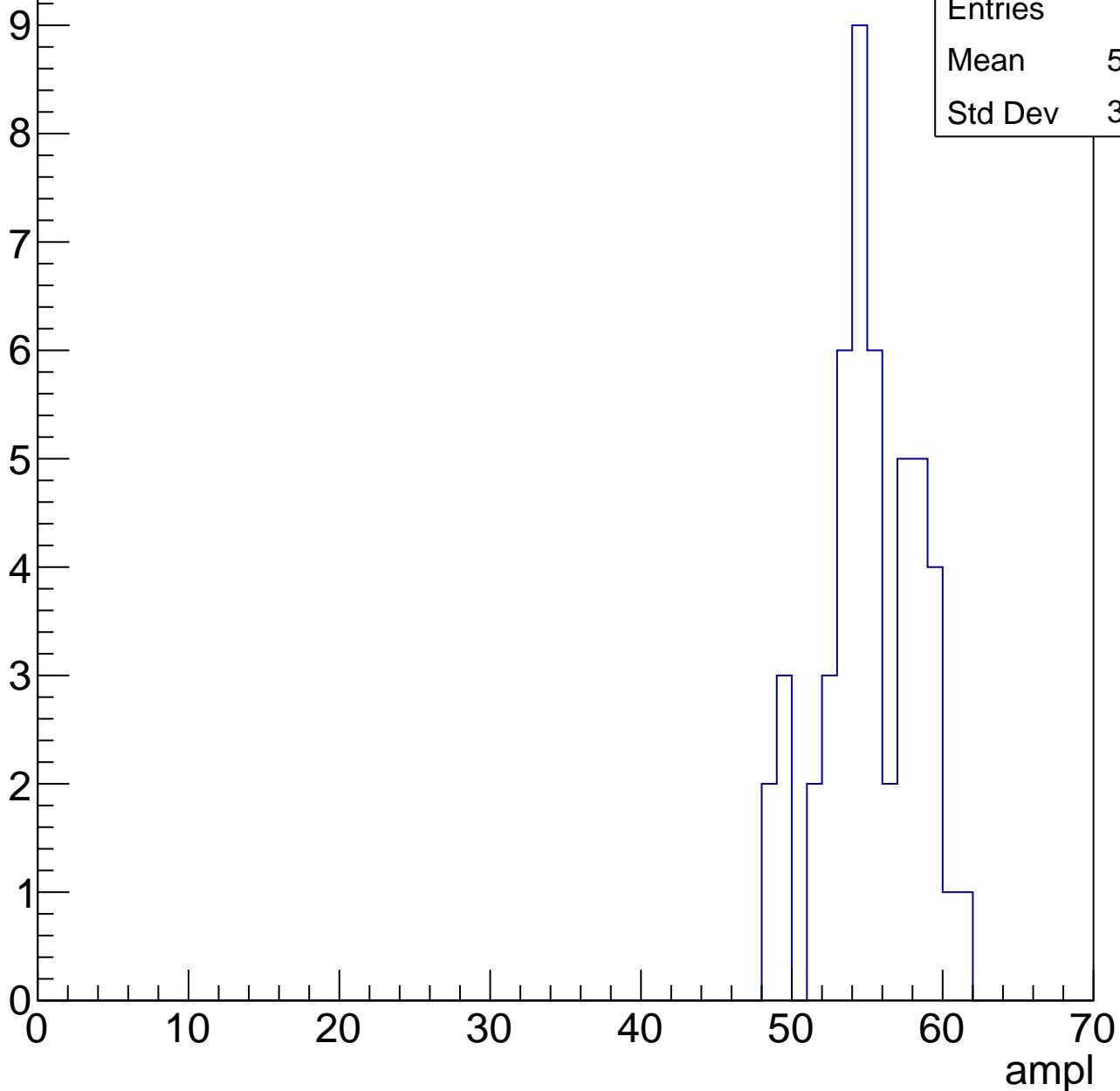


B1L103S, U6-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

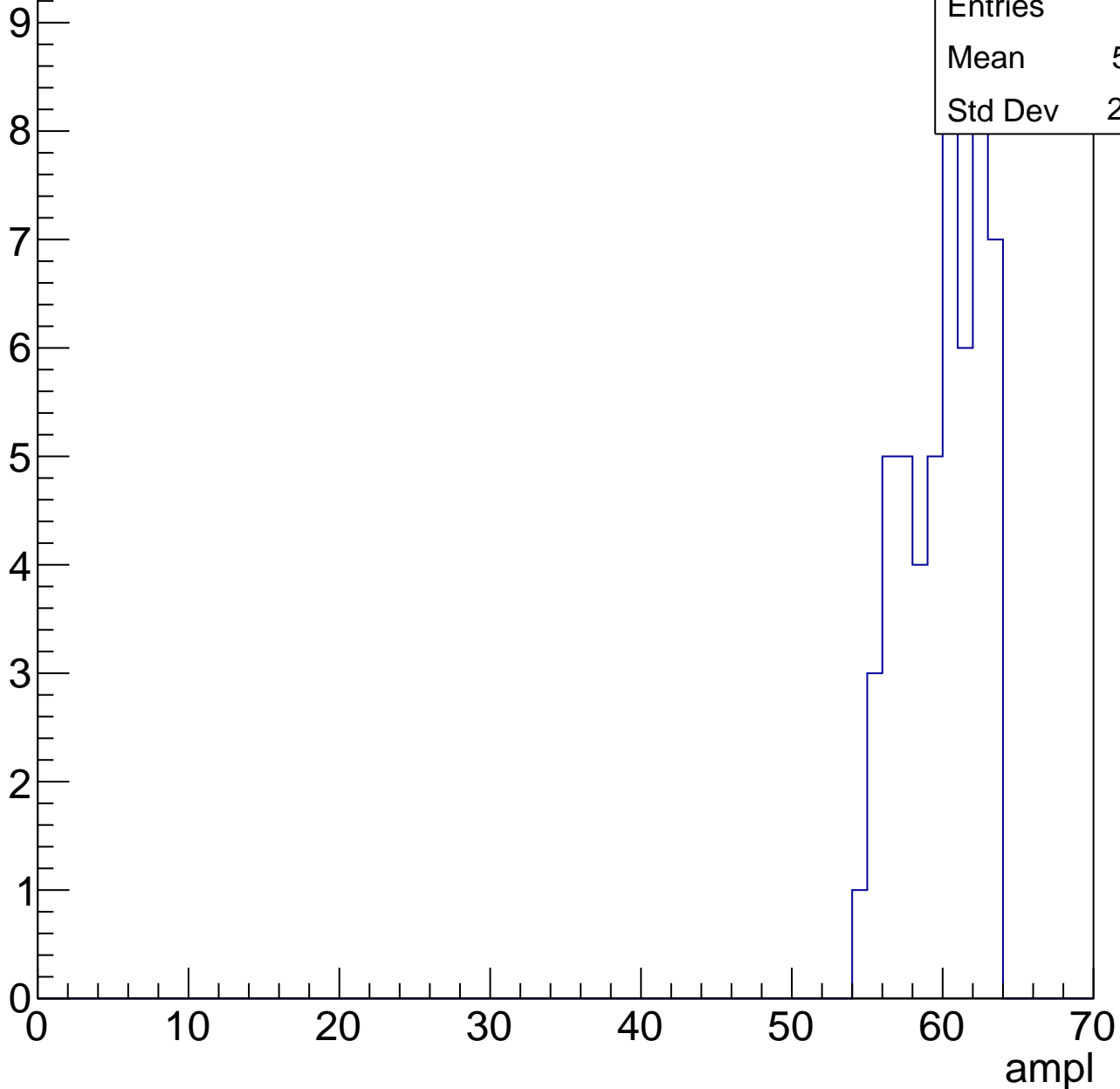
Entries	49
Mean	54.67
Std Dev	3.145



B1L103S, U6-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

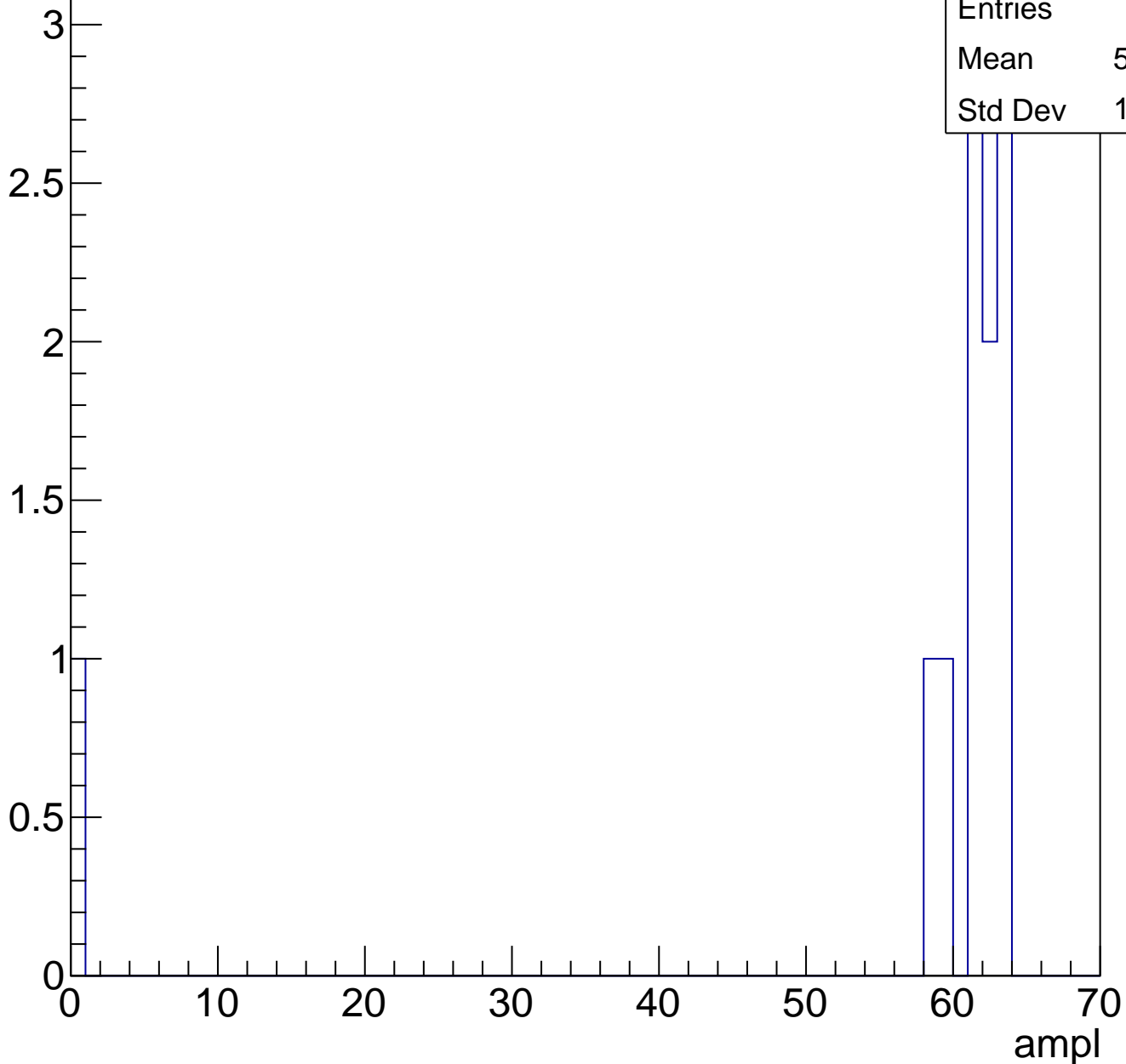
Entry



B1L103S, U6-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	26.48
Std Dev	7.602

Gaus mean : 28.8940

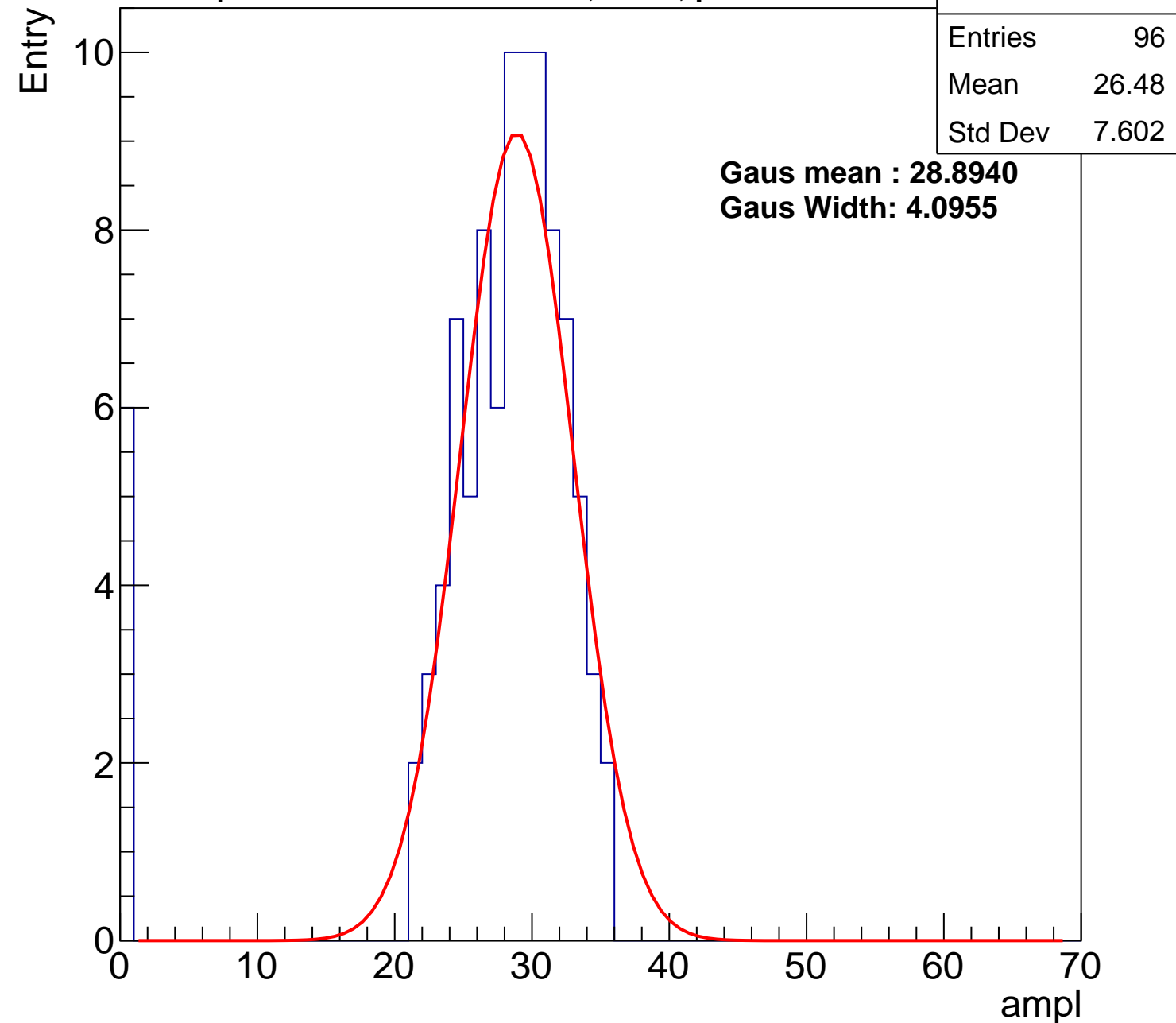
Gaus Width: 4.0955

Entry

10
8
6
4
2
0

ampl

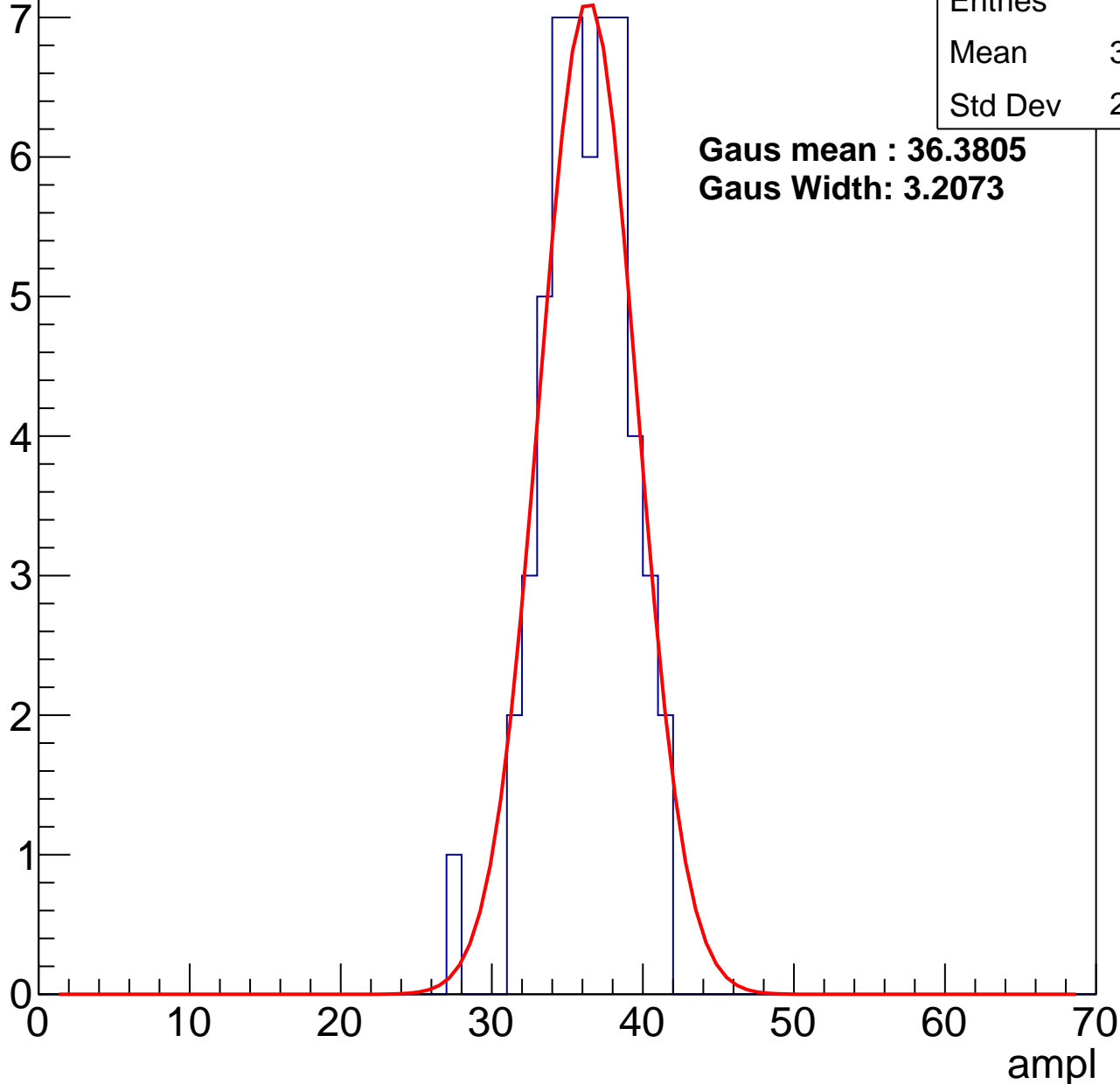
0 10 20 30 40 50 60 70



B1L103S, U6-ch71, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch71, adc2

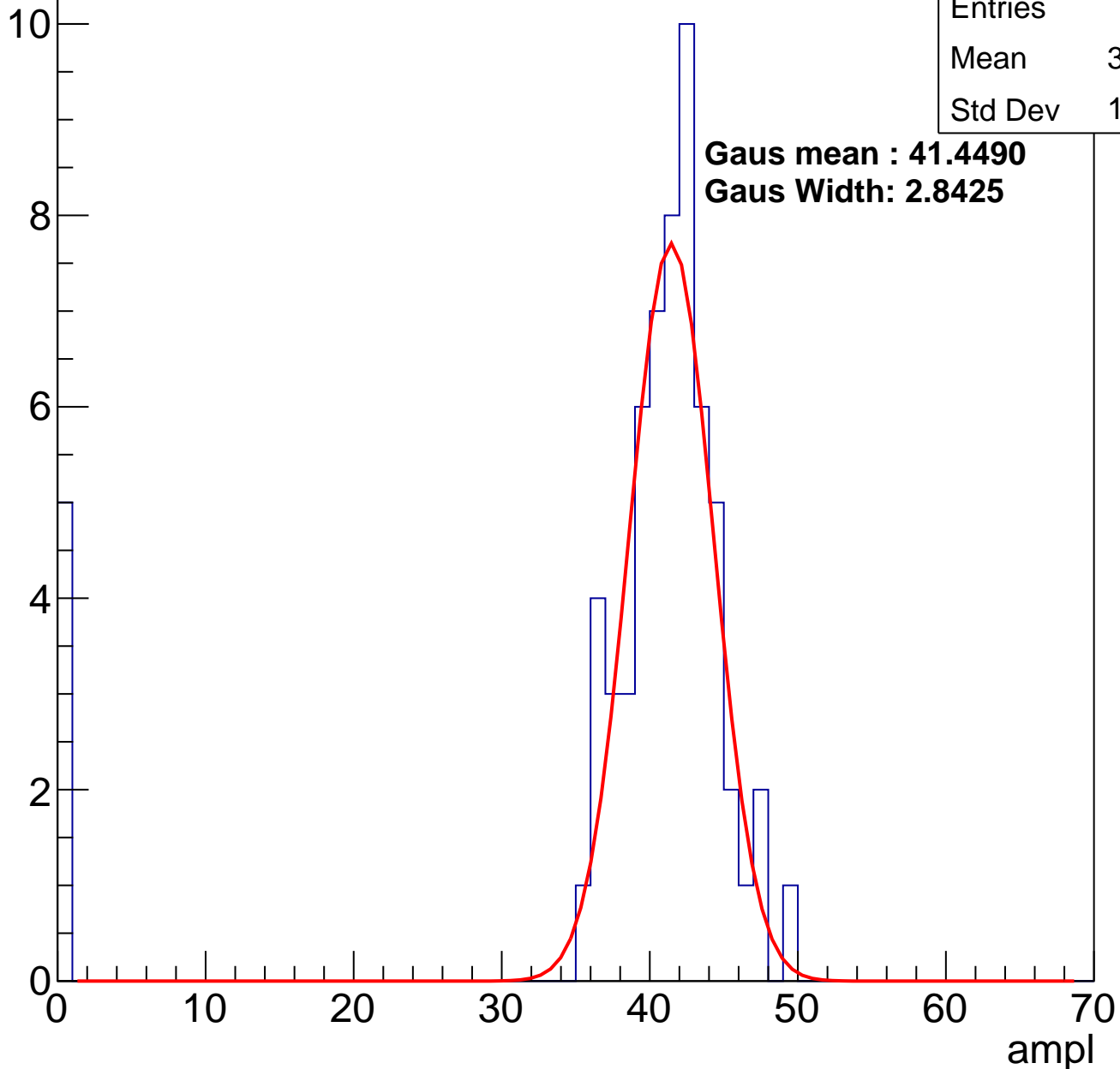
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	37.86
Std Dev	11.38

Gaus mean : 41.4490

Gaus Width: 2.8425

Entry

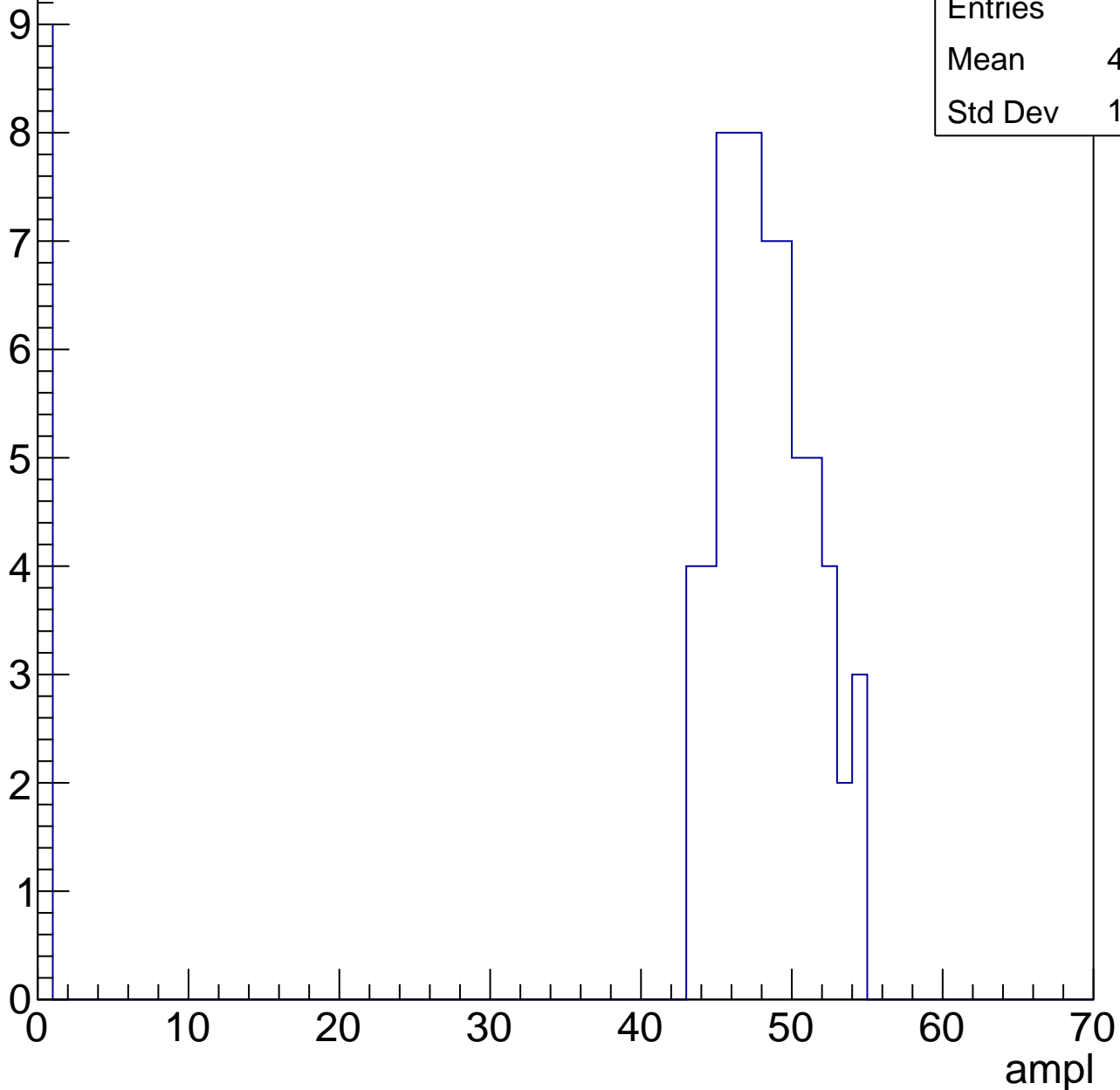


B1L103S, U6-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

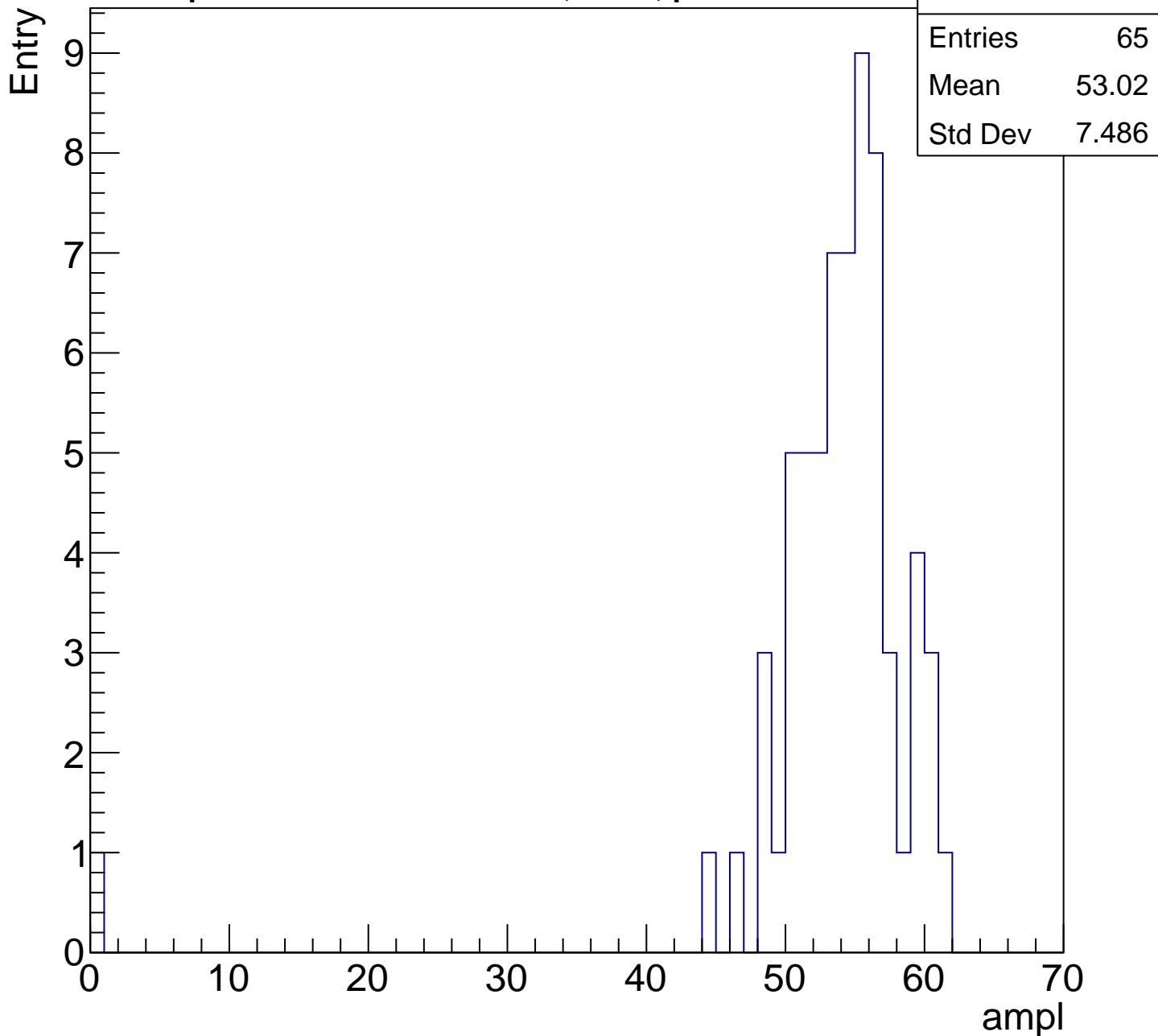
Entry

Entries	74
Mean	42.05
Std Dev	15.89



B1L103S, U6-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

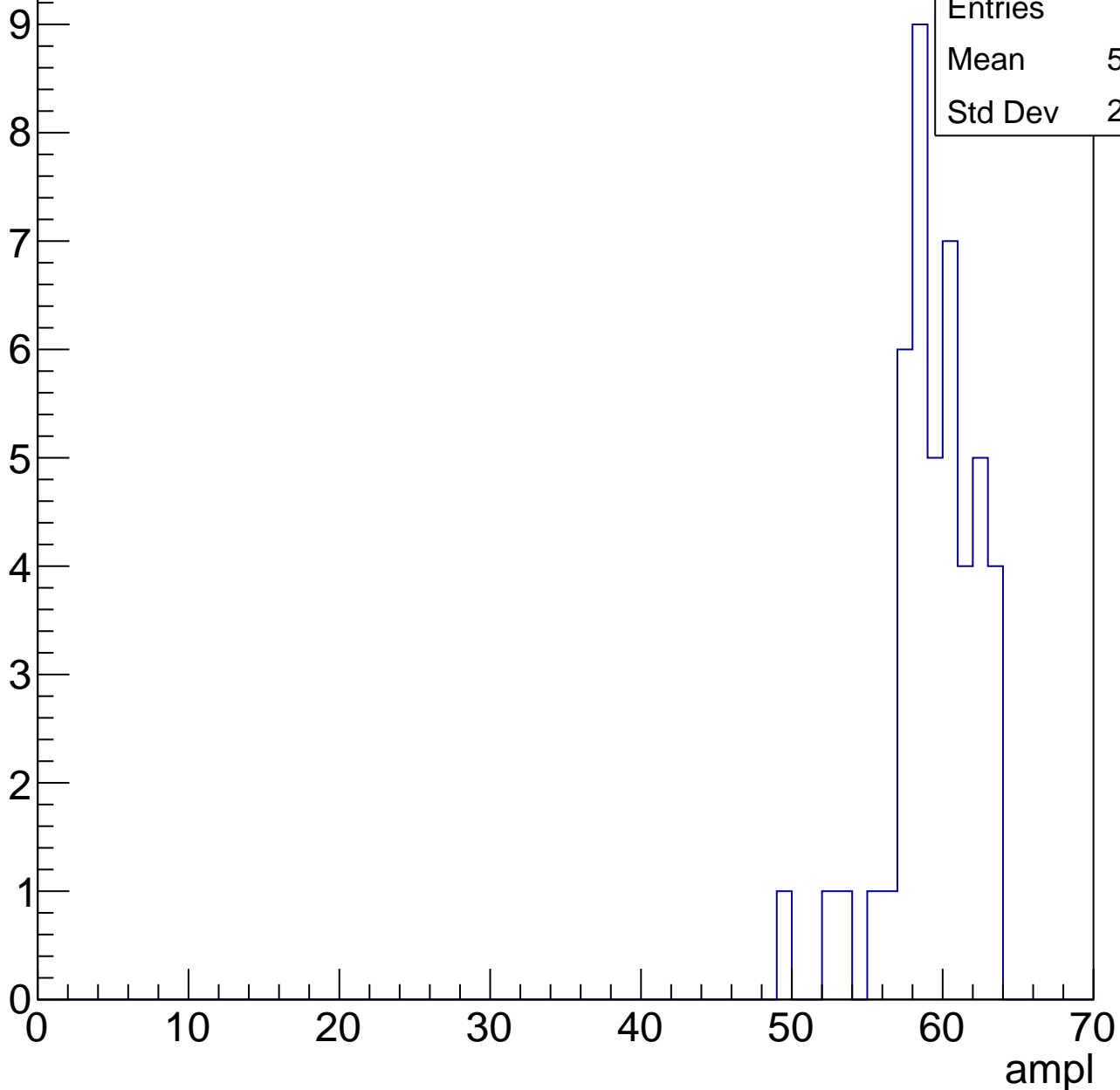


B1L103S, U6-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.89
Std Dev	2.885

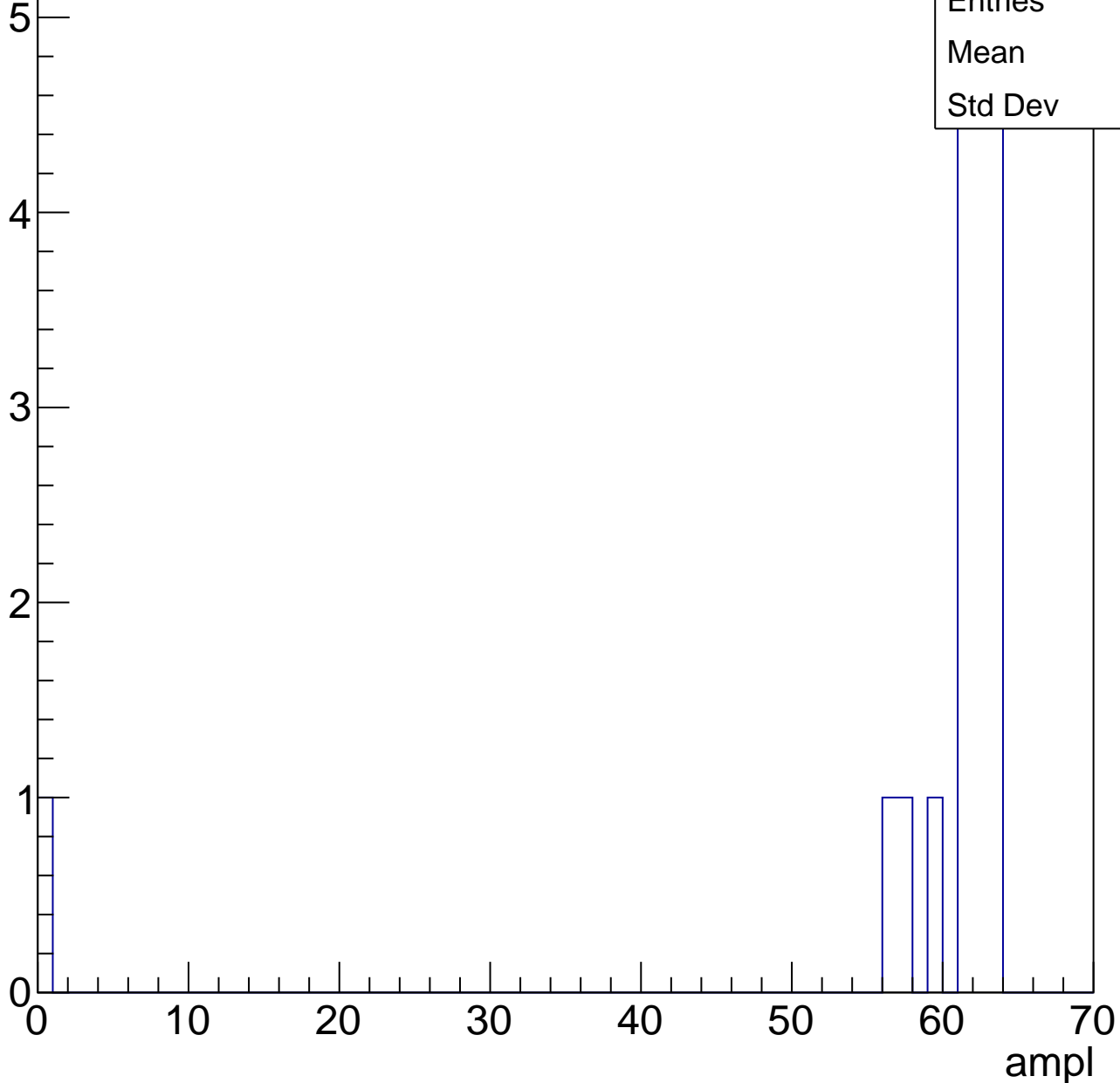


B1L103S, U6-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58
Std Dev	13.8



B1L103S, U6-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

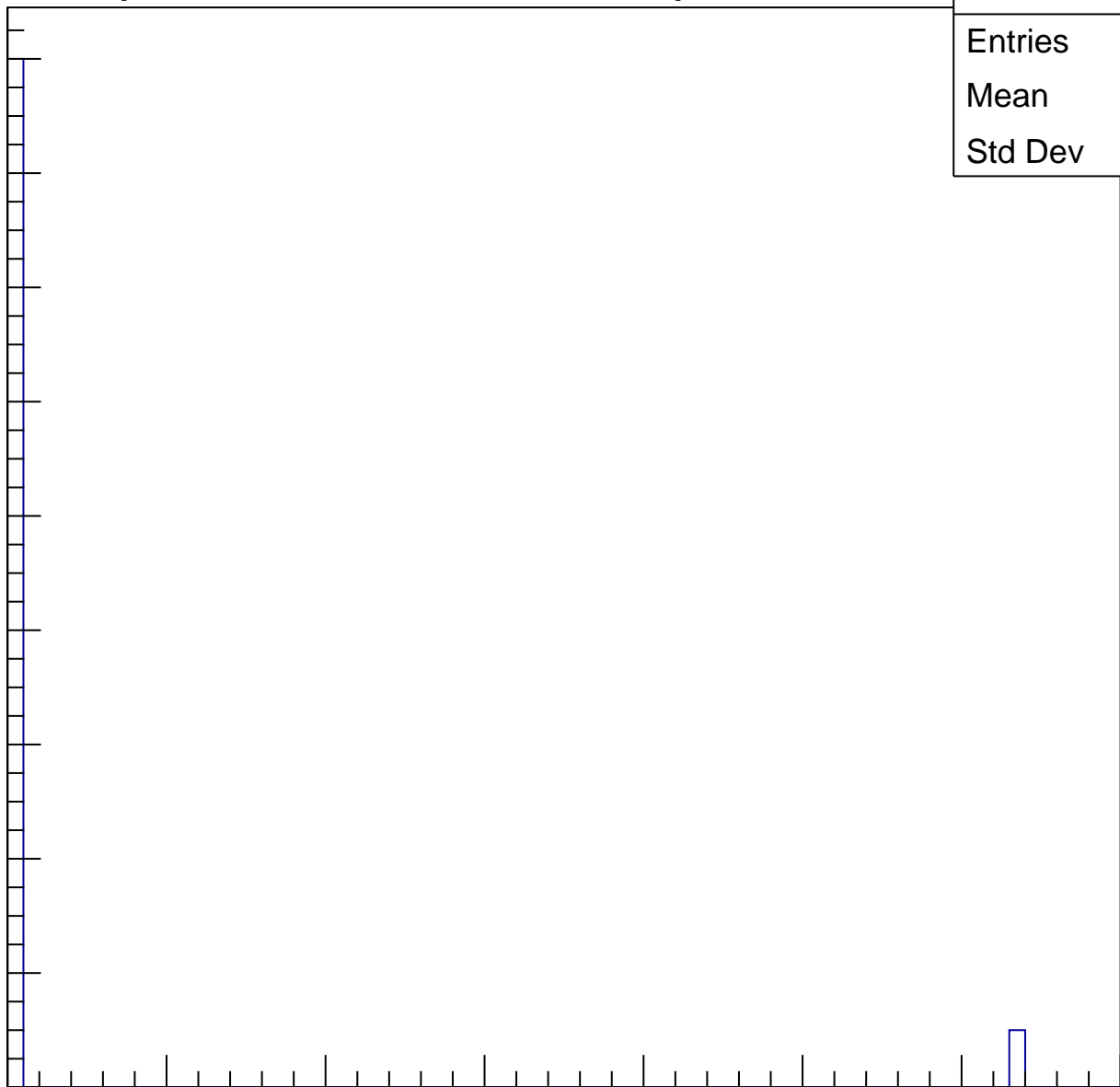
Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	3.316
Std Dev	14.07

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch72, adc0

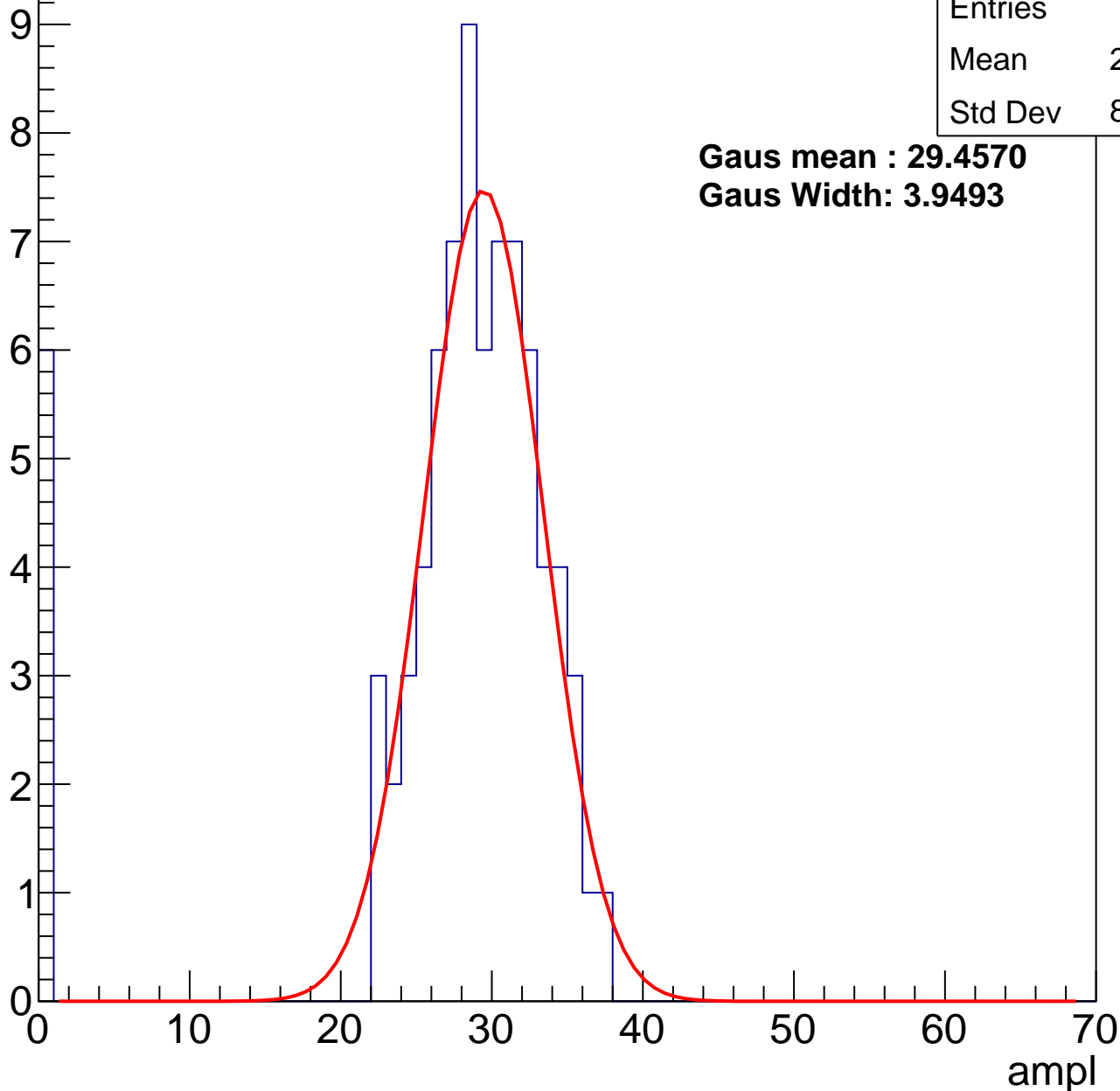
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	26.84
Std Dev	8.419

Gaus mean : 29.4570

Gaus Width: 3.9493



B1L103S, U6-ch72, adc1

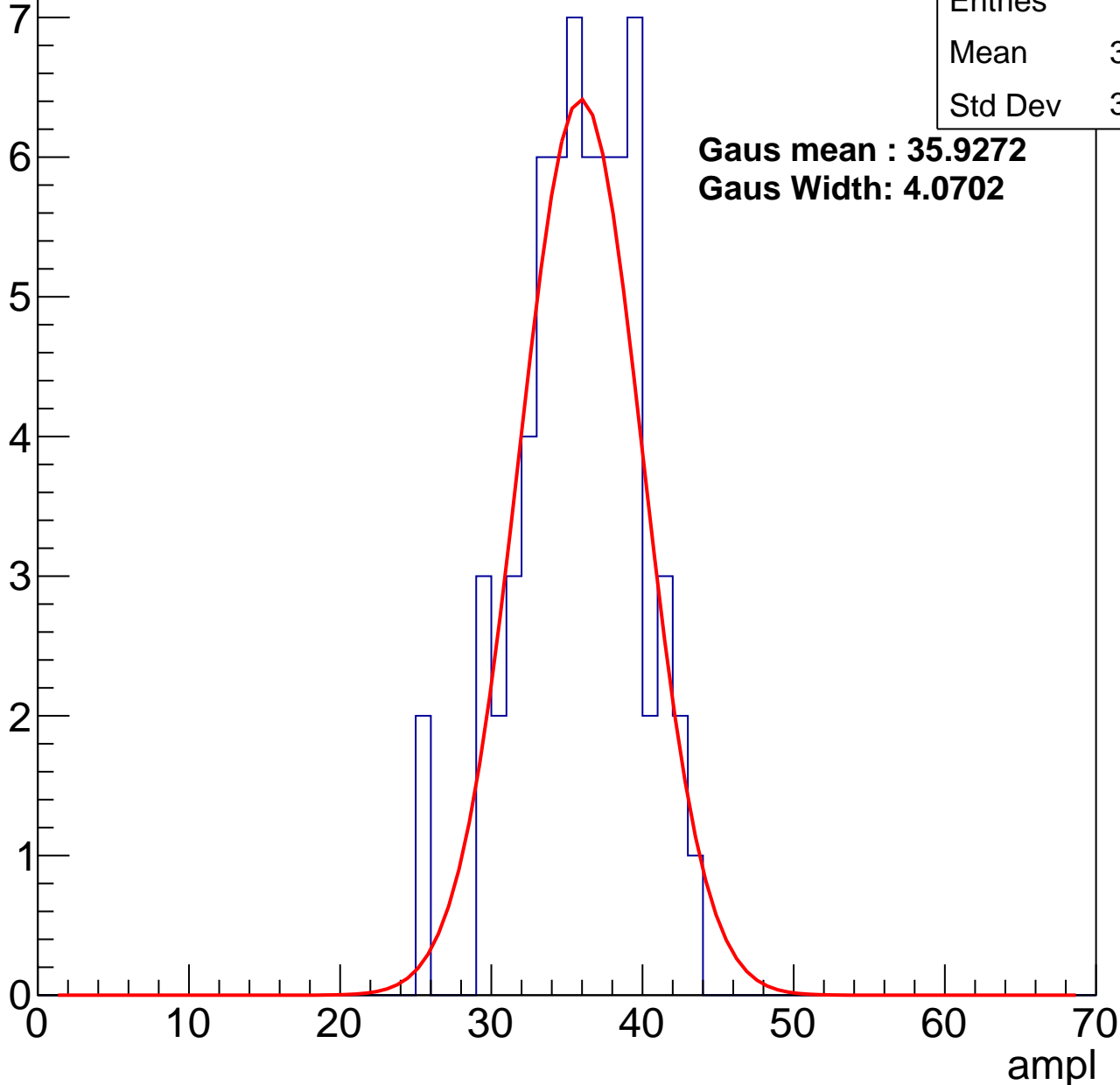
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.36
Std Dev	3.852

Gaus mean : 35.9272

Gaus Width: 4.0702



B1L103S, U6-ch72, adc2

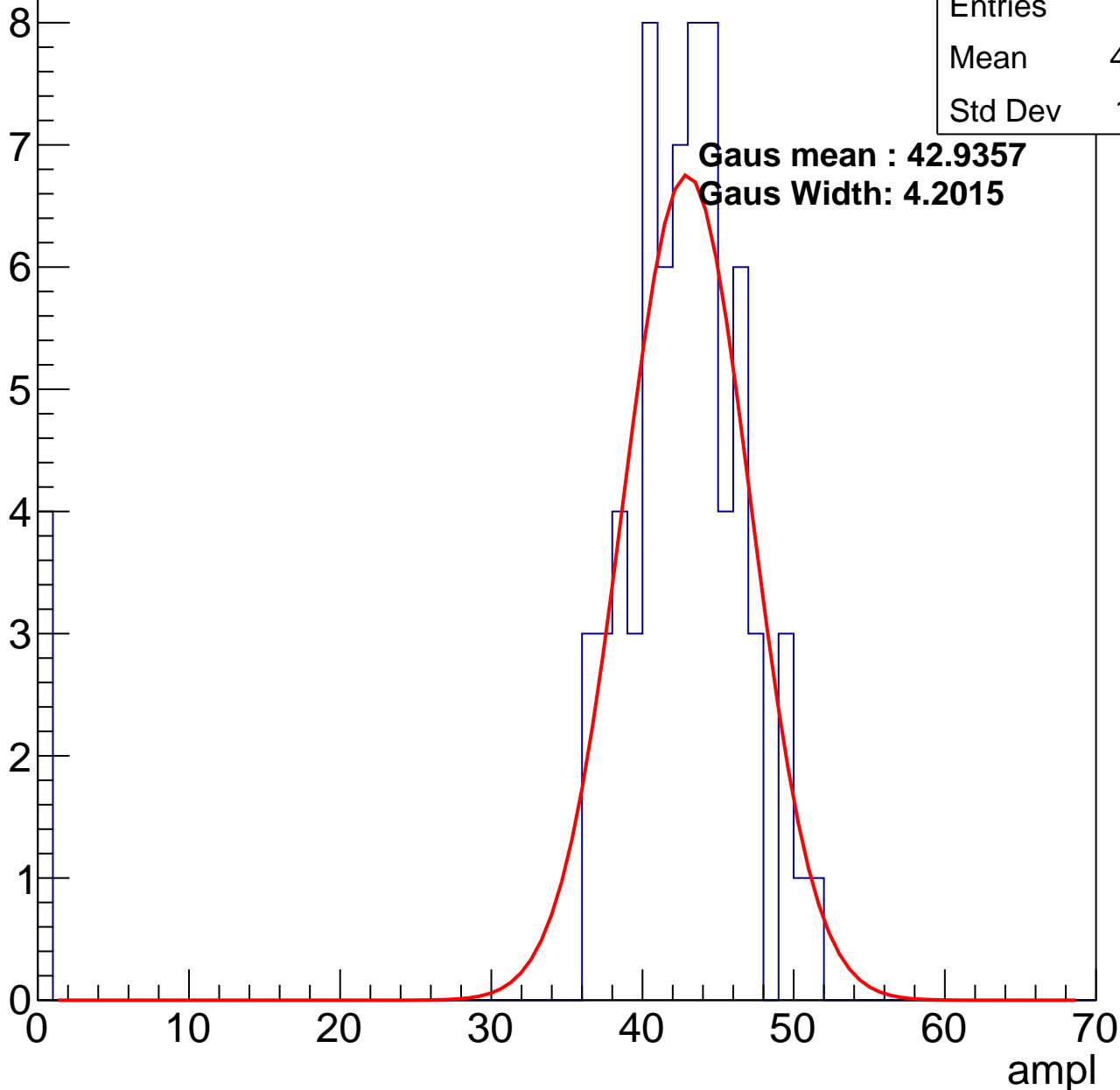
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	40.12
Std Dev	10.31

Gaus mean : 42.9357

Gaus Width: 4.2015

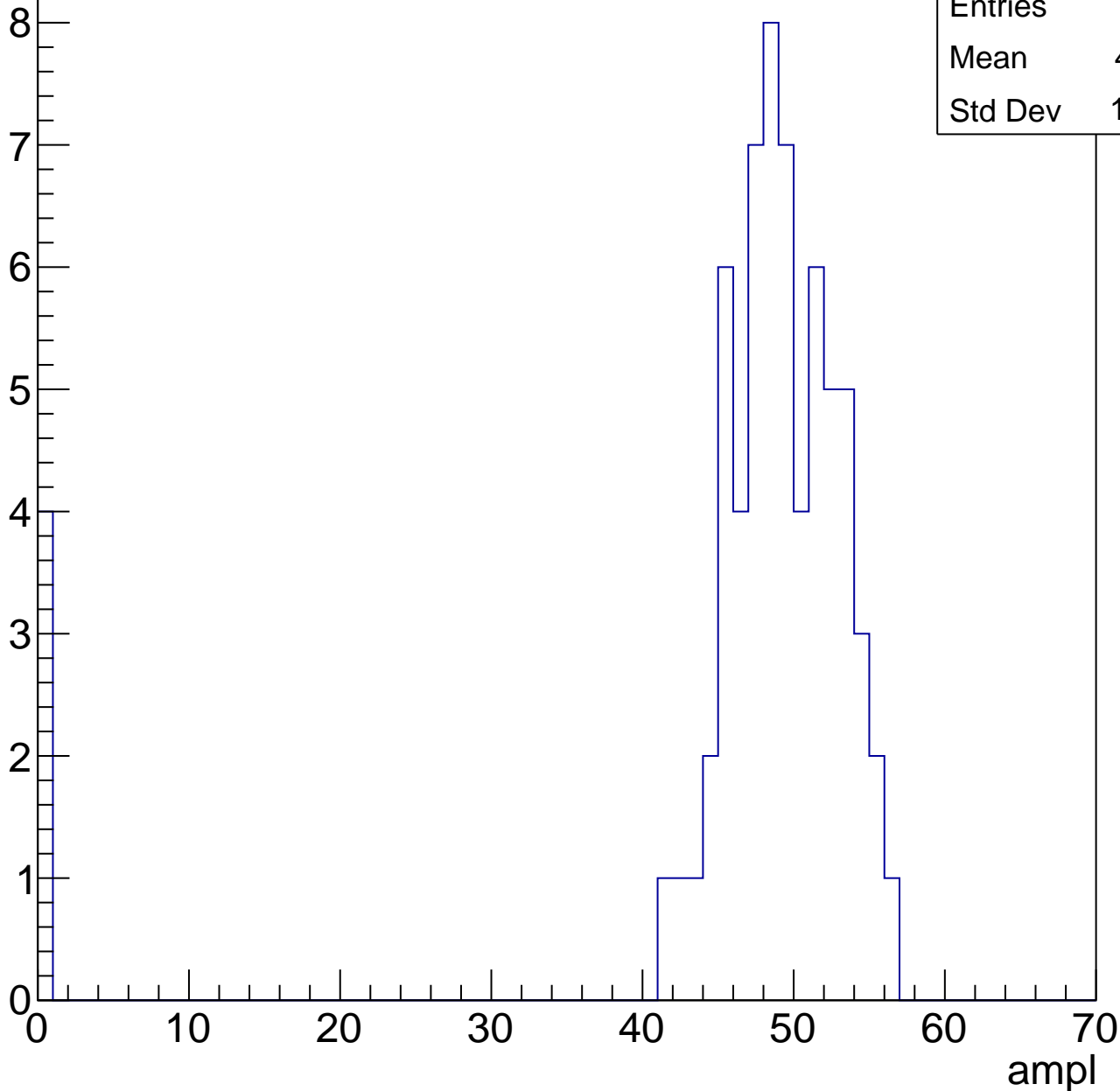


B1L103S, U6-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	46.01
Std Dev	12.05

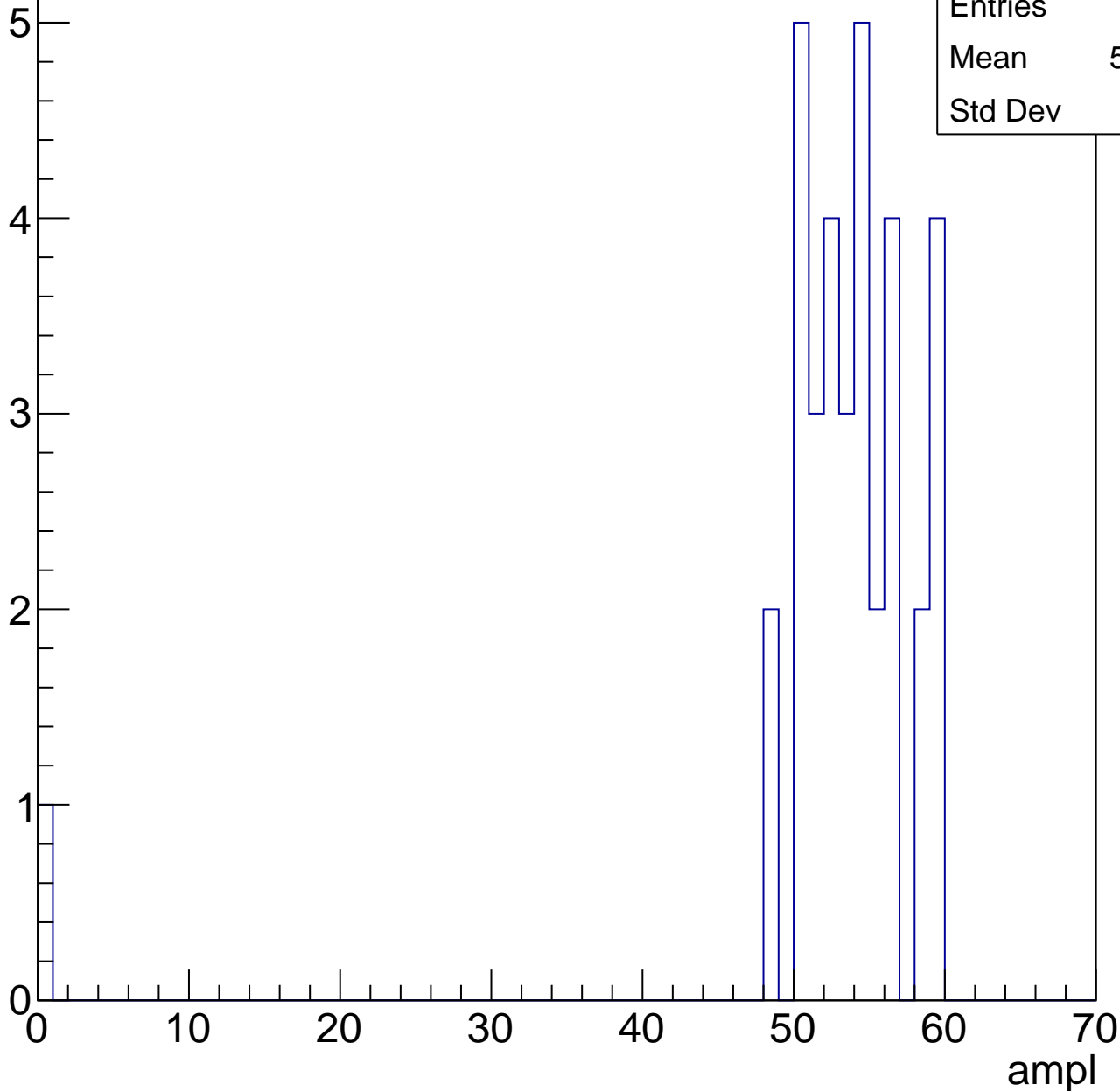


B1L103S, U6-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	52.06
Std Dev	9.46



B1L103S, U6-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

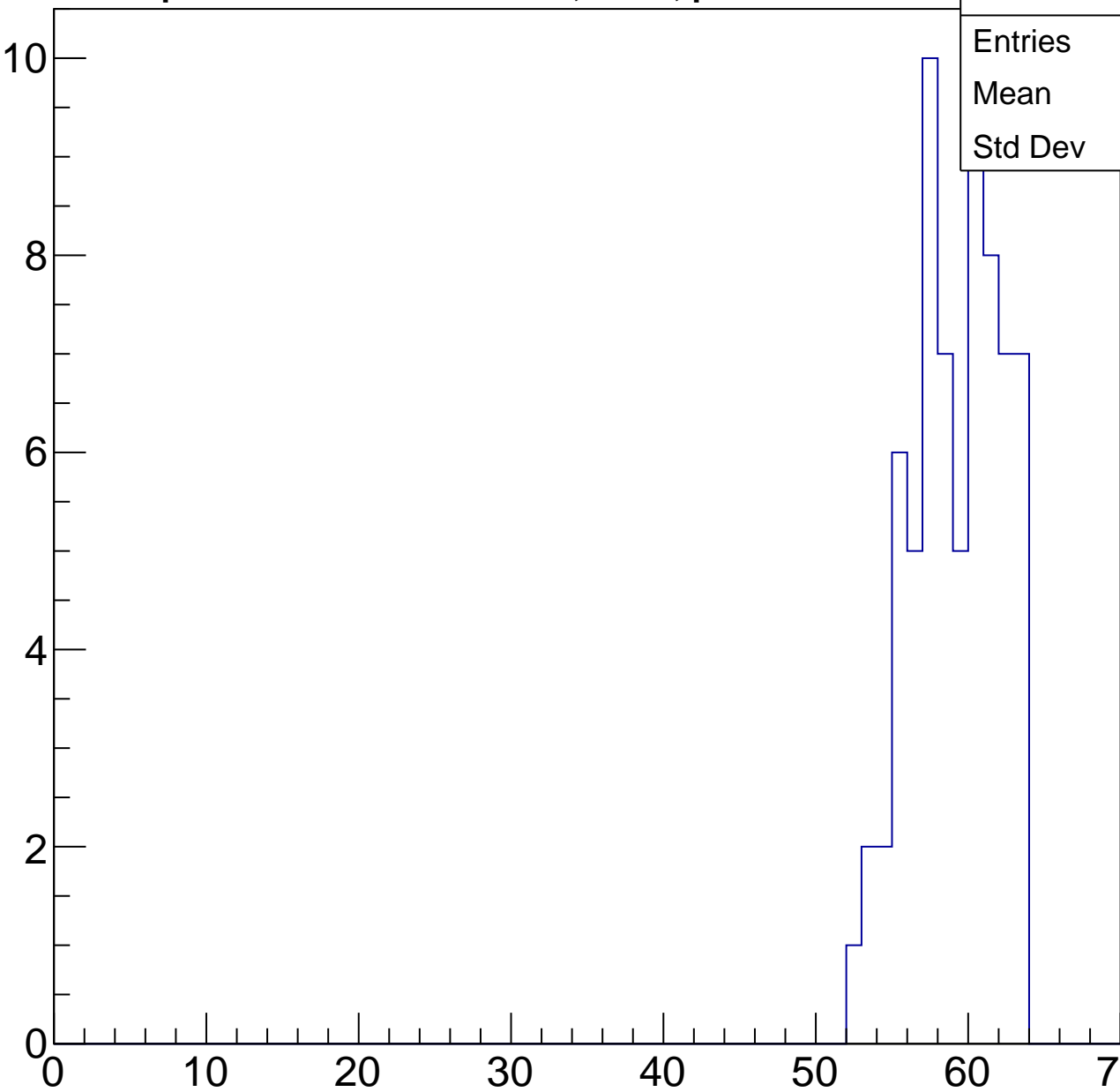
Entries	70
Mean	58.71
Std Dev	2.854

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

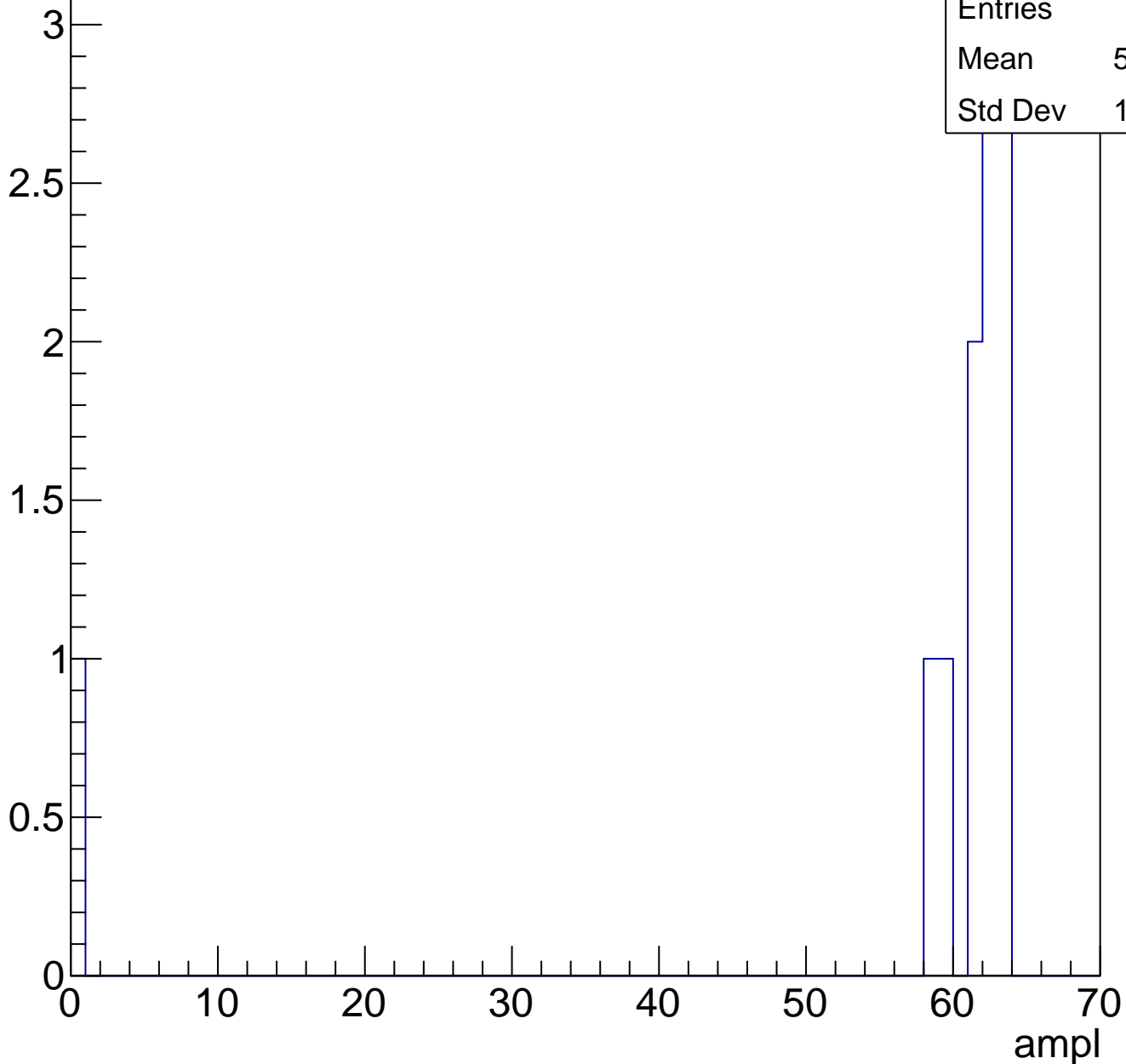
ampl



B1L103S, U6-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

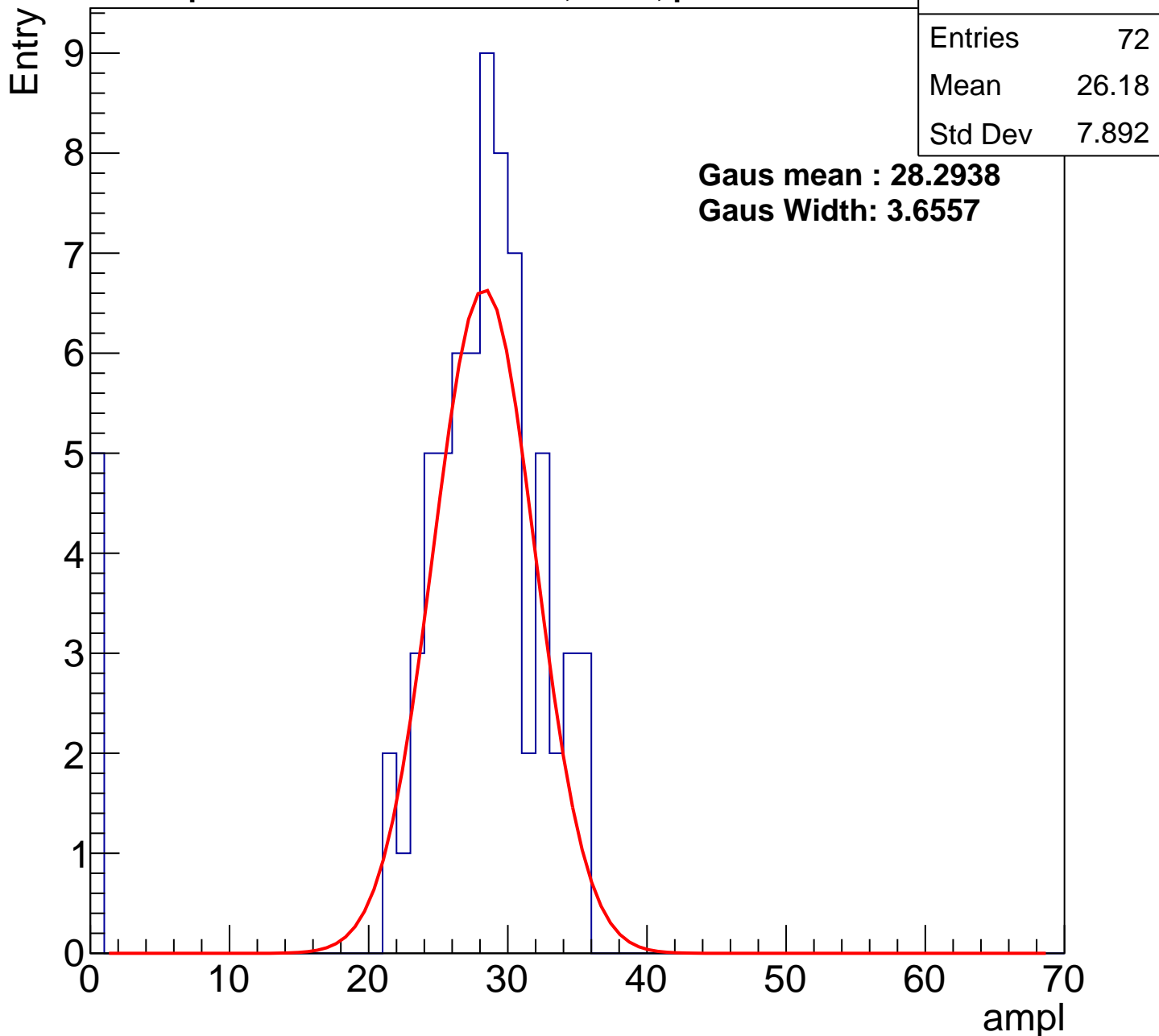
18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch73, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch73, adc1

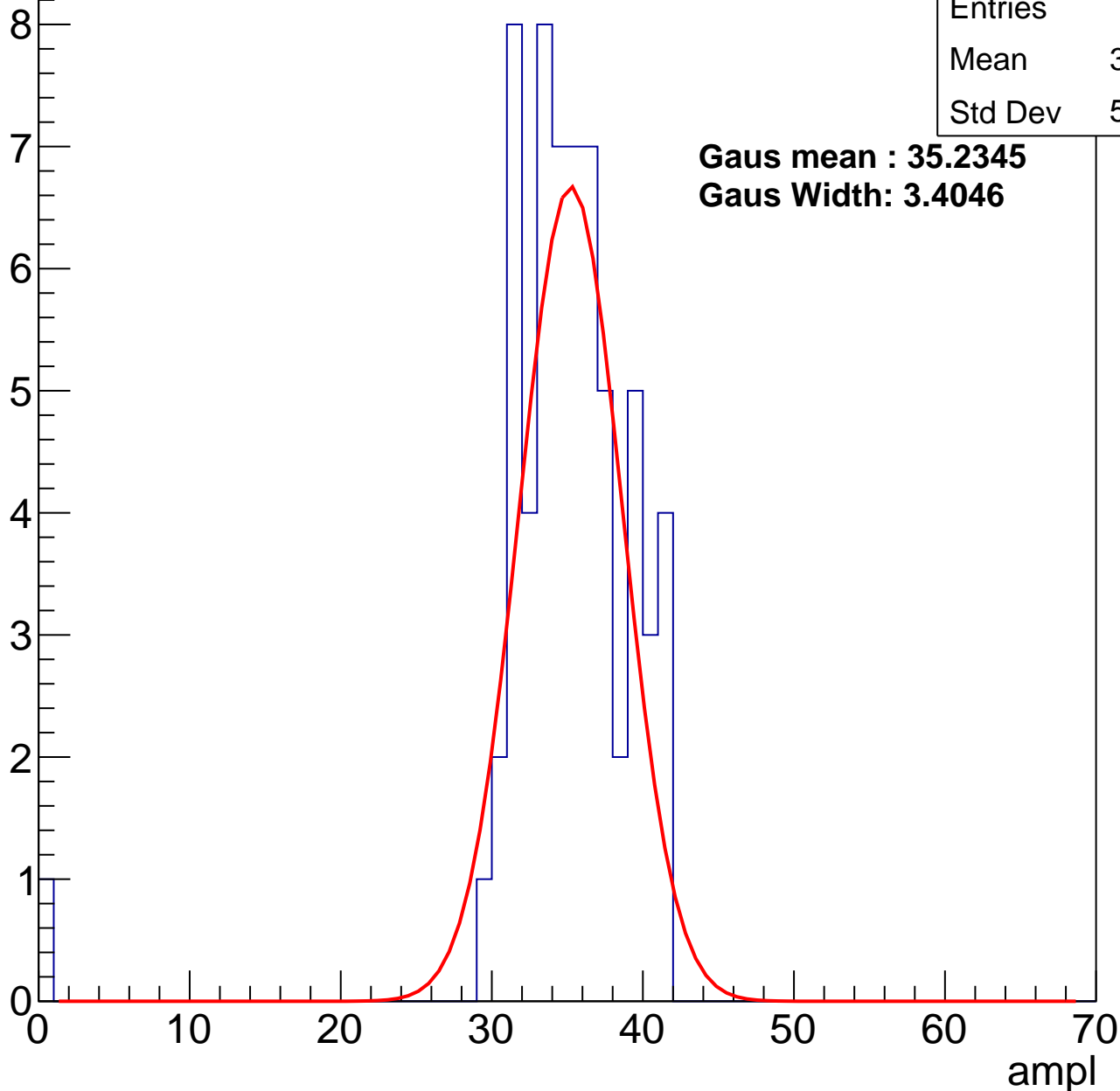
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.44
Std Dev	5.359

Gaus mean : 35.2345

Gaus Width: 3.4046



B1L103S, U6-ch73, adc2

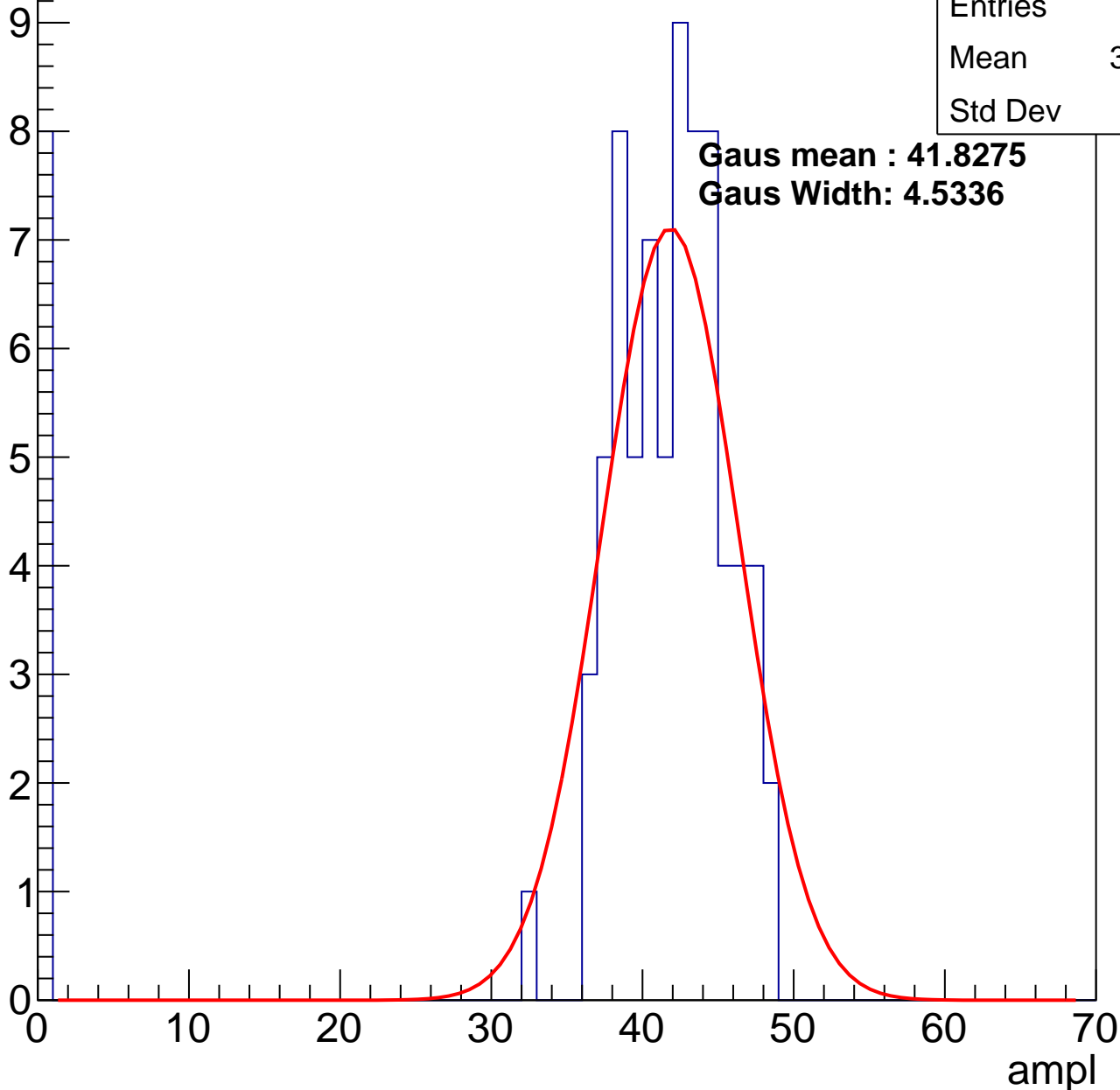
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	37.42
Std Dev	12.8

Gaus mean : 41.8275

Gaus Width: 4.5336



B1L103S, U6-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

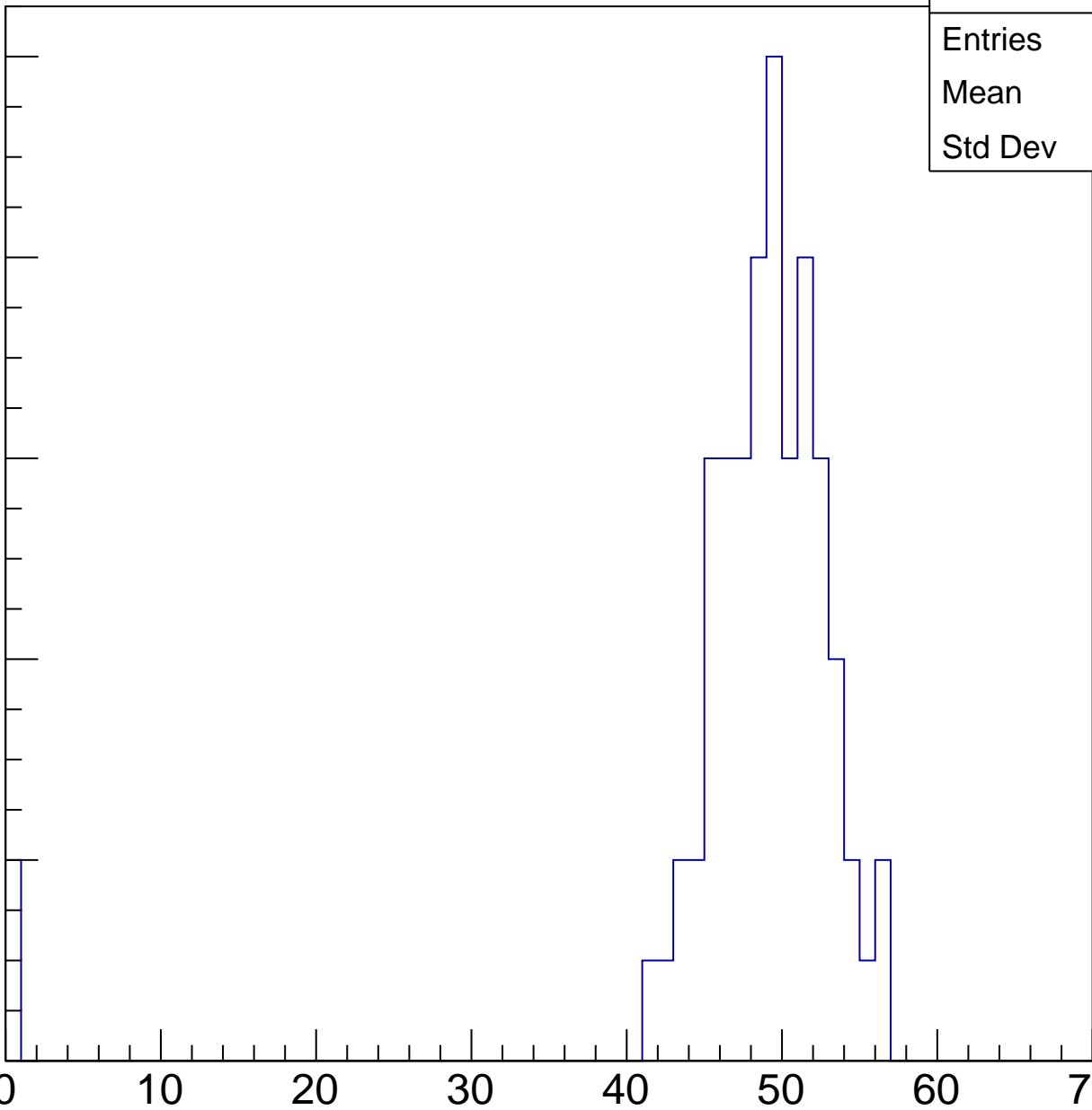
Entries	73
Mean	47.48
Std Dev	8.593

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

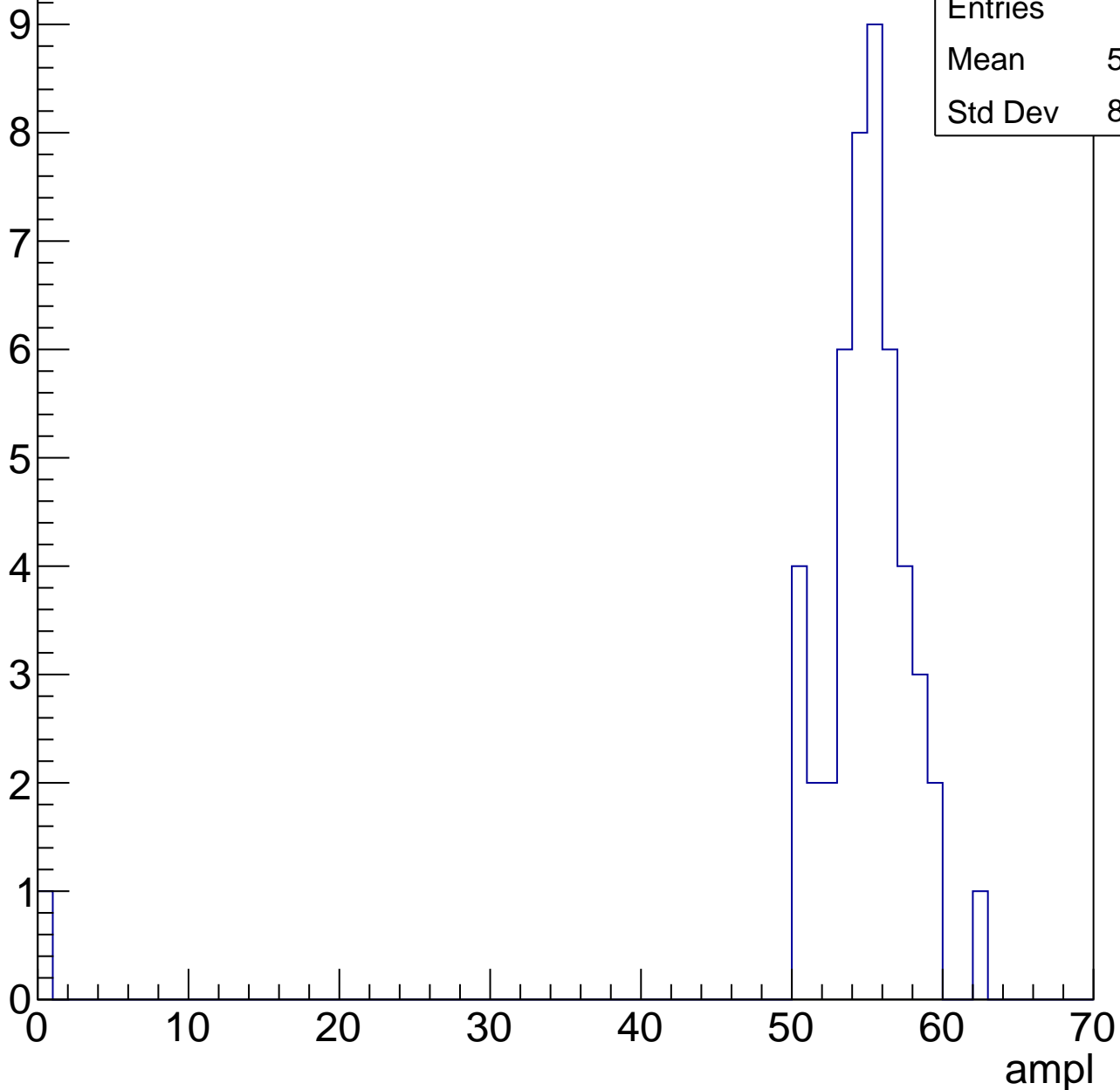


B1L103S, U6-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.52
Std Dev	8.206



B1L103S, U6-ch73, adc5

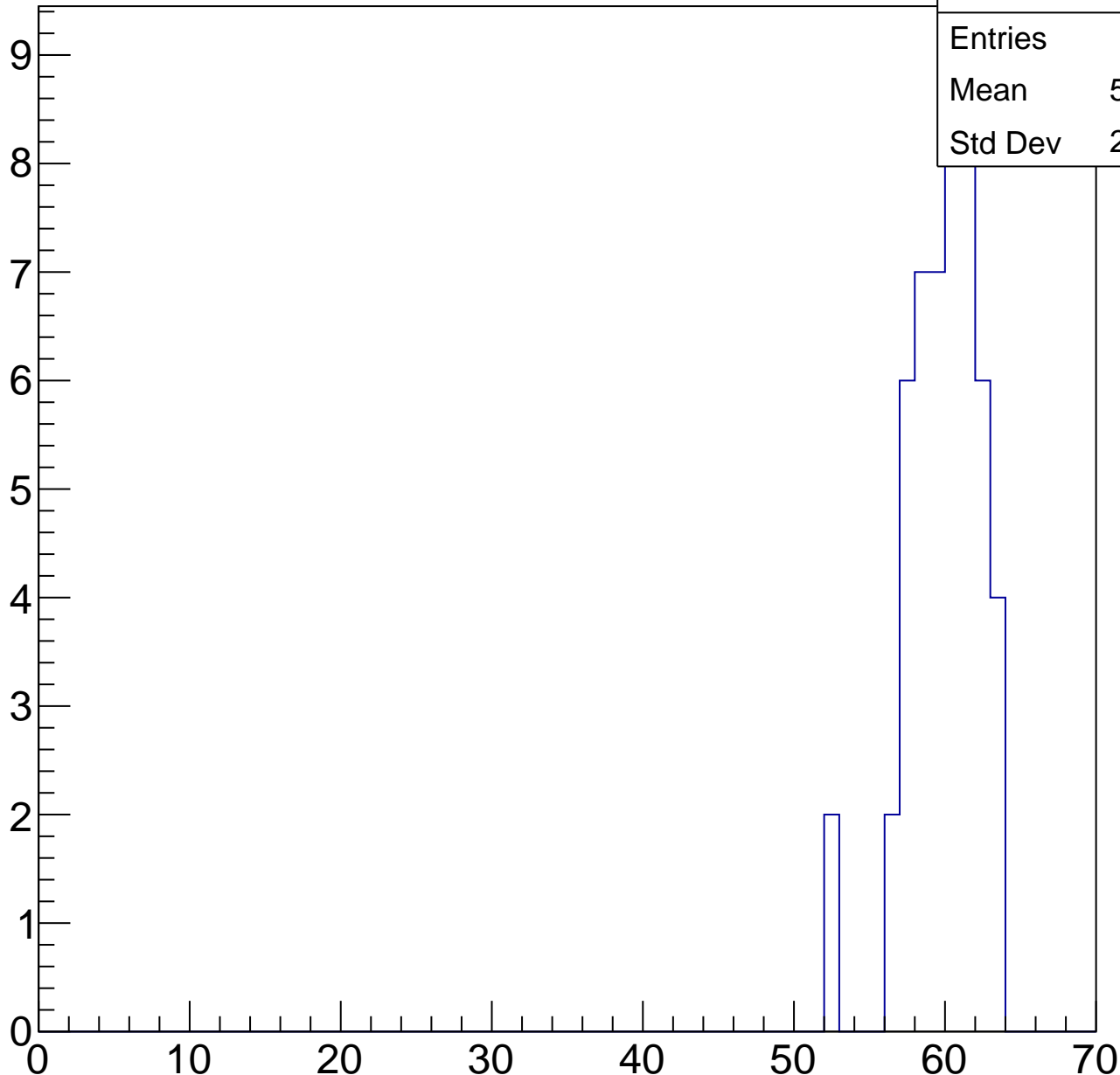
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.42
Std Dev	2.405

ampl

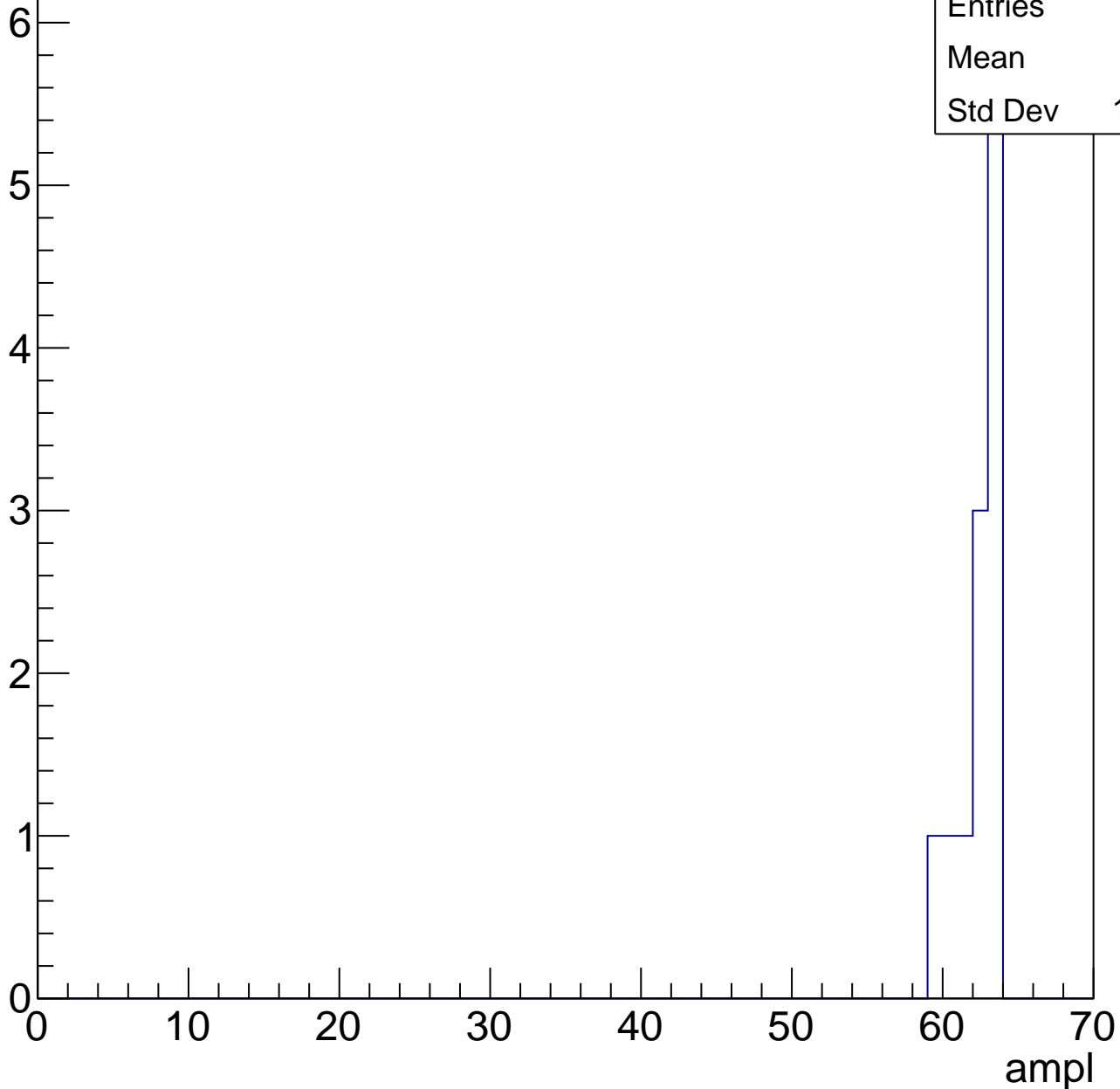


B1L103S, U6-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62
Std Dev	1.291



B1L103S, U6-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch74, adc0

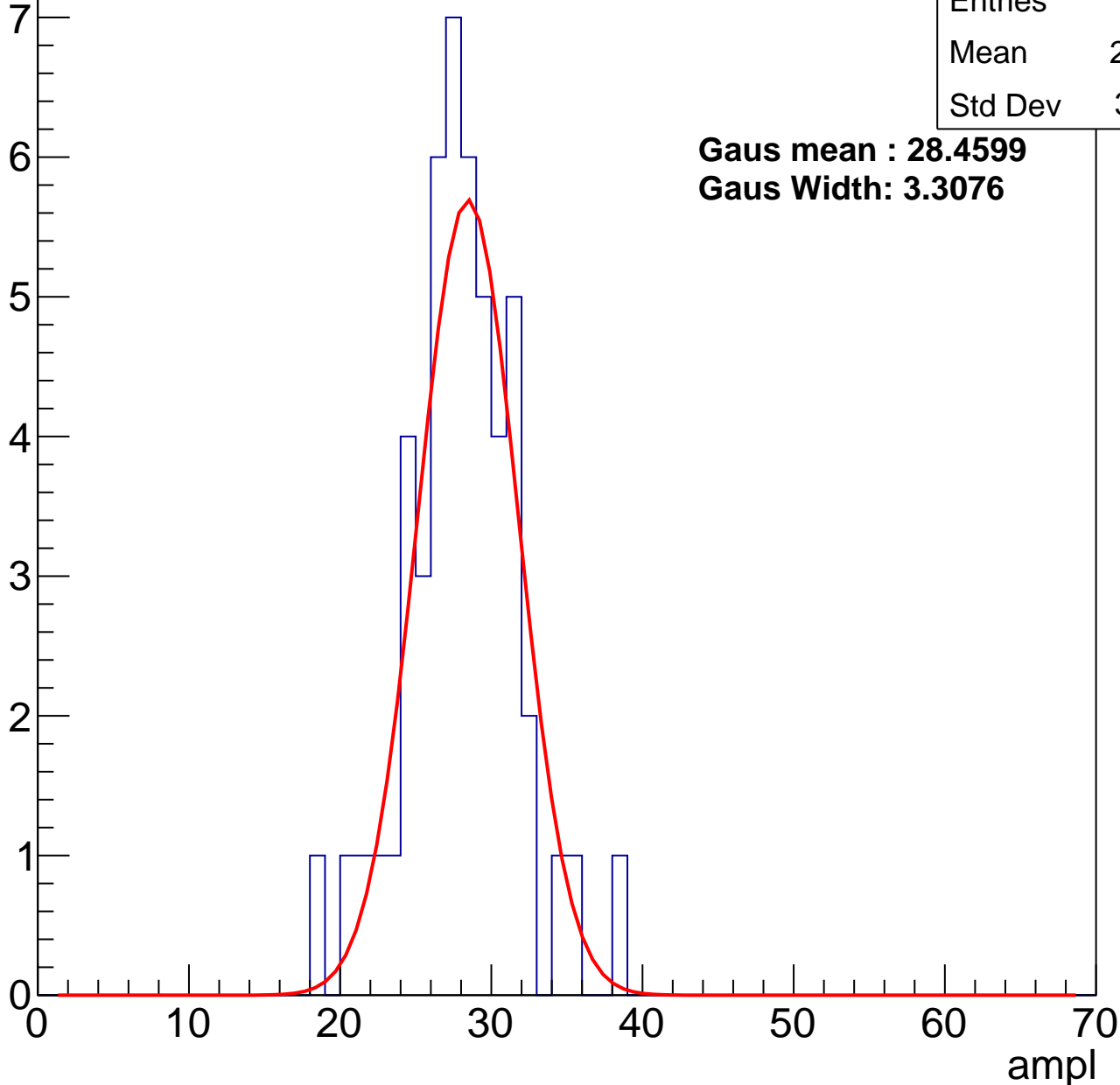
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	27.58
Std Dev	3.661

Gaus mean : 28.4599

Gaus Width: 3.3076



B1L103S, U6-ch74, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	32.81
Std Dev	6.293

Gaus mean : 34.1584

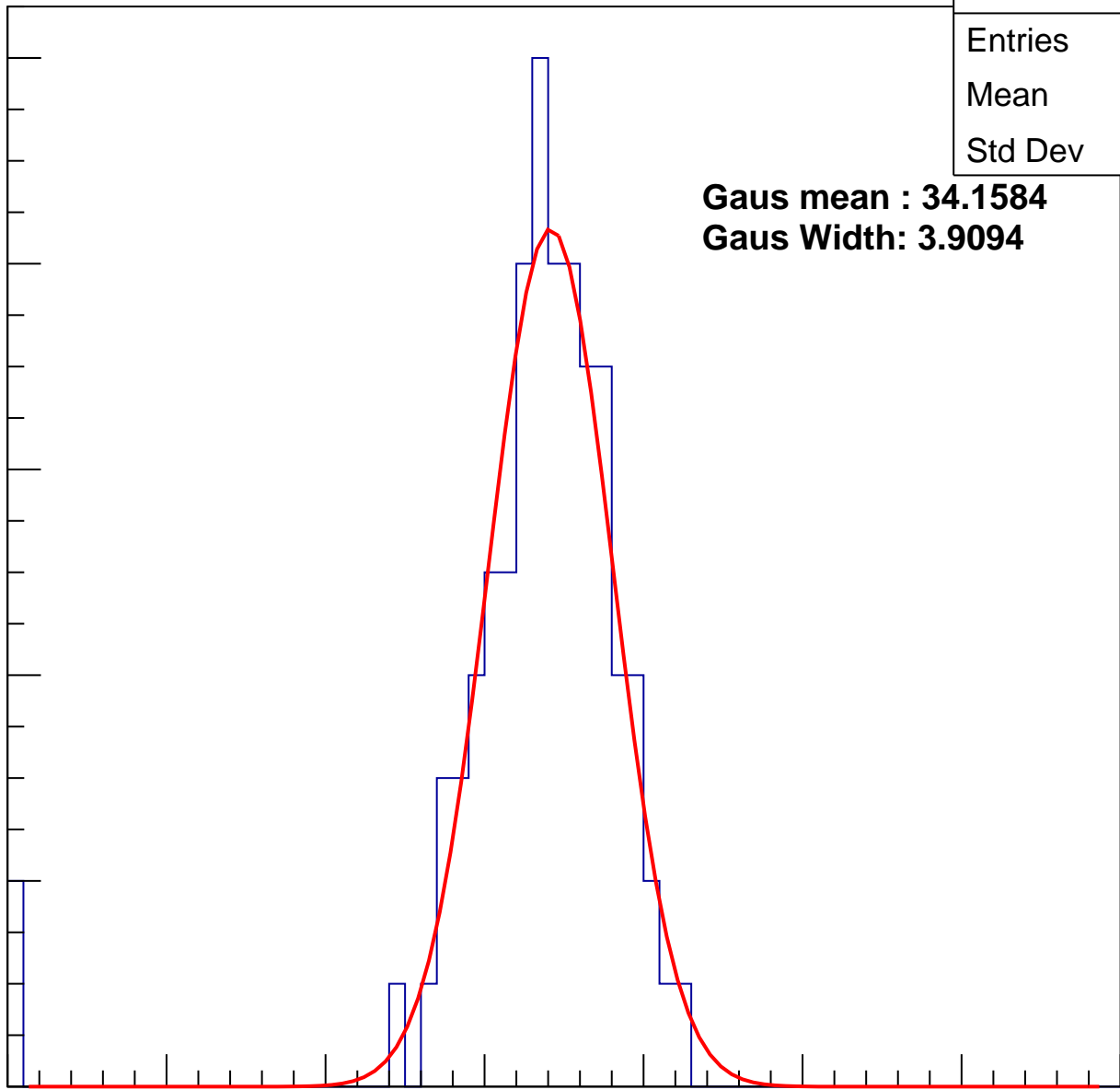
Gaus Width: 3.9094

Entry

10
8
6
4
2
0

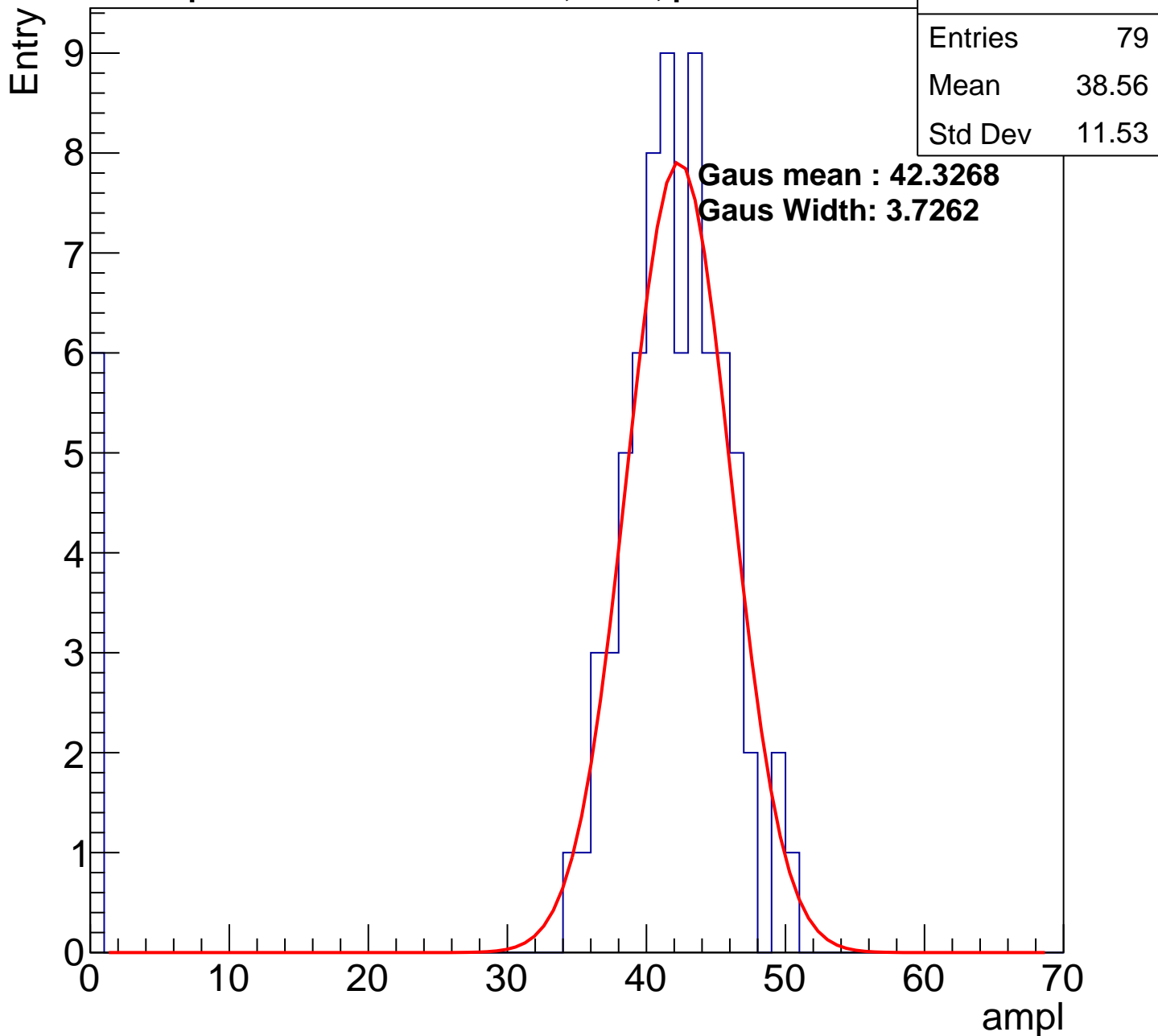
ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch74, adc2

calib_packv5_041523_1651.root, FC#0, port C2

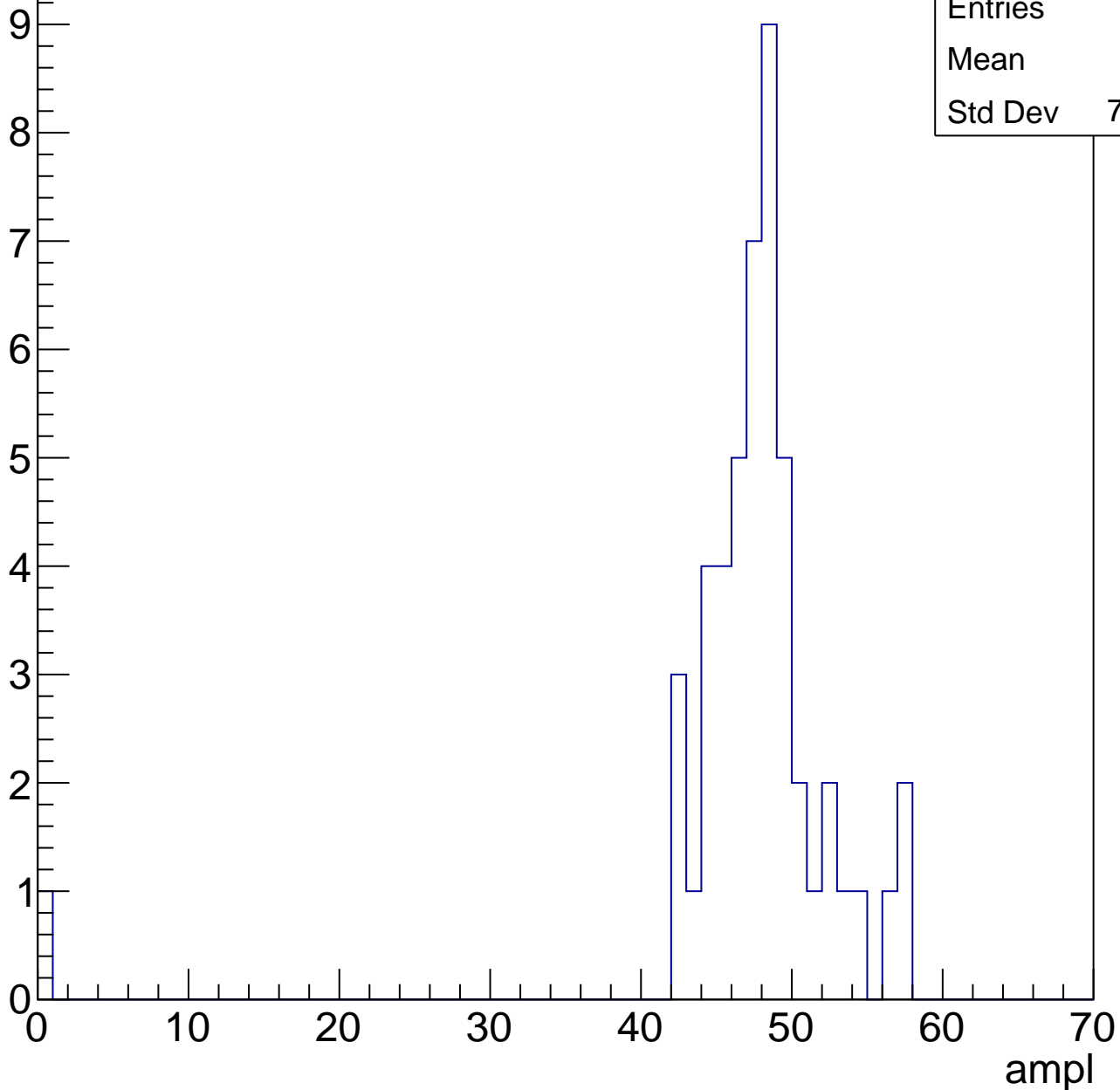


B1L103S, U6-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	46.8
Std Dev	7.605

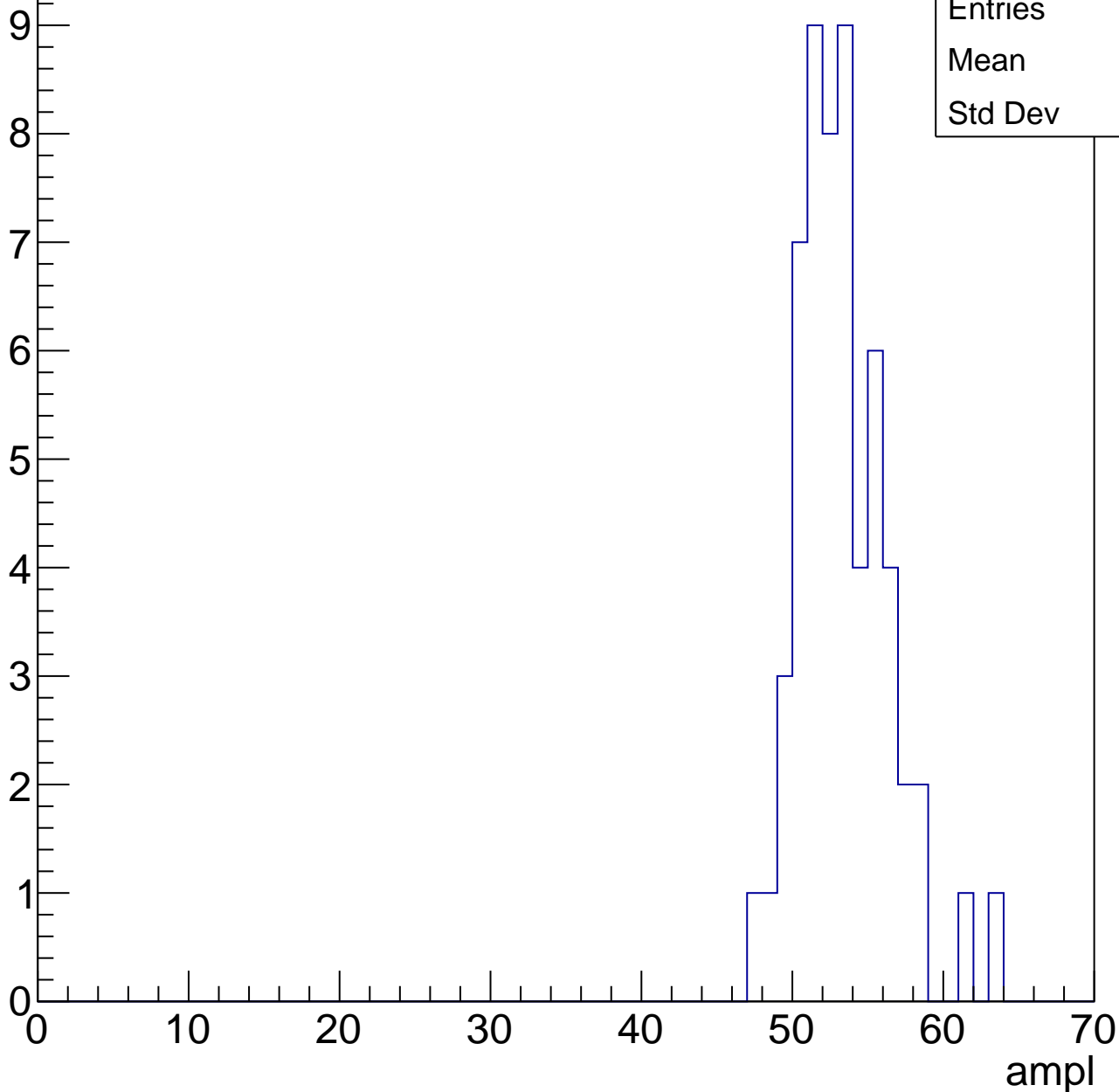


B1L103S, U6-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.9
Std Dev	3.01

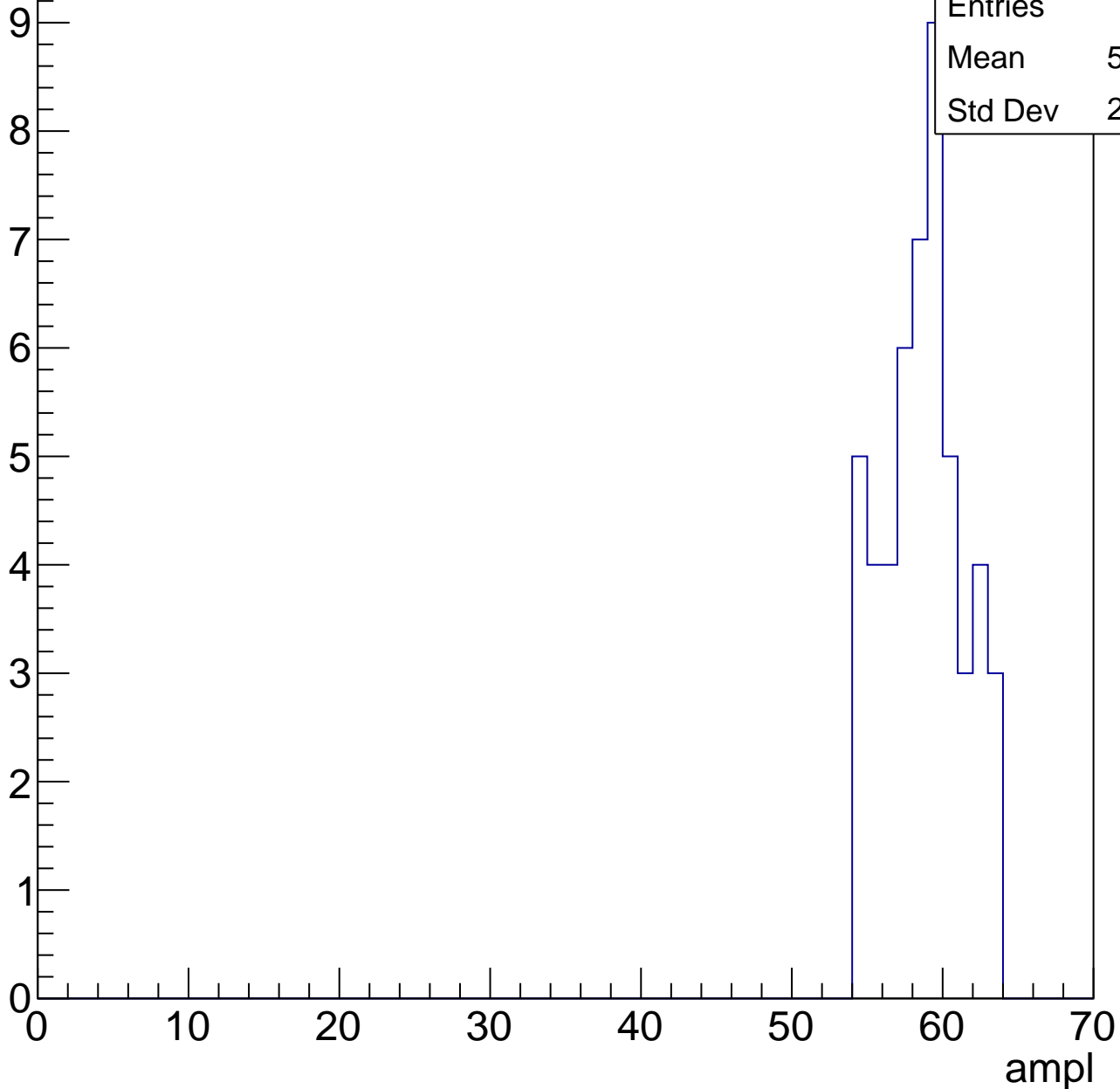


B1L103S, U6-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.26
Std Dev	2.568

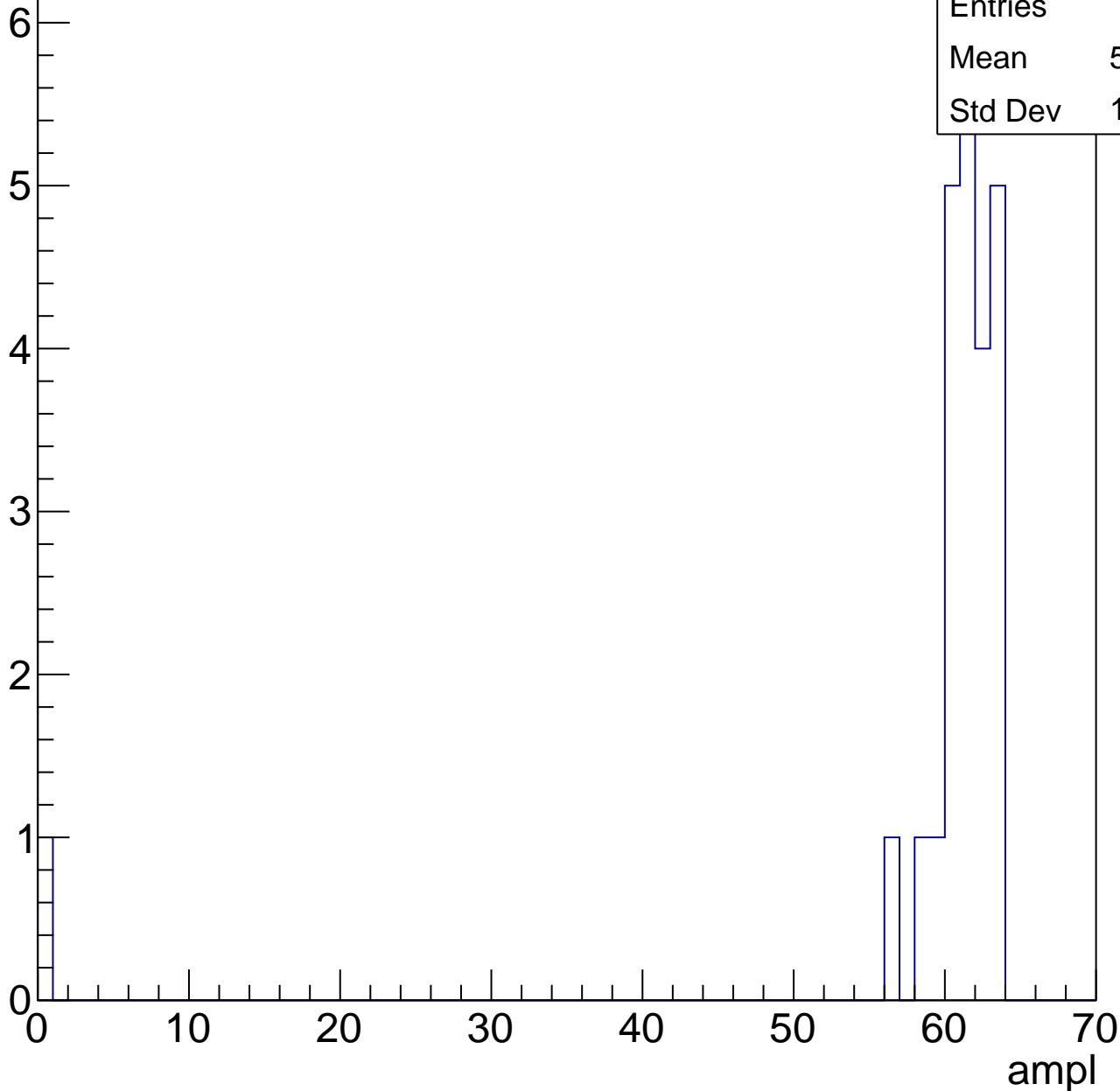


B1L103S, U6-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.42
Std Dev	12.29

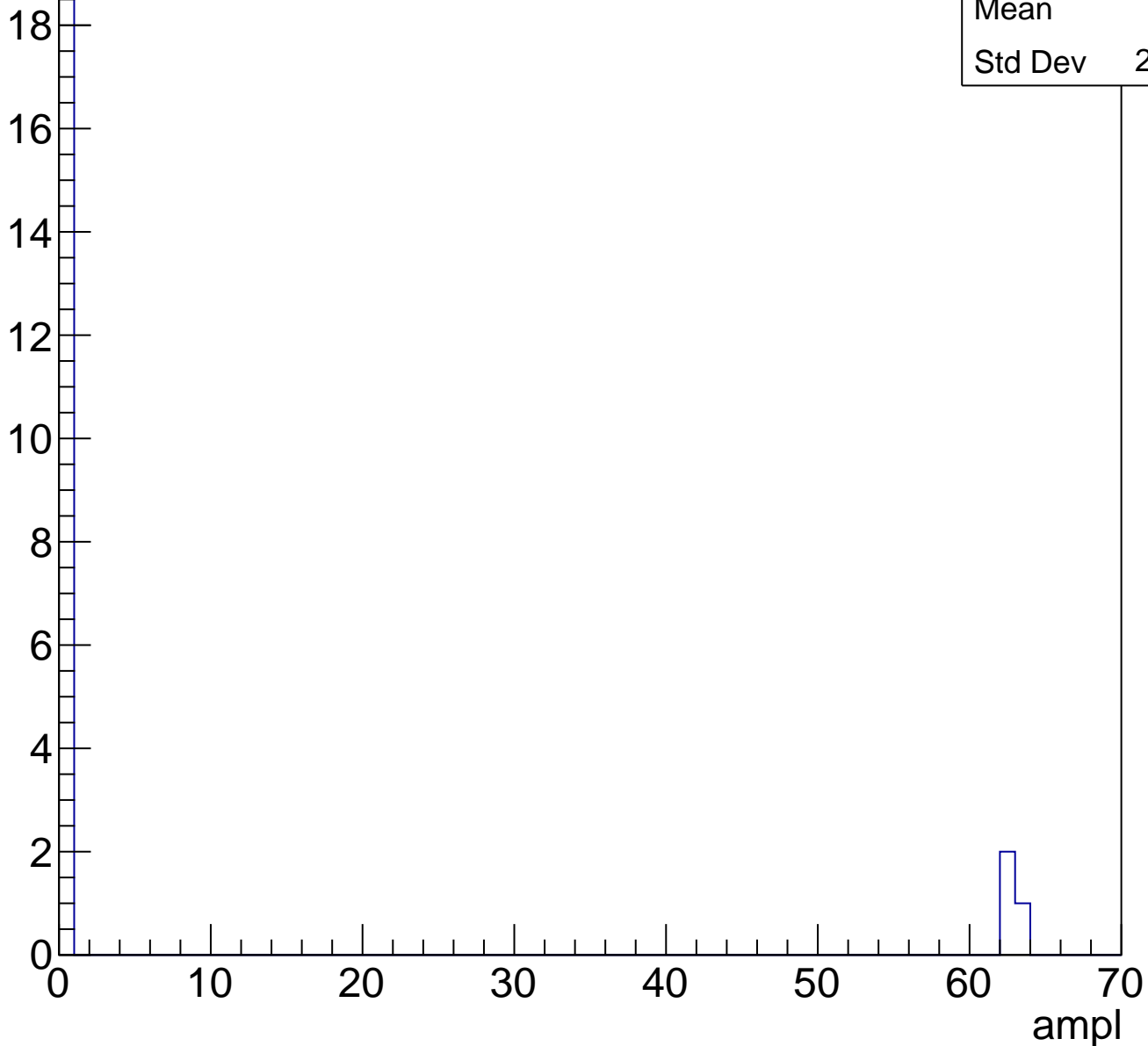


B1L103S, U6-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U6-ch75, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.47
Std Dev	13.13

Gaus mean : 29.0388

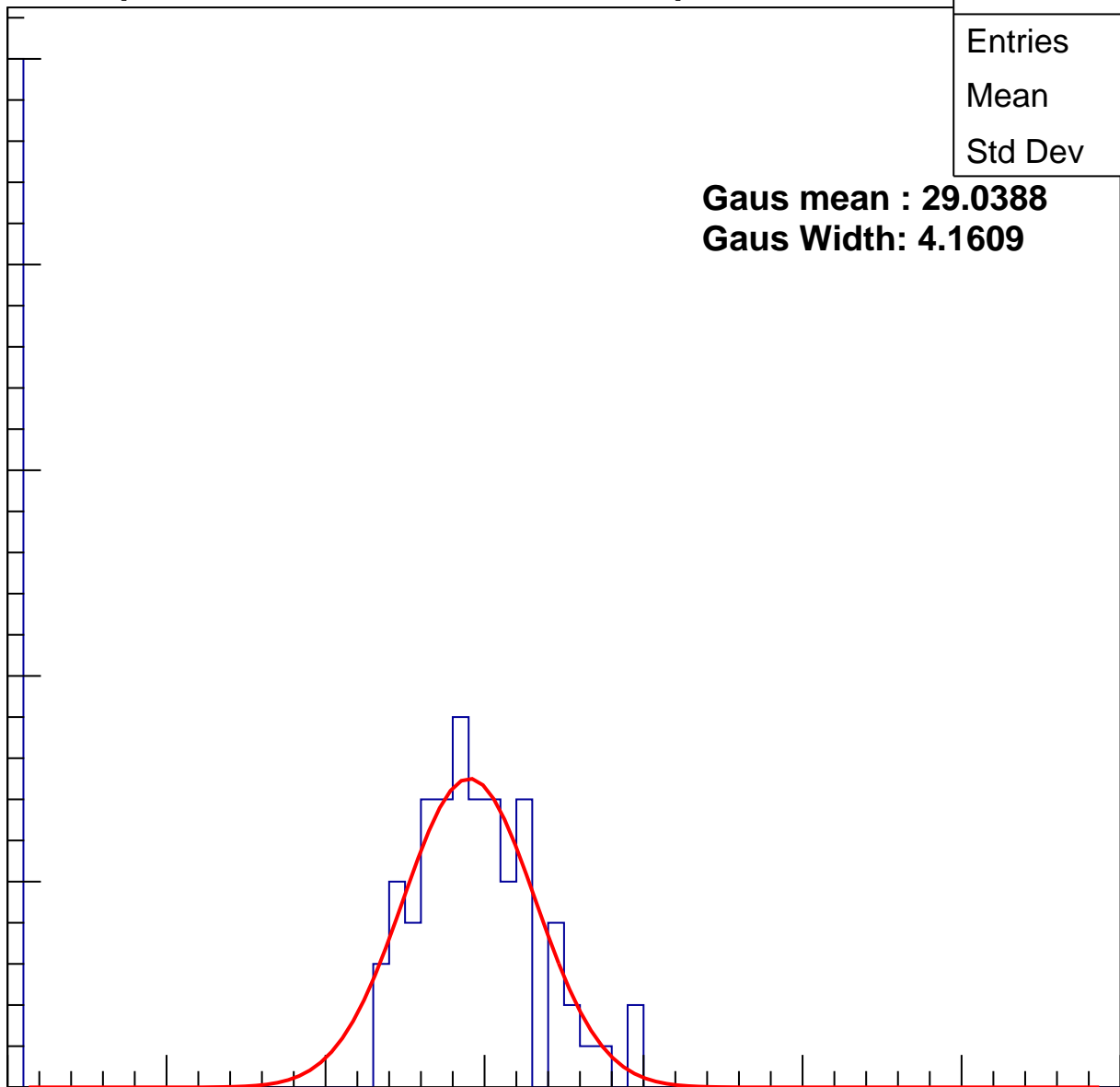
Gaus Width: 4.1609

Entry

25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch75, adc1

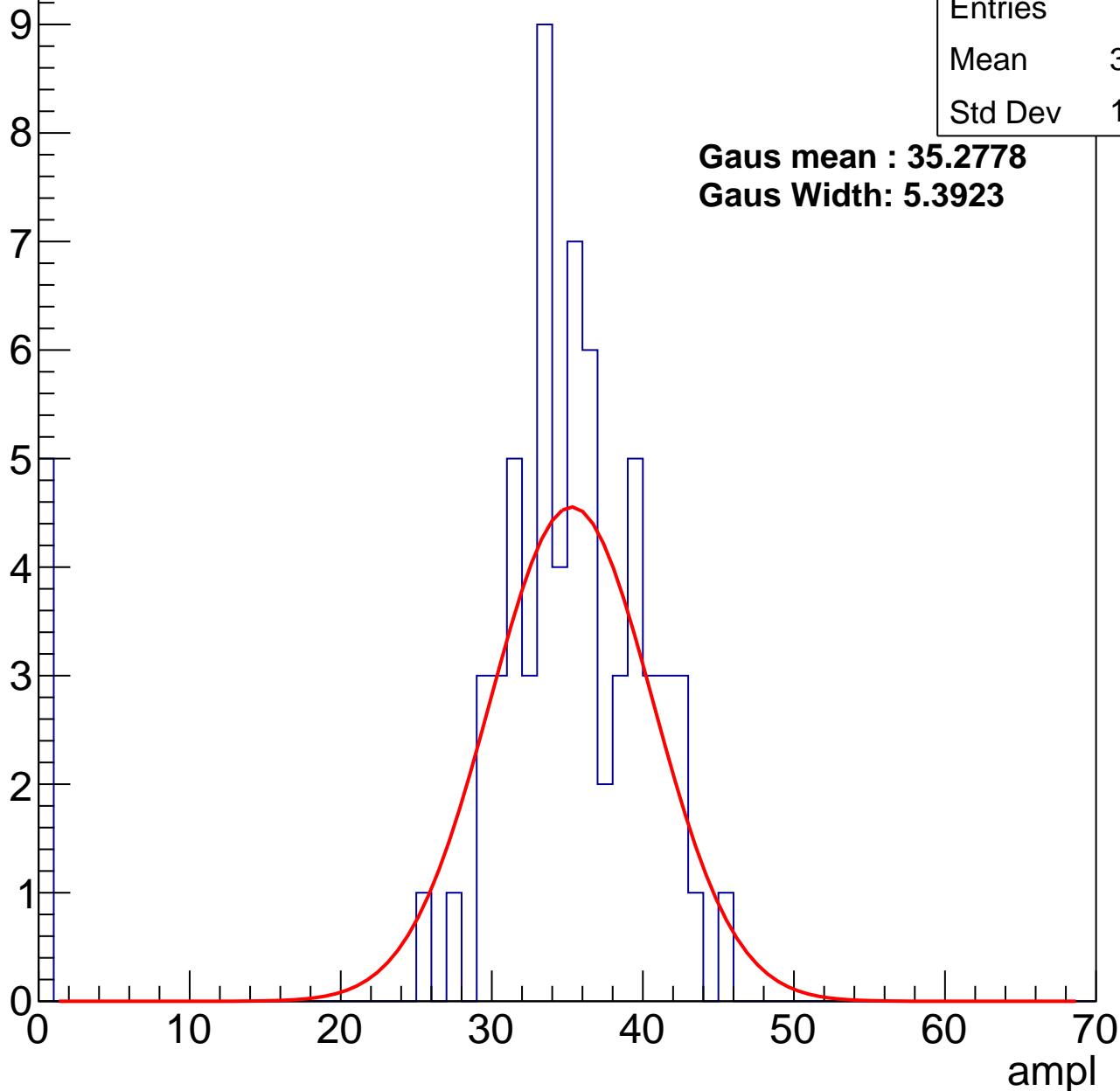
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	32.56
Std Dev	10.02

Gaus mean : 35.2778

Gaus Width: 5.3923



B1L103S, U6-ch75, adc2

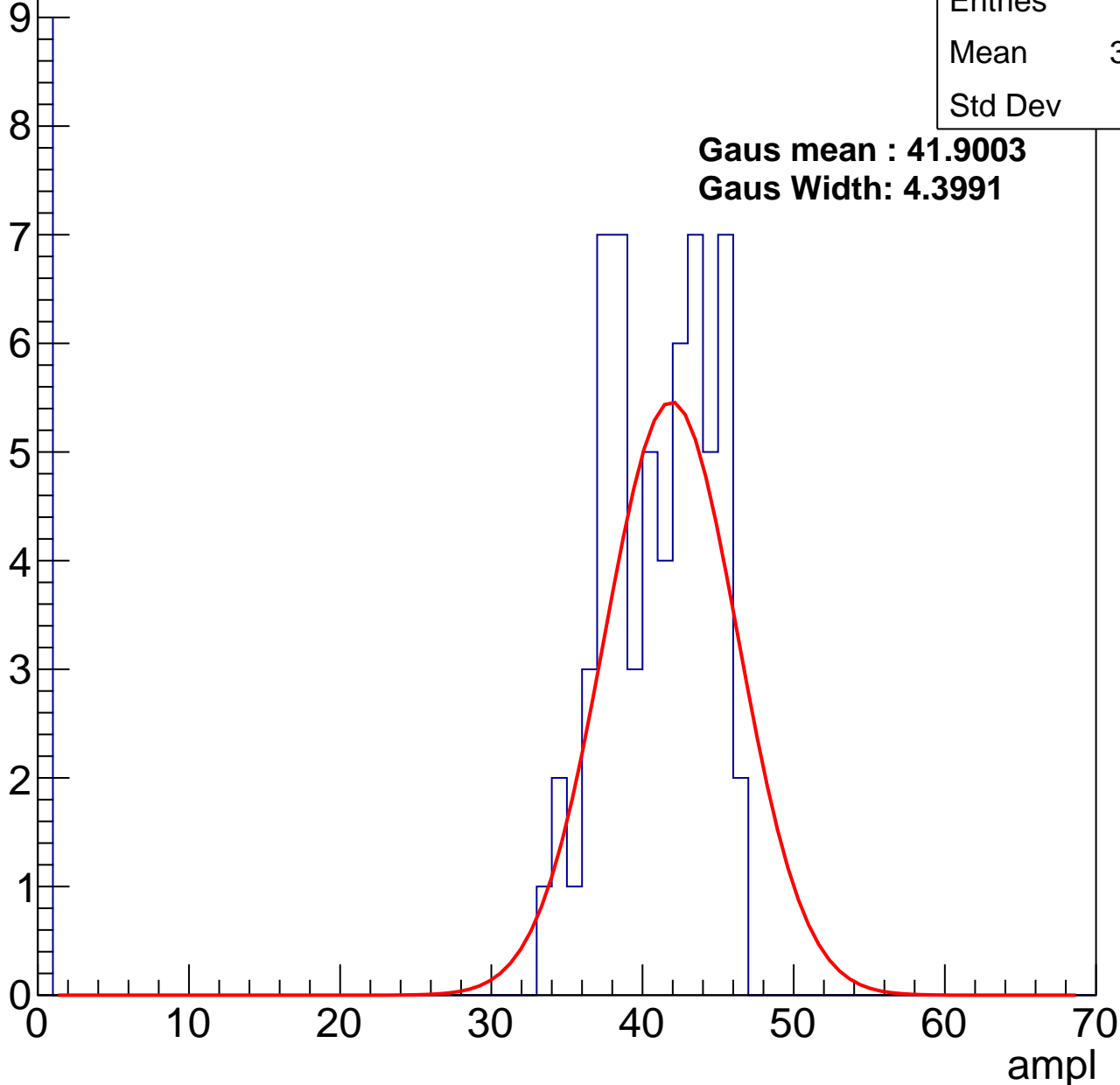
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.22
Std Dev	14

Gaus mean : 41.9003

Gaus Width: 4.3991



B1L103S, U6-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

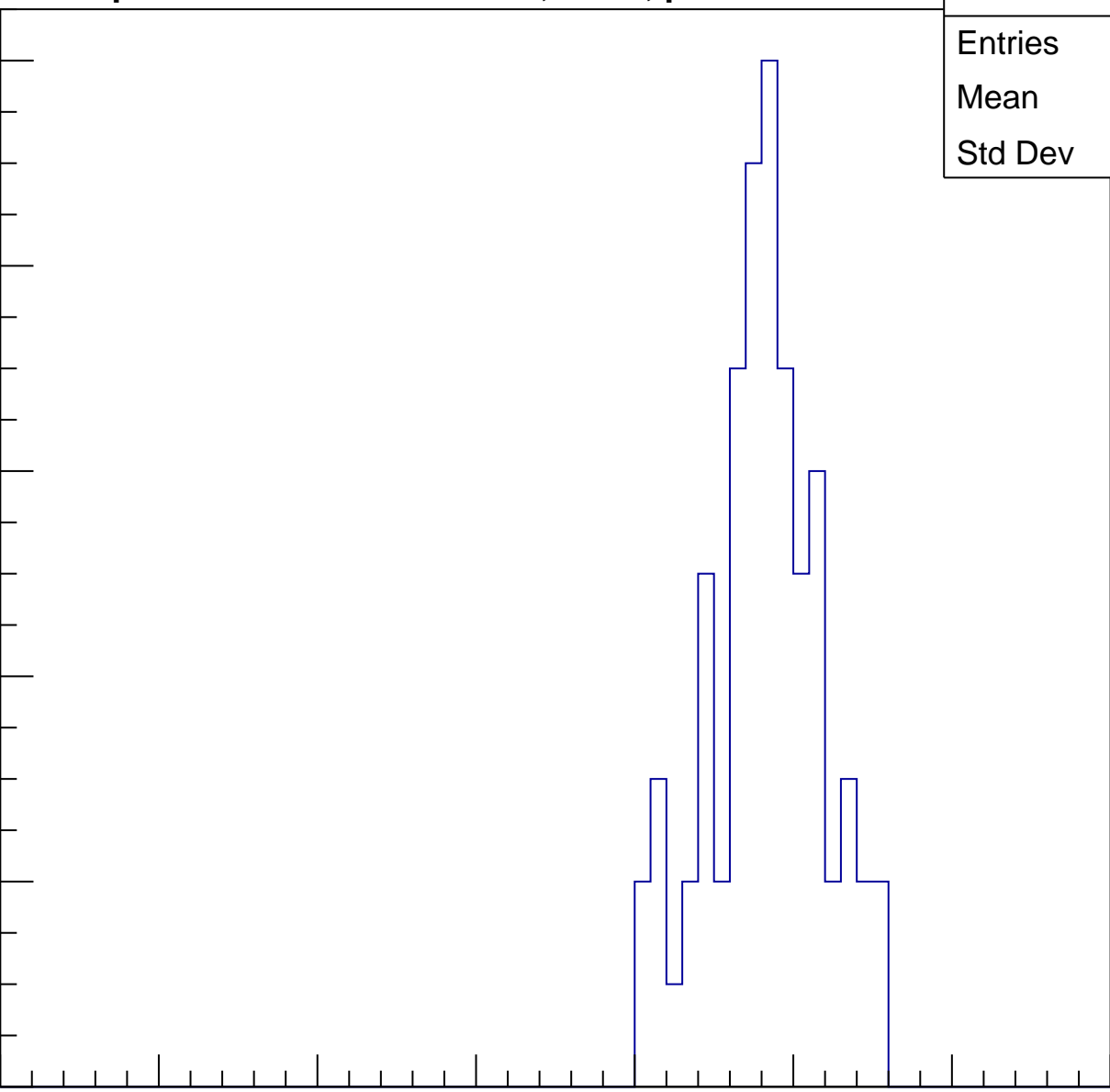
Entries	68
Mean	47.74
Std Dev	3.534

Entry

10
8
6
4
2
0

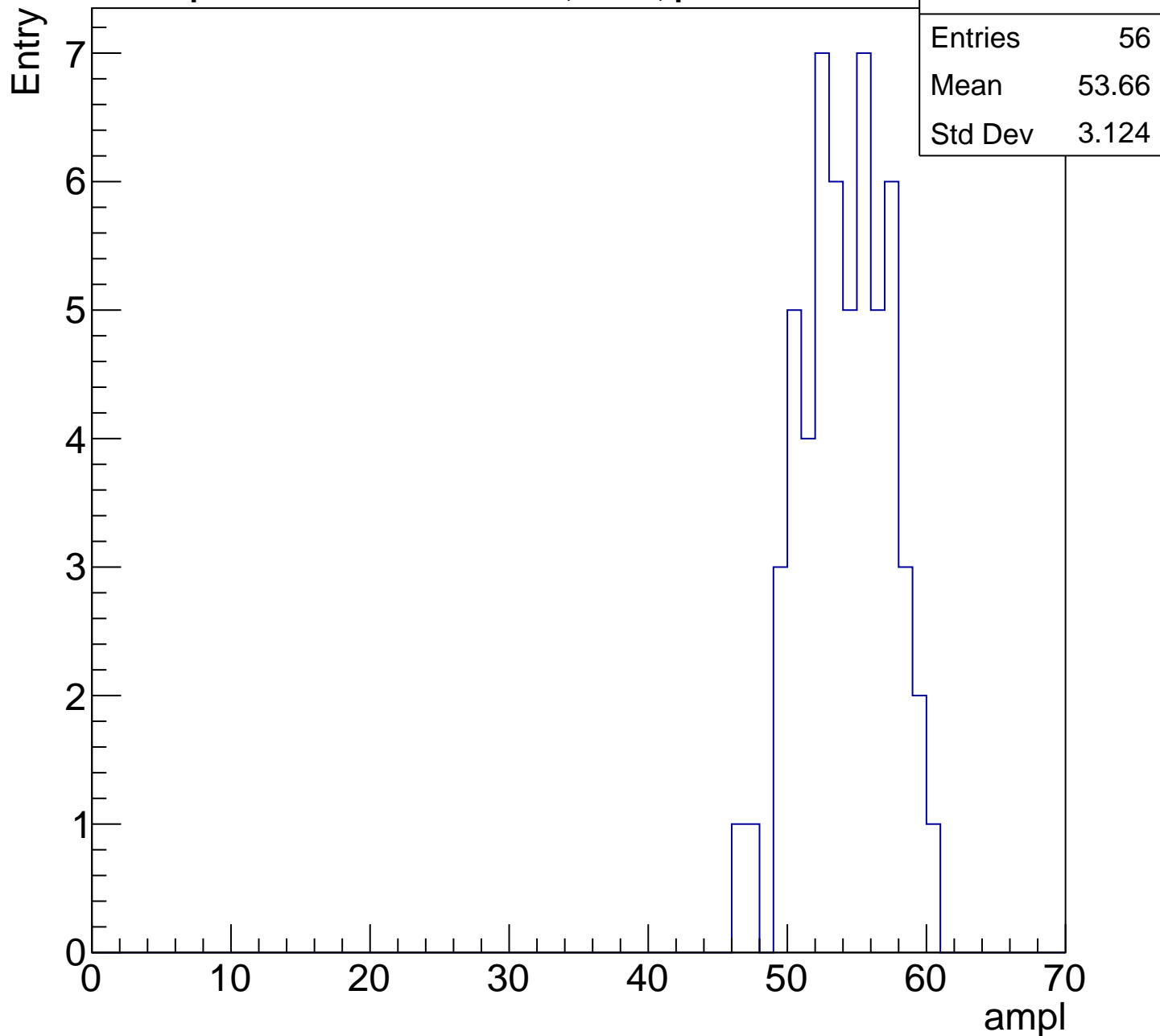
0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 51

Mean 58.9

Std Dev 2.802

0

10

20

30

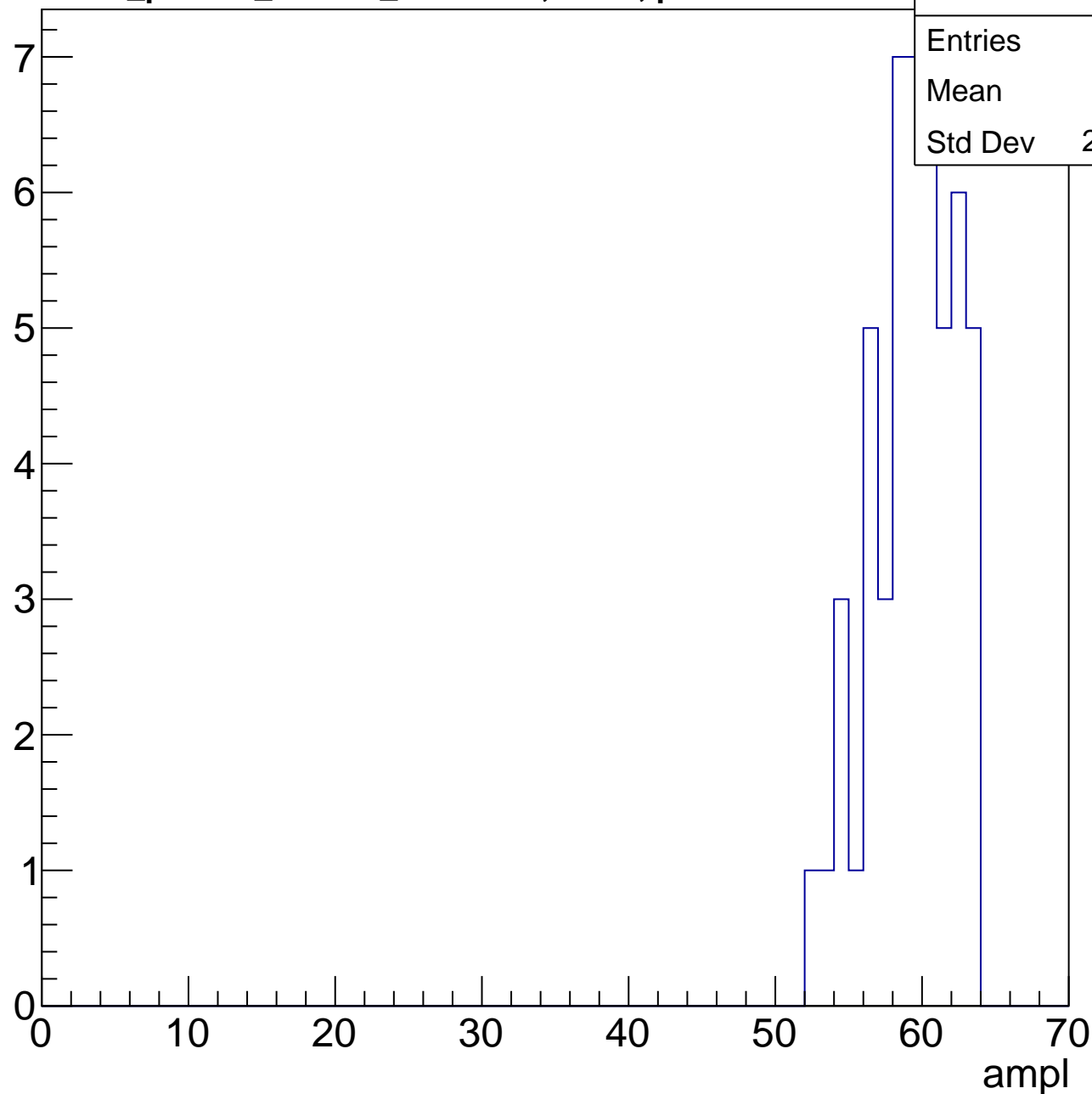
40

50

60

70

ampl

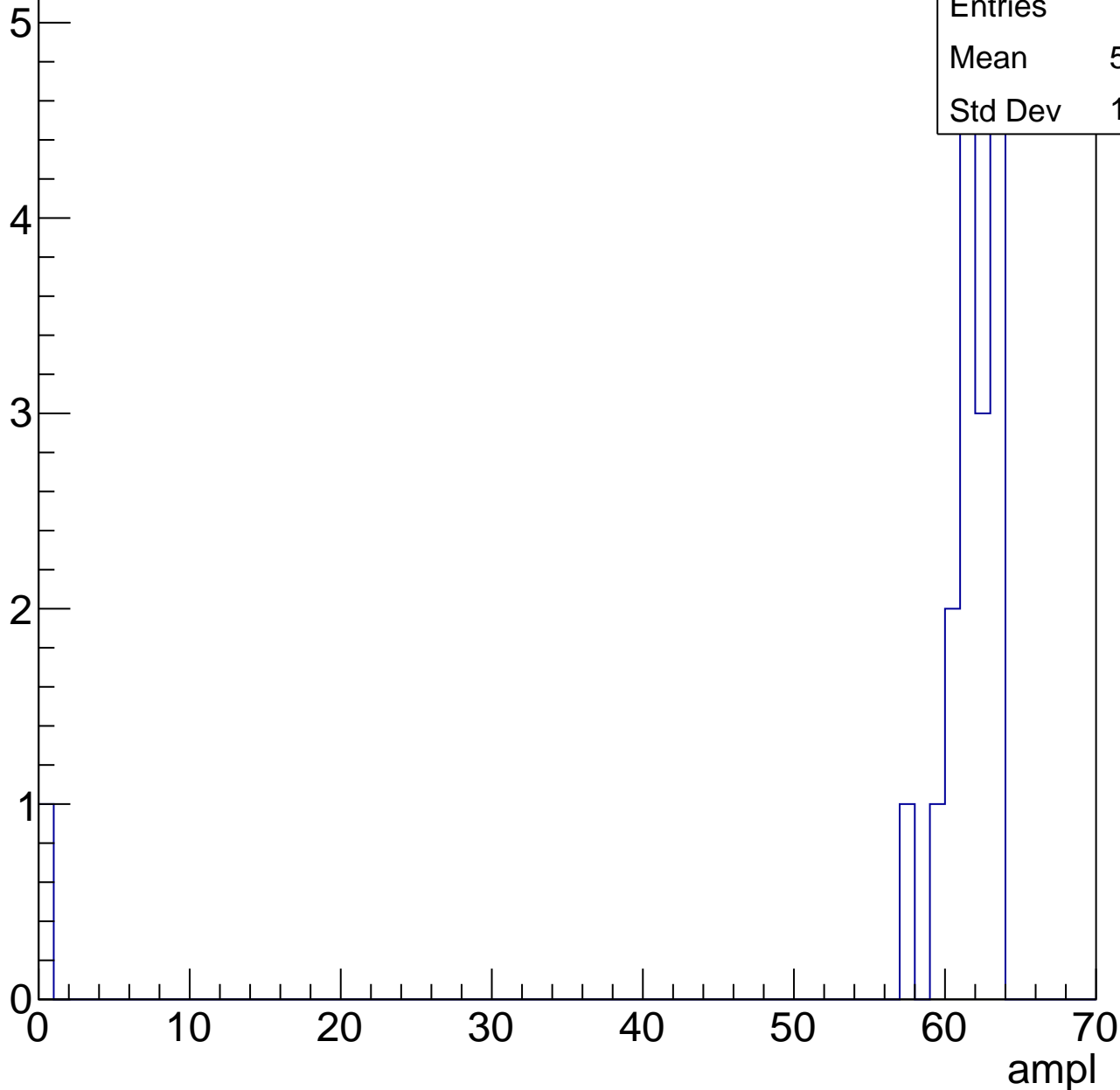


B1L103S, U6-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.89
Std Dev	14.13

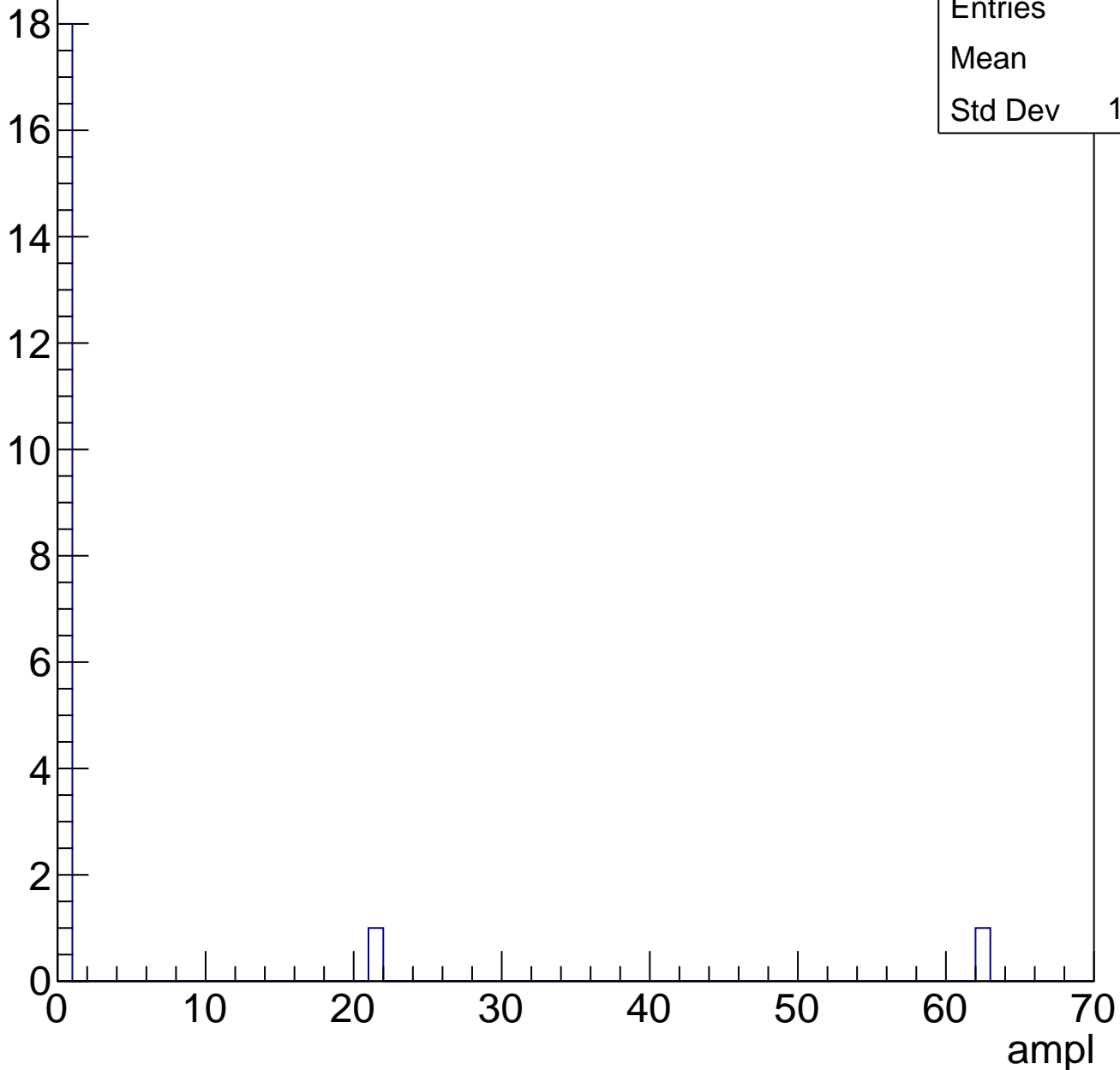


B1L103S, U6-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.04

Entry



B1L103S, U6-ch76, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	24.43
Std Dev	9.573

Gaus mean : 28.1949

Gaus Width: 4.4730

Entry

12

10

8

6

4

2

0

0

10

20

30

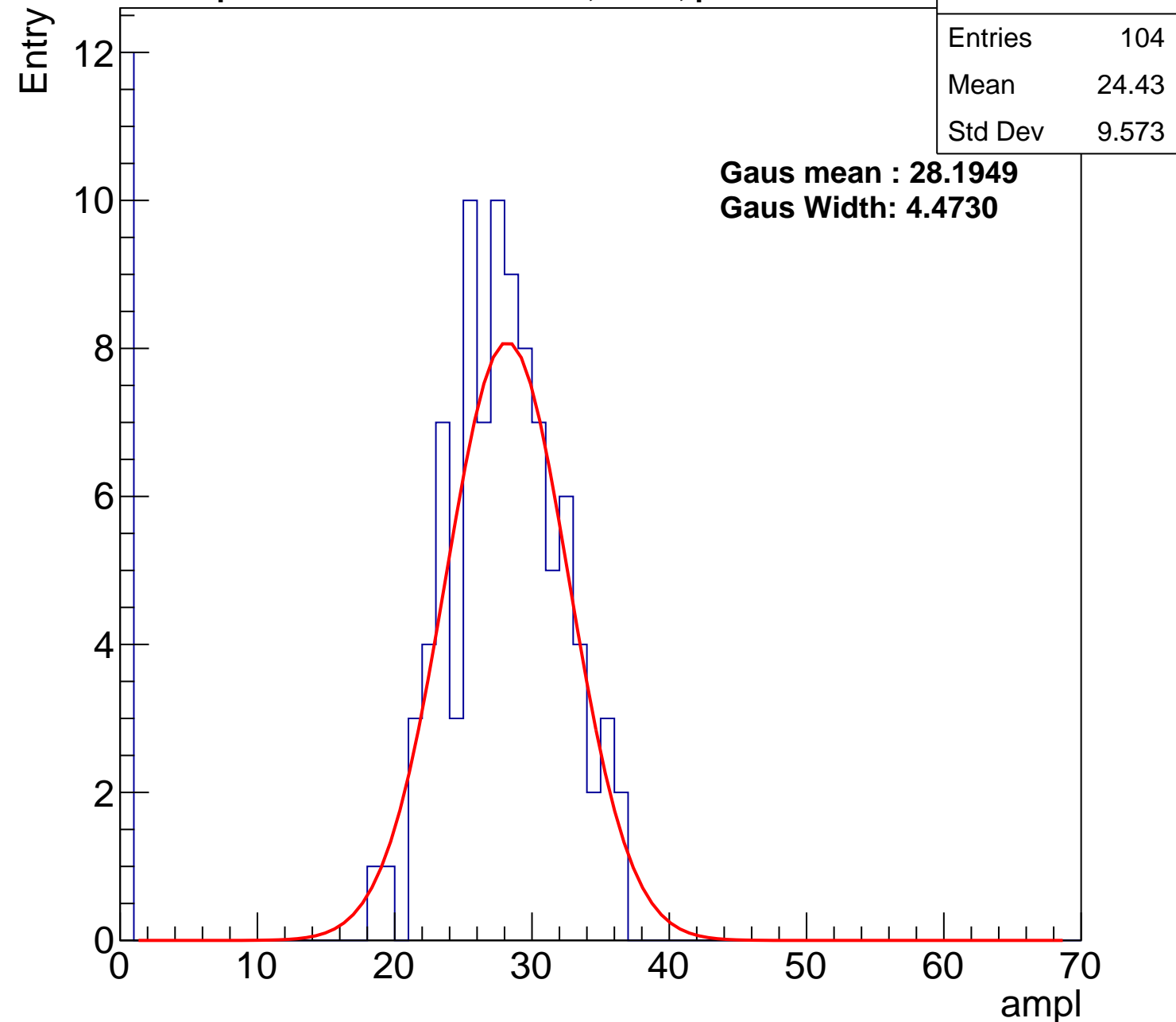
40

50

60

70

ampl



B1L103S, U6-ch76, adc1

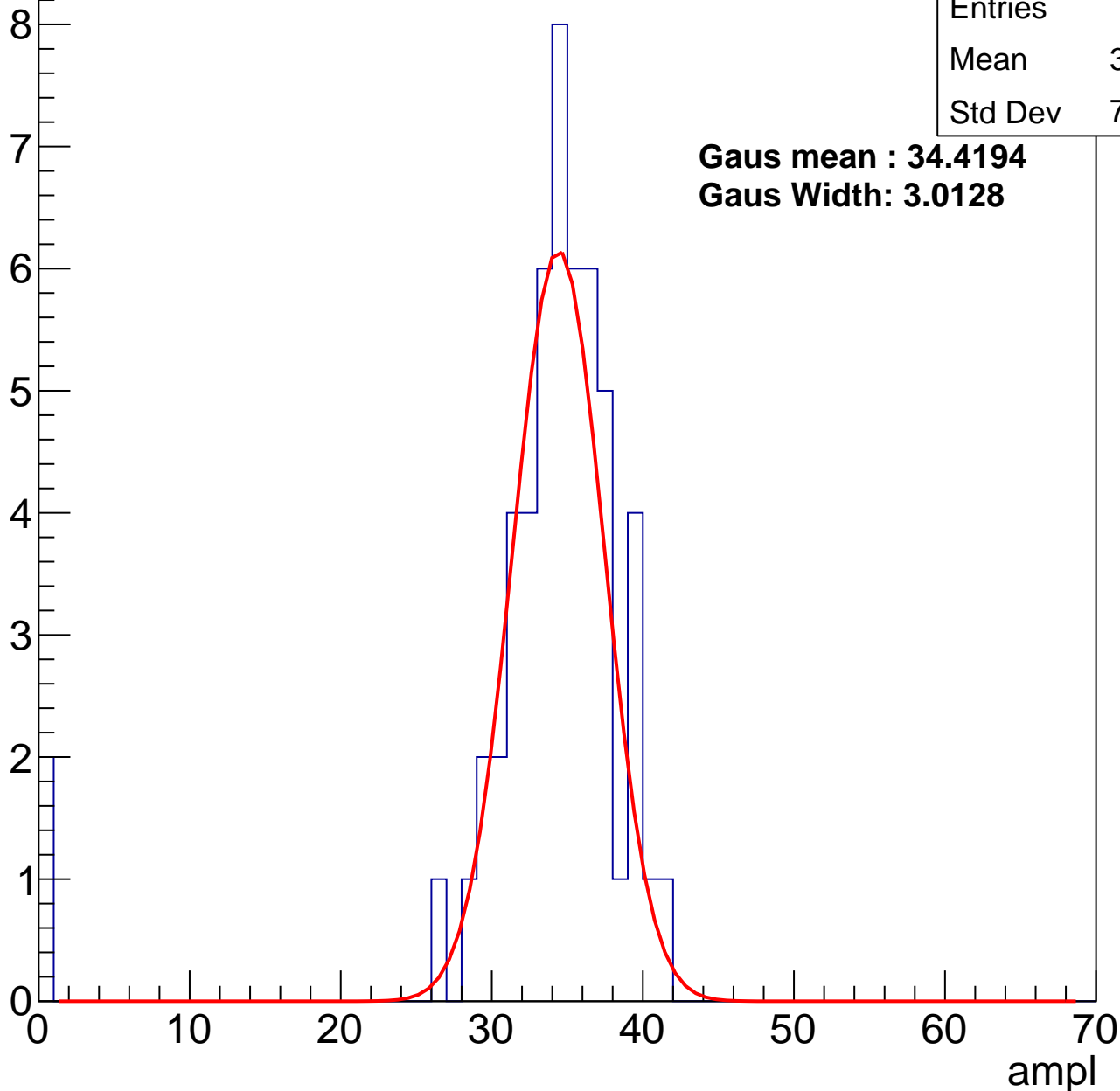
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	32.96
Std Dev	7.162

Gaus mean : 34.4194

Gaus Width: 3.0128



B1L103S, U6-ch76, adc2

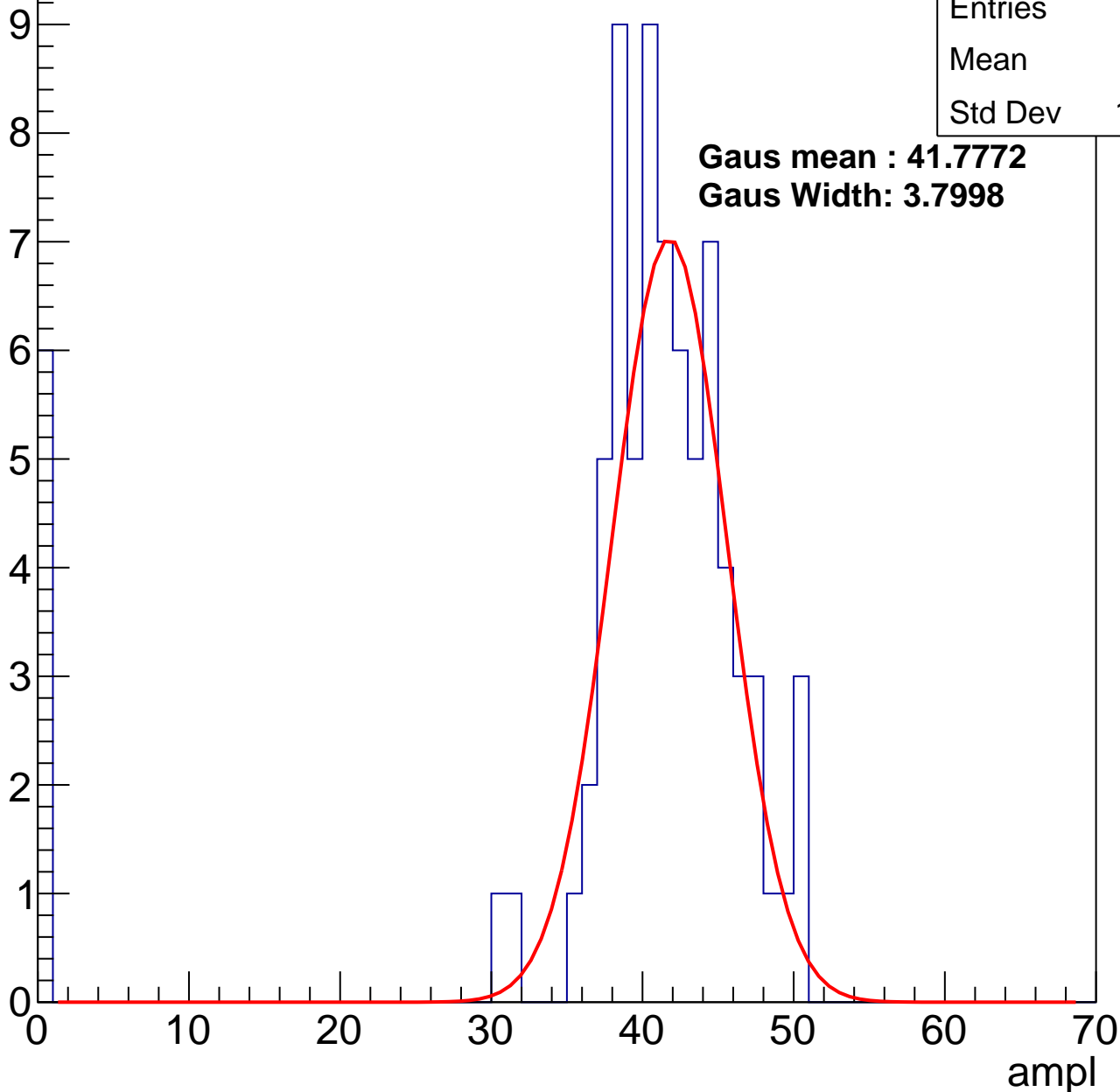
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	38.2
Std Dev	11.61

Gaus mean : 41.7772

Gaus Width: 3.7998

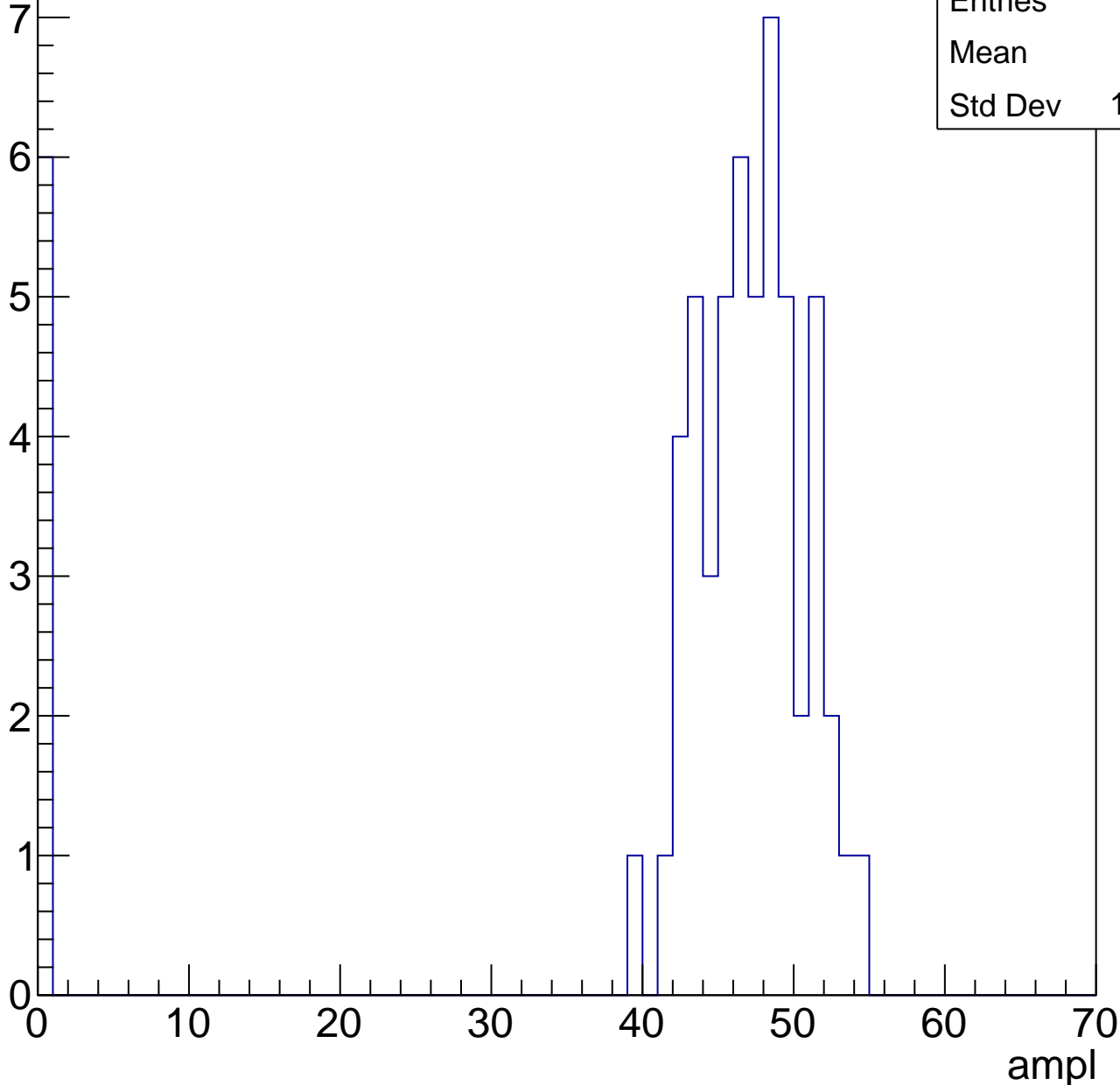


B1L103S, U6-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42
Std Dev	14.48

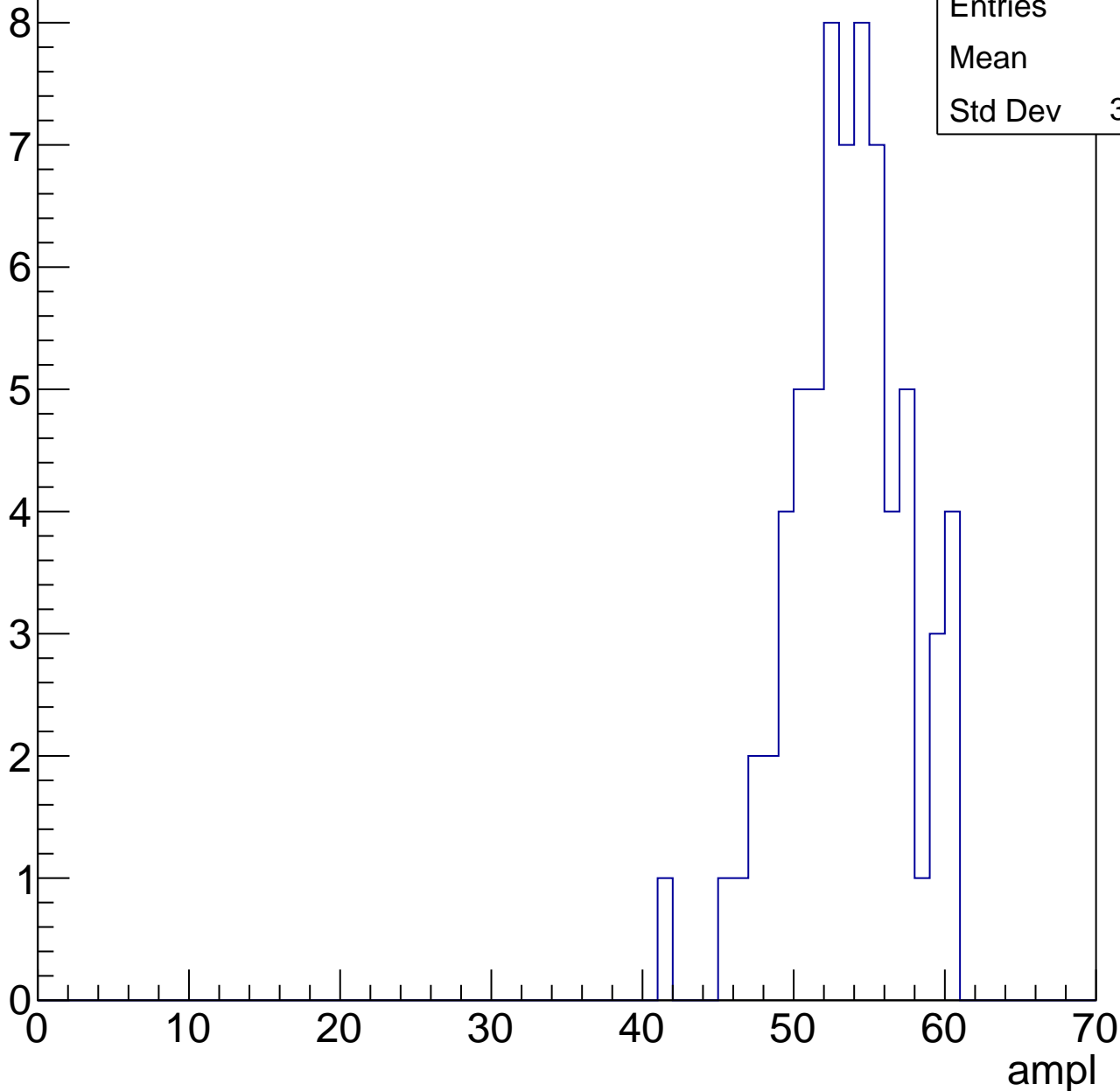


B1L103S, U6-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.1
Std Dev	3.828

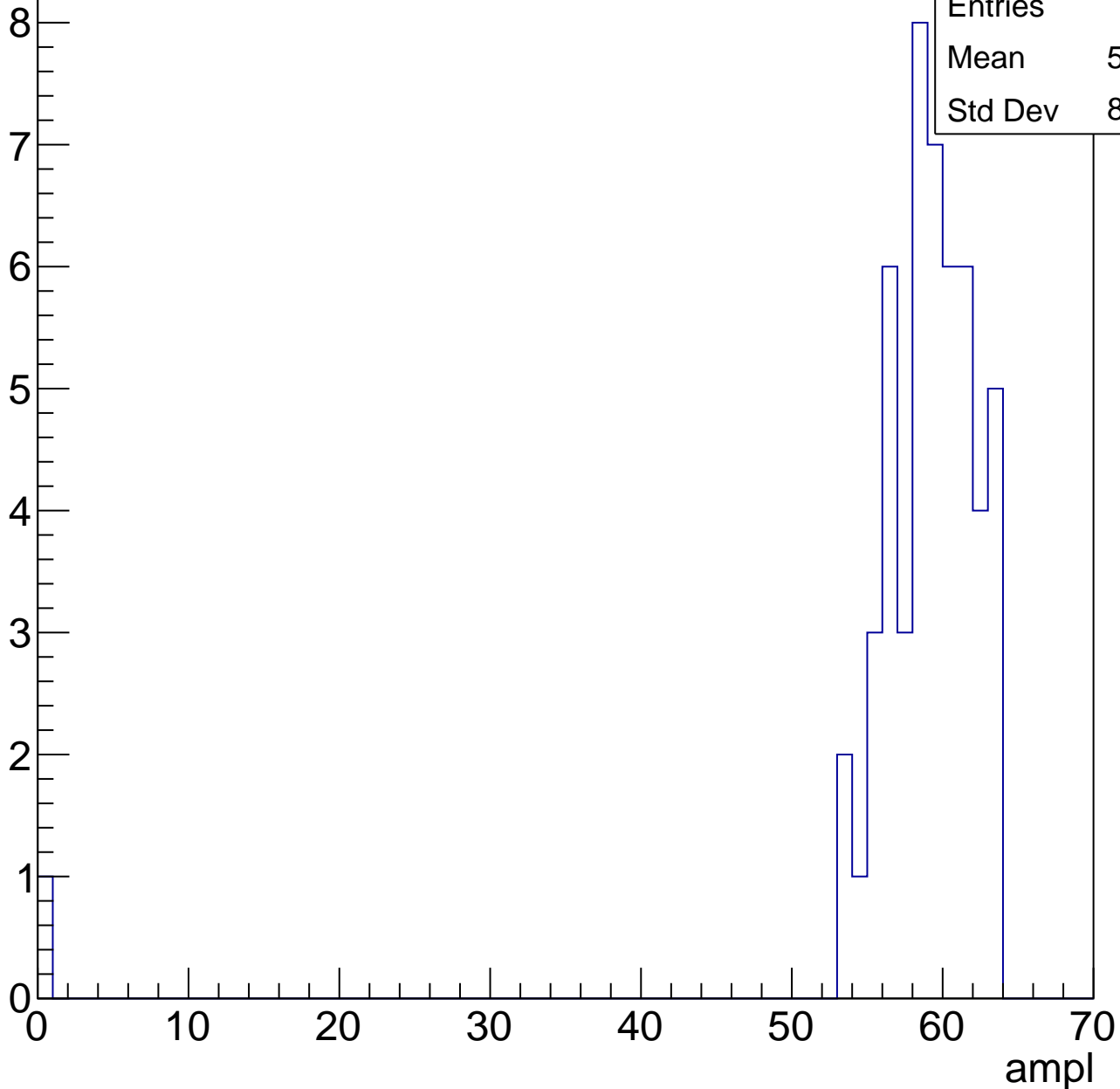


B1L103S, U6-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	57.65
Std Dev	8.494

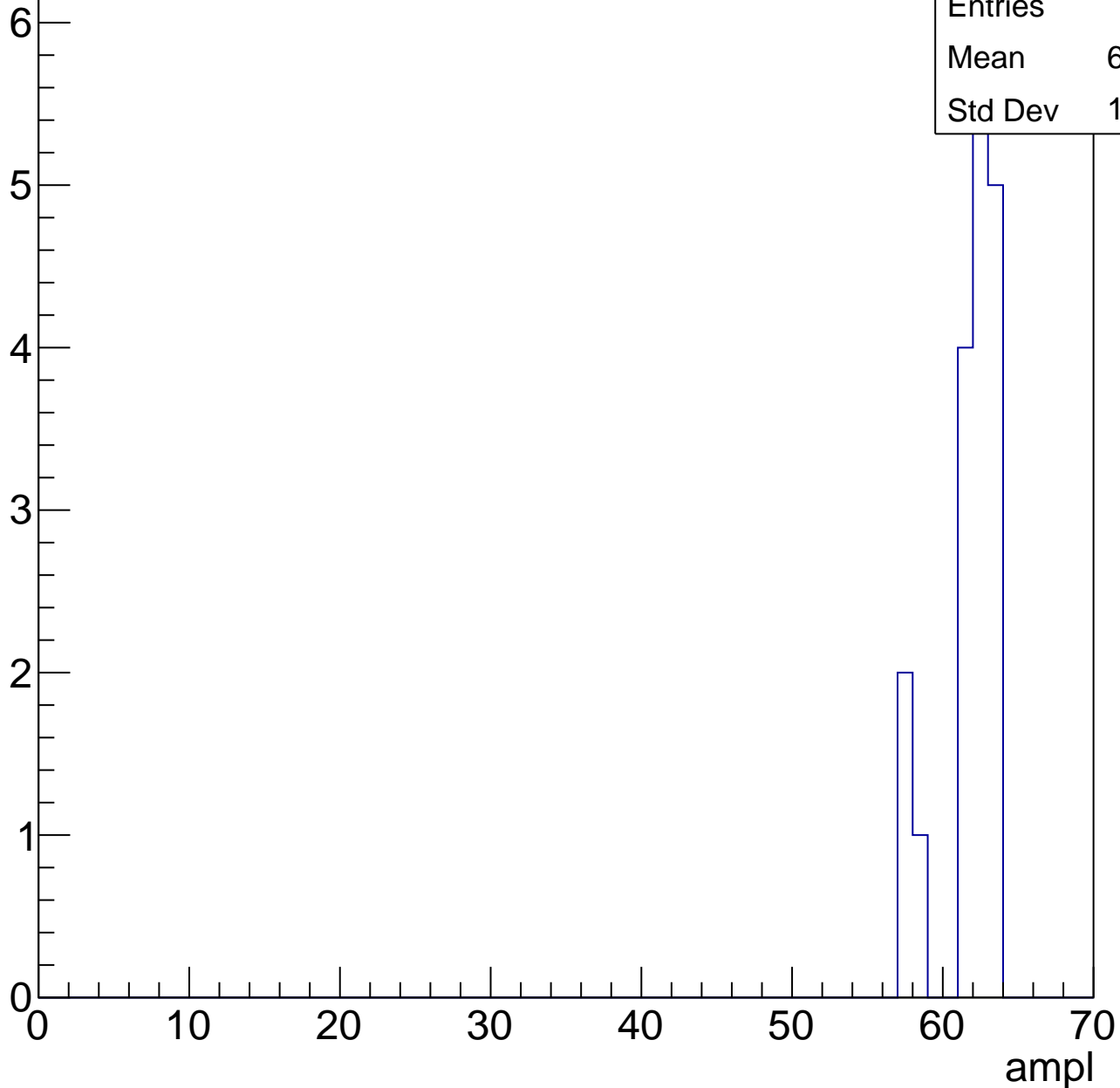


B1L103S, U6-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.28
Std Dev	1.909



B1L103S, U6-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch77, adc0

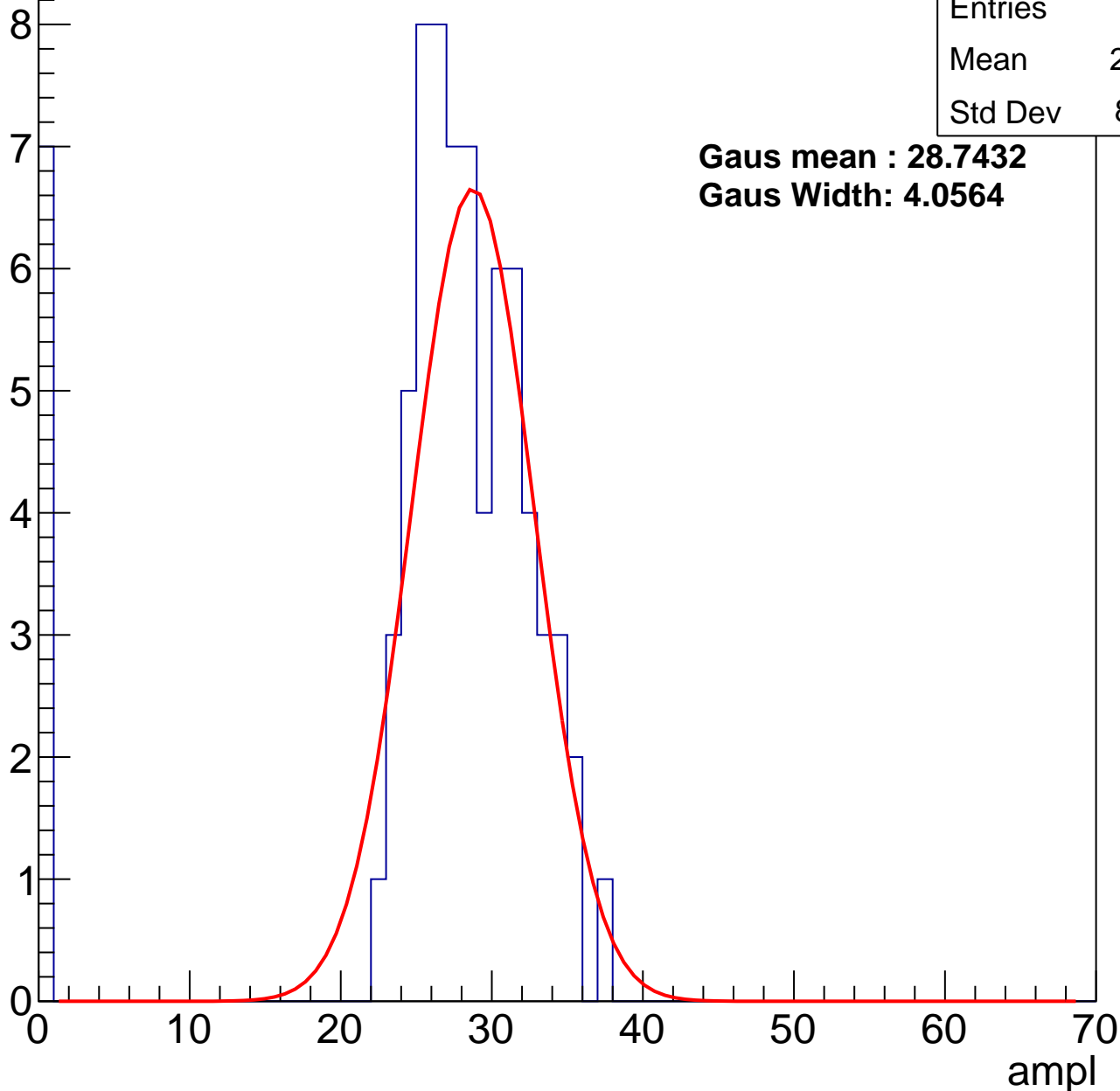
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.63
Std Dev	8.851

Gaus mean : 28.7432

Gaus Width: 4.0564



B1L103S, U6-ch77, adc1

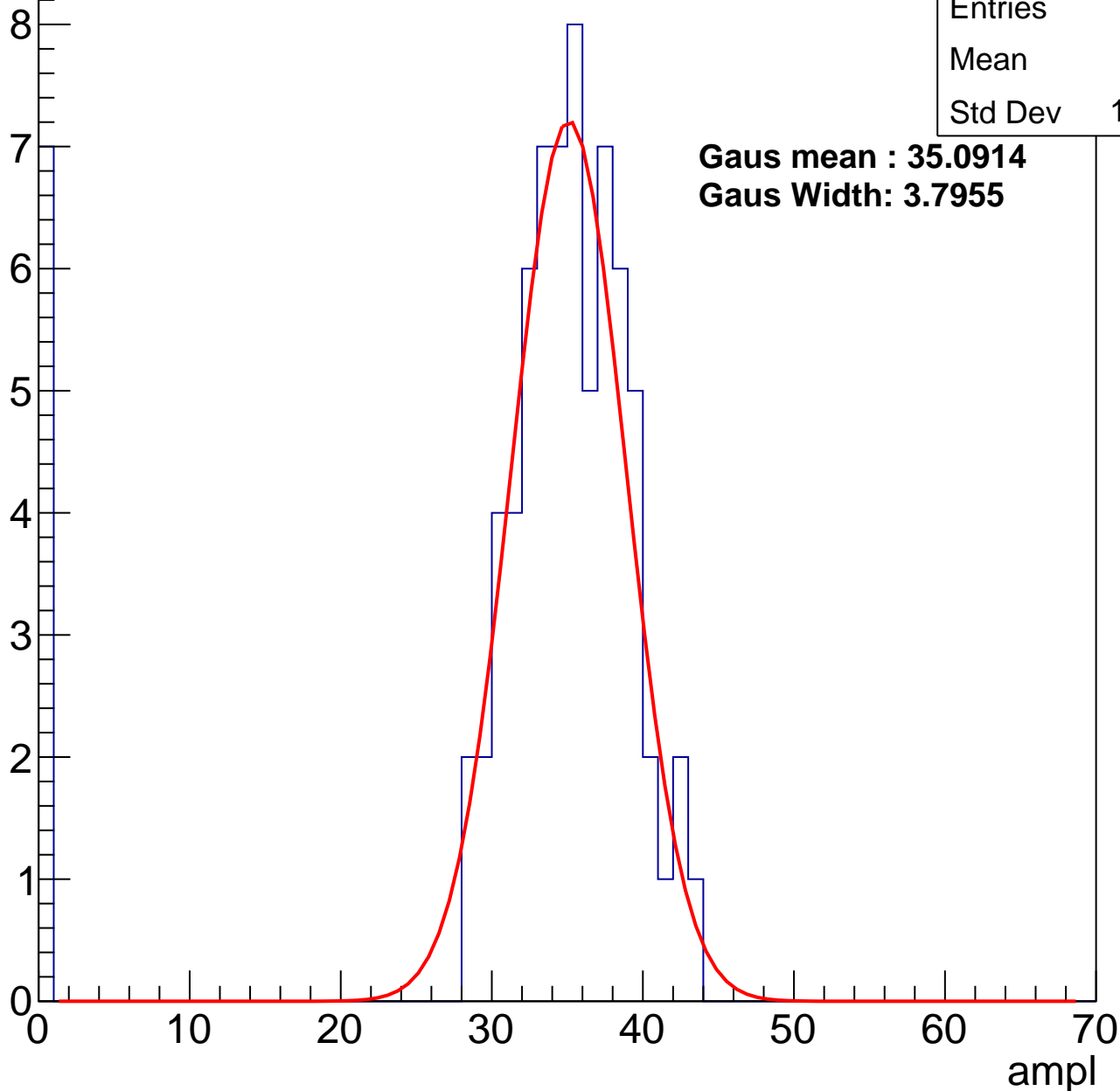
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	31.7
Std Dev	10.63

Gaus mean : 35.0914

Gaus Width: 3.7955



B1L103S, U6-ch77, adc2

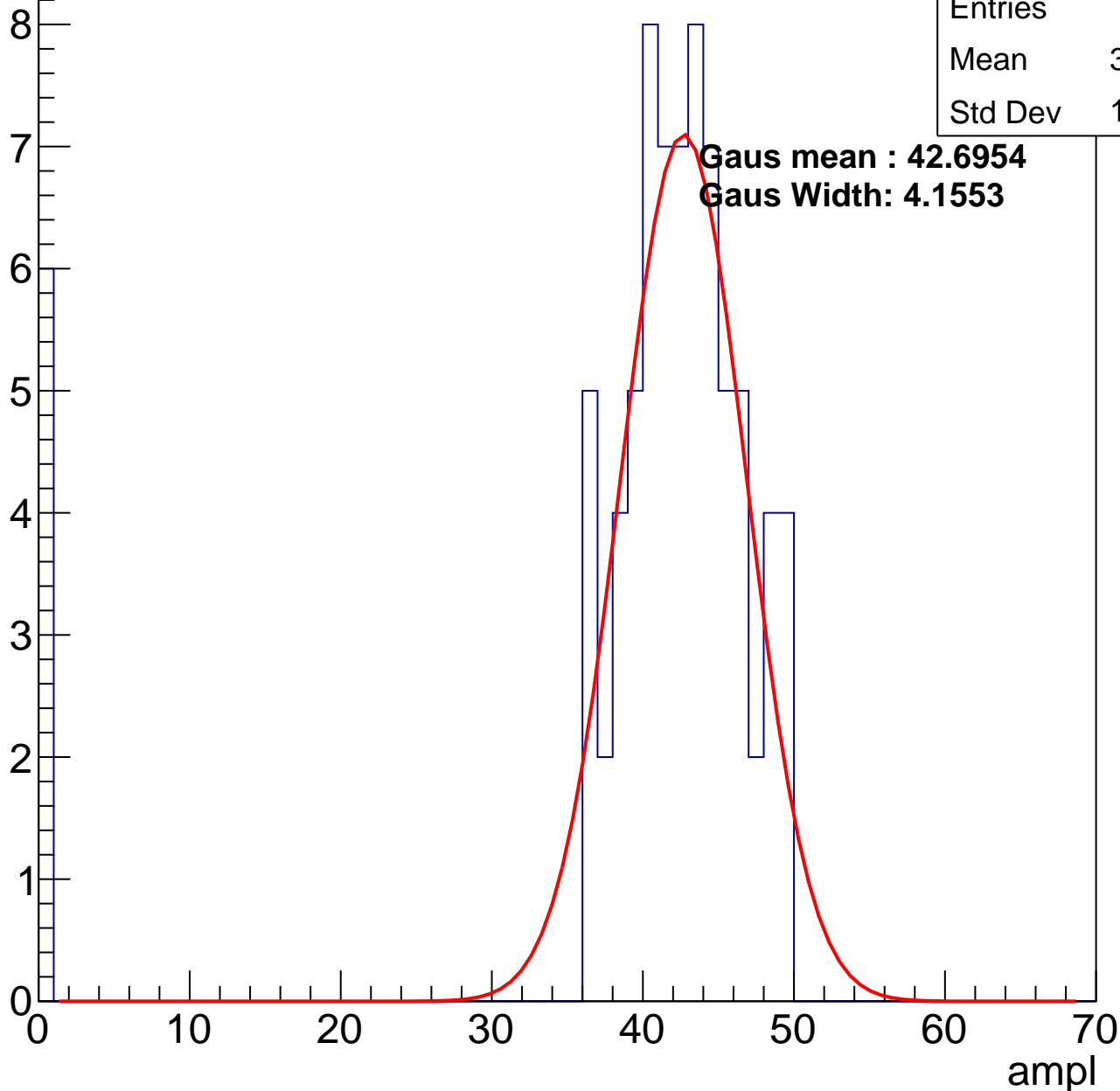
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	39.13
Std Dev	11.73

Gaus mean : 42.6954

Gaus Width: 4.1553

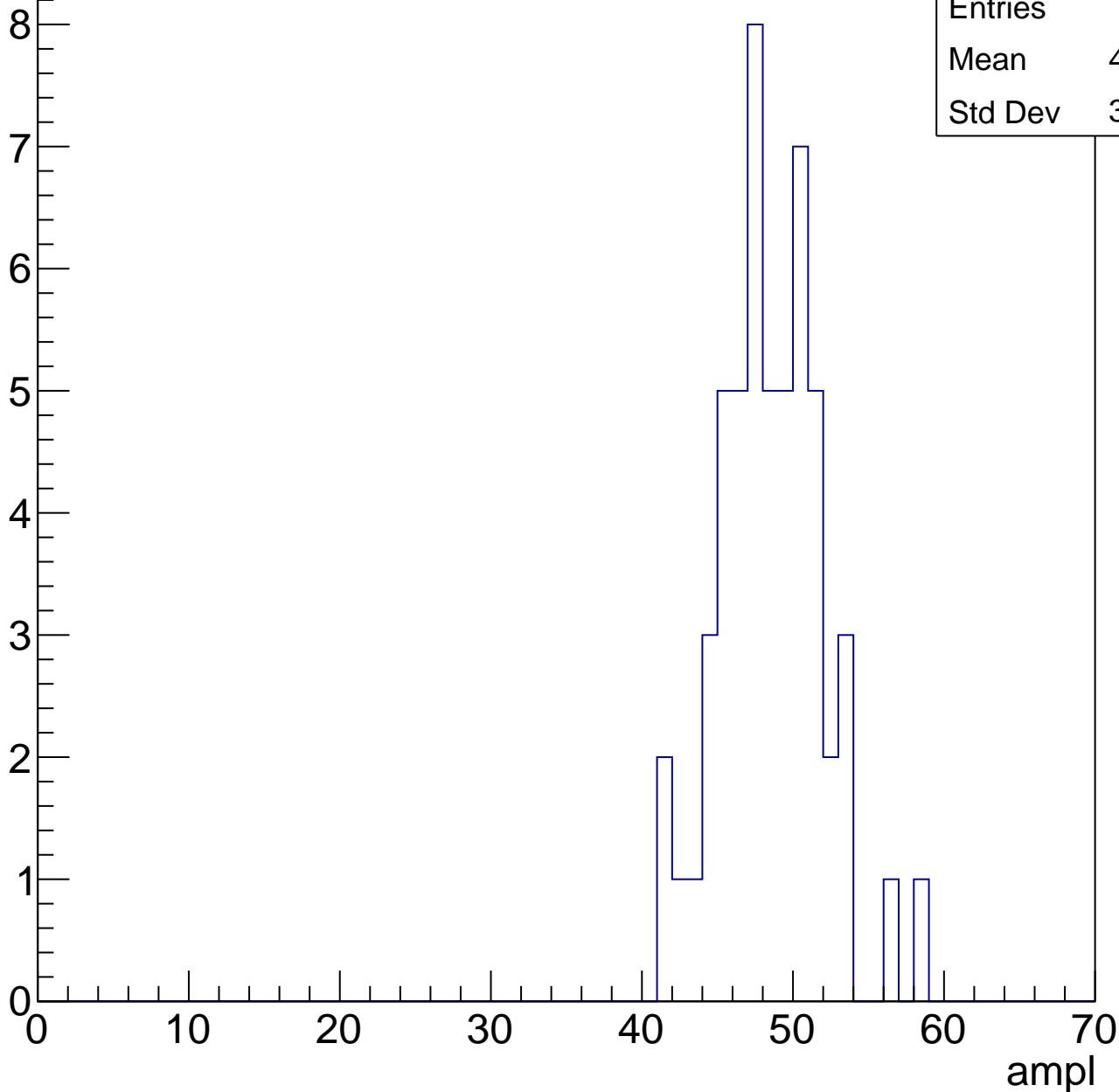


B1L103S, U6-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	48.09
Std Dev	3.412

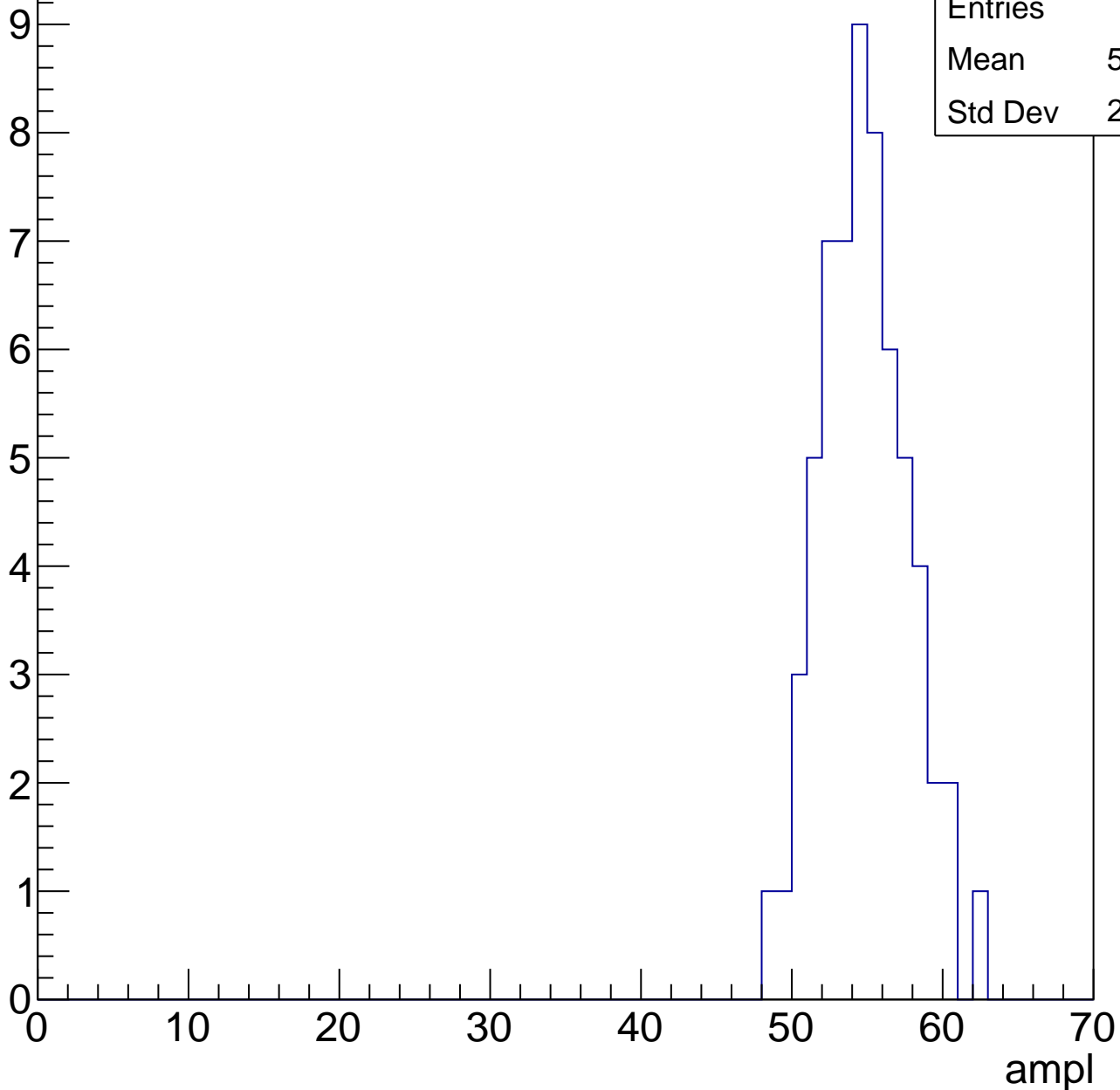


B1L103S, U6-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	54.36
Std Dev	2.886

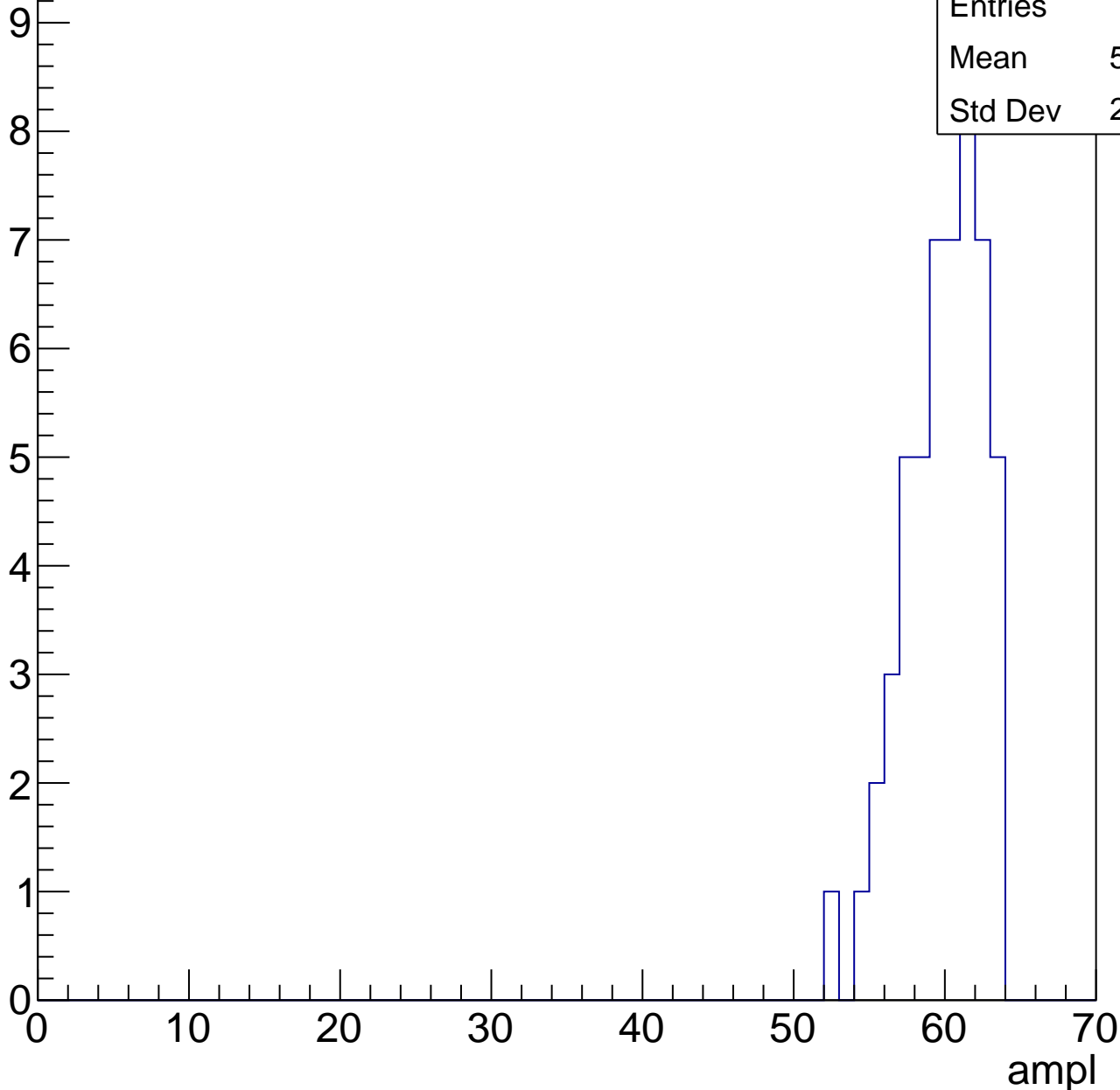


B1L103S, U6-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	59.42
Std Dev	2.537

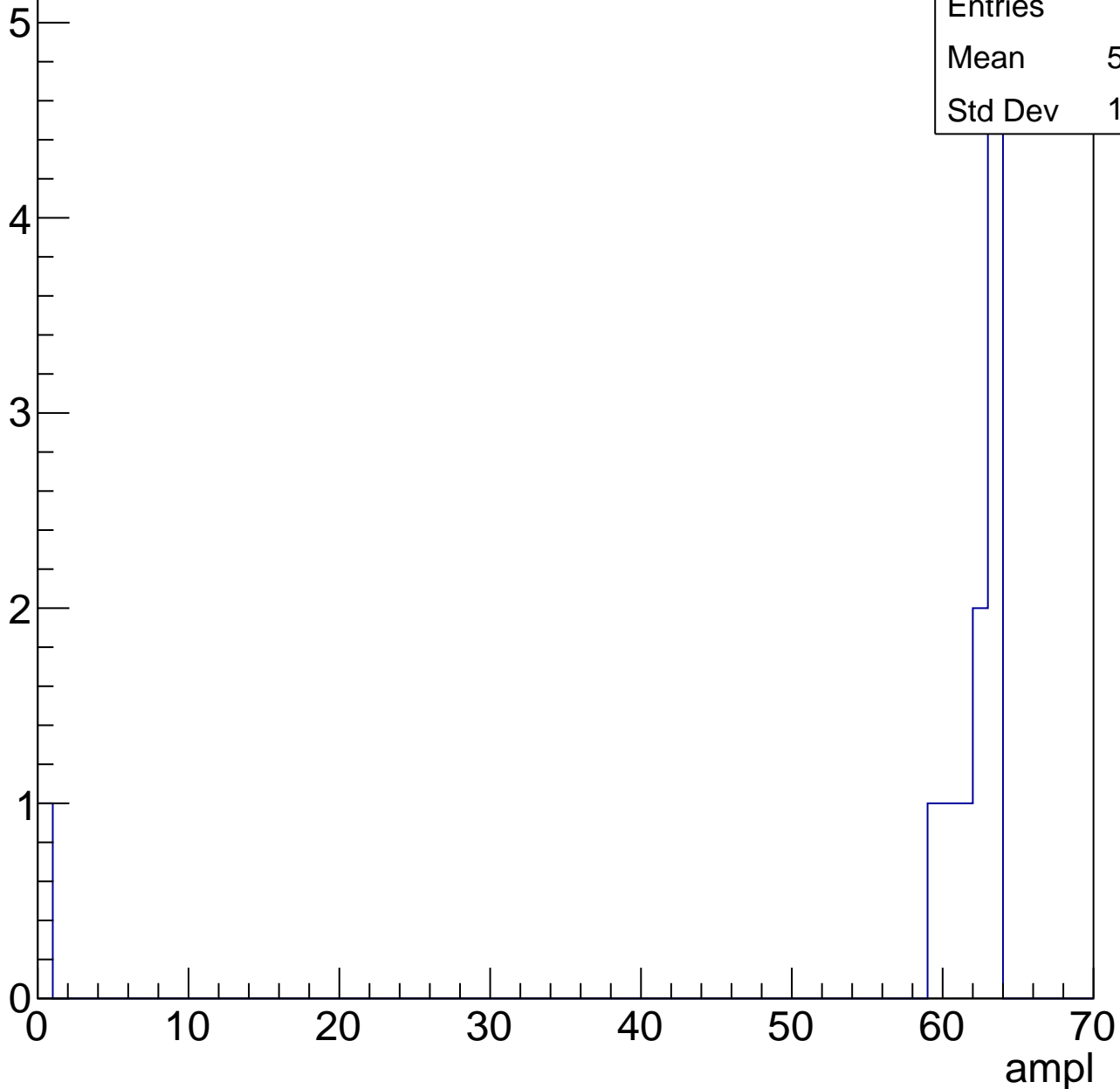


B1L103S, U6-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

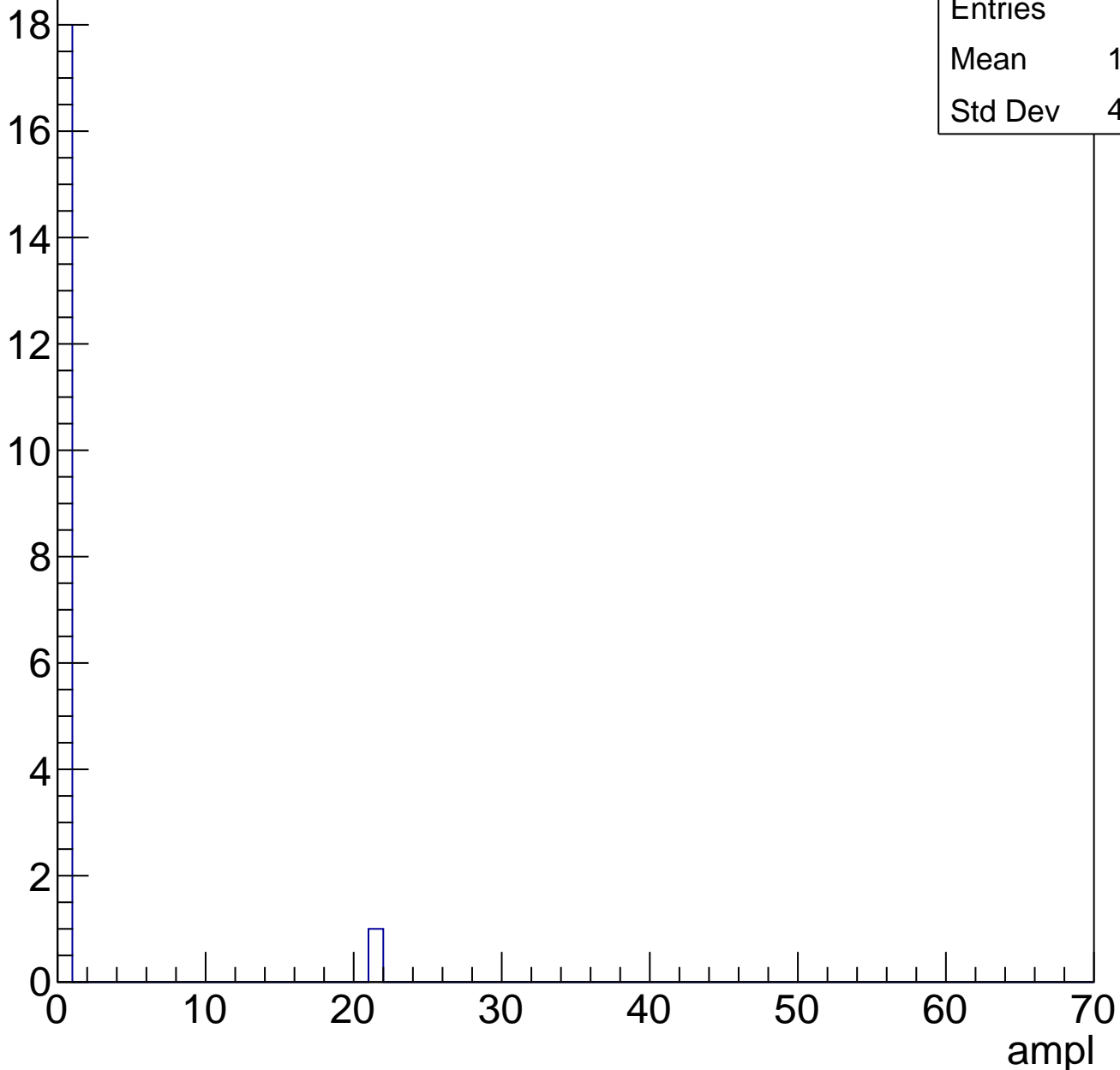
Entries	11
Mean	56.27
Std Dev	17.84



B1L103S, U6-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.105
Std Dev	4.689

B1L103S, U6-ch78, adc0

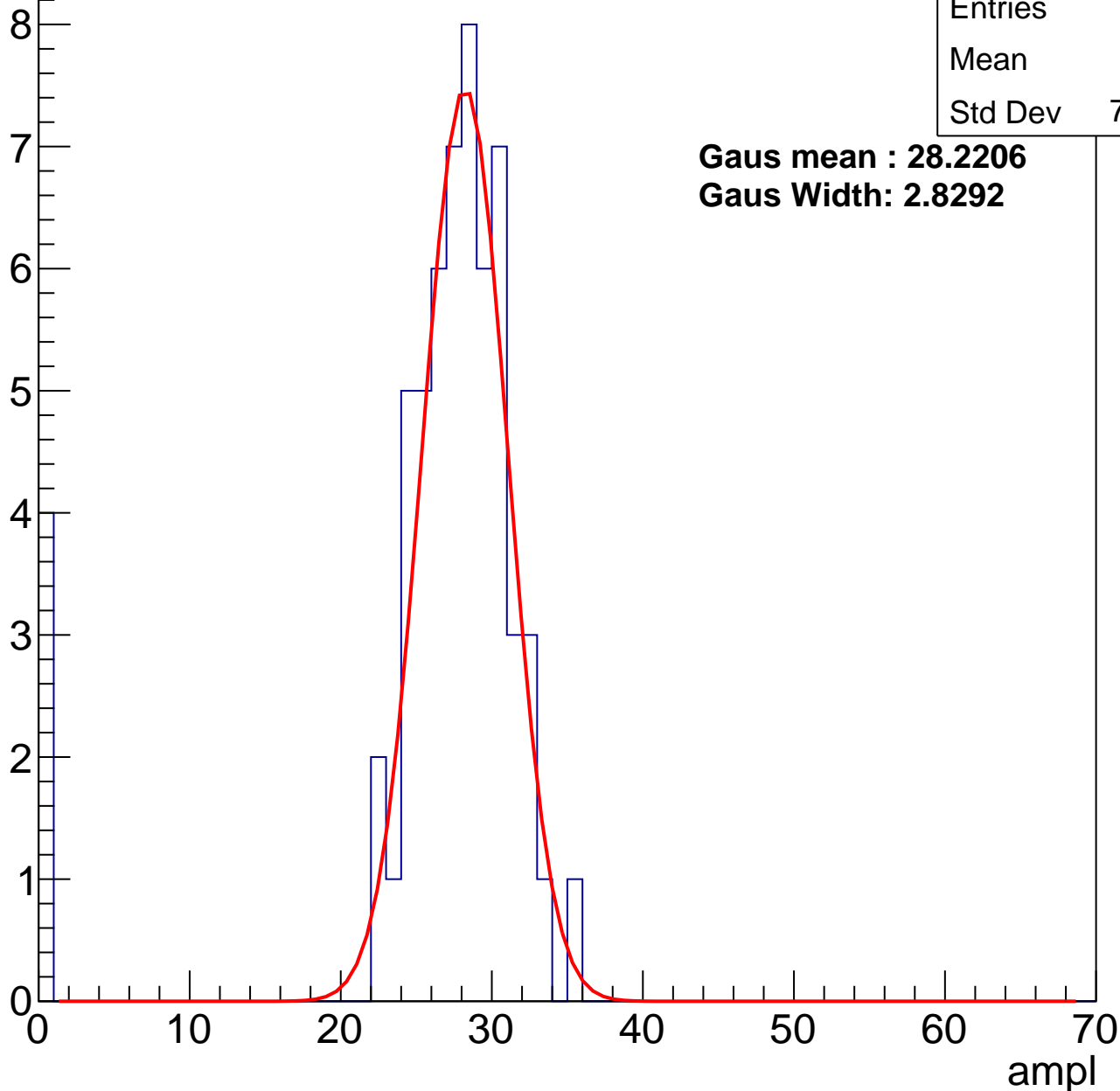
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	25.8
Std Dev	7.462

Gaus mean : 28.2206

Gaus Width: 2.8292



B1L103S, U6-ch78, adc1

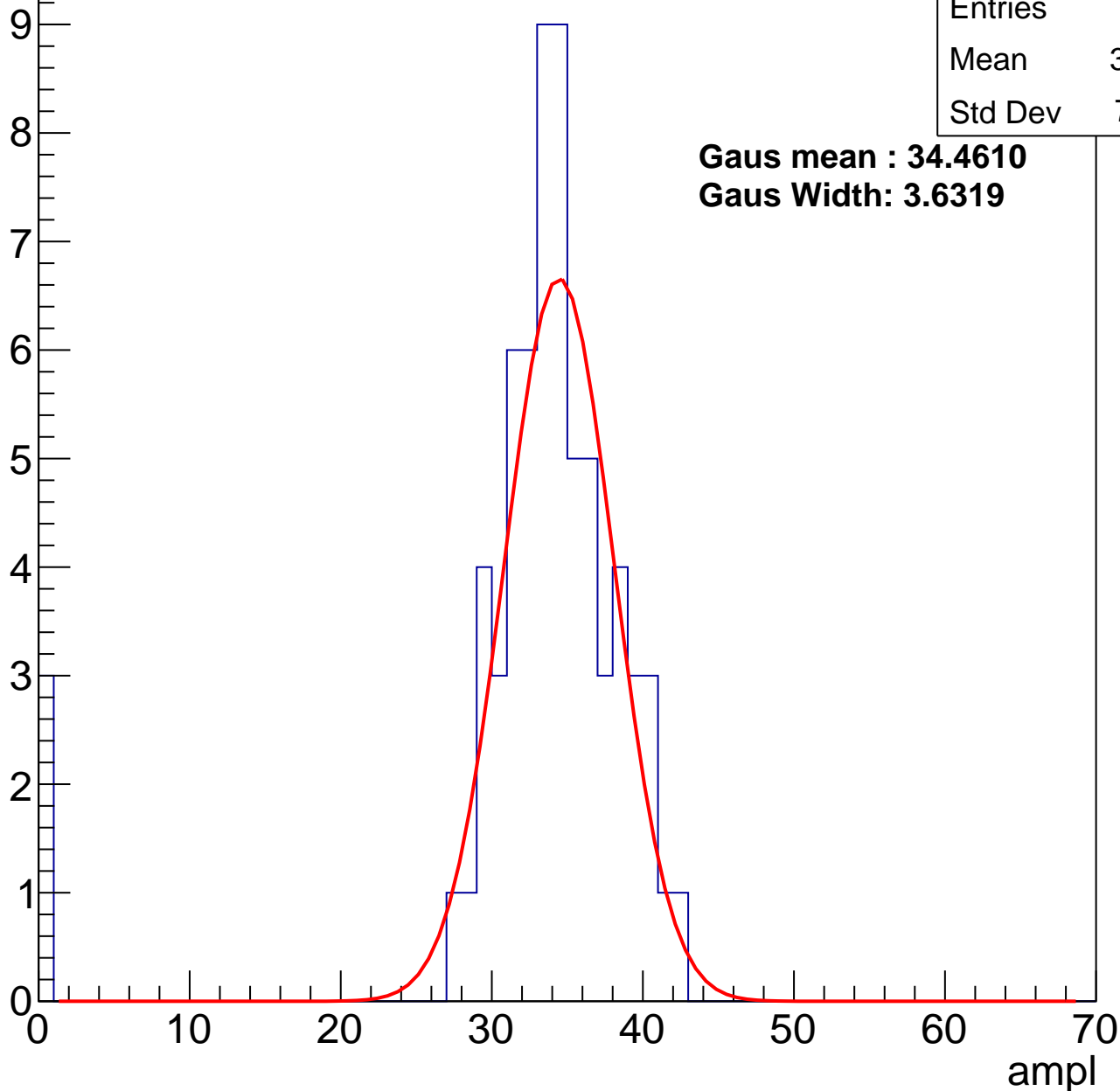
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.54
Std Dev	7.781

Gaus mean : 34.4610

Gaus Width: 3.6319



B1L103S, U6-ch78, adc2

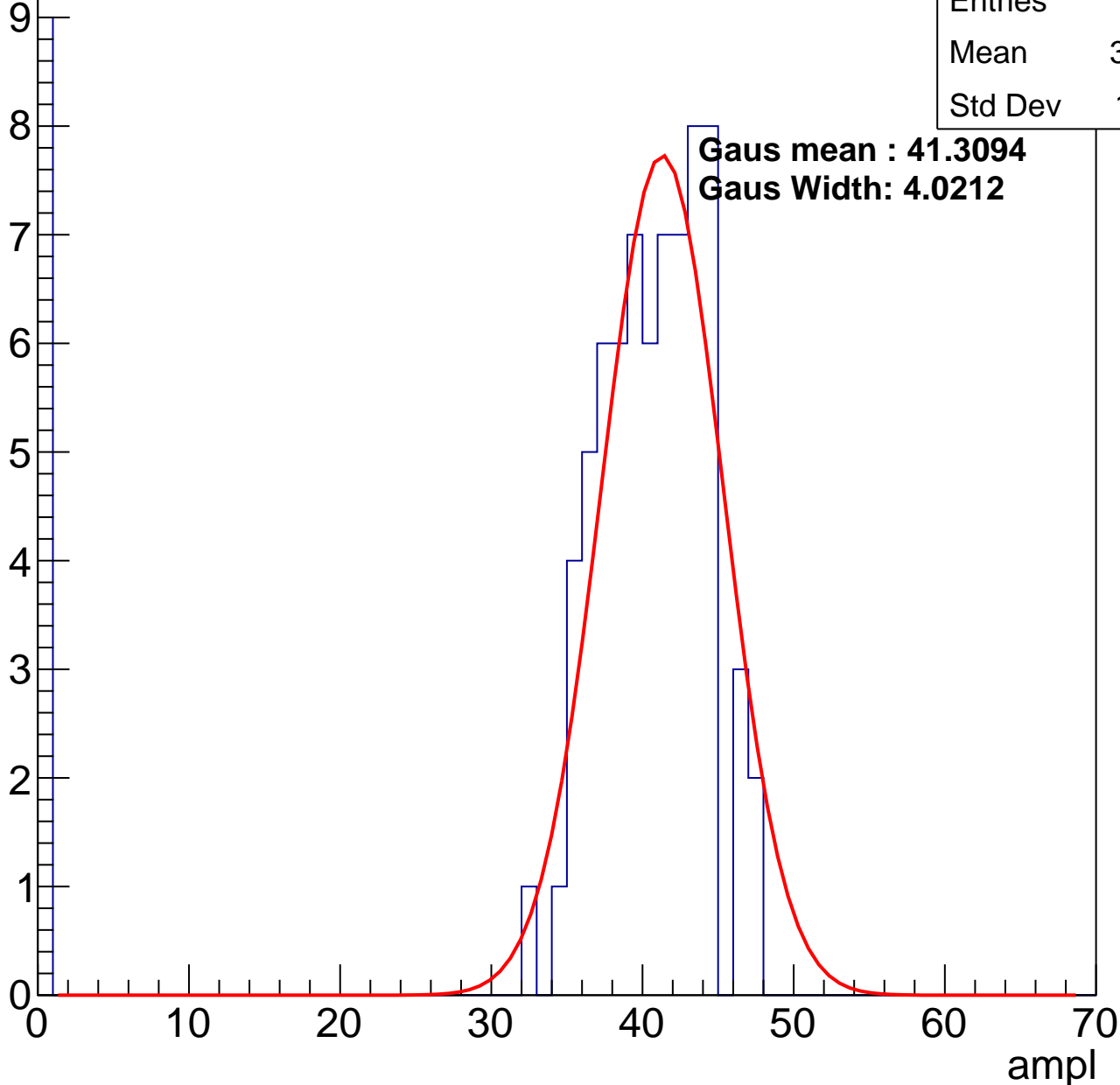
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	35.73
Std Dev	13.11

Gaus mean : 41.3094

Gaus Width: 4.0212

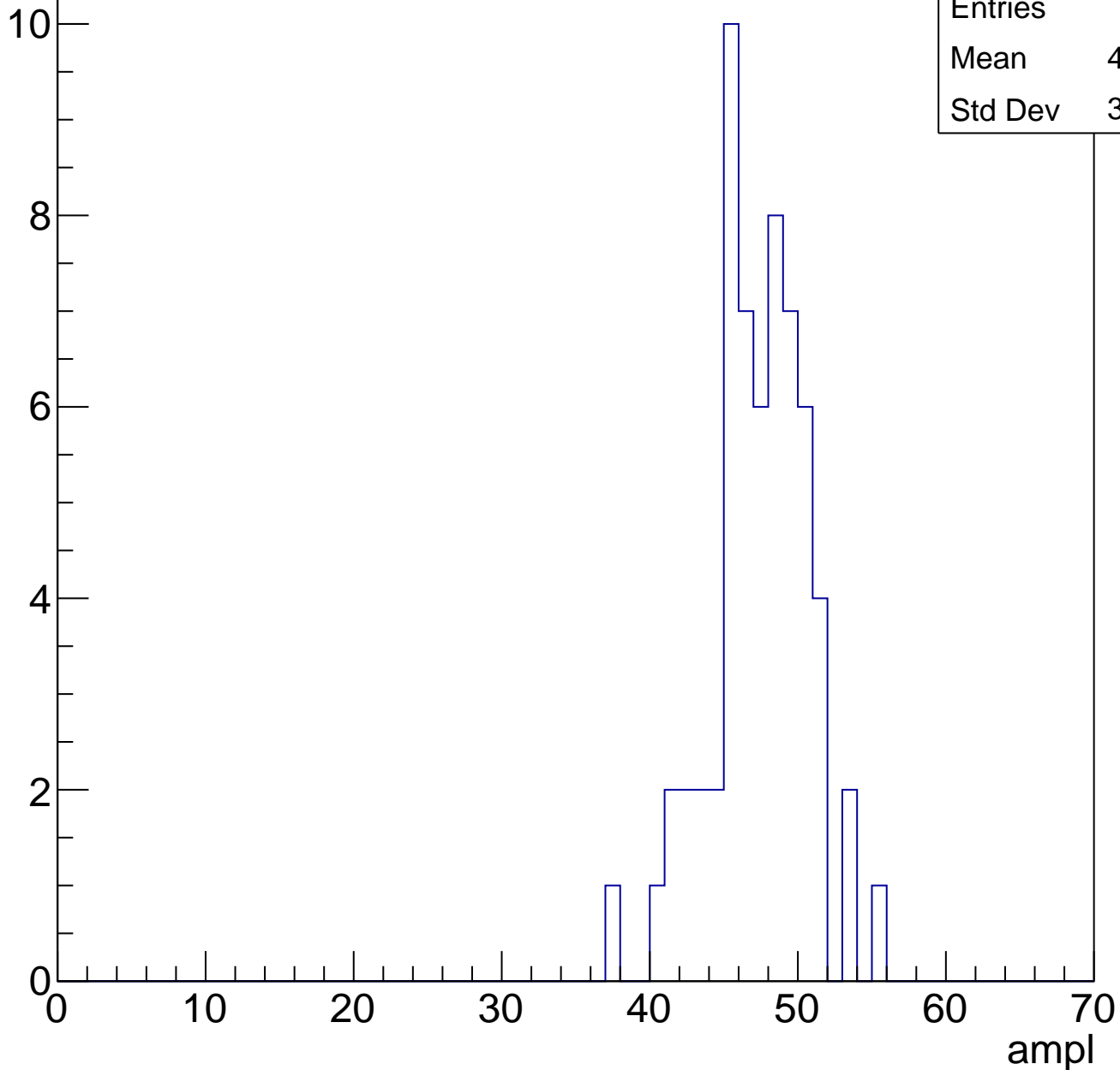


B1L103S, U6-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	46.93
Std Dev	3.284

Entry

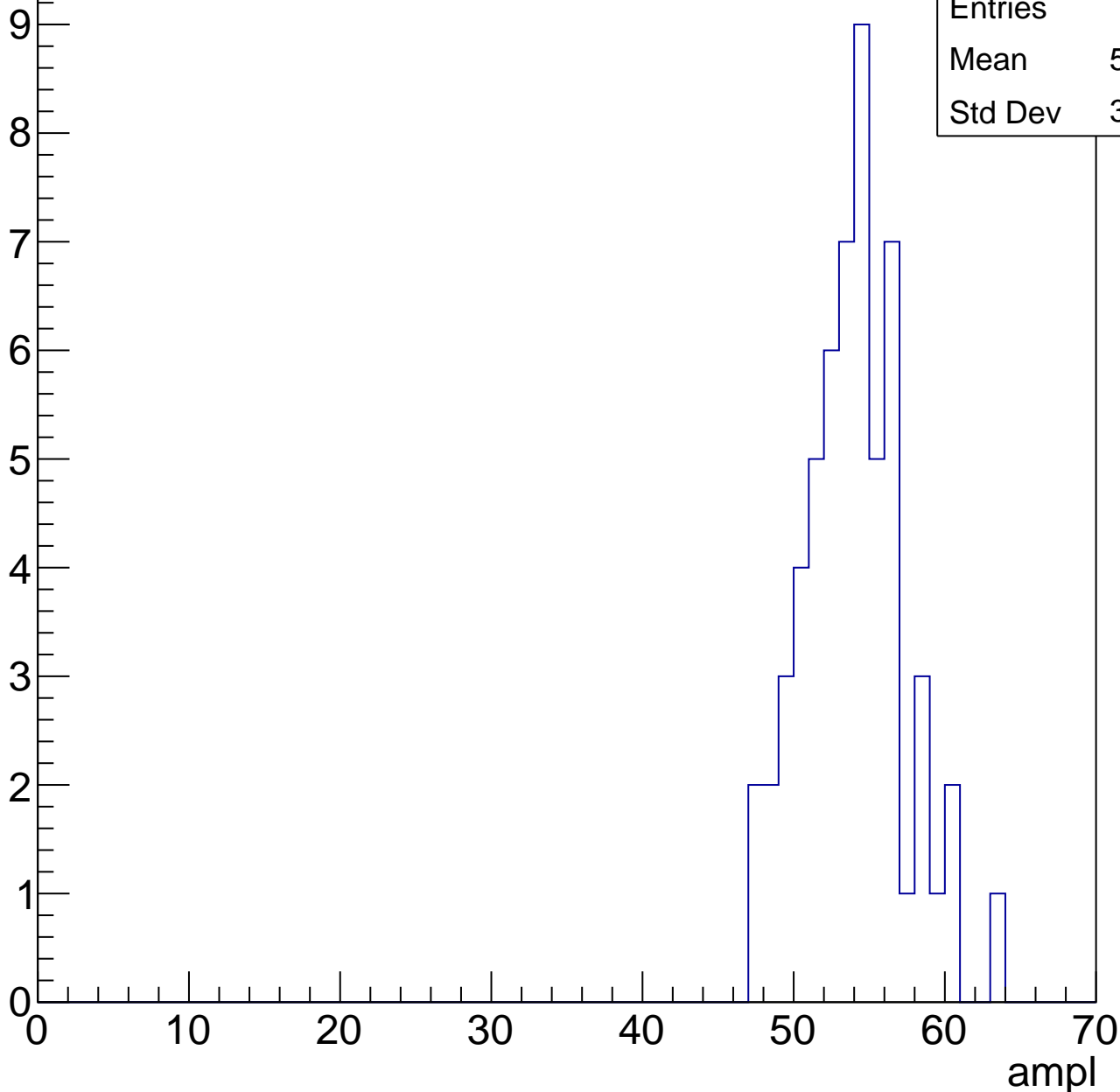


B1L103S, U6-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

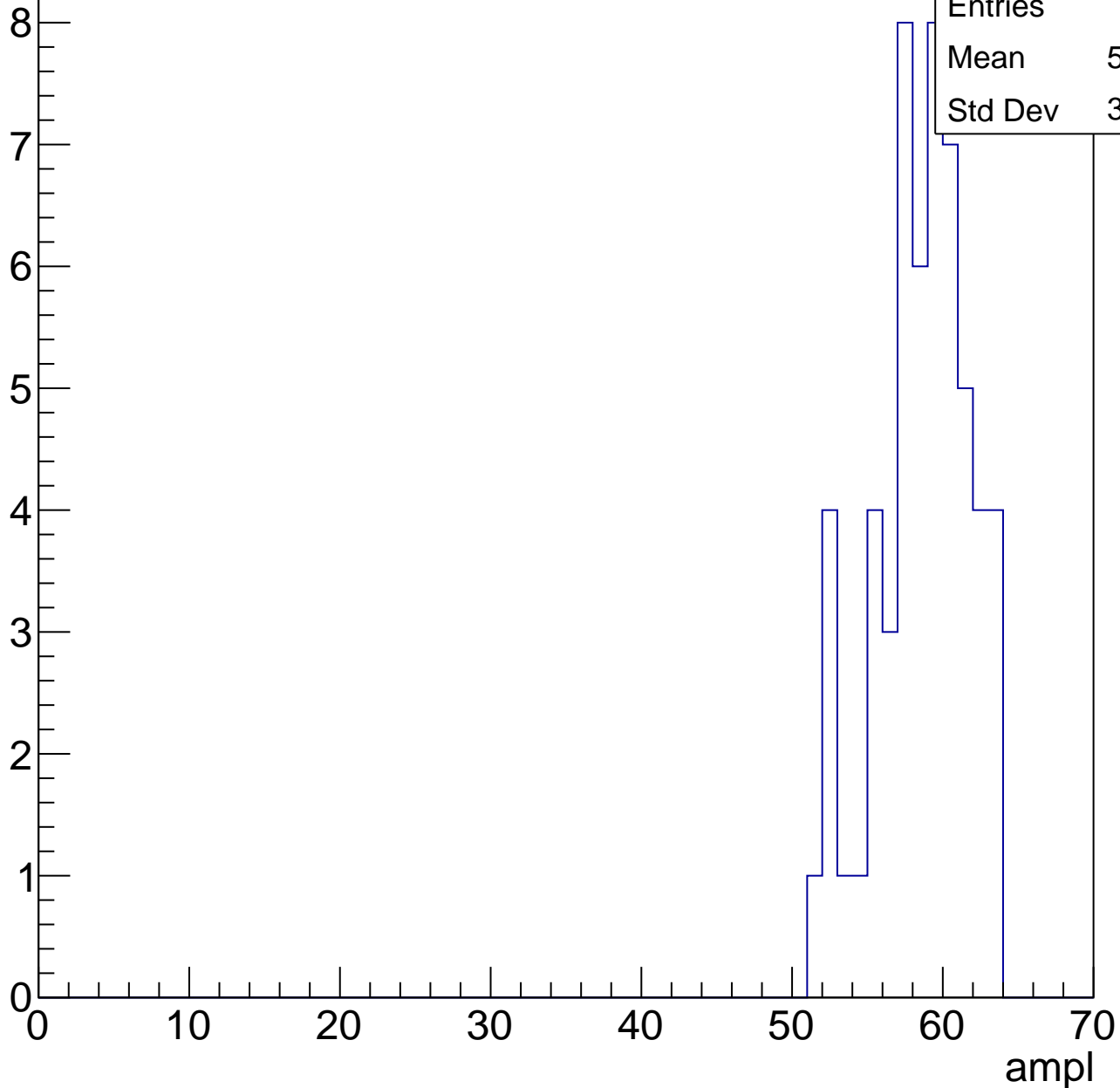
Entries	58
Mean	53.47
Std Dev	3.313



B1L103S, U6-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



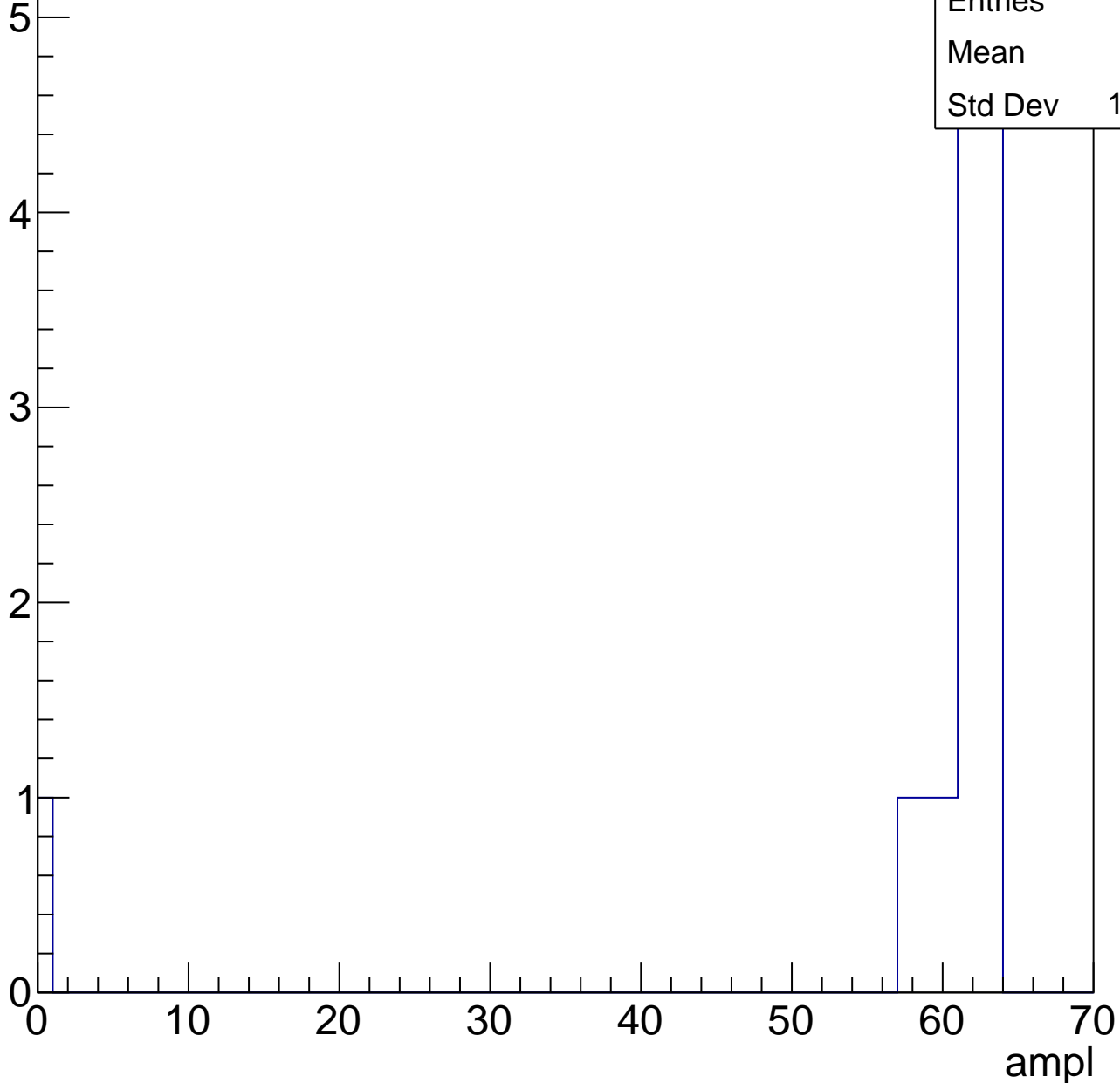
Entries	56
Mean	58.12
Std Dev	3.088

B1L103S, U6-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.2
Std Dev	13.45



B1L103S, U6-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U6-ch79, adc0

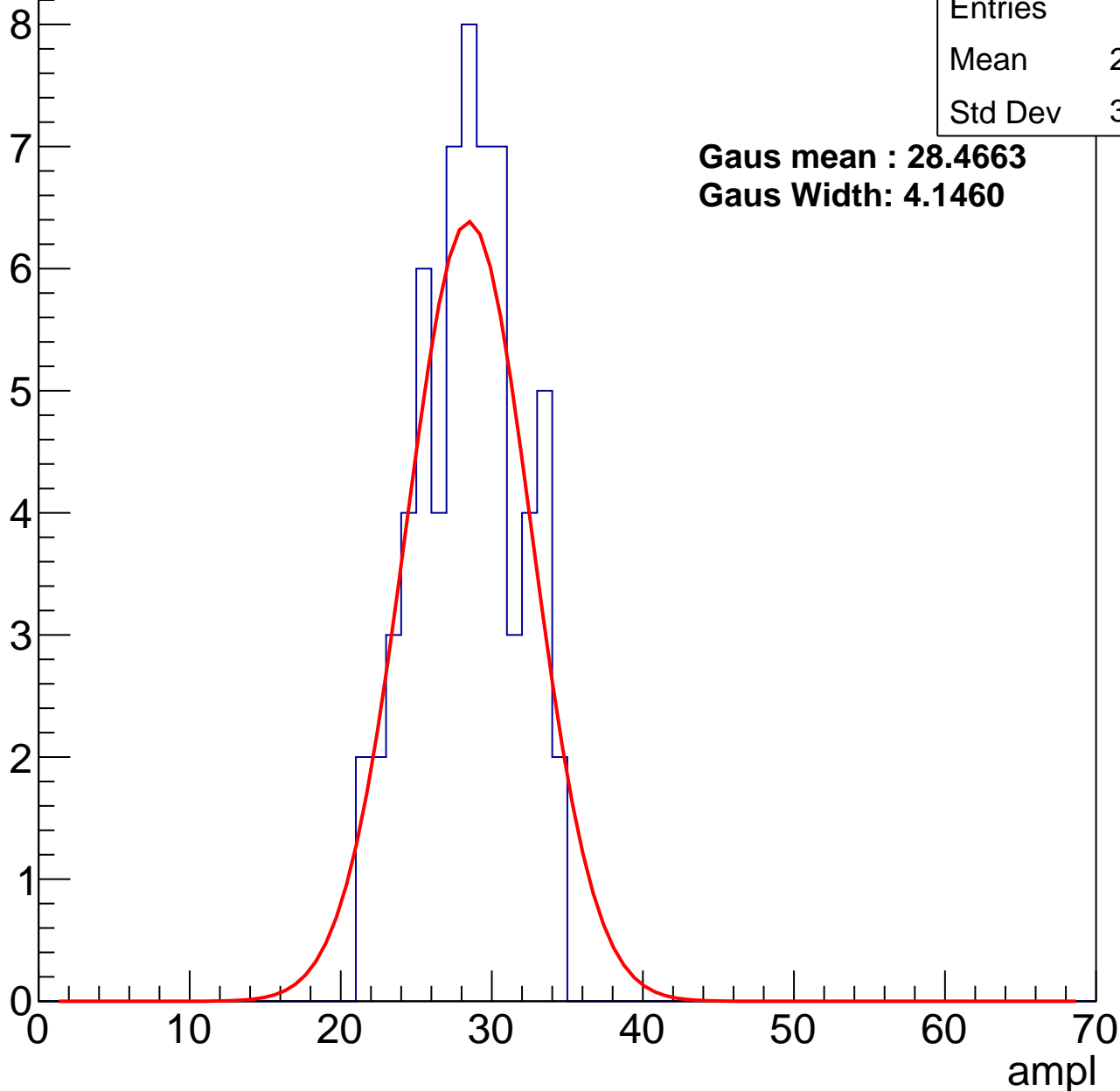
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	27.89
Std Dev	3.327

Gaus mean : 28.4663

Gaus Width: 4.1460



B1L103S, U6-ch79, adc1

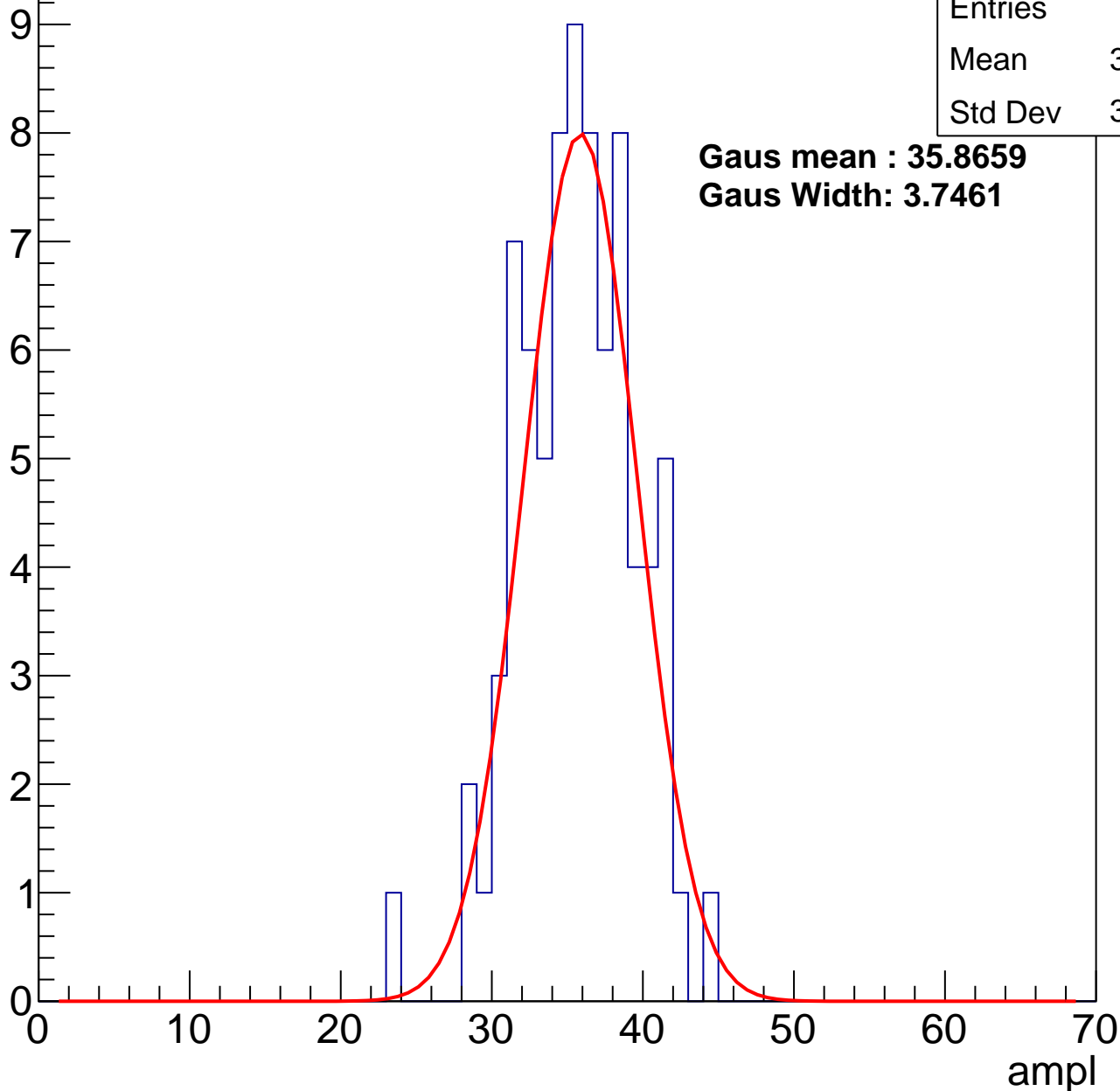
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	35.19
Std Dev	3.769

Gaus mean : 35.8659

Gaus Width: 3.7461



B1L103S, U6-ch79, adc2

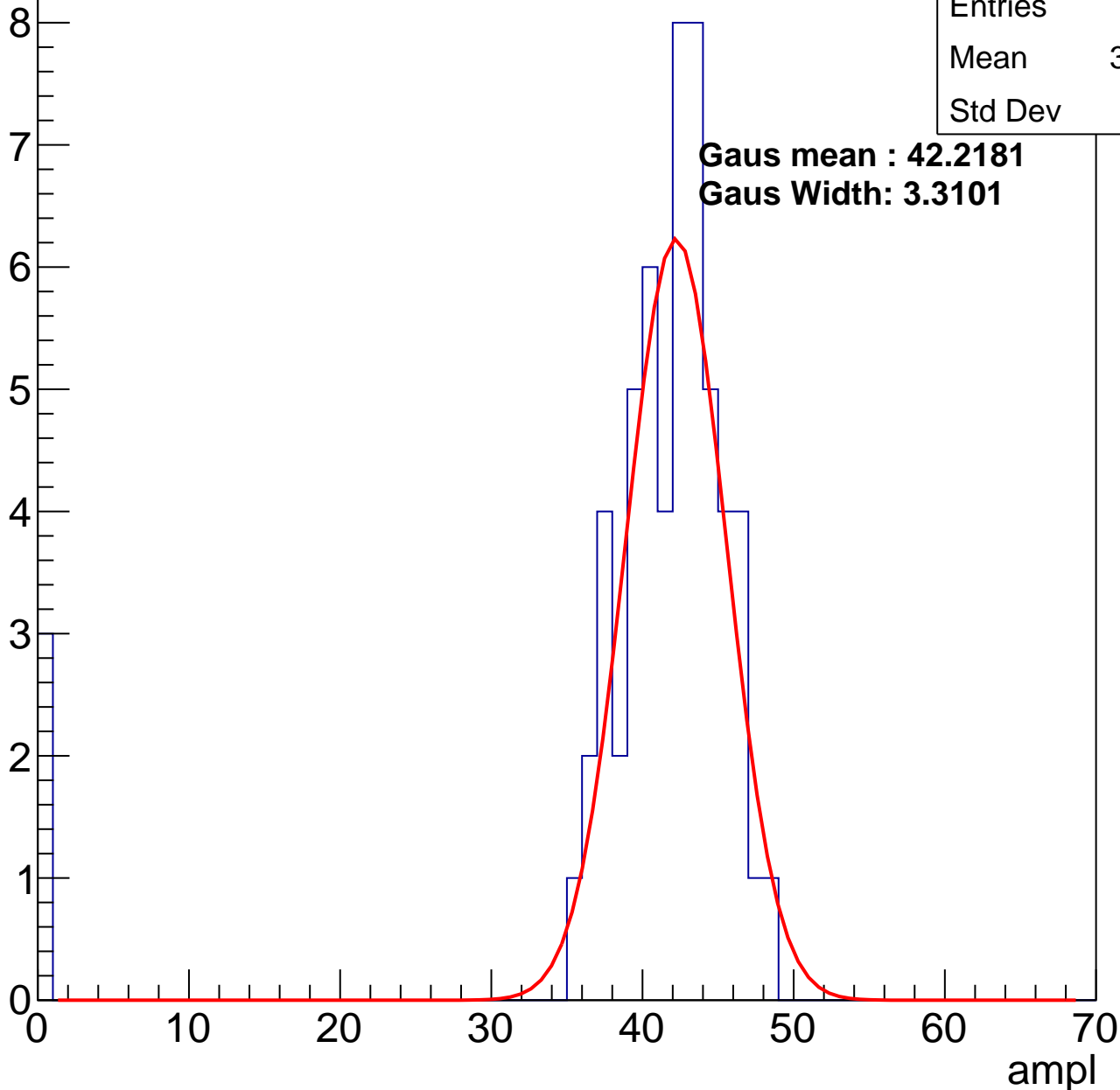
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	39.47
Std Dev	9.68

Gaus mean : 42.2181

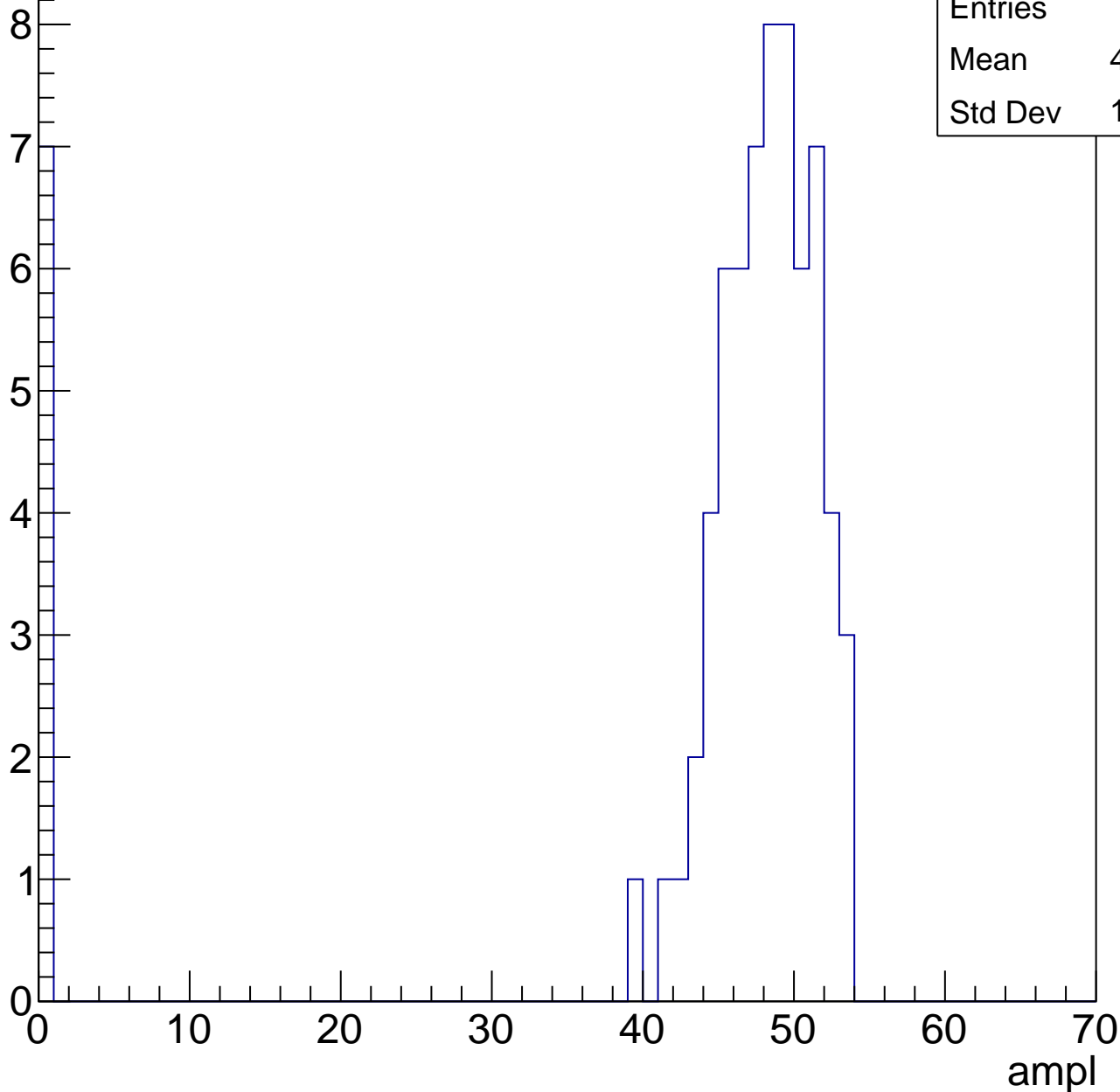
Gaus Width: 3.3101



B1L103S, U6-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

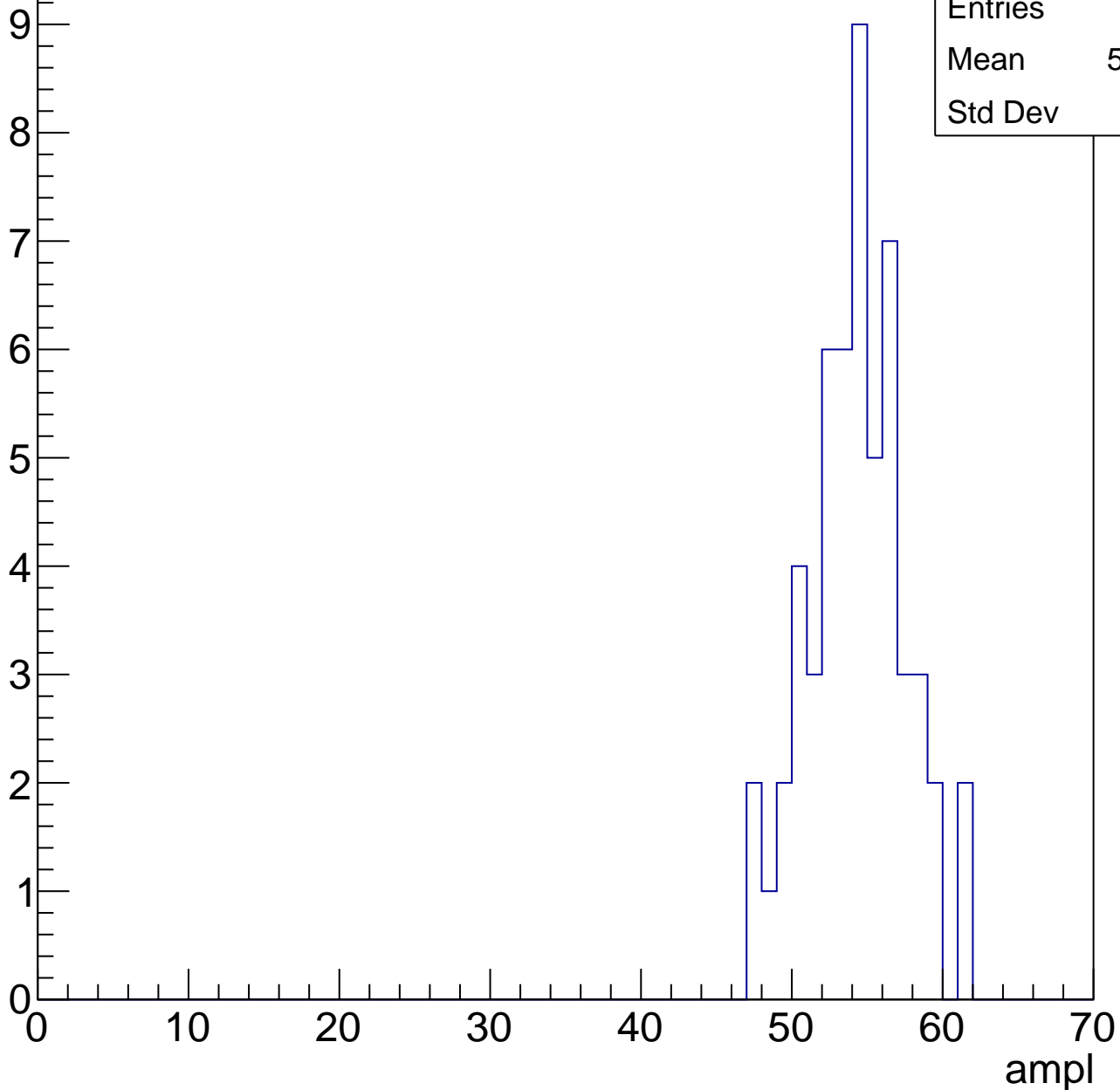


B1L103S, U6-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

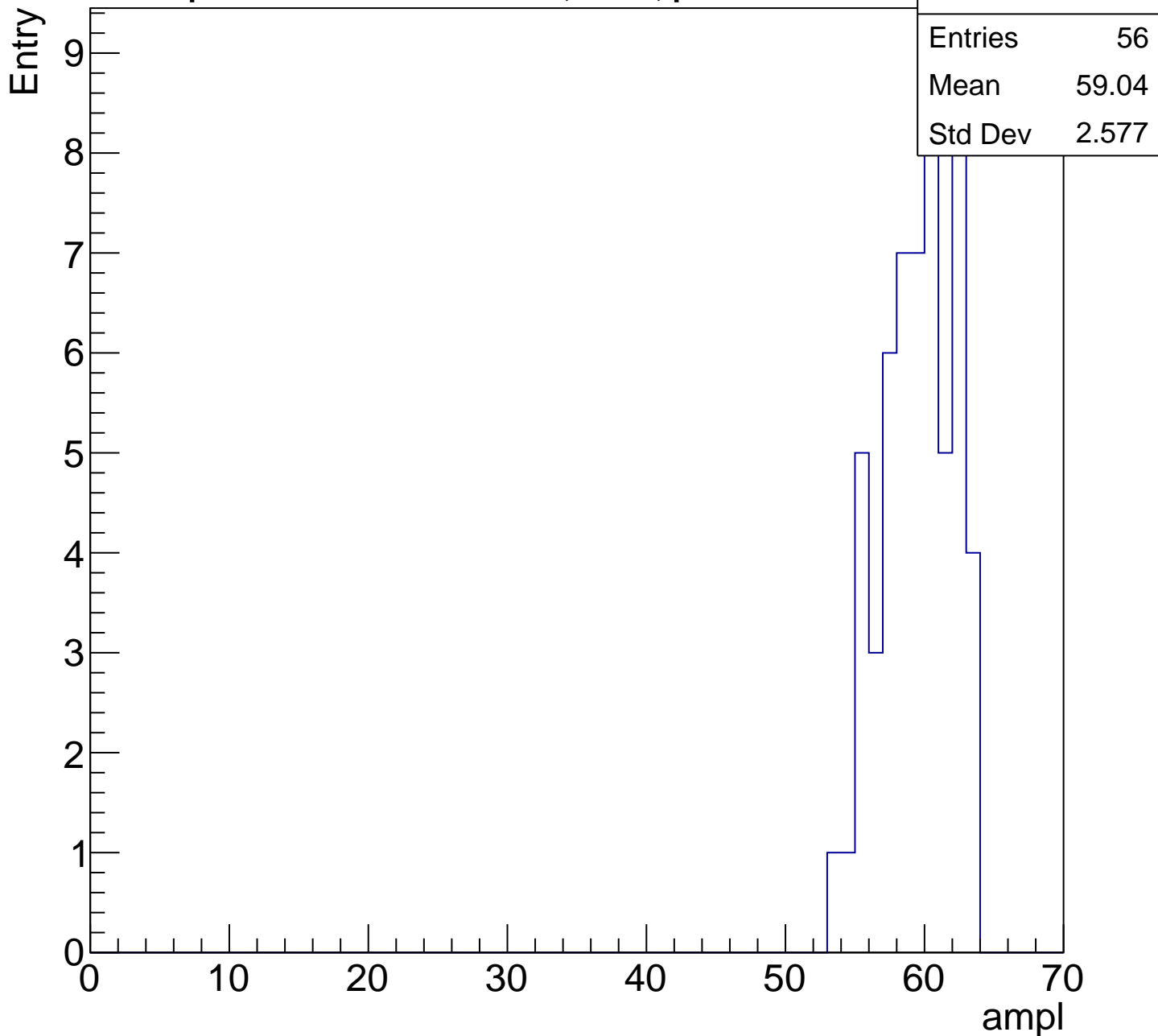
Entry

Entries	55
Mean	53.84
Std Dev	3.19



B1L103S, U6-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

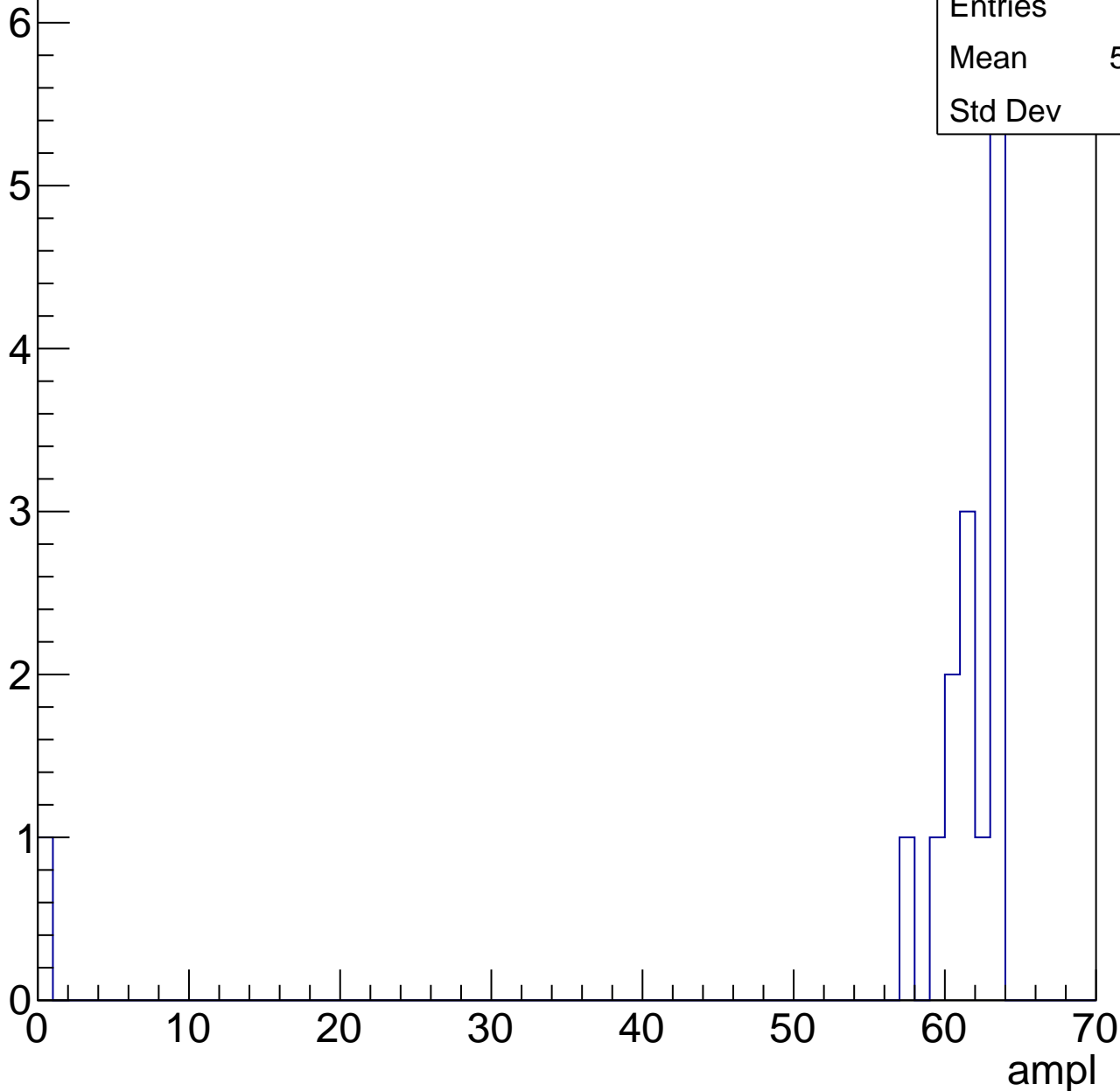


B1L103S, U6-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.27
Std Dev	15.4



B1L103S, U6-ch79, adc7

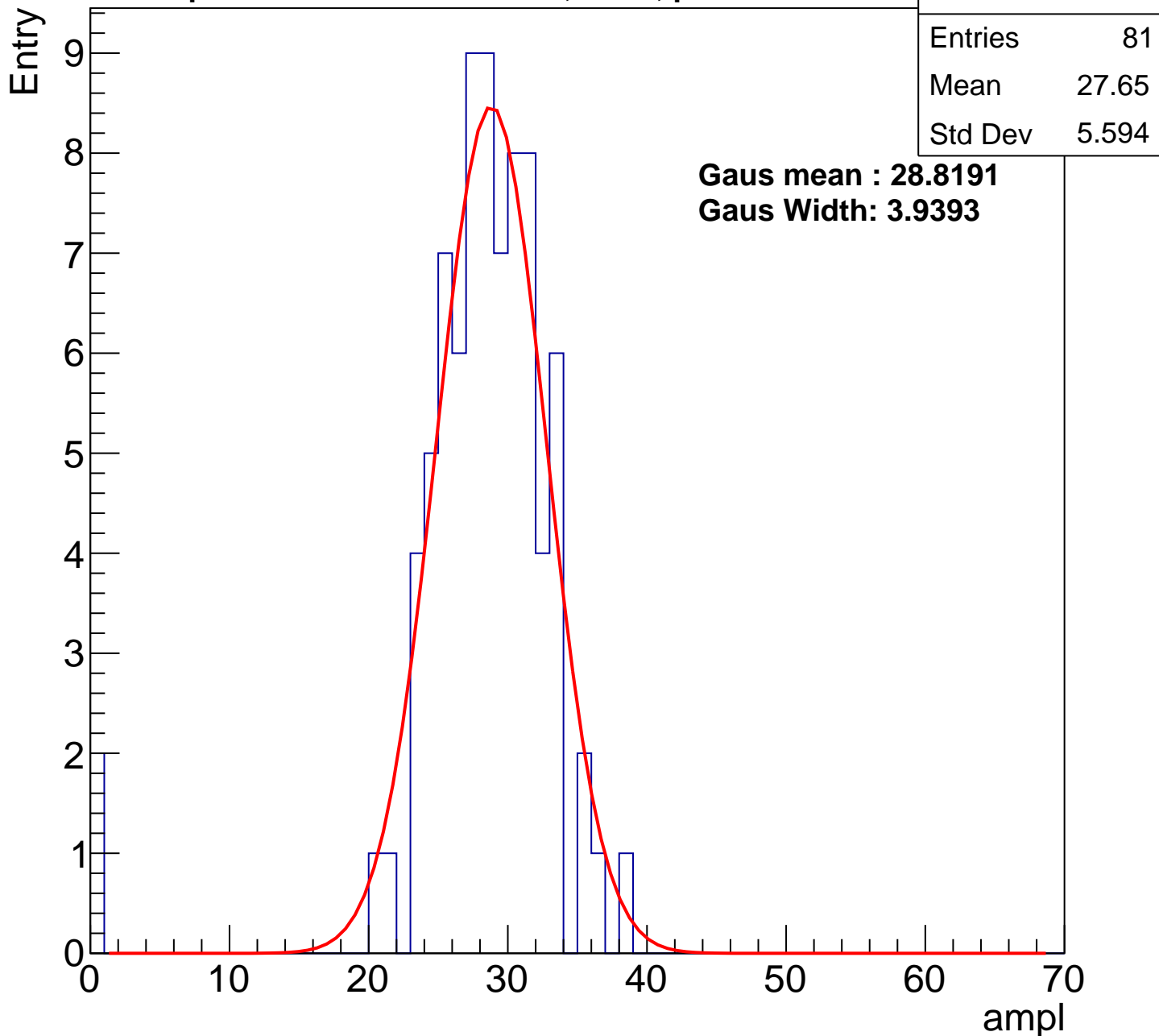
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch80, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch80, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	32.84
Std Dev	9.857

Gaus mean : 35.9517

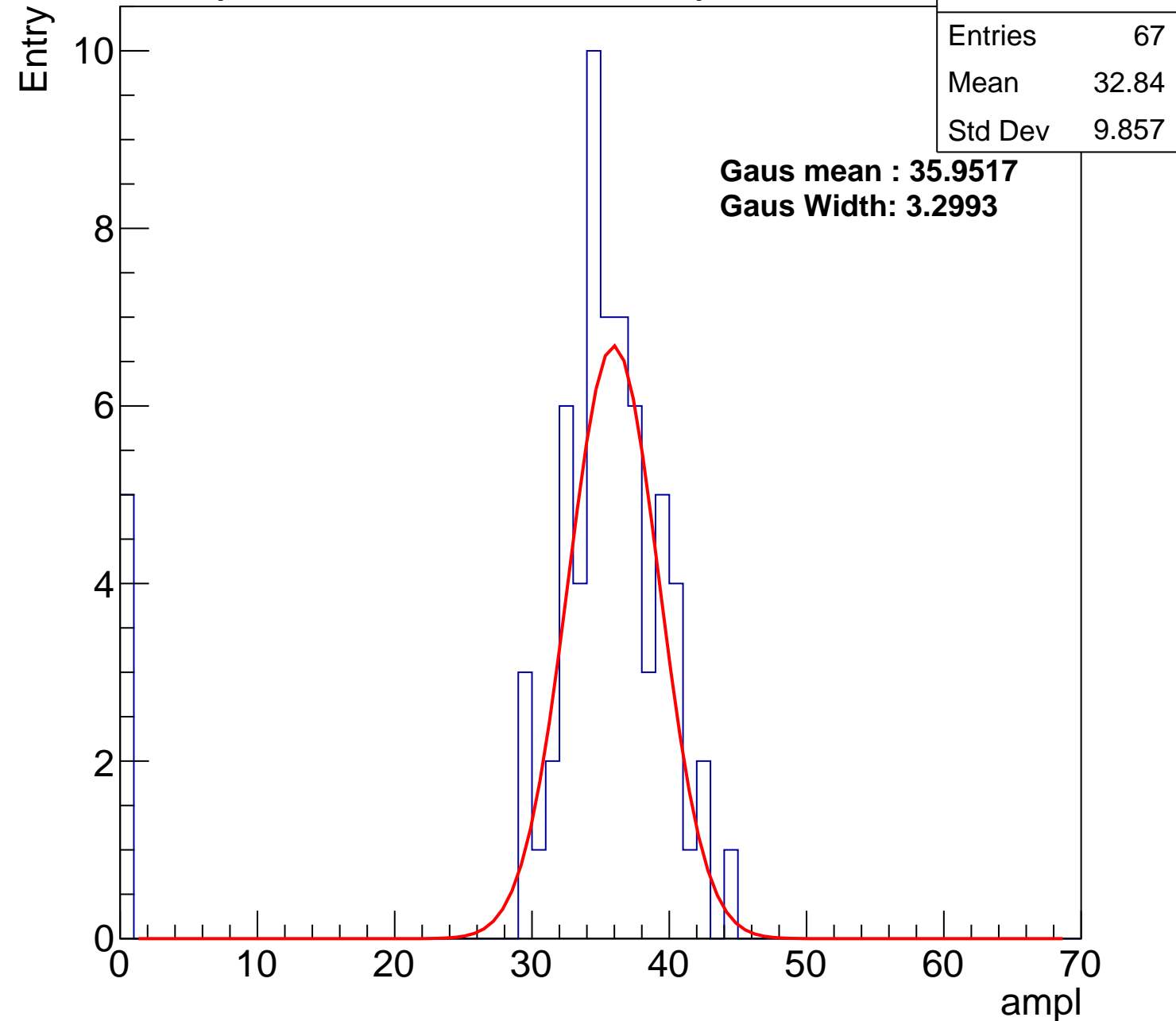
Gaus Width: 3.2993

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch80, adc2

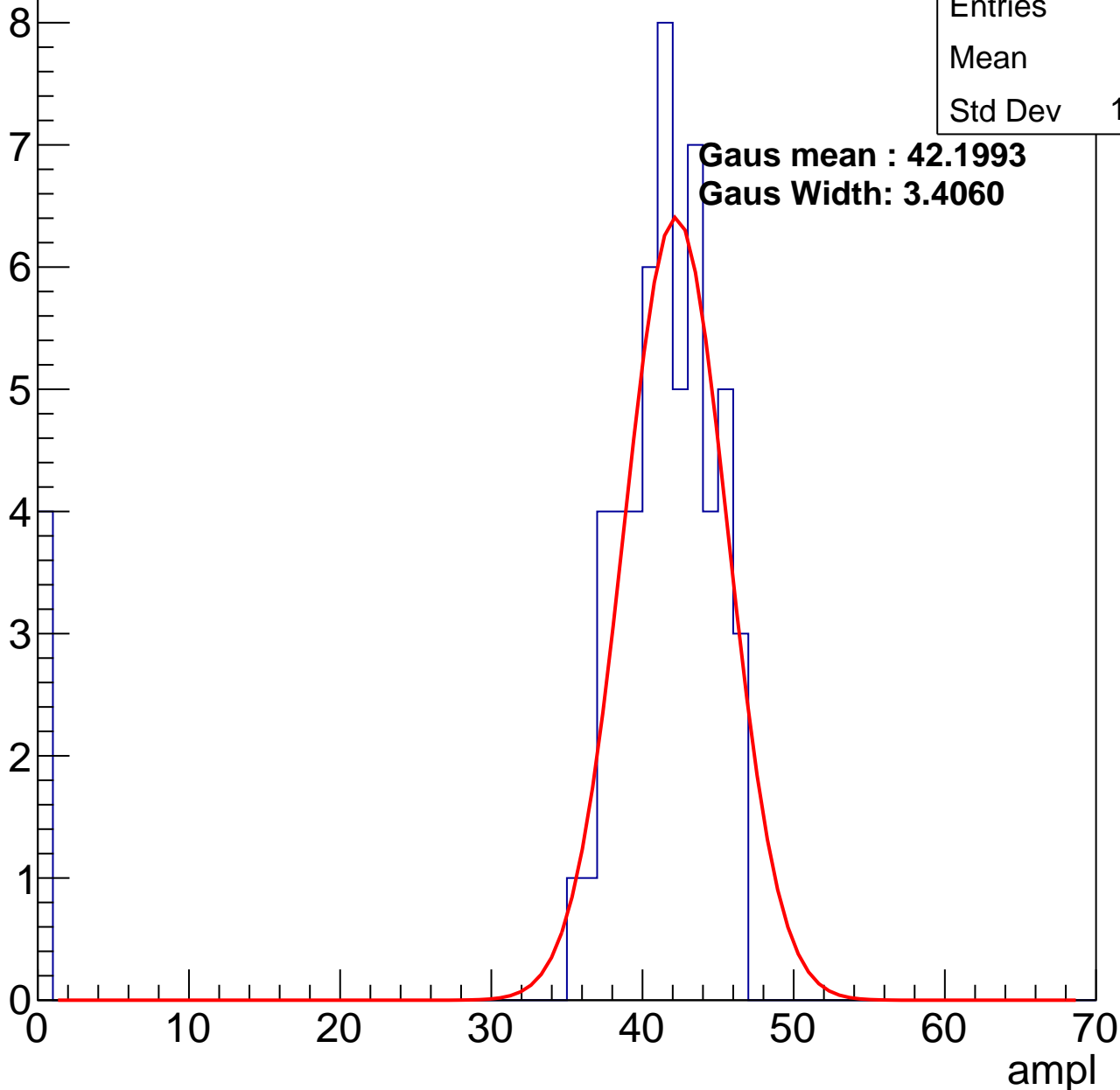
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	38.3
Std Dev	10.96

Gaus mean : 42.1993

Gaus Width: 3.4060

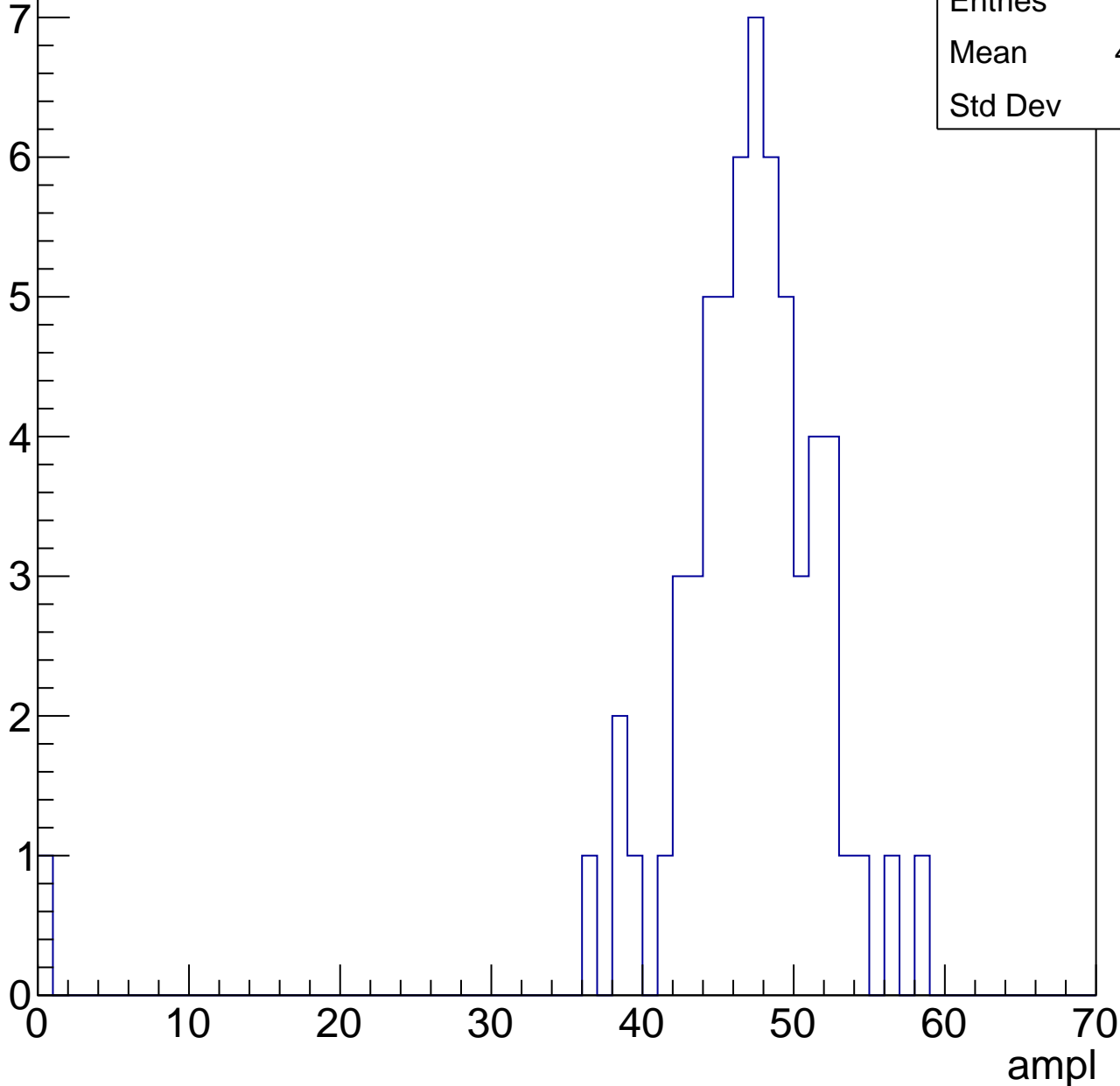


B1L103S, U6-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	46.11
Std Dev	7.29

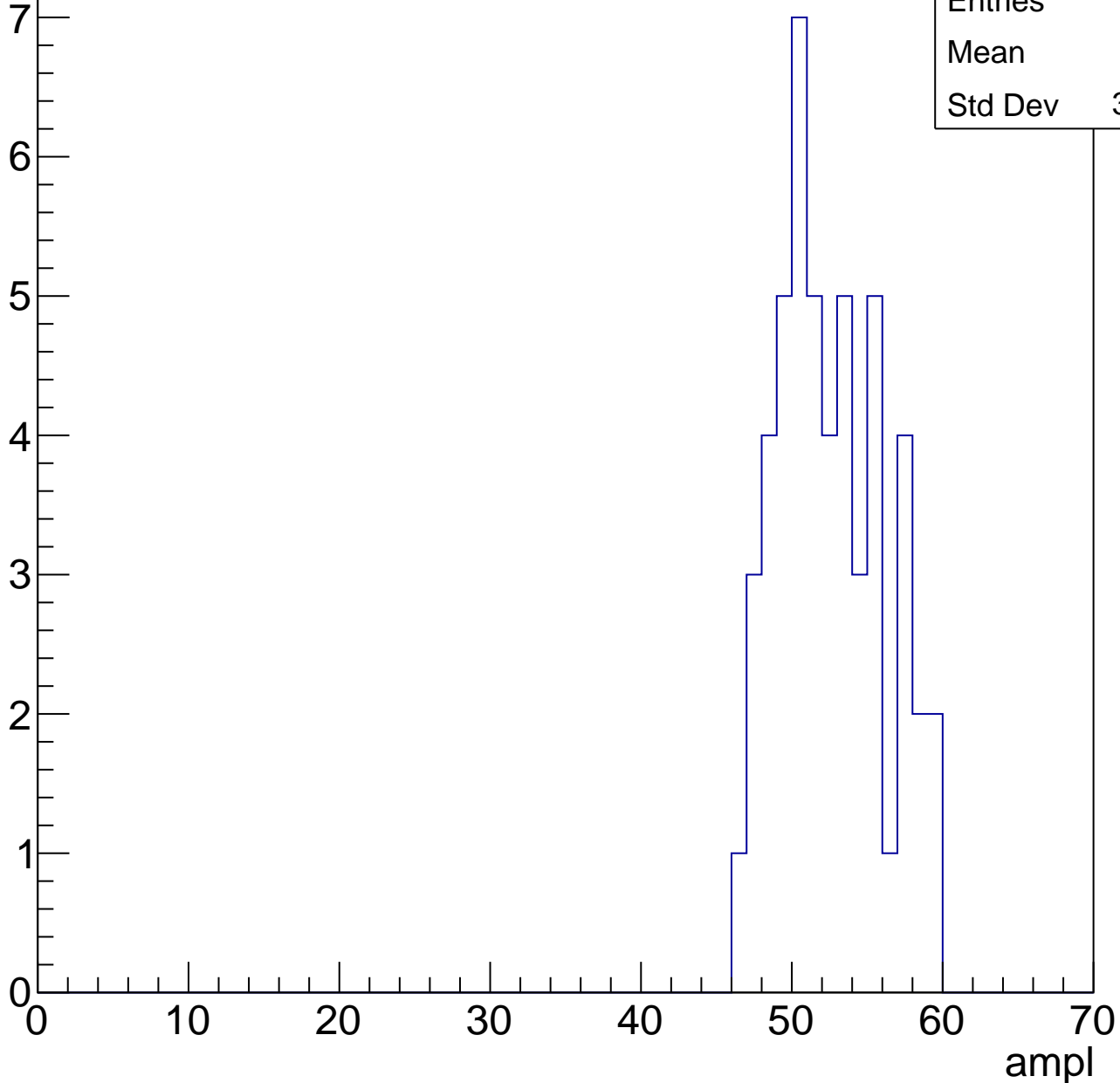


B1L103S, U6-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	52.1
Std Dev	3.431



B1L103S, U6-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 61

Mean 57.51

Std Dev 3.181

0

0

10

20

30

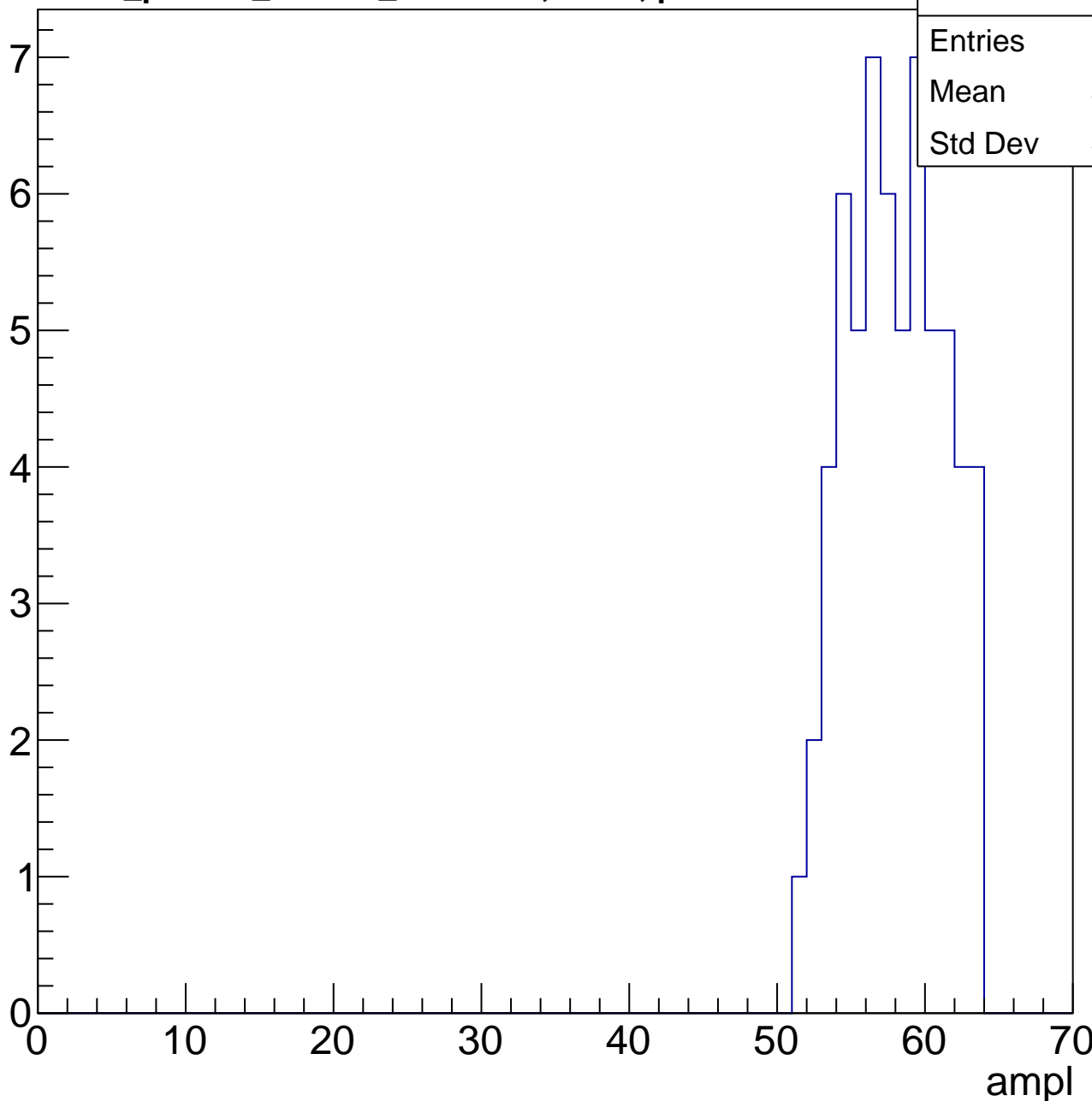
40

50

60

70

ampl

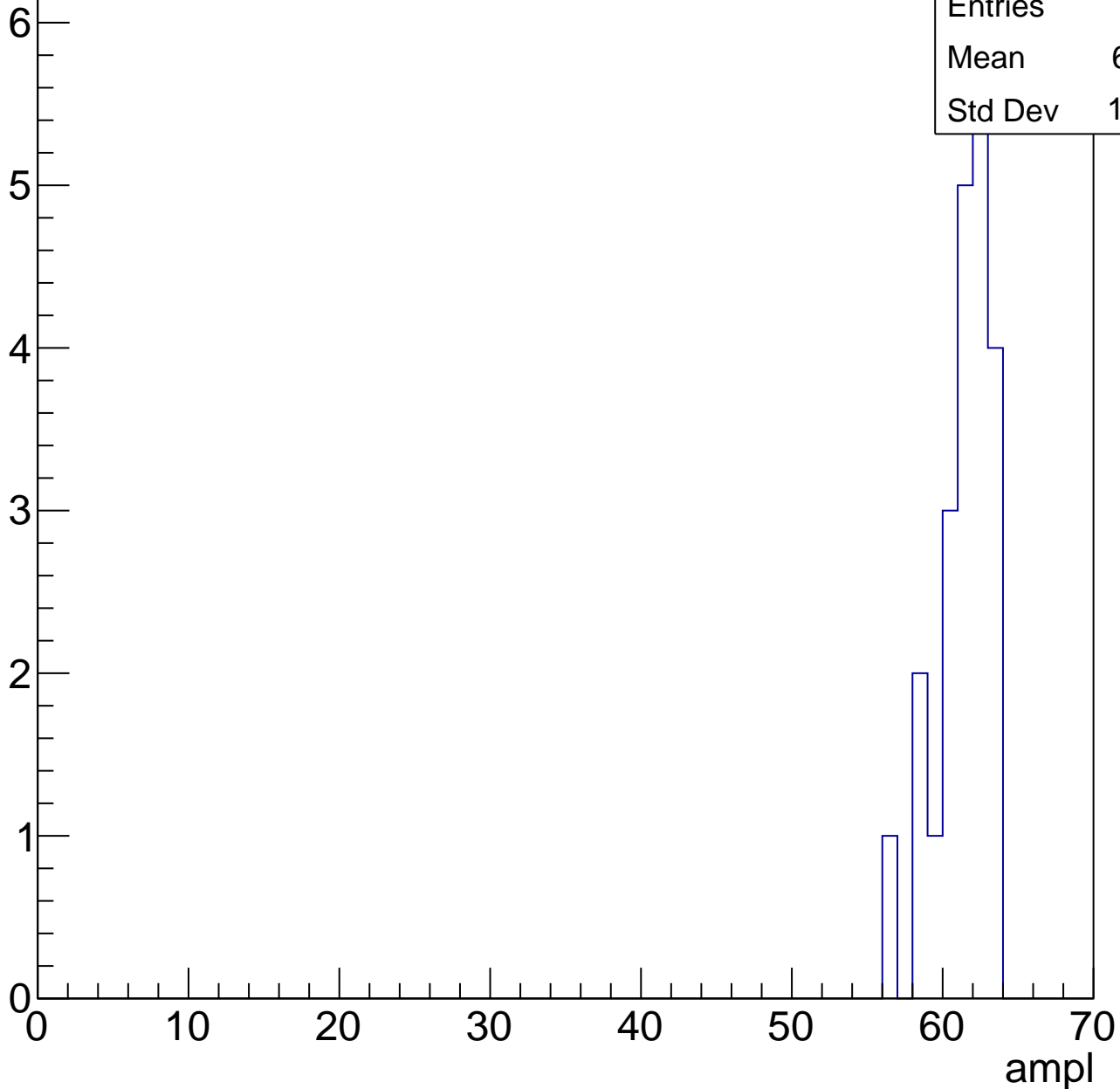


B1L103S, U6-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	60.91
Std Dev	1.807

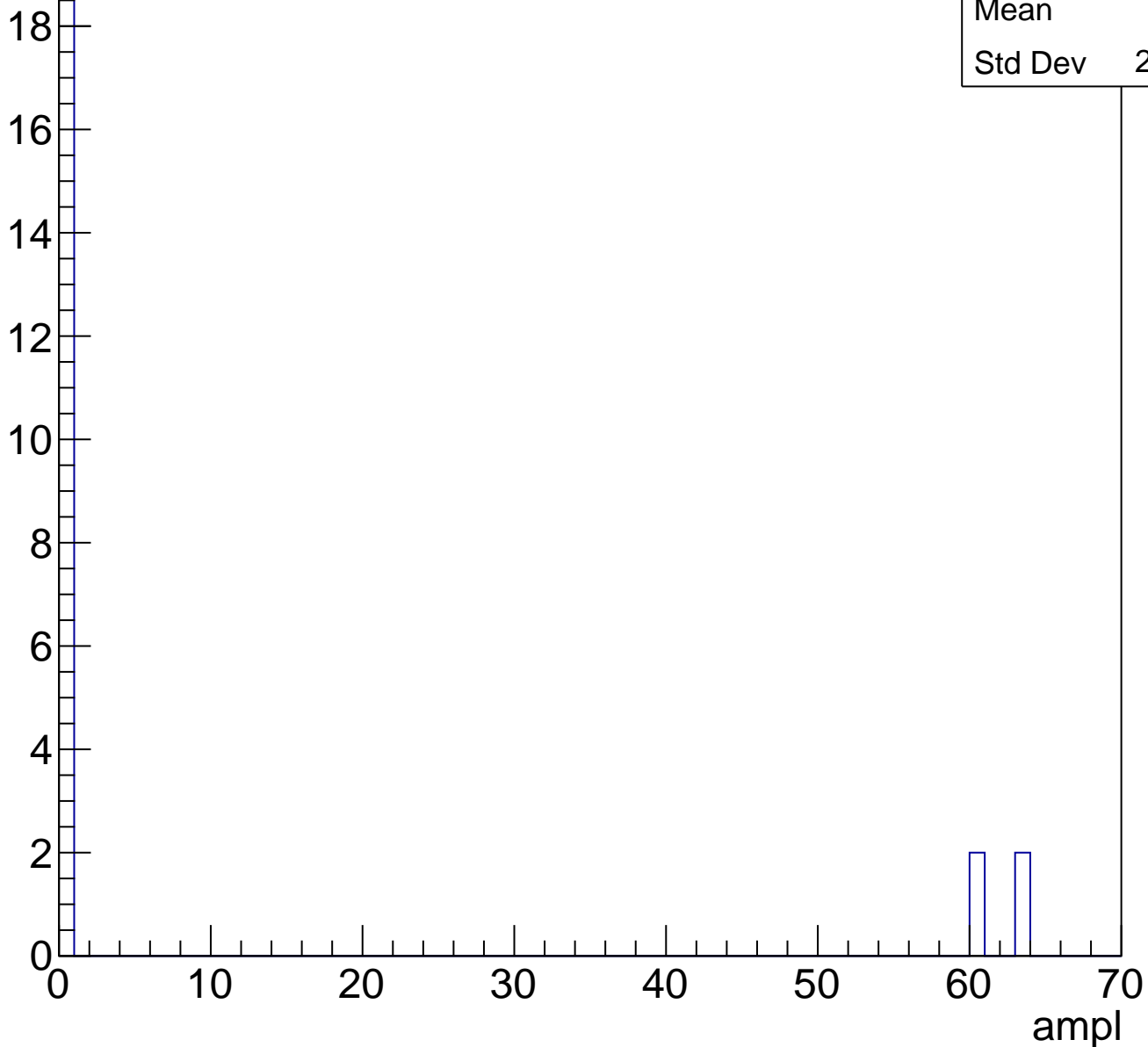


B1L103S, U6-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.7
Std Dev	23.32

Entry



B1L103S, U6-ch81, adc0

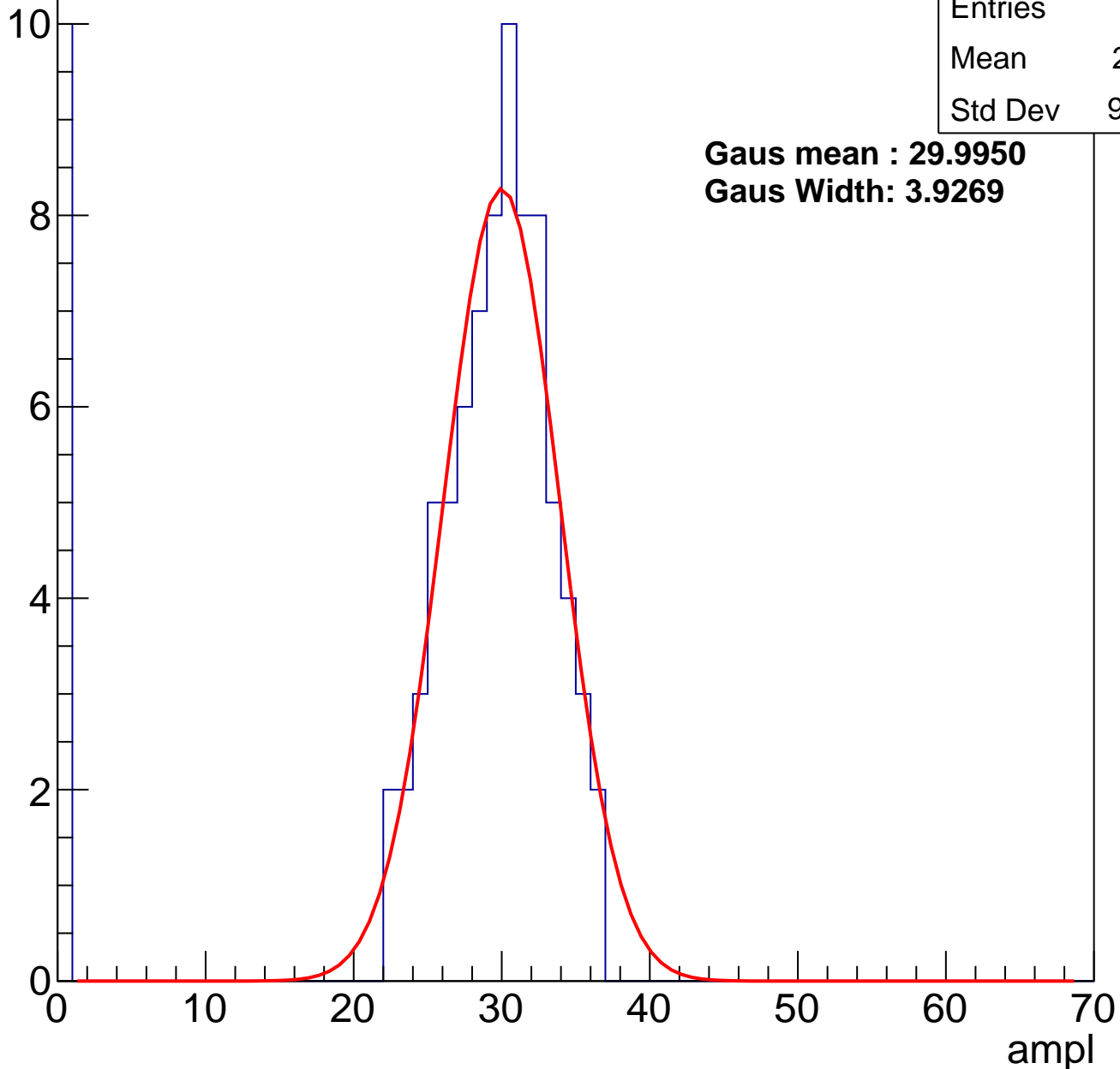
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	26.01
Std Dev	9.843

Gaus mean : 29.9950

Gaus Width: 3.9269

Entry



B1L103S, U6-ch81, adc1

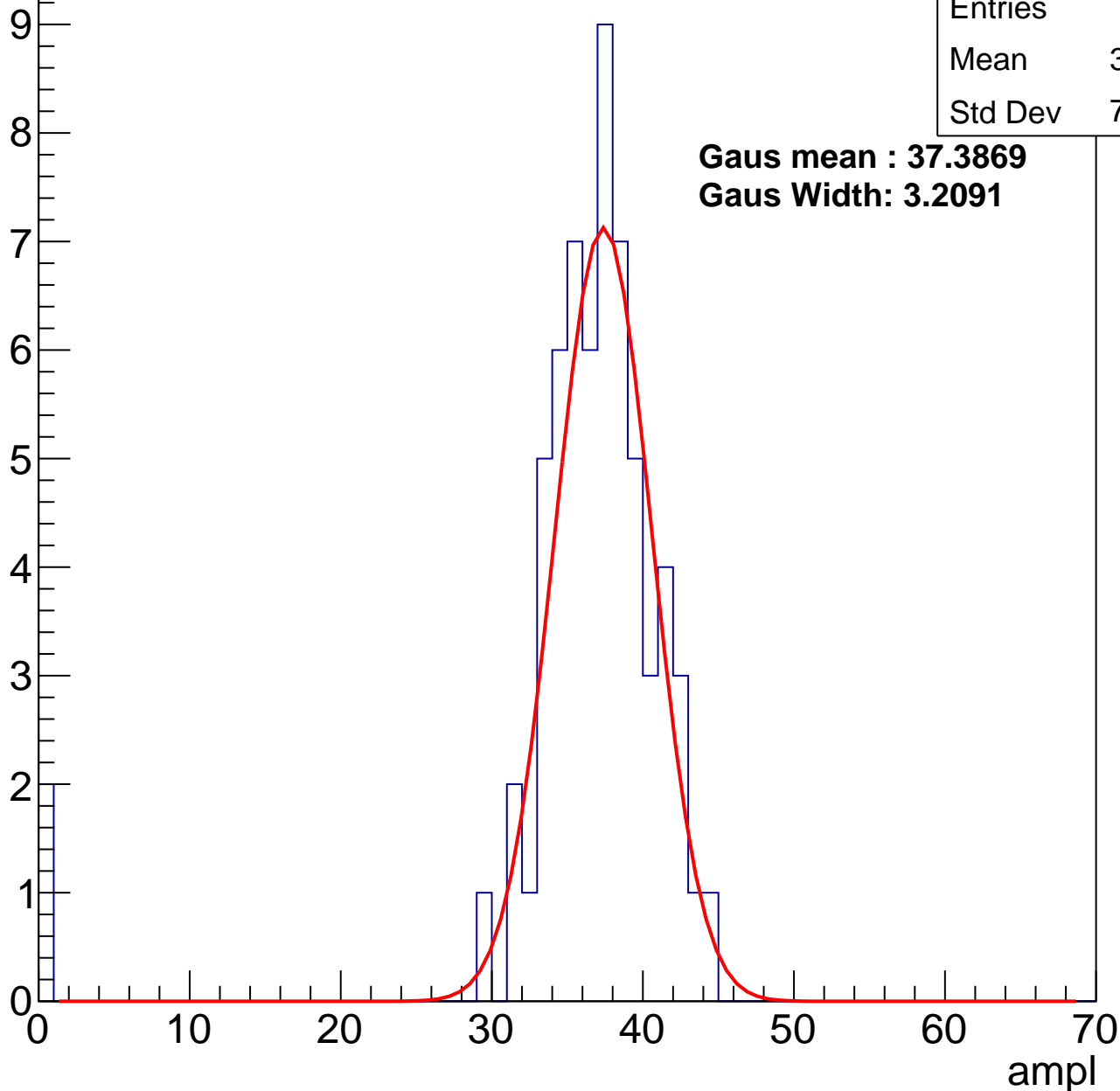
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	35.62
Std Dev	7.155

Gaus mean : 37.3869

Gaus Width: 3.2091



B1L103S, U6-ch81, adc2

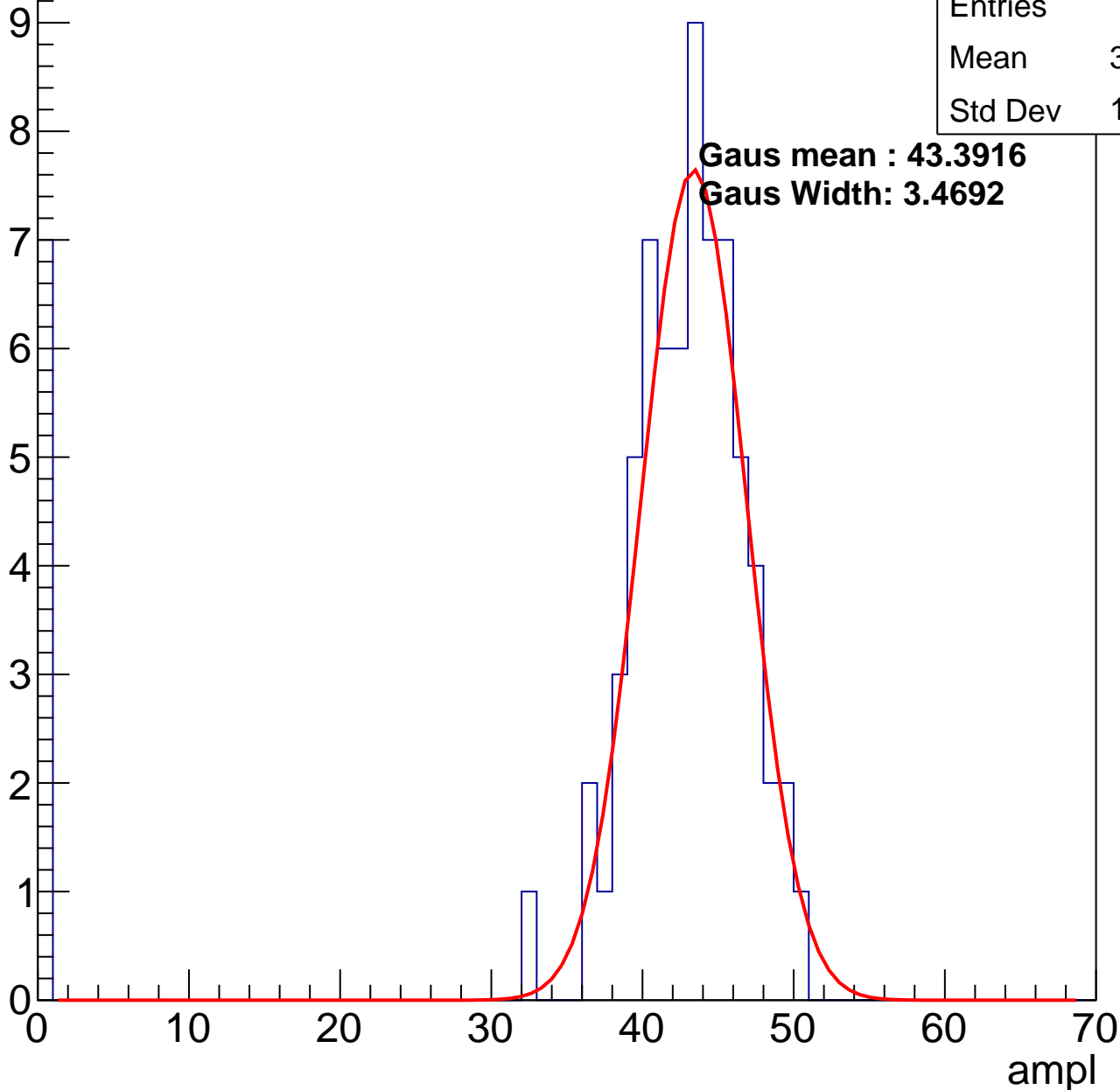
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	38.67
Std Dev	12.83

Gaus mean : 43.3916

Gaus Width: 3.4692

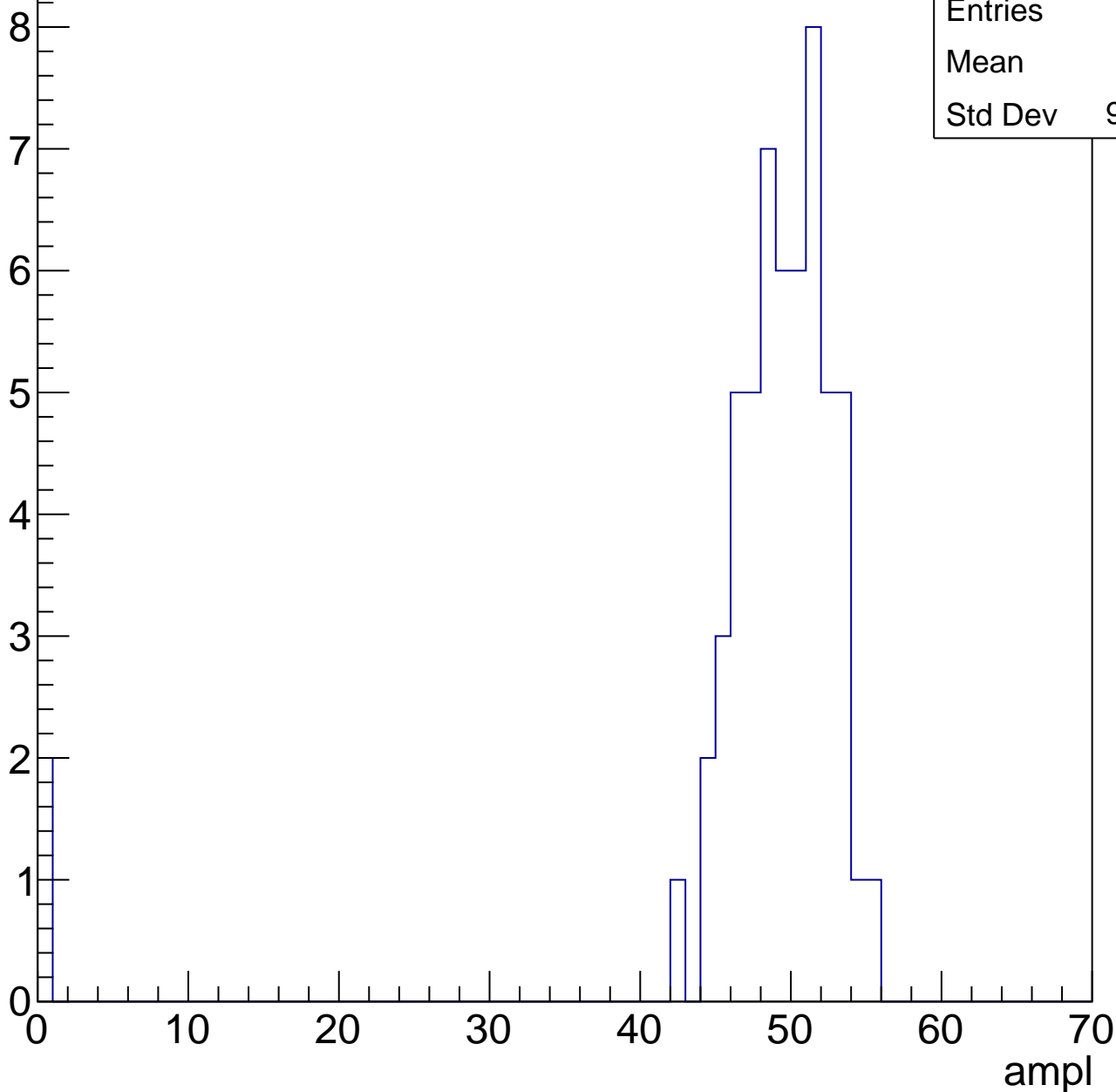


B1L103S, U6-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.4
Std Dev	9.459

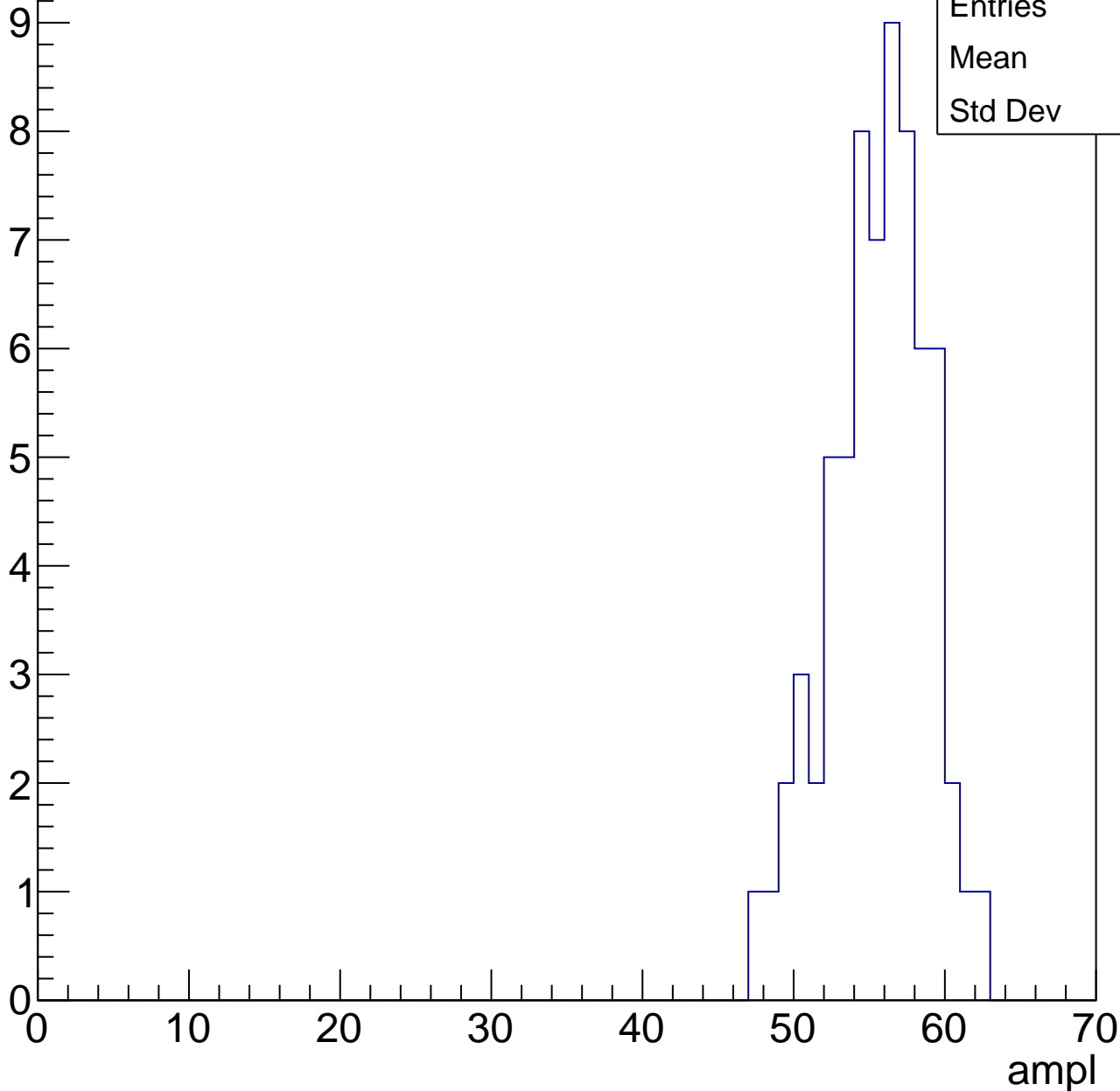


B1L103S, U6-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	55.1
Std Dev	3.21



B1L103S, U6-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

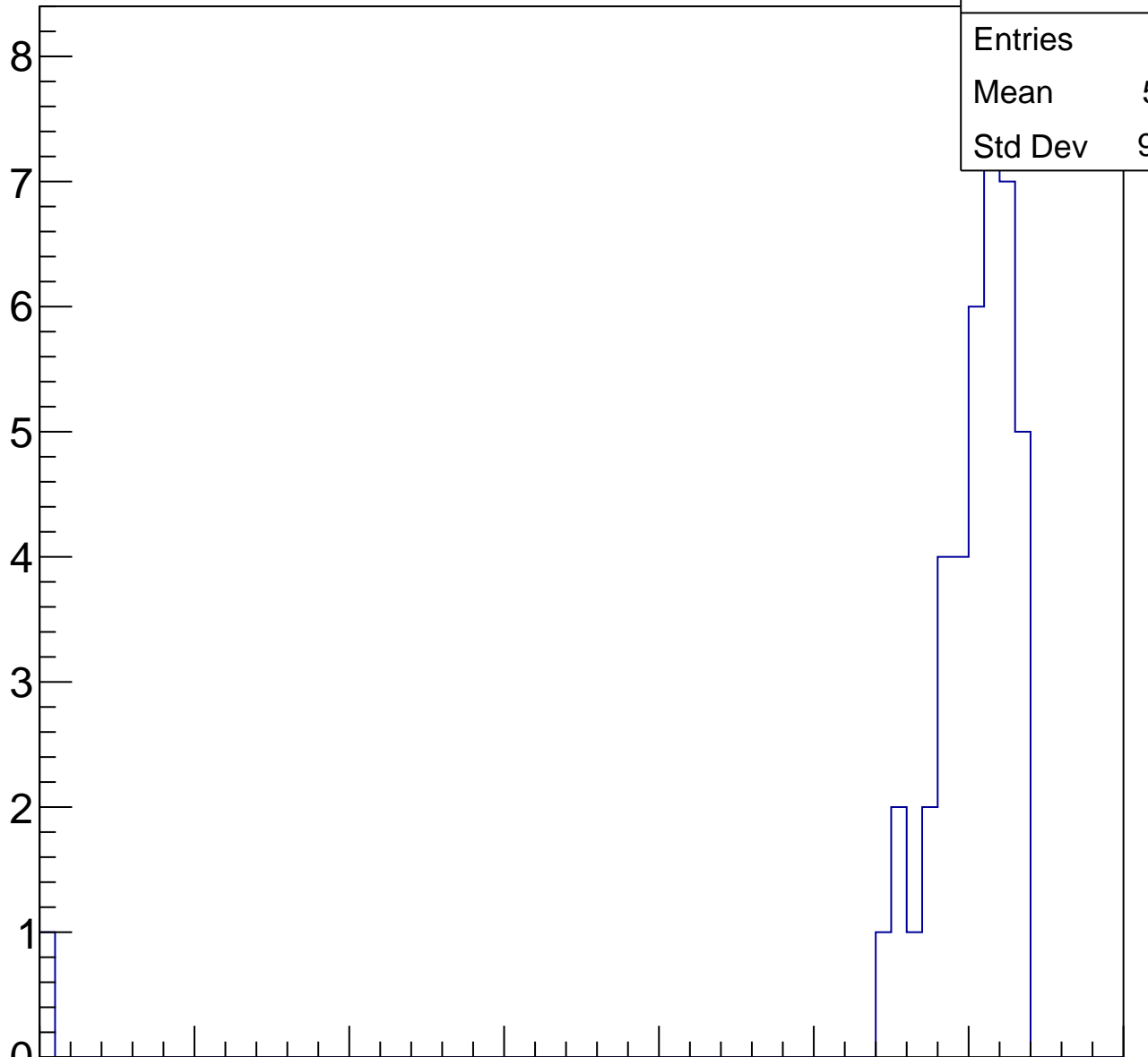
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.51
Std Dev	9.538

ampl

0 10 20 30 40 50 60 70

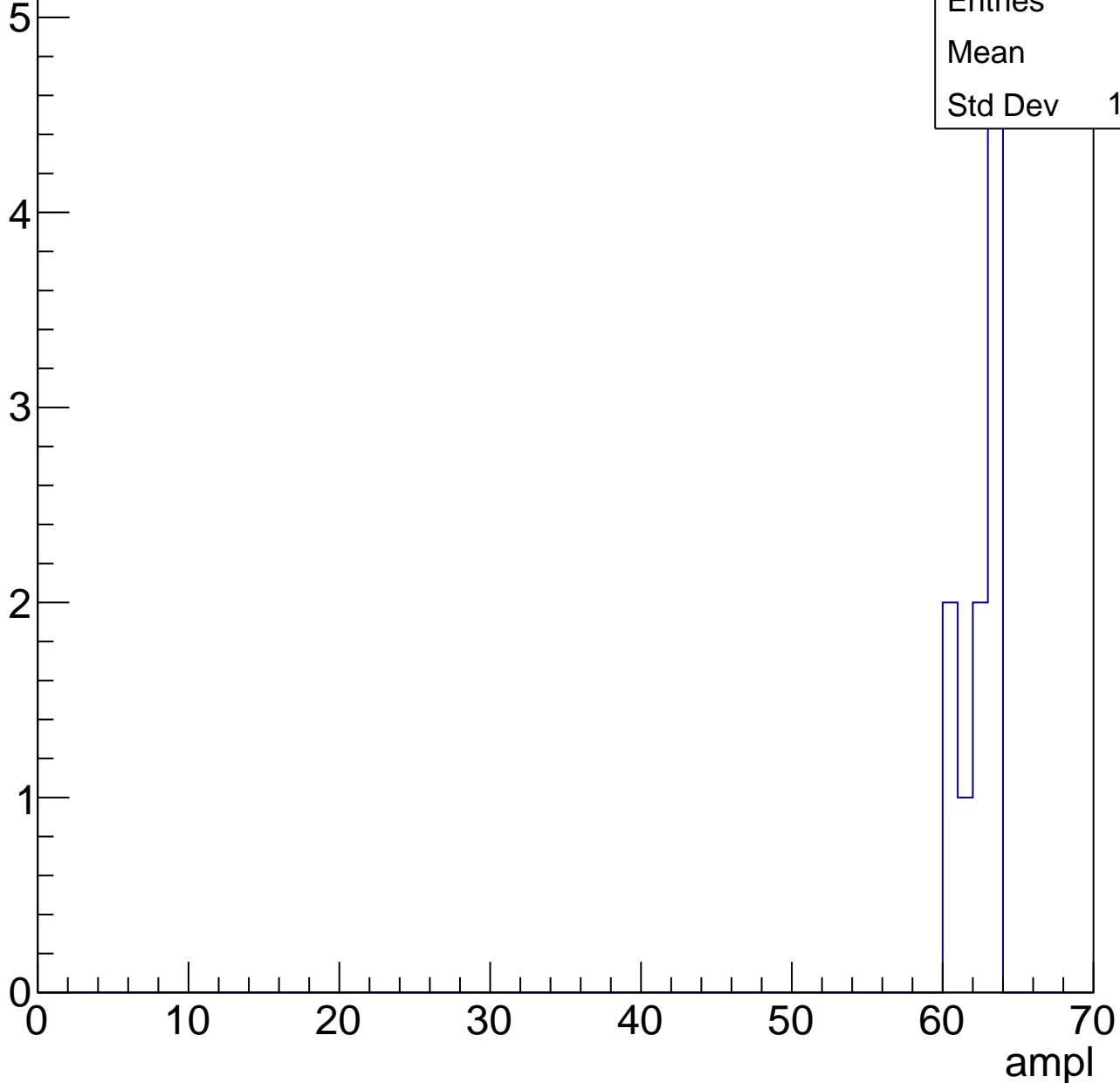


B1L103S, U6-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

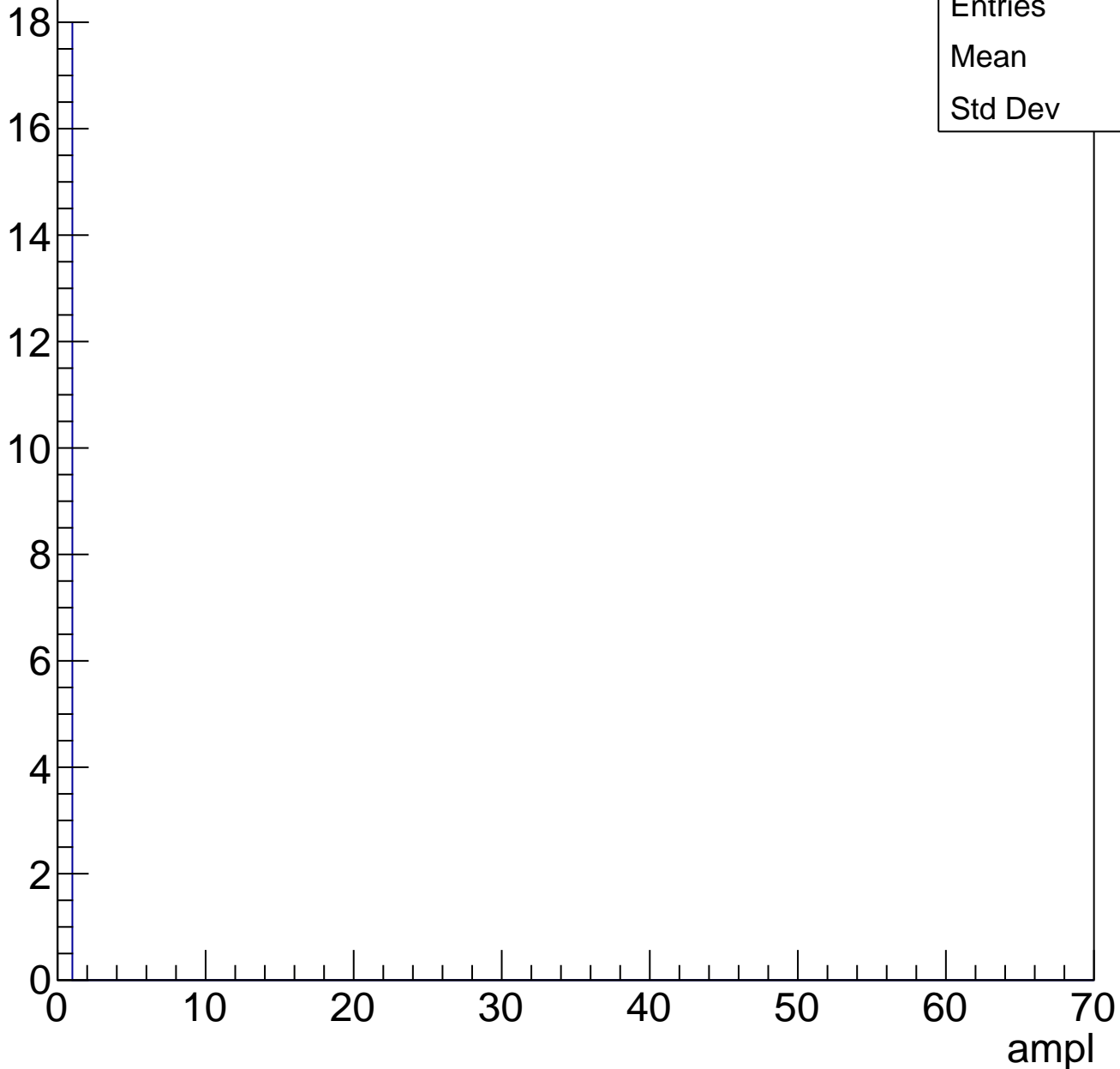
Entries	10
Mean	62
Std Dev	1.183



B1L103S, U6-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	129
Mean	16.88
Std Dev	13.65

Gaus mean : 29.9797

Gaus Width: 4.6026

Entry

50

40

30

20

10

0

0

10

20

30

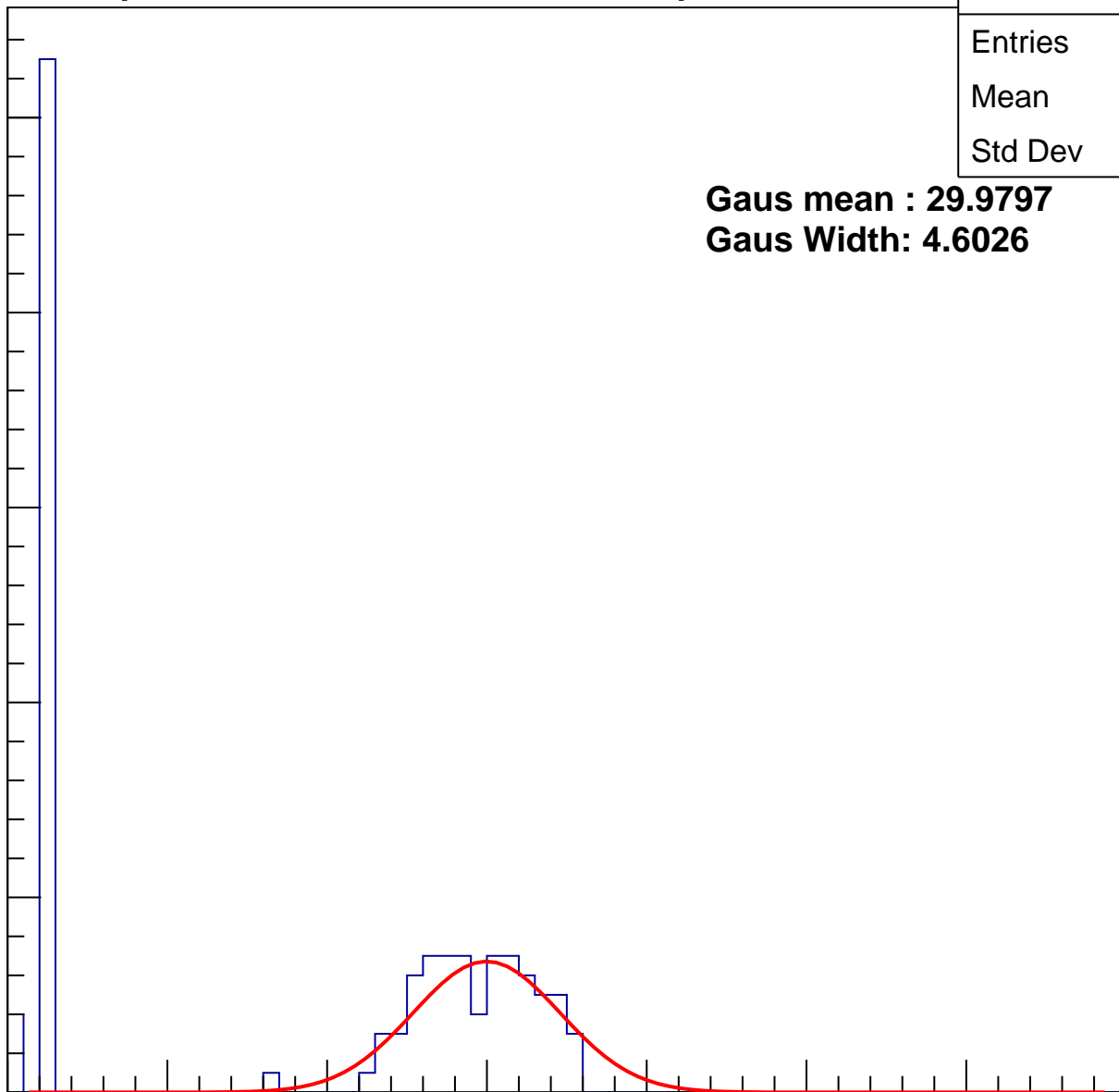
40

50

60

70

ampl



B1L103S, U6-ch82, adc1

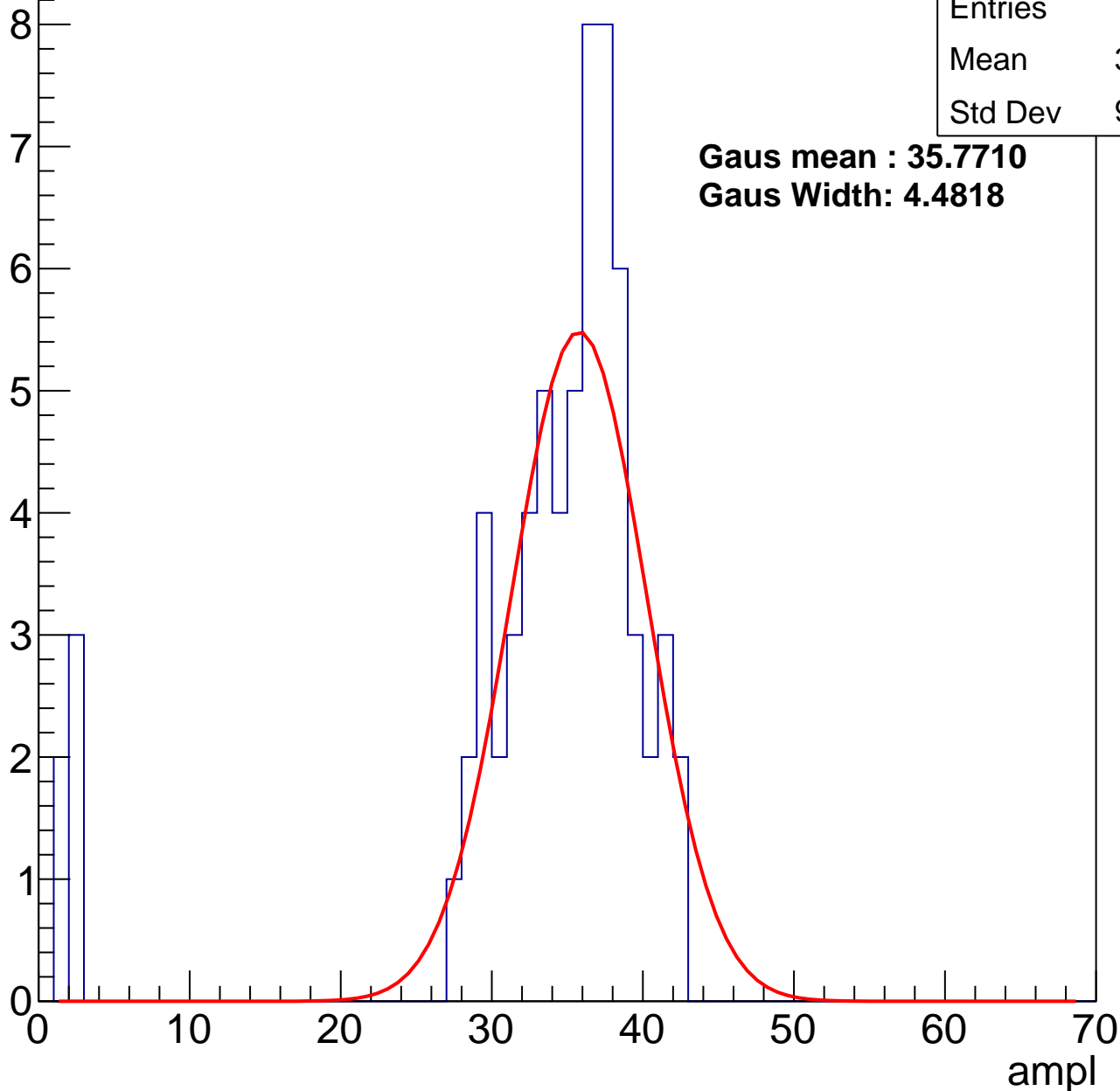
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.51
Std Dev	9.591

Gaus mean : 35.7710

Gaus Width: 4.4818



B1L103S, U6-ch82, adc2

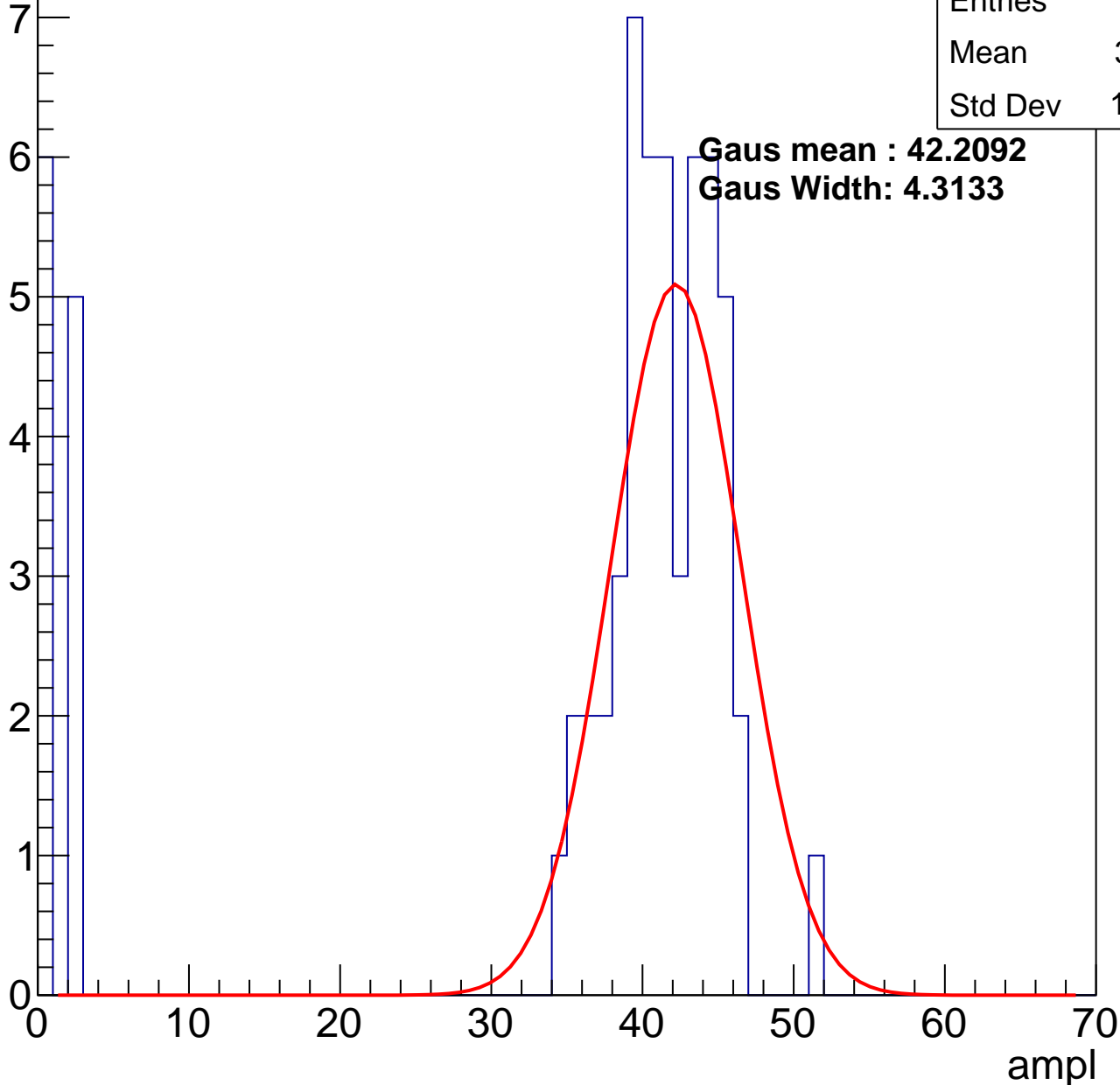
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.11
Std Dev	15.57

Gaus mean : 42.2092

Gaus Width: 4.3133

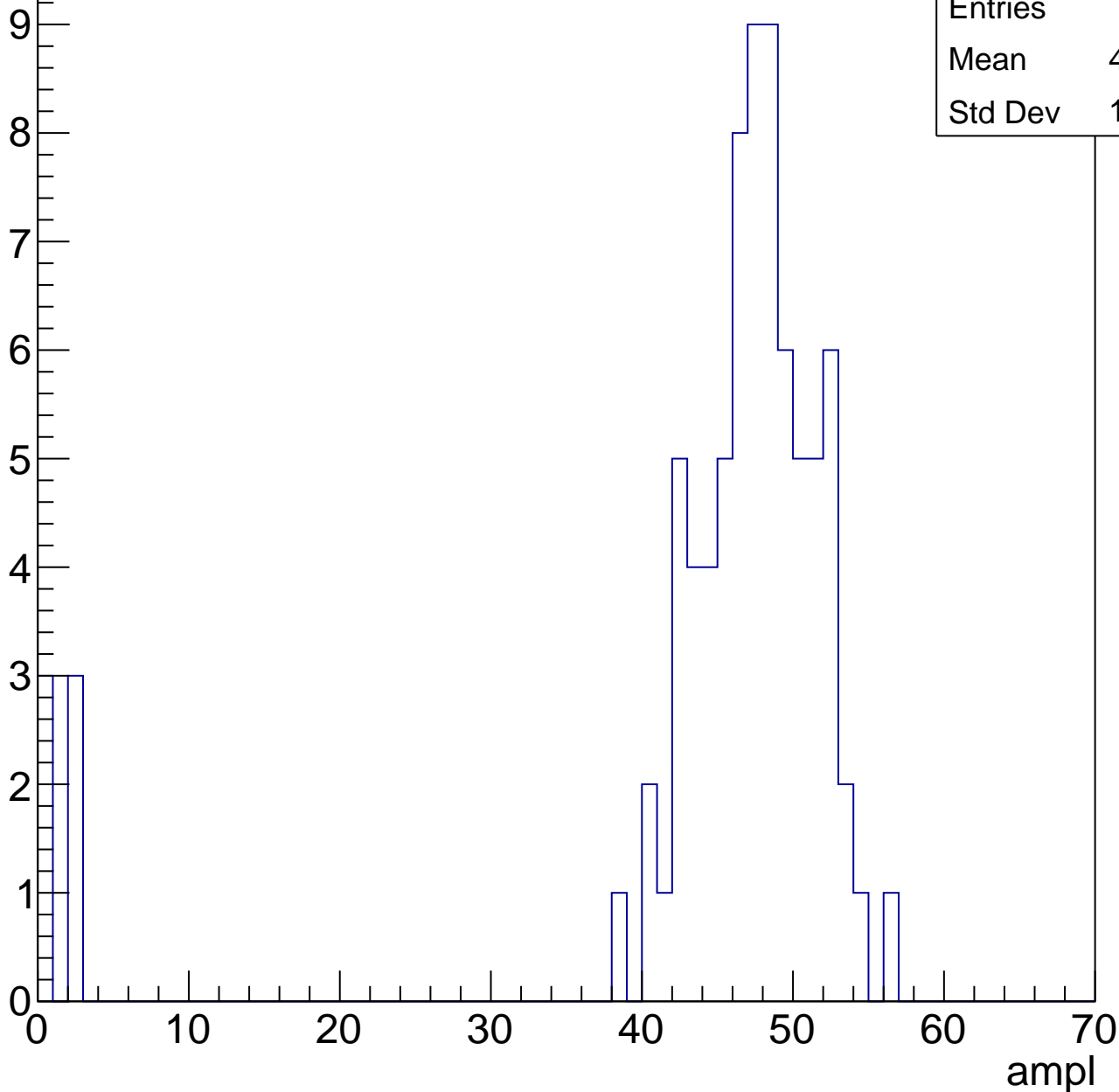


B1L103S, U6-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	43.73
Std Dev	12.66

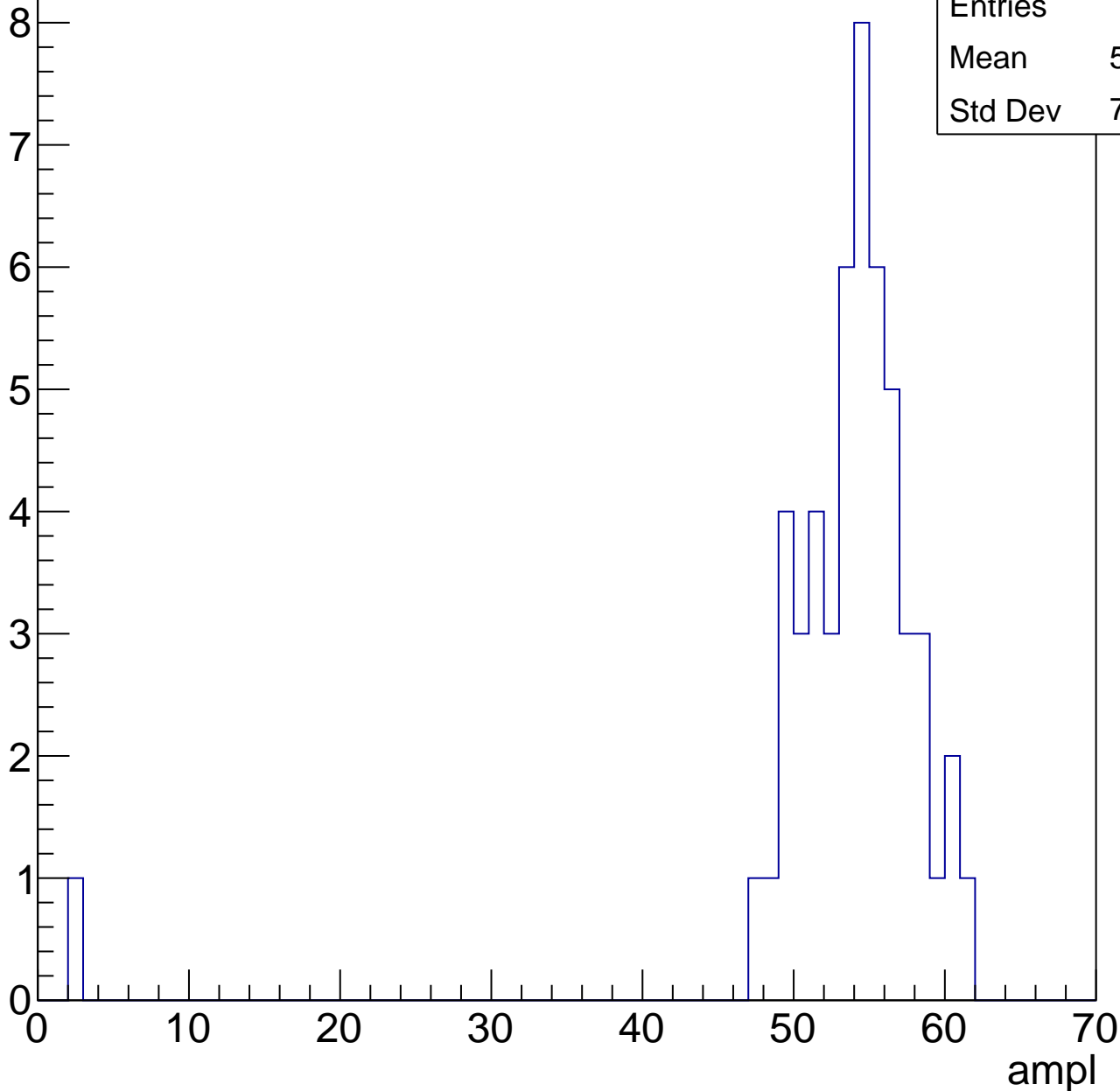


B1L103S, U6-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

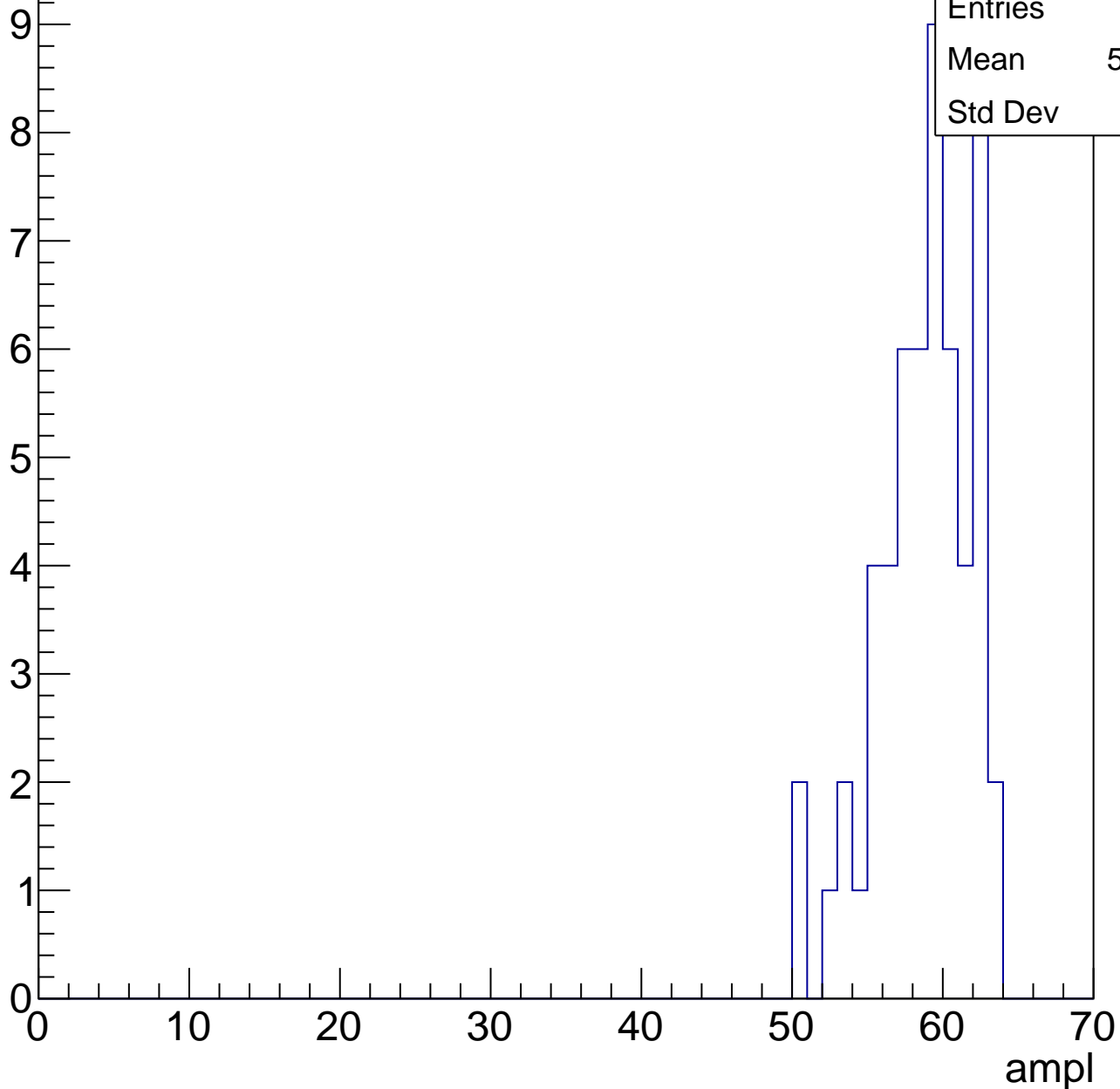
Entries	52
Mean	52.85
Std Dev	7.806



B1L103S, U6-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

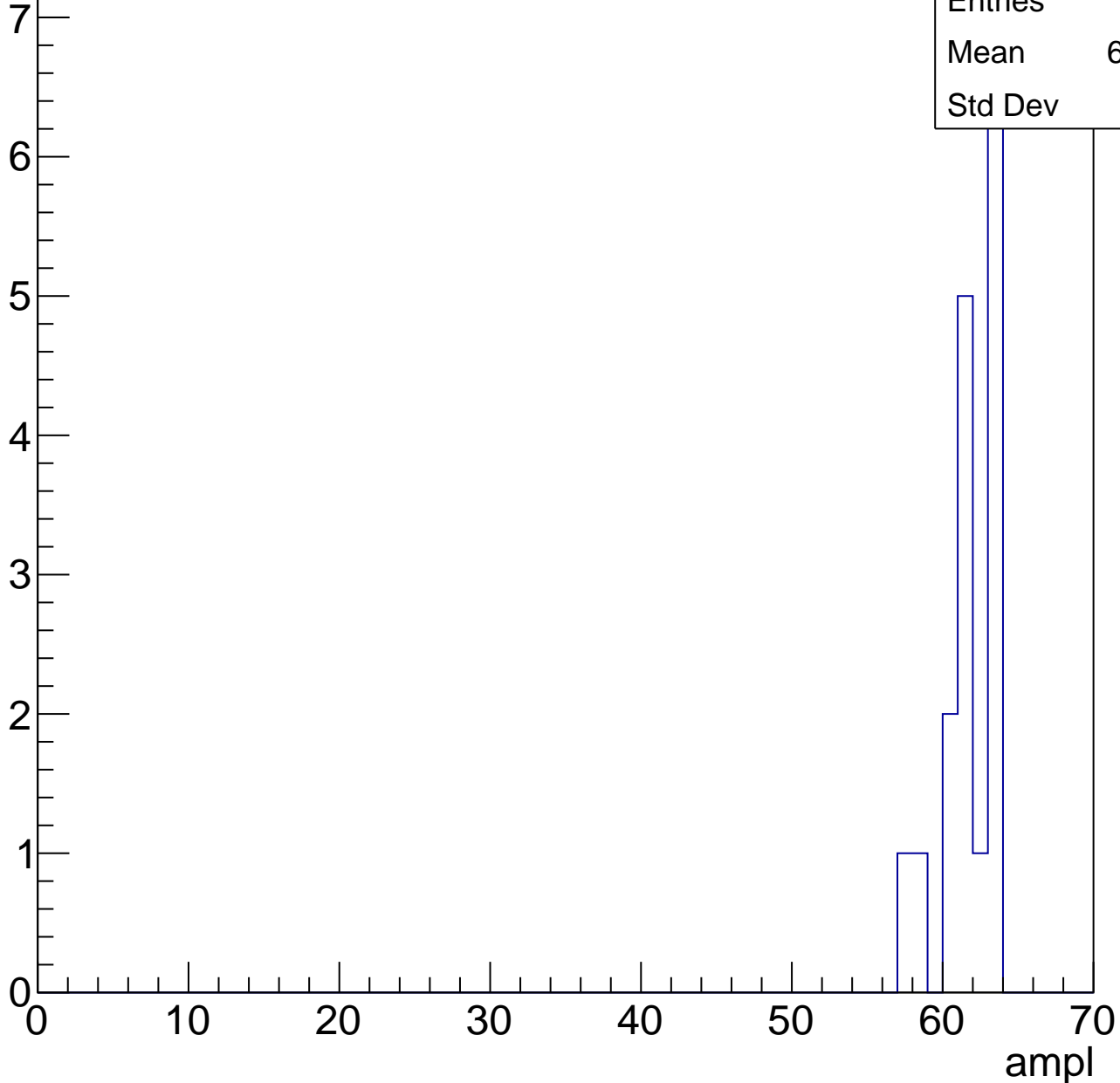


B1L103S, U6-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.78

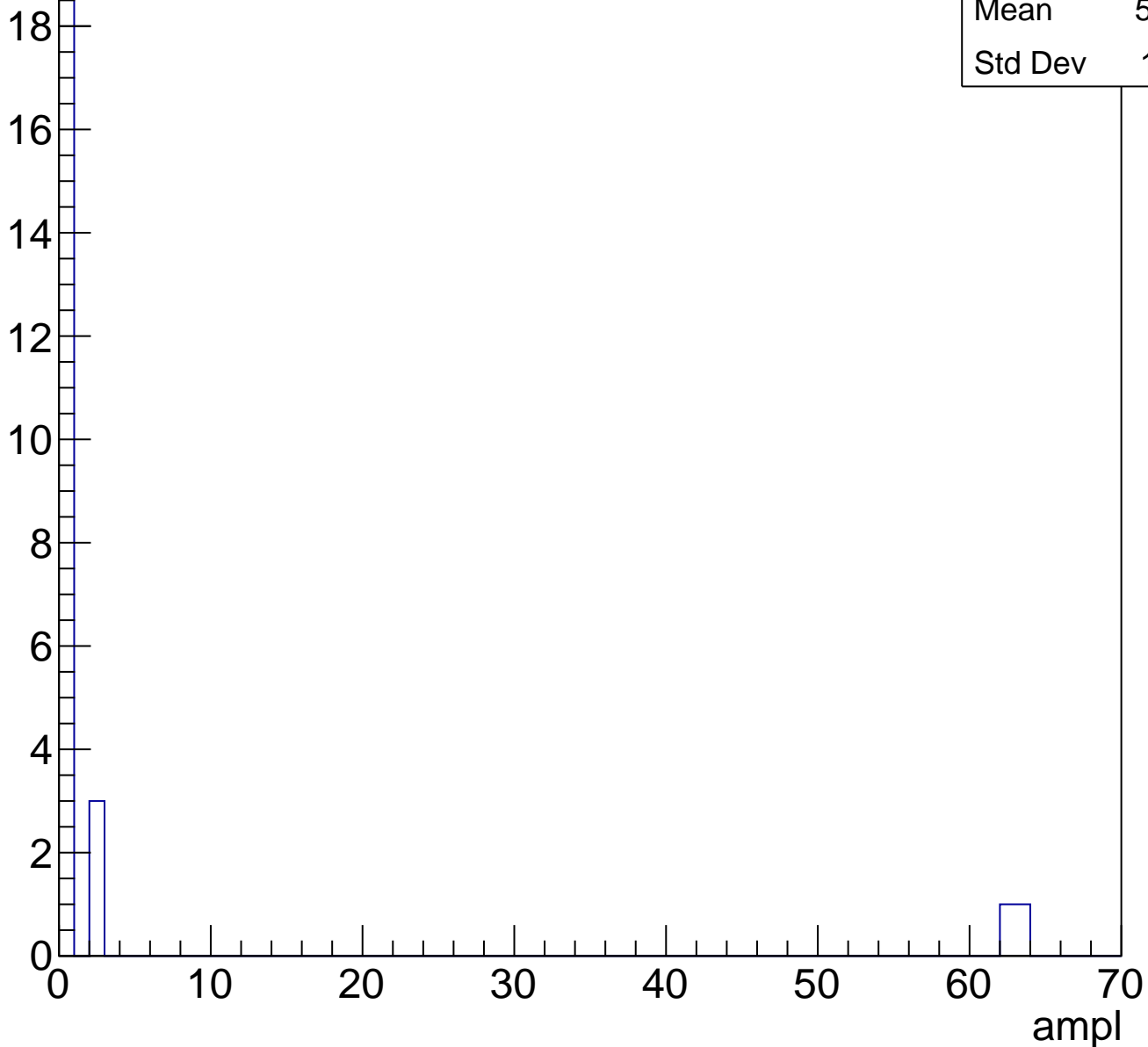


B1L103S, U6-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	5.458
Std Dev	17.21

Entry



B1L103S, U6-ch83, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	22.95
Std Dev	11.95

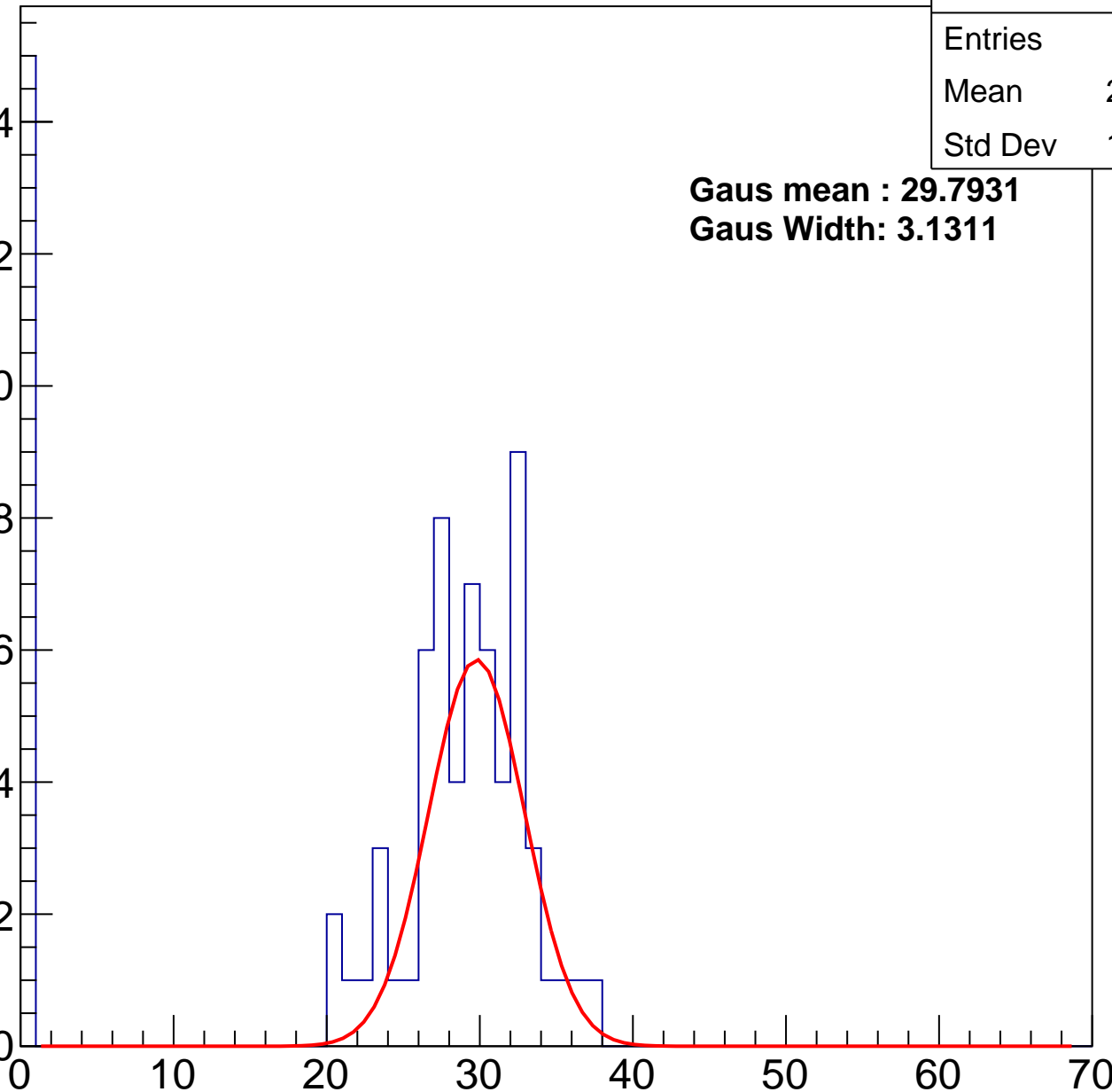
Gaus mean : 29.7931

Gaus Width: 3.1311

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch83, adc1

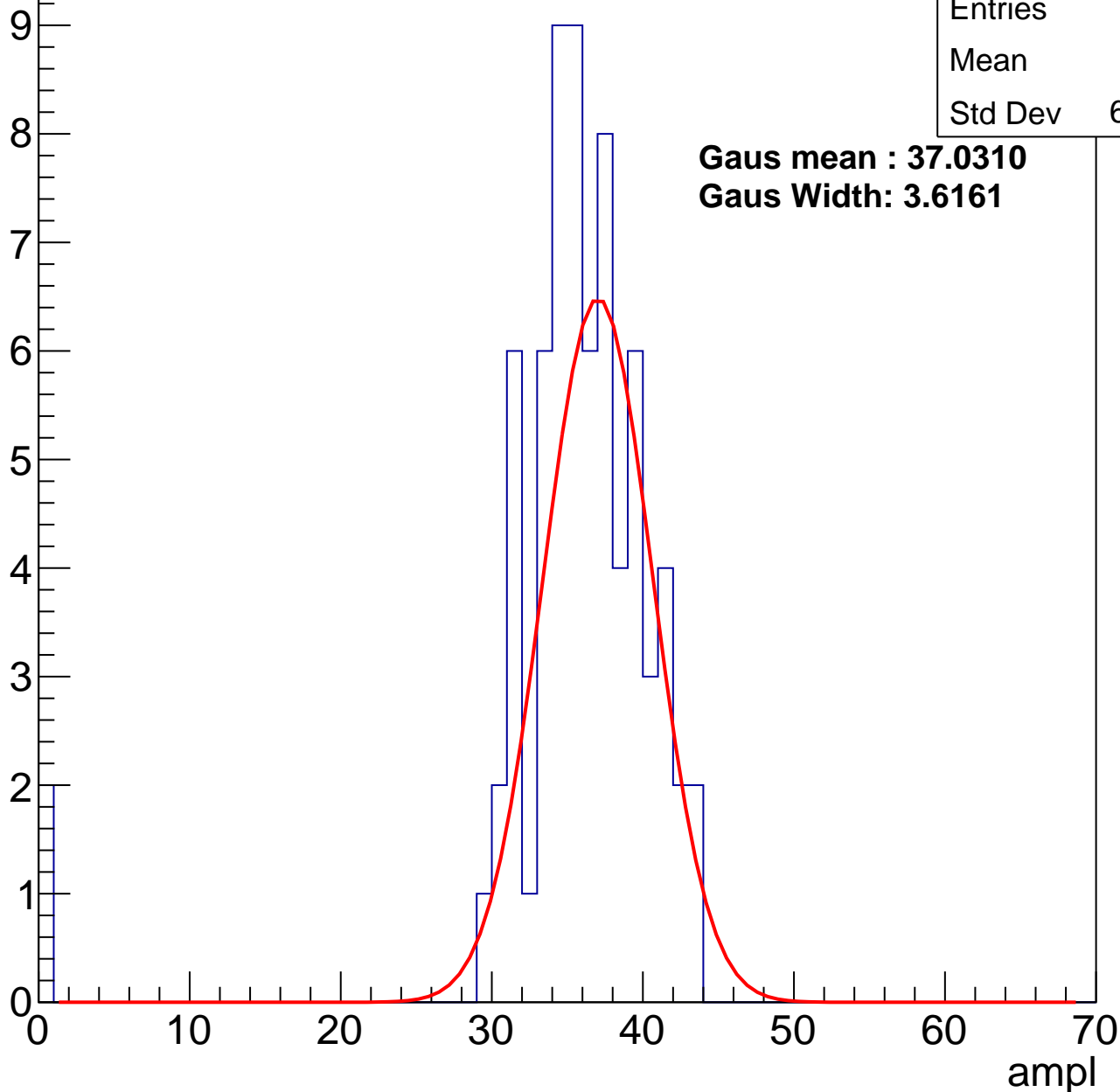
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.9
Std Dev	6.805

Gaus mean : 37.0310

Gaus Width: 3.6161



B1L103S, U6-ch83, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	35.88
Std Dev	15.82

Gaus mean : 43.2723

Gaus Width: 3.8352

Entry

12

10

8

6

4

2

0

0

10

20

30

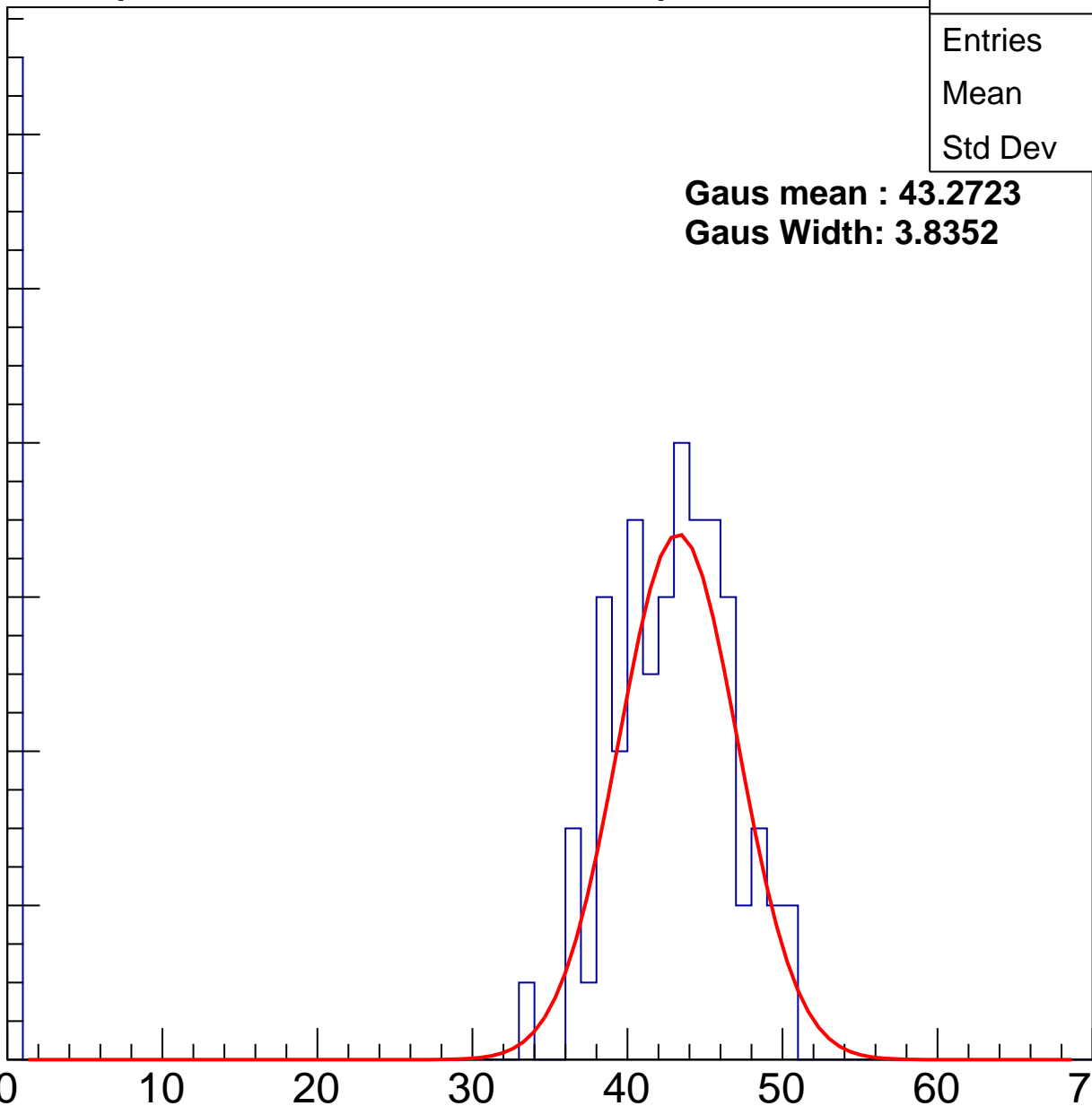
40

50

60

70

ampl

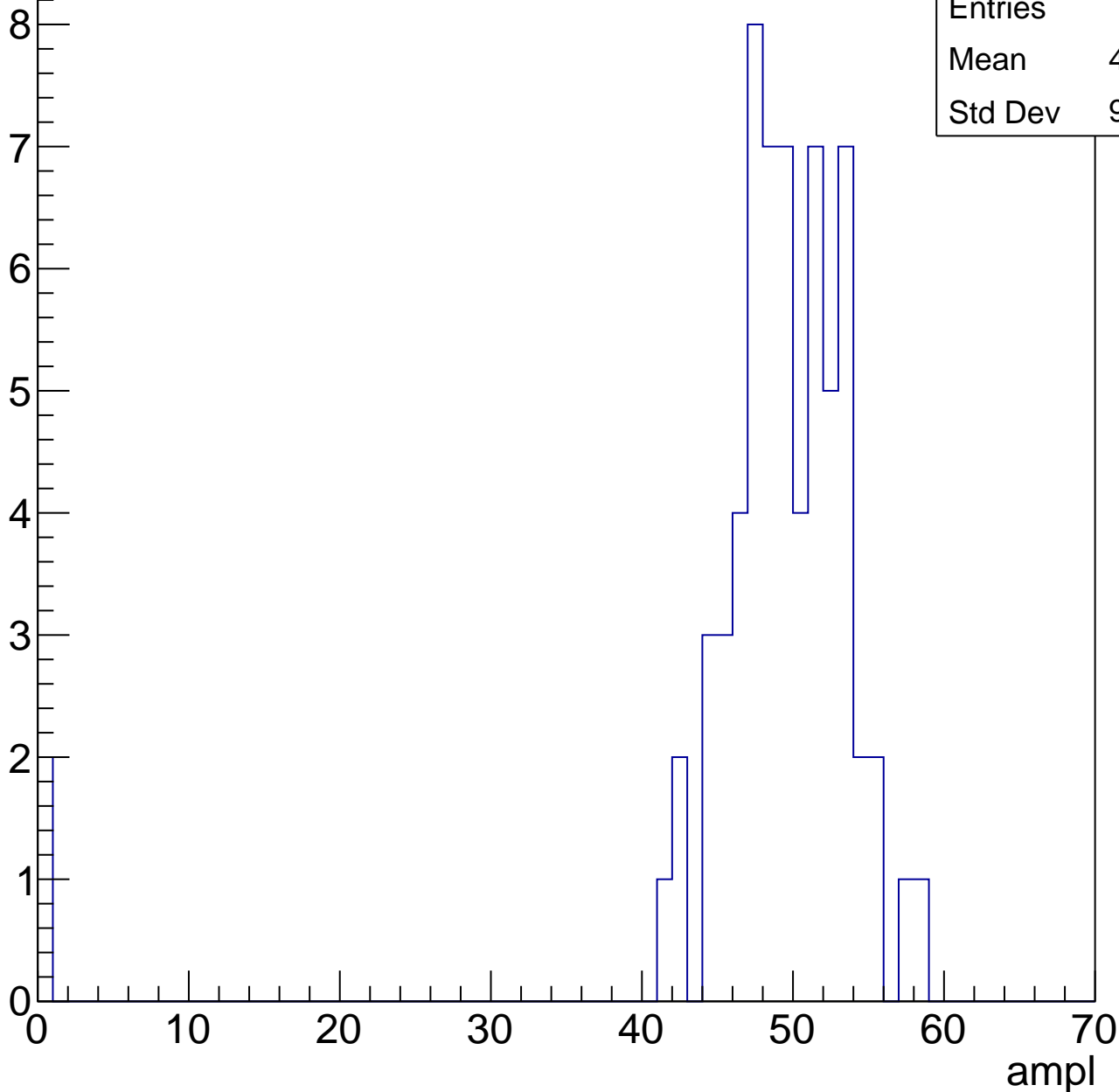


B1L103S, U6-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

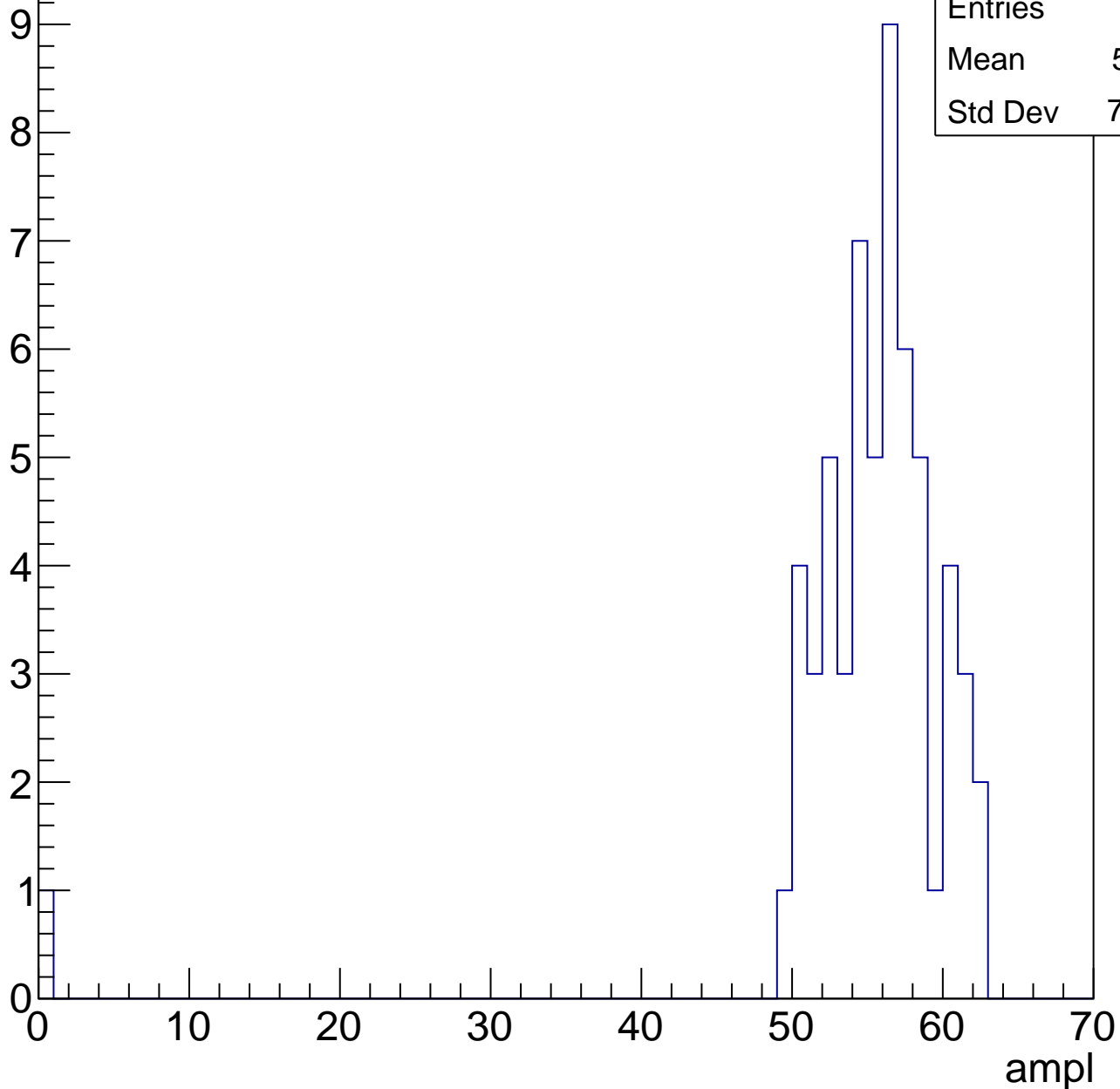
Entries	66
Mean	47.76
Std Dev	9.142



B1L103S, U6-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

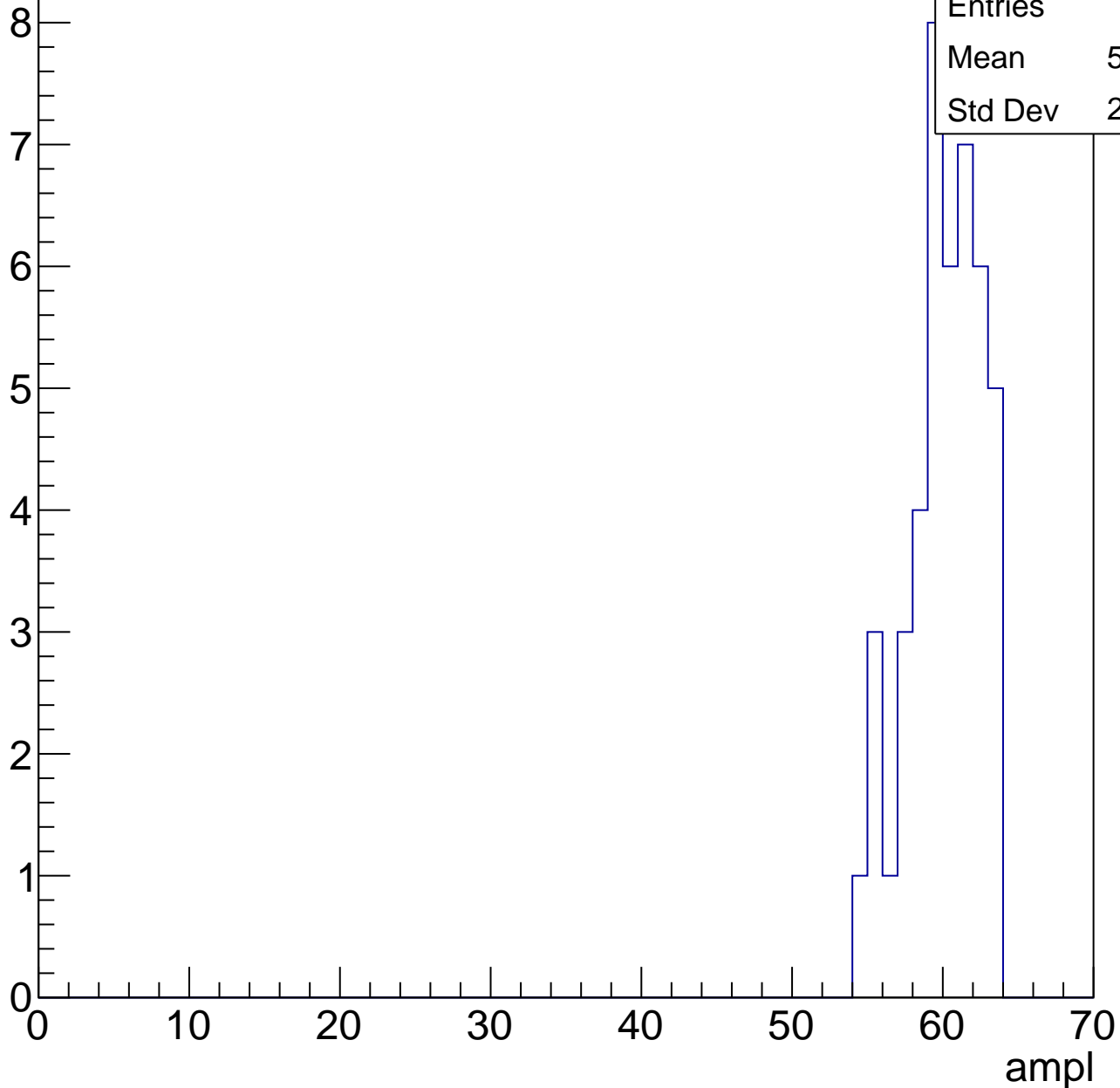


B1L103S, U6-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

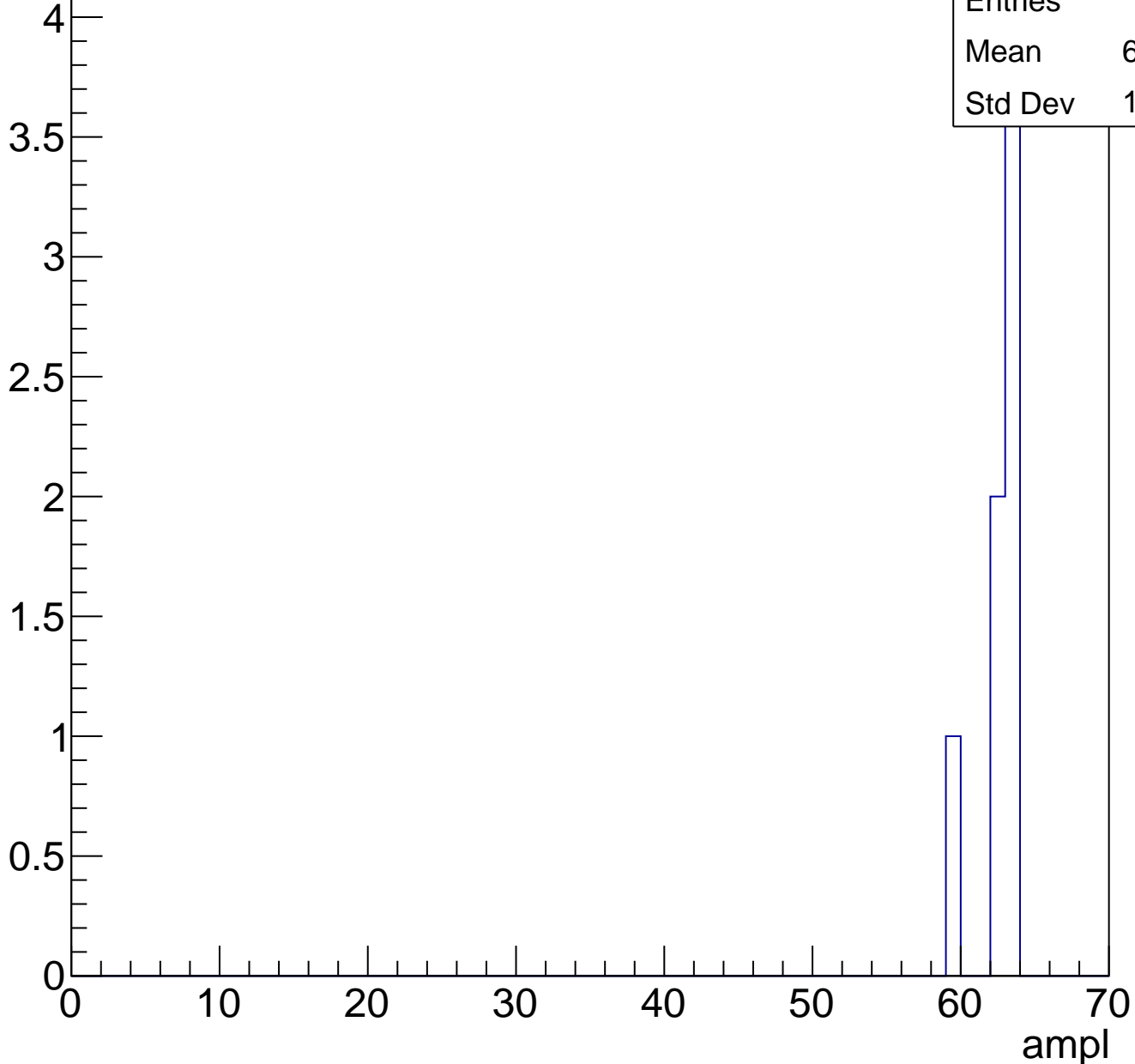
Entries	44
Mean	59.64
Std Dev	2.375



B1L103S, U6-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U6-ch84, adc0

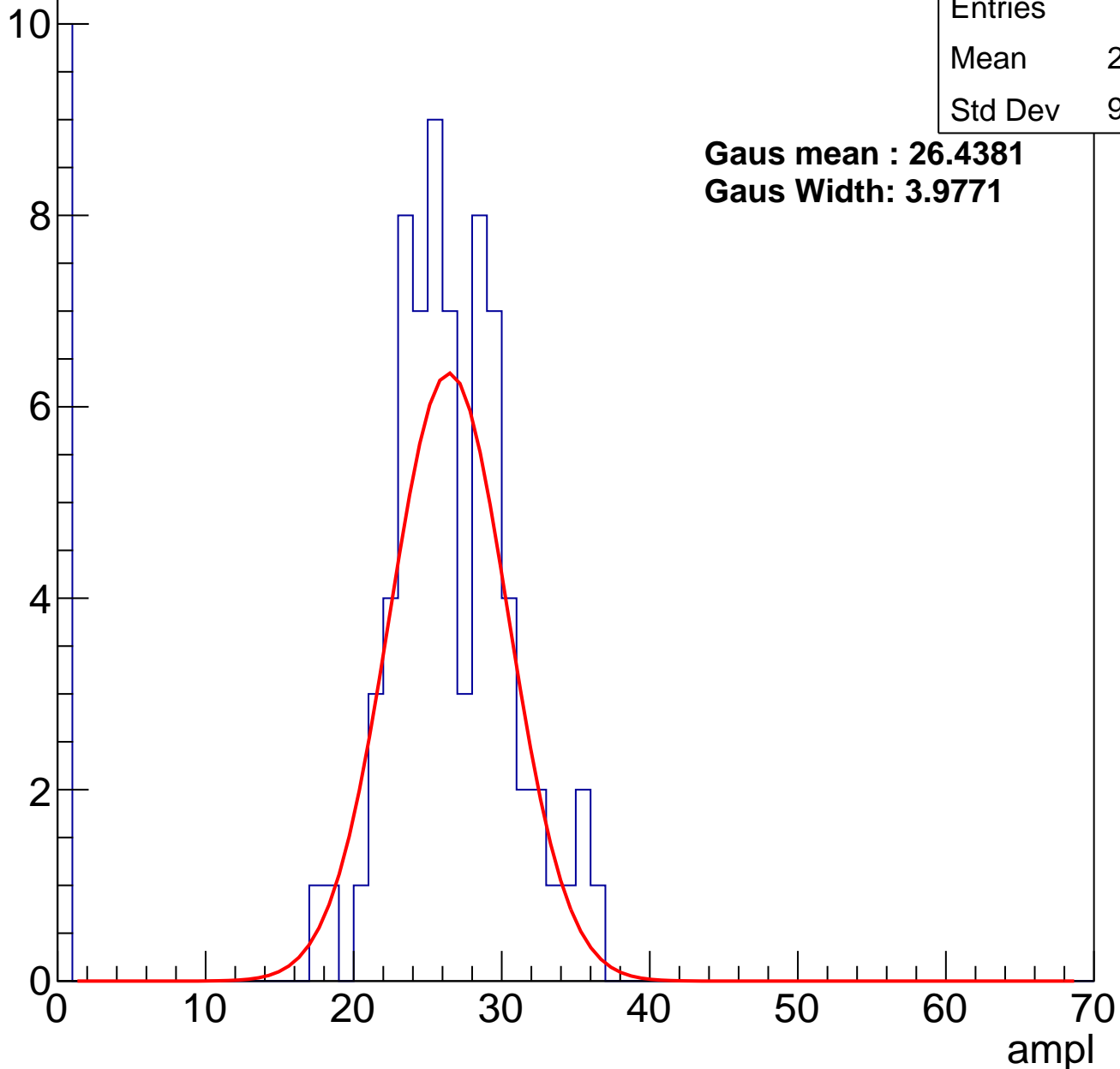
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.07
Std Dev	9.335

Gaus mean : 26.4381

Gaus Width: 3.9771

Entry



B1L103S, U6-ch84, adc1

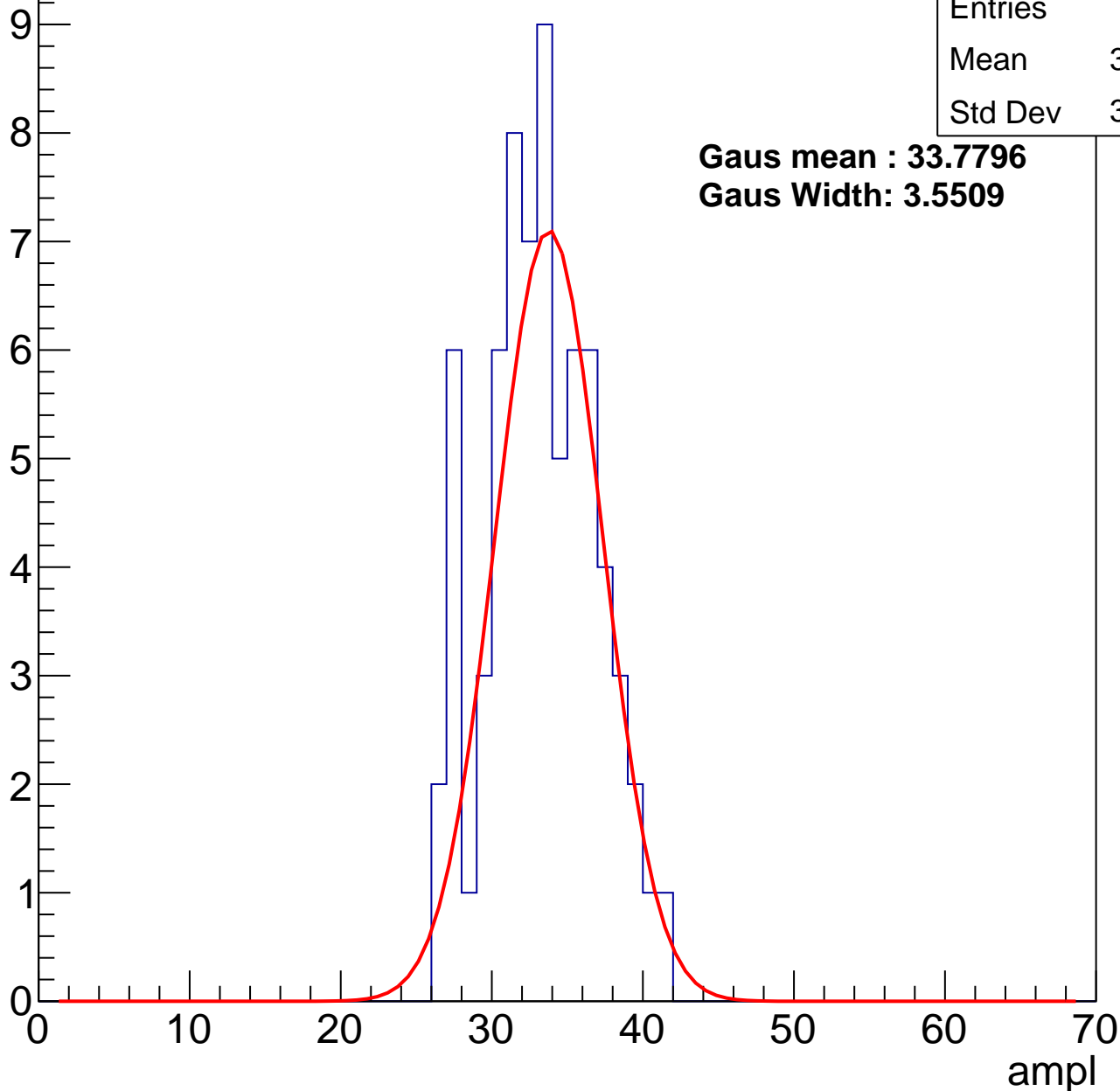
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.79
Std Dev	3.553

Gaus mean : 33.7796

Gaus Width: 3.5509



B1L103S, U6-ch84, adc2

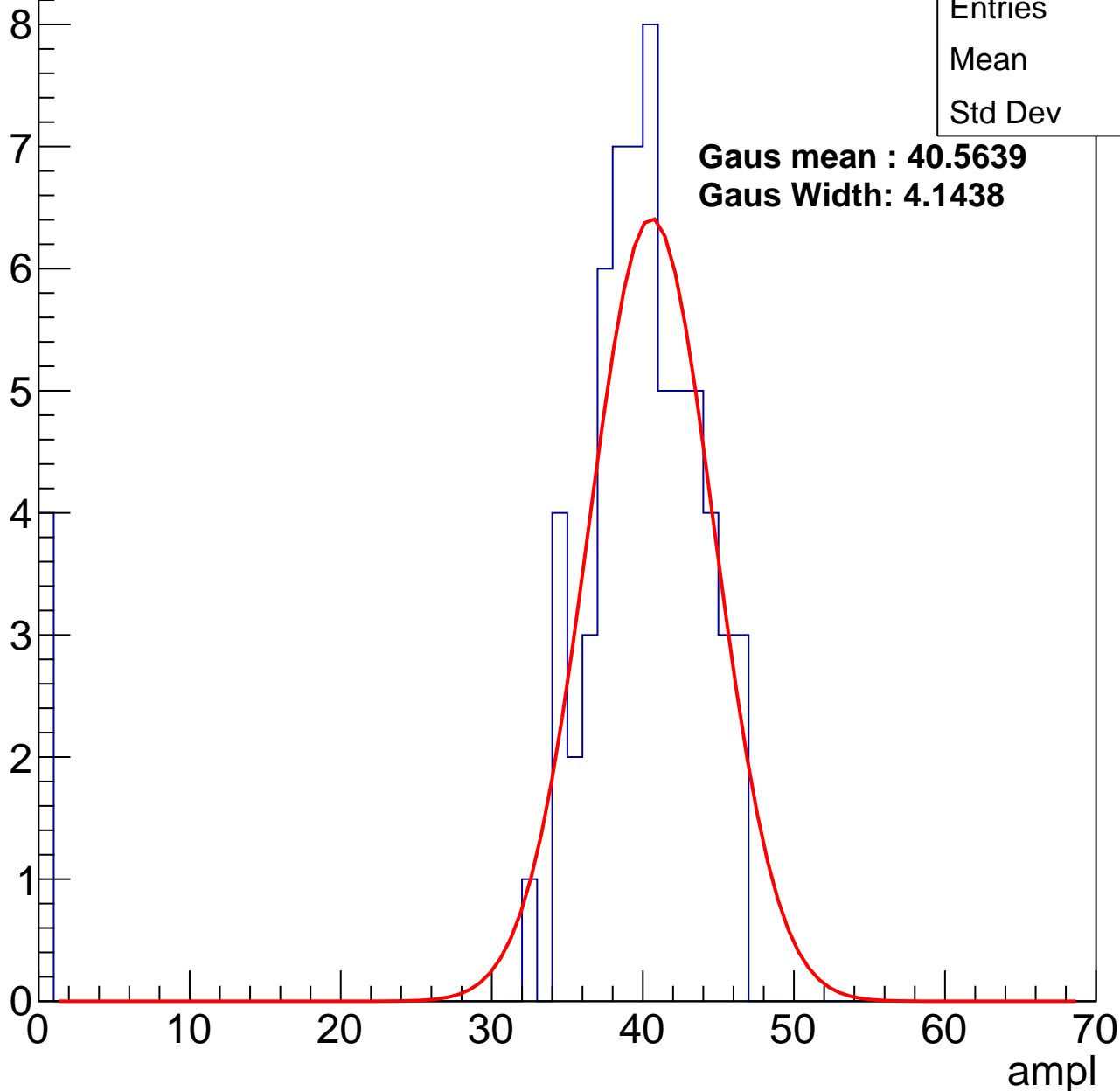
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.4
Std Dev	9.97

Gaus mean : 40.5639

Gaus Width: 4.1438



B1L103S, U6-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

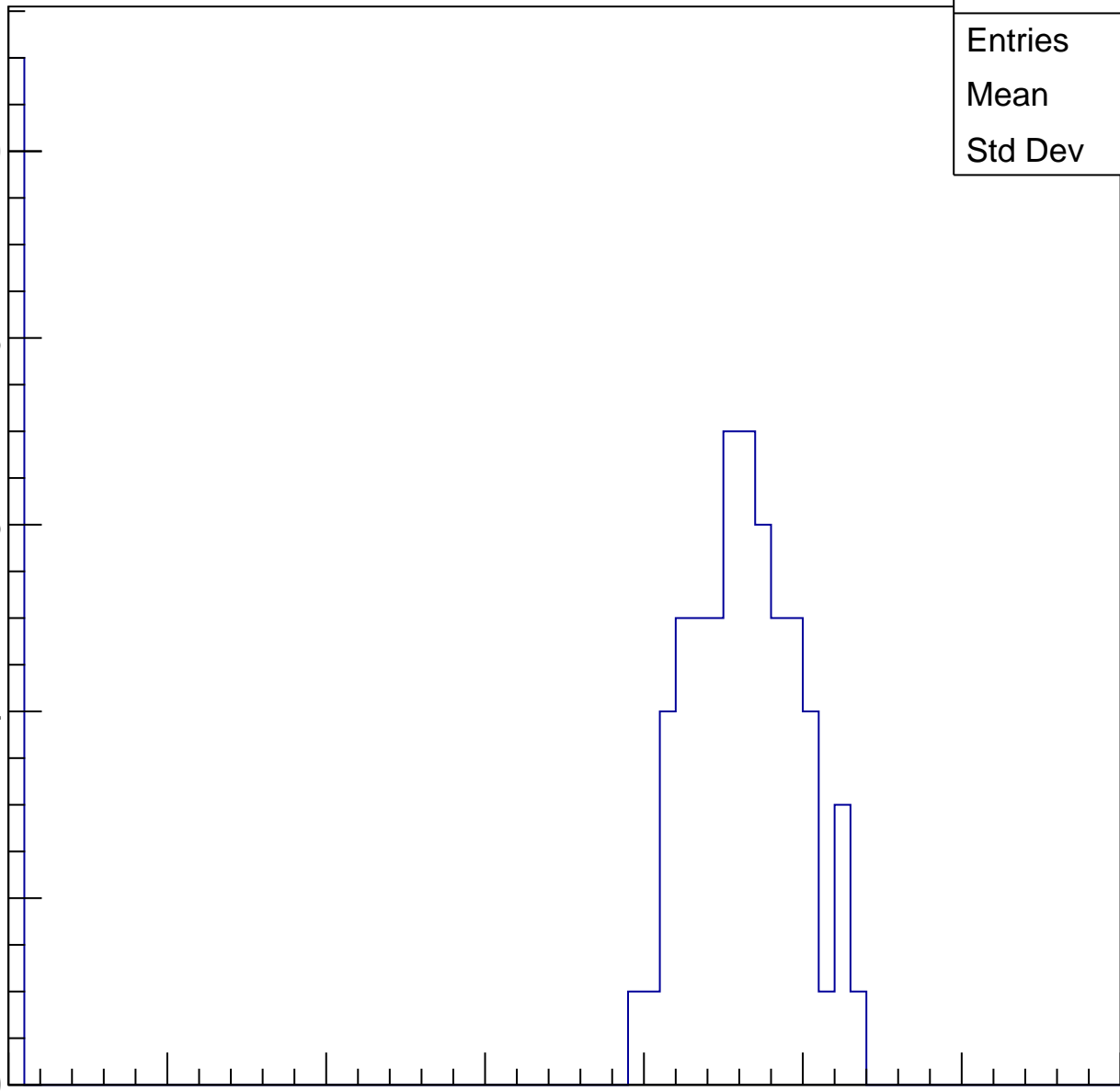
50

60

70

ampl

Entries	71
Mean	38.76
Std Dev	16.87

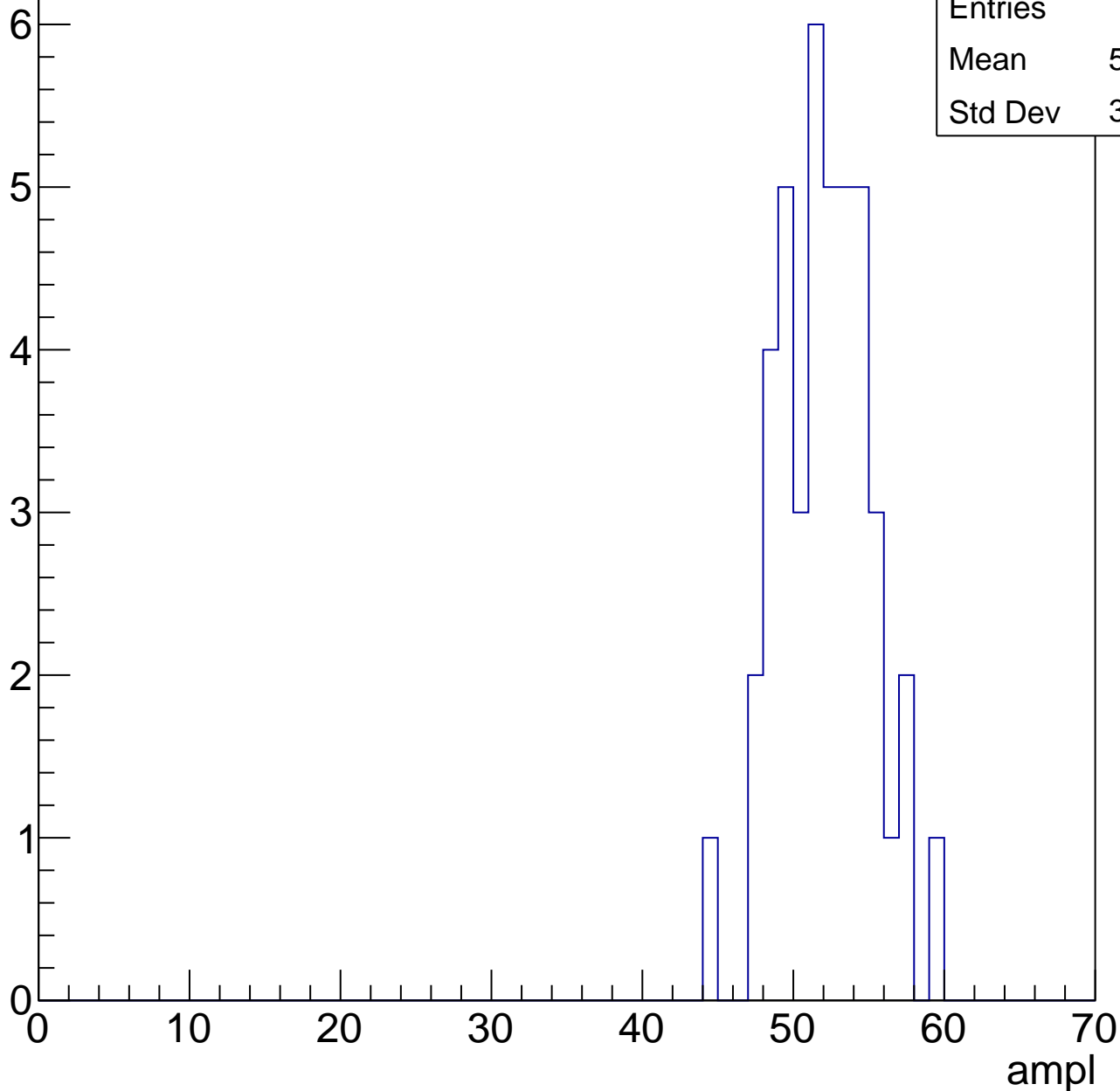


B1L103S, U6-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	51.63
Std Dev	3.058

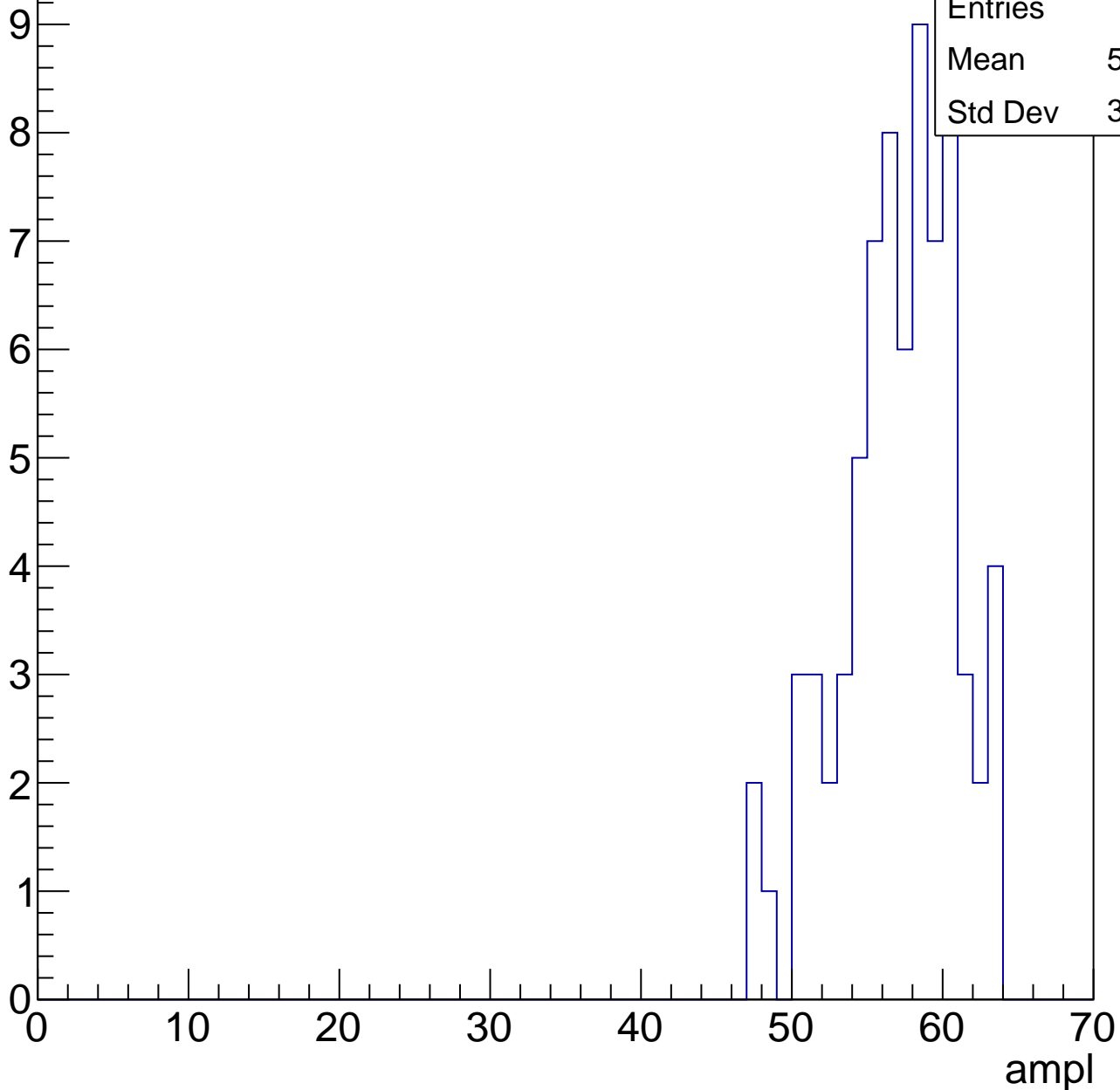


B1L103S, U6-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	56.53
Std Dev	3.793

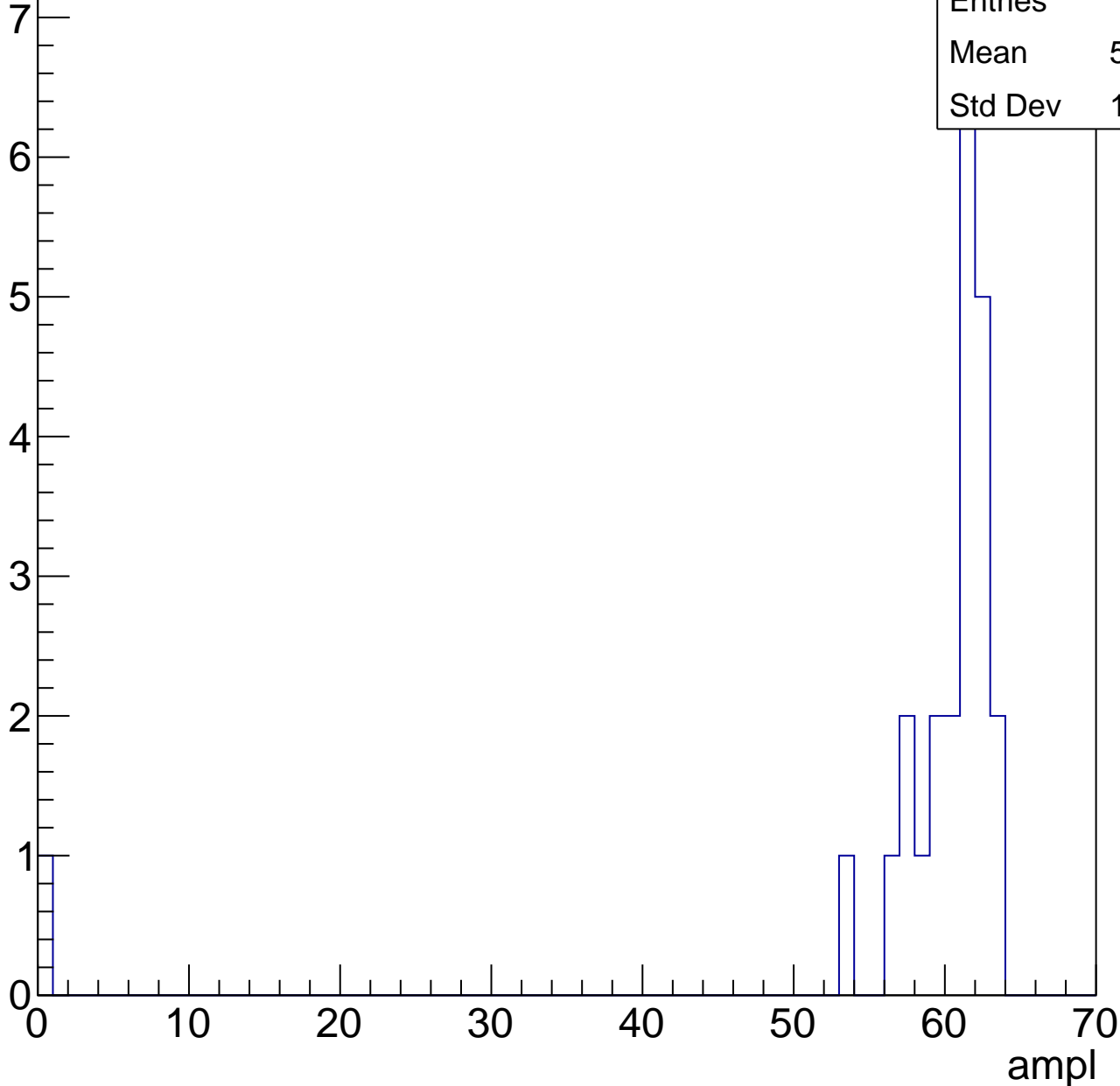


B1L103S, U6-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	57.58
Std Dev	12.24

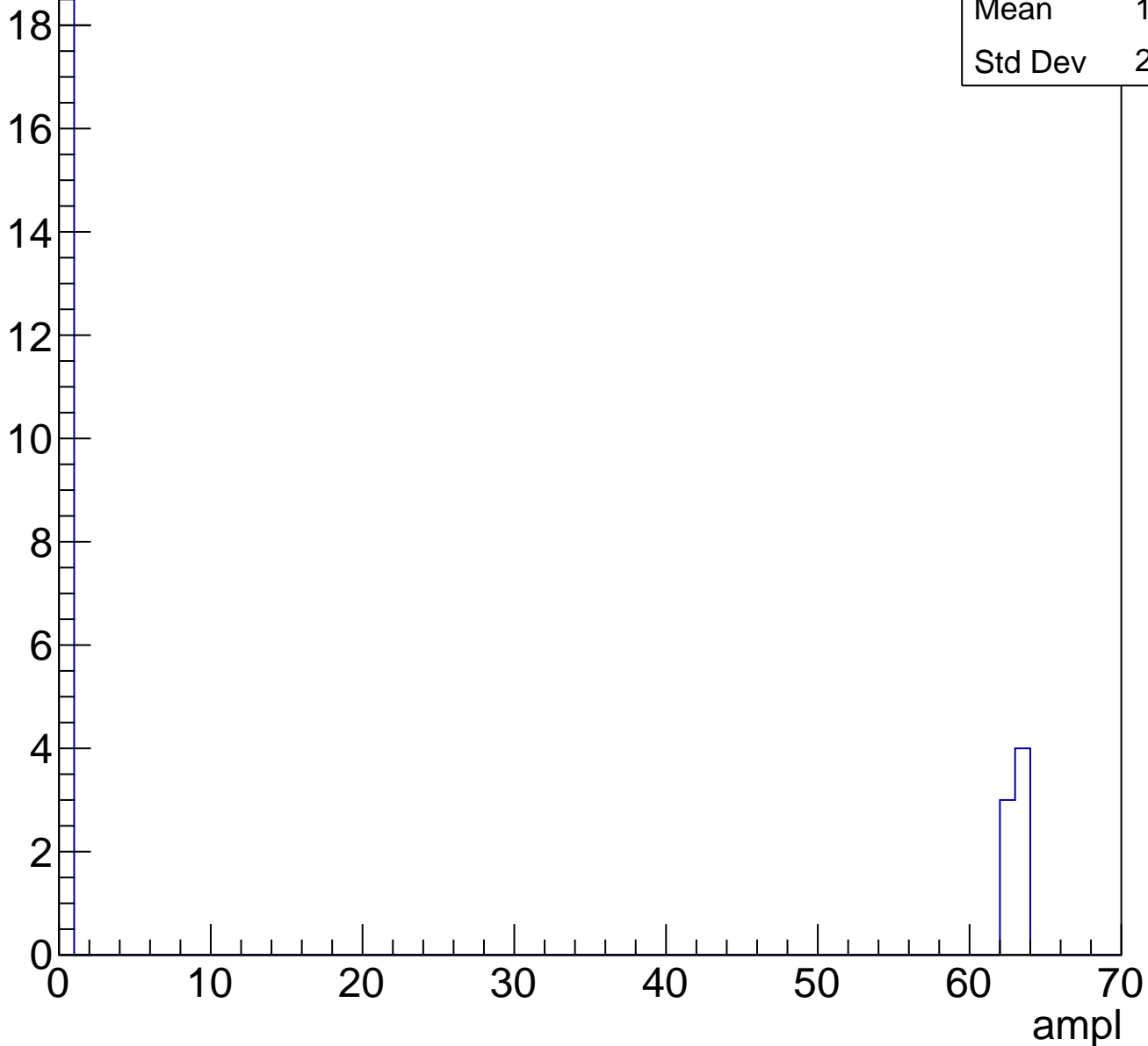


B1L103S, U6-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.85
Std Dev	27.76

Entry



B1L103S, U6-ch85, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.49
Std Dev	11.57

Gaus mean : 28.9411

Gaus Width: 3.6744

Entry

14

12

10

8

6

4

2

0

0

10

20

30

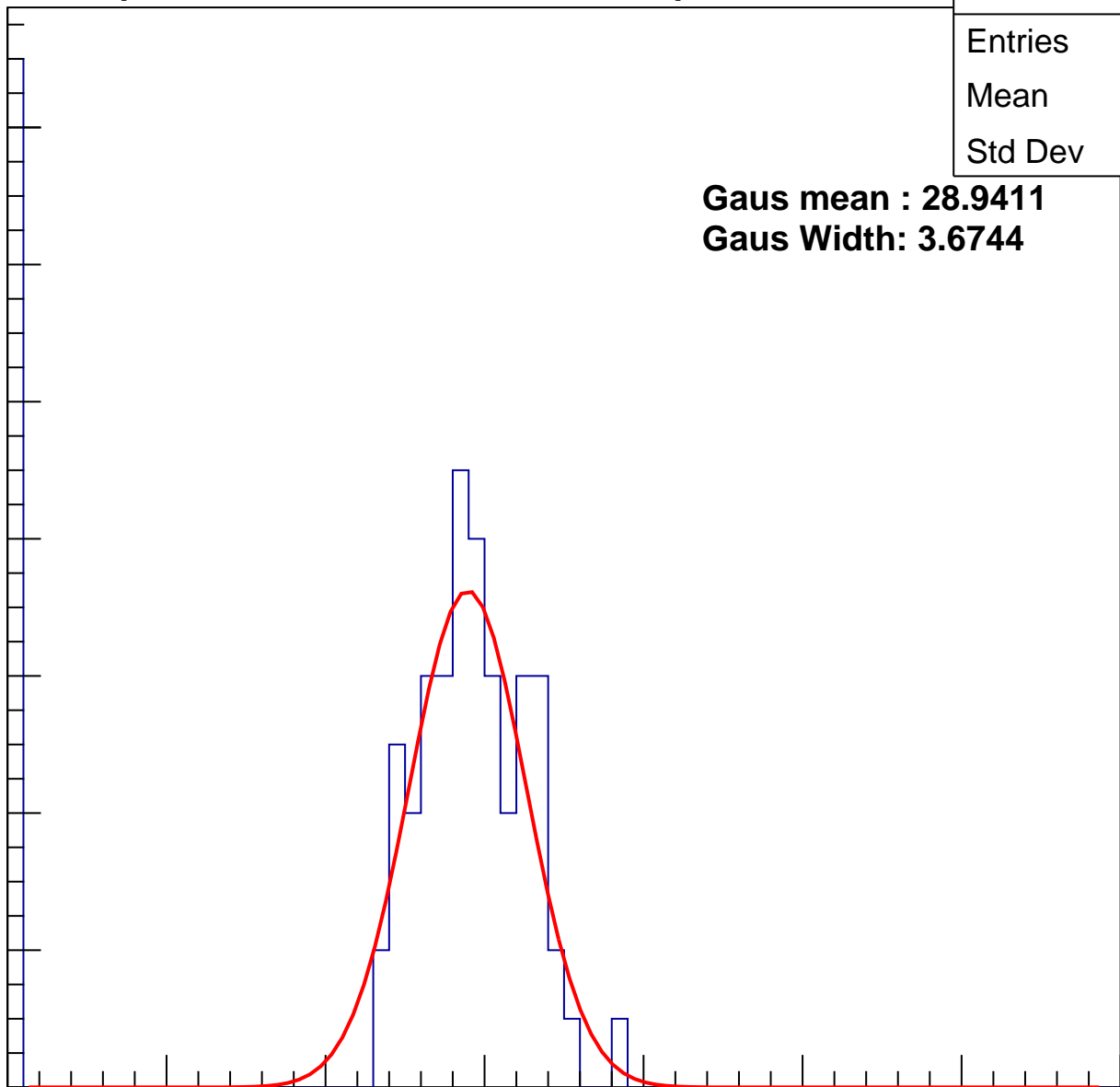
40

50

60

70

ampl



B1L103S, U6-ch85, adc1

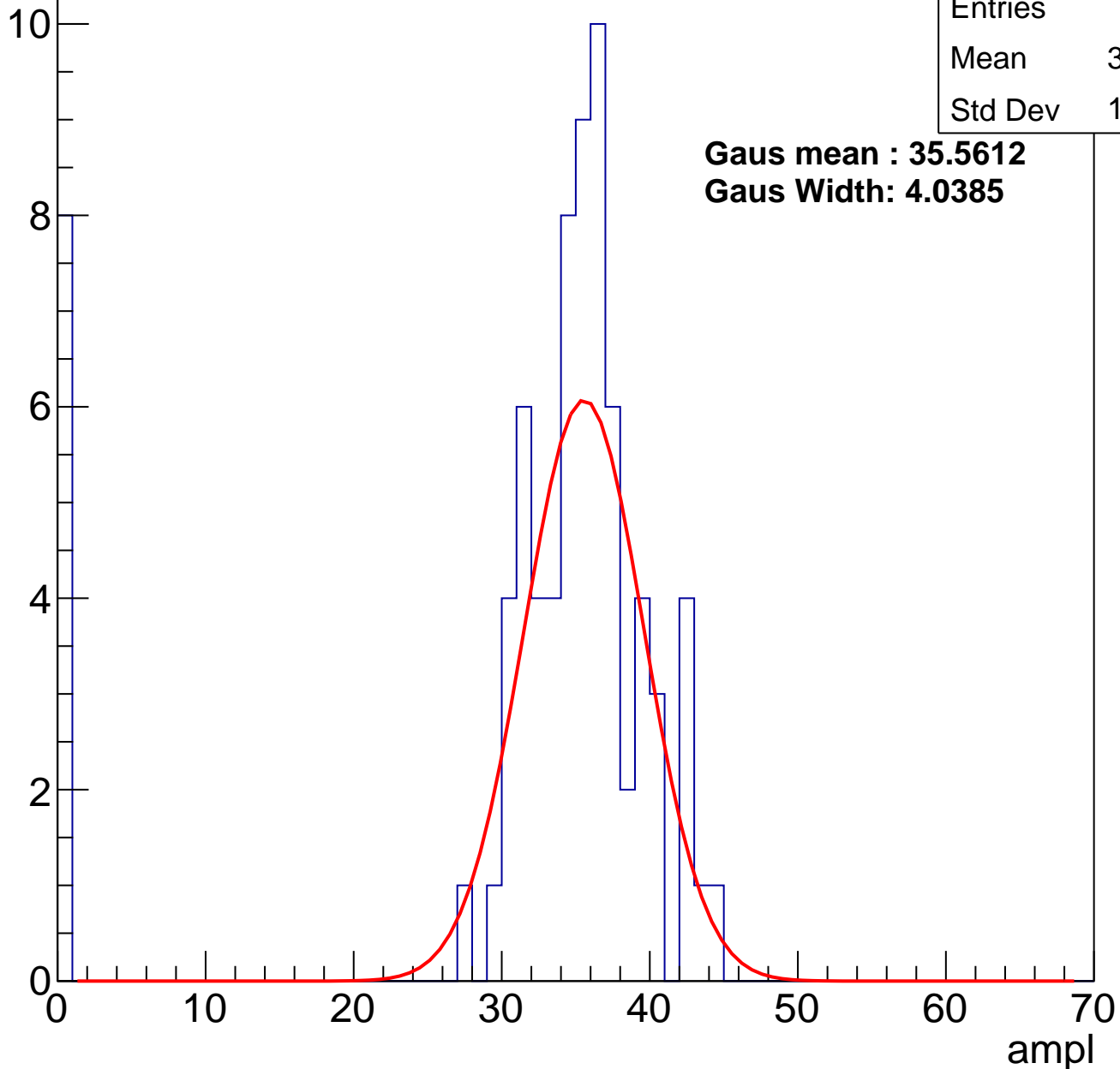
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	31.55
Std Dev	11.35

Gaus mean : 35.5612

Gaus Width: 4.0385

Entry



B1L103S, U6-ch85, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	37.96
Std Dev	13.12

Gaus mean : 41.6404

Gaus Width: 4.6360

Entry

10

8

6

4

2

0

0

10

20

30

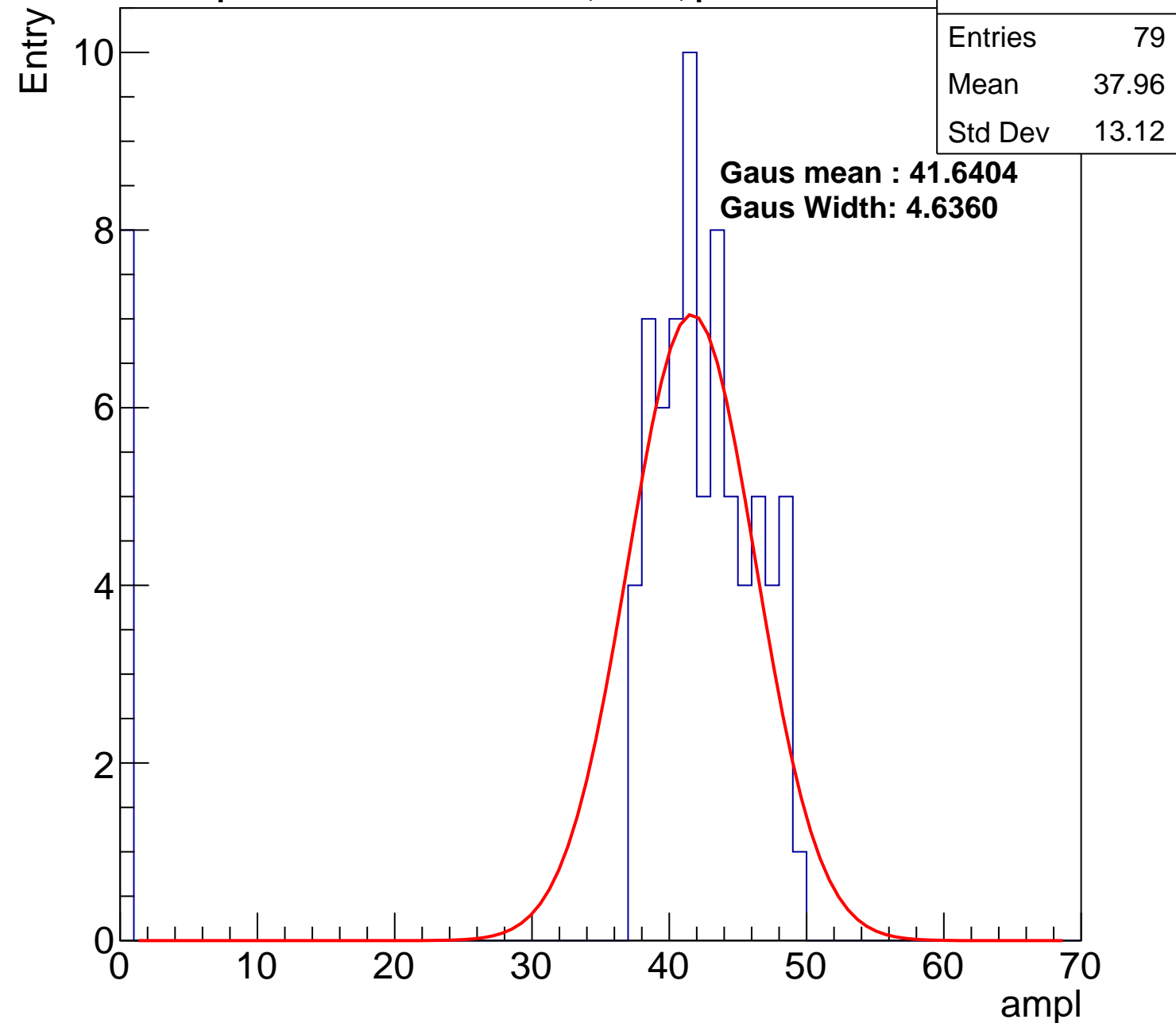
40

50

60

70

ampl

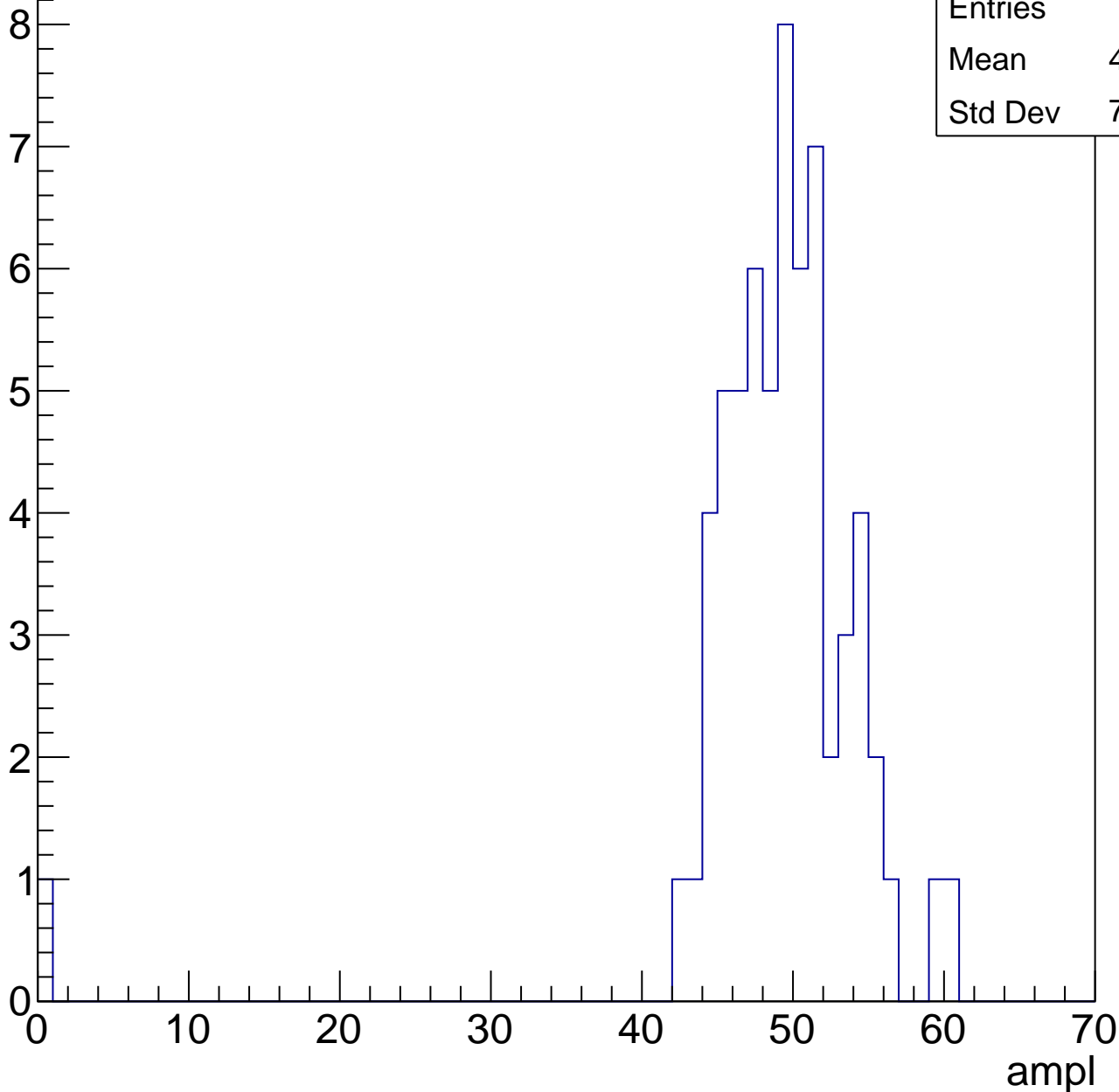


B1L103S, U6-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

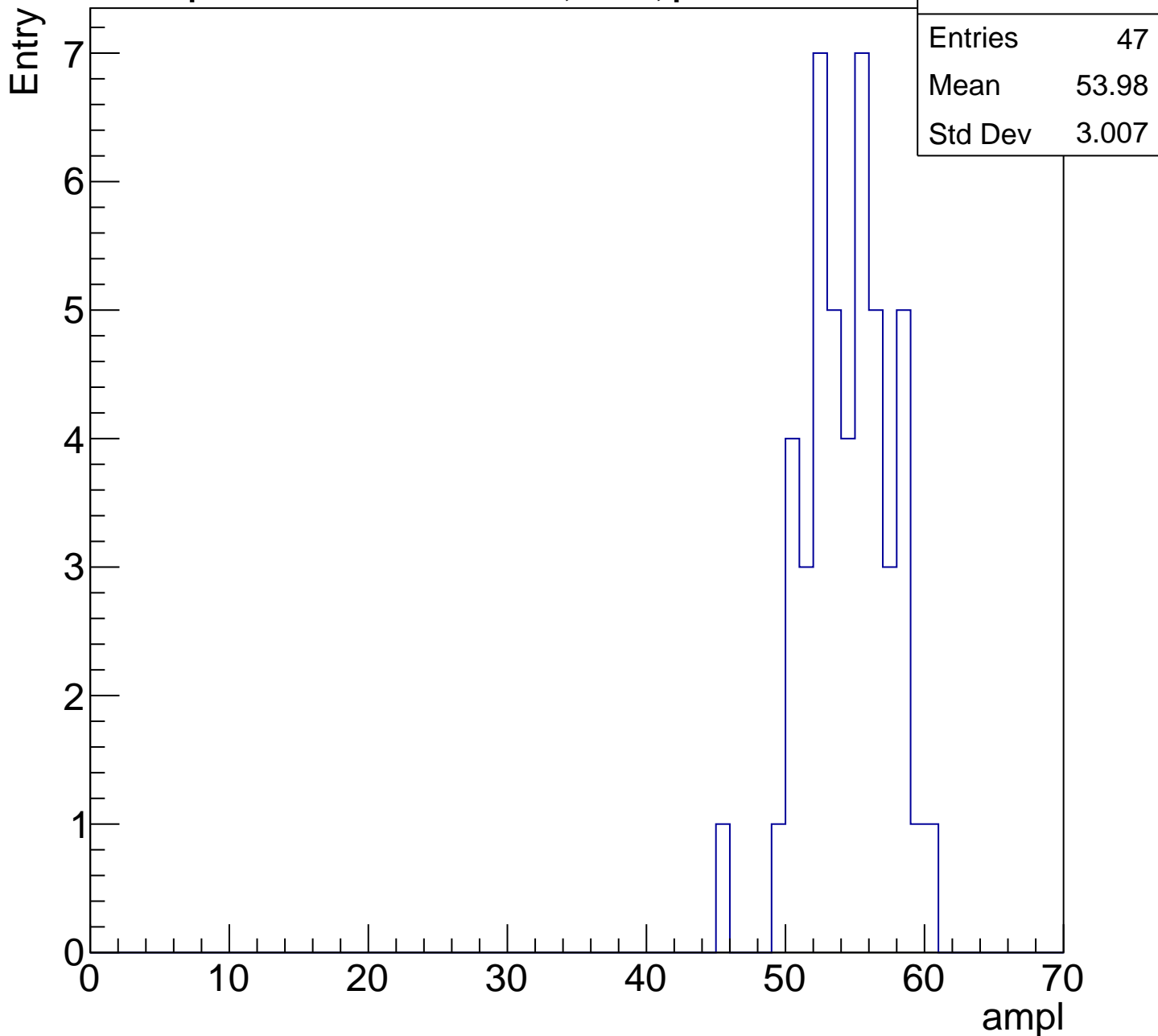
Entry

Entries	63
Mean	48.43
Std Dev	7.195



B1L103S, U6-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

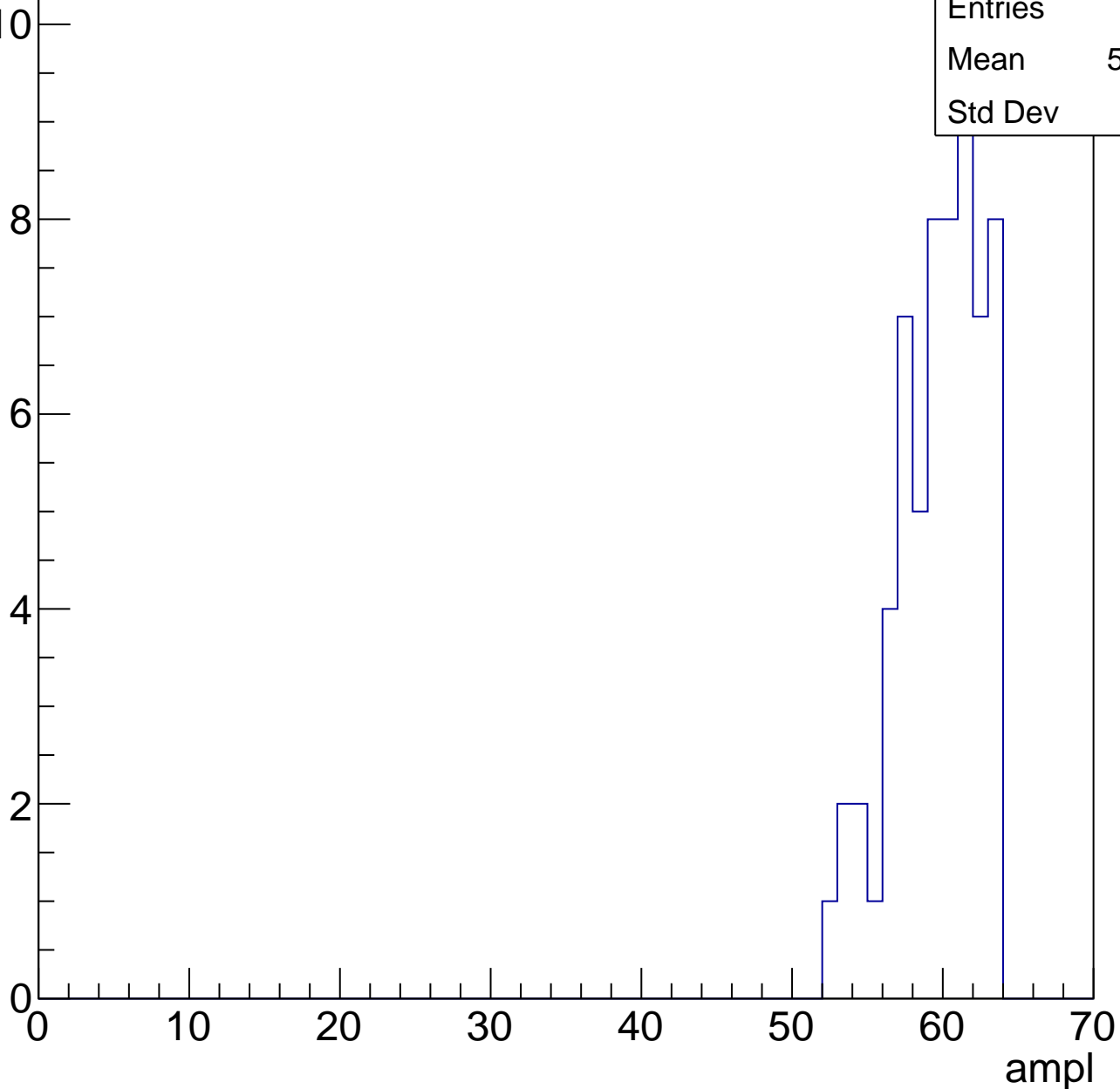


B1L103S, U6-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

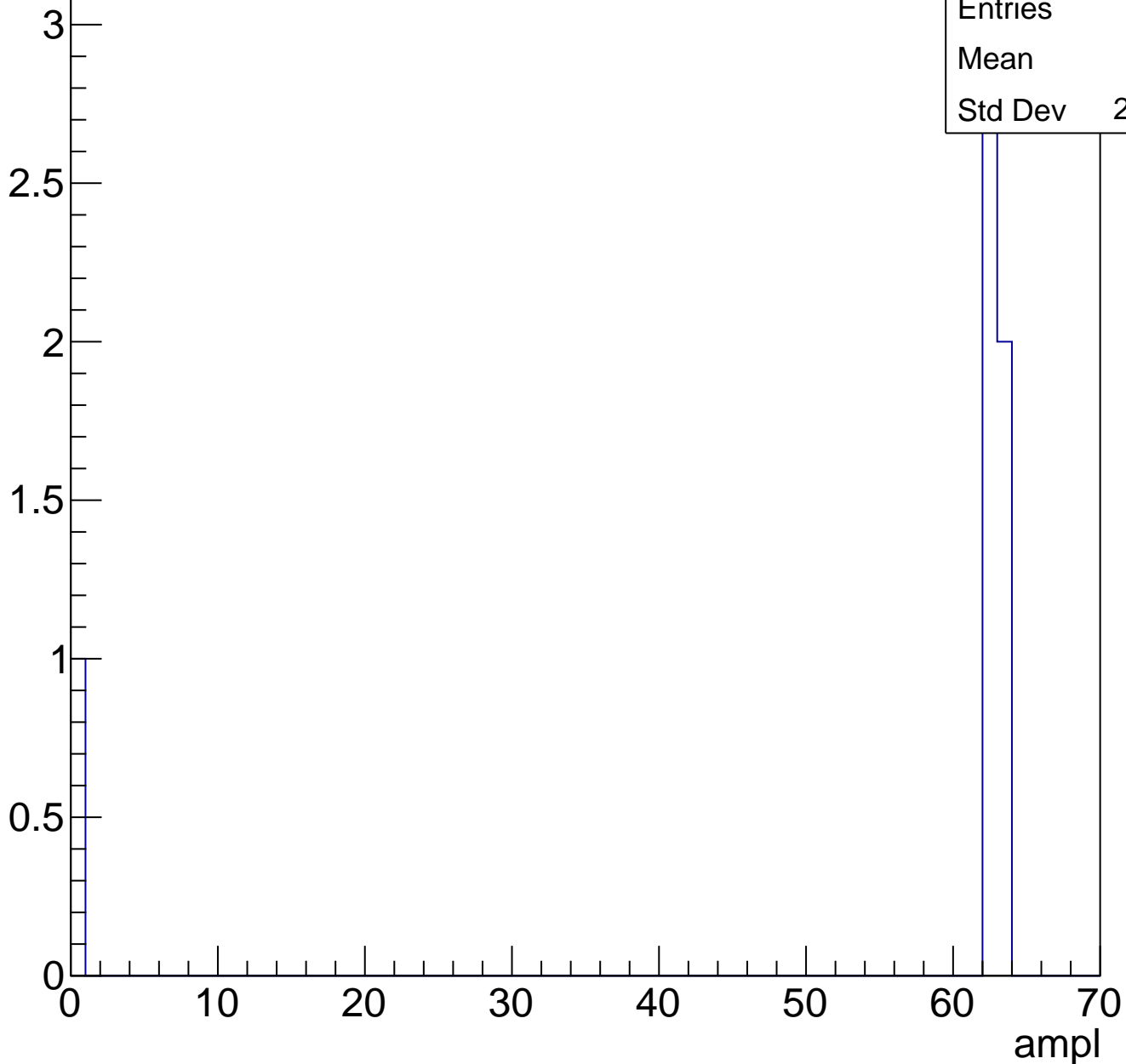
Entries	63
Mean	59.27
Std Dev	2.79



B1L103S, U6-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch86, adc0

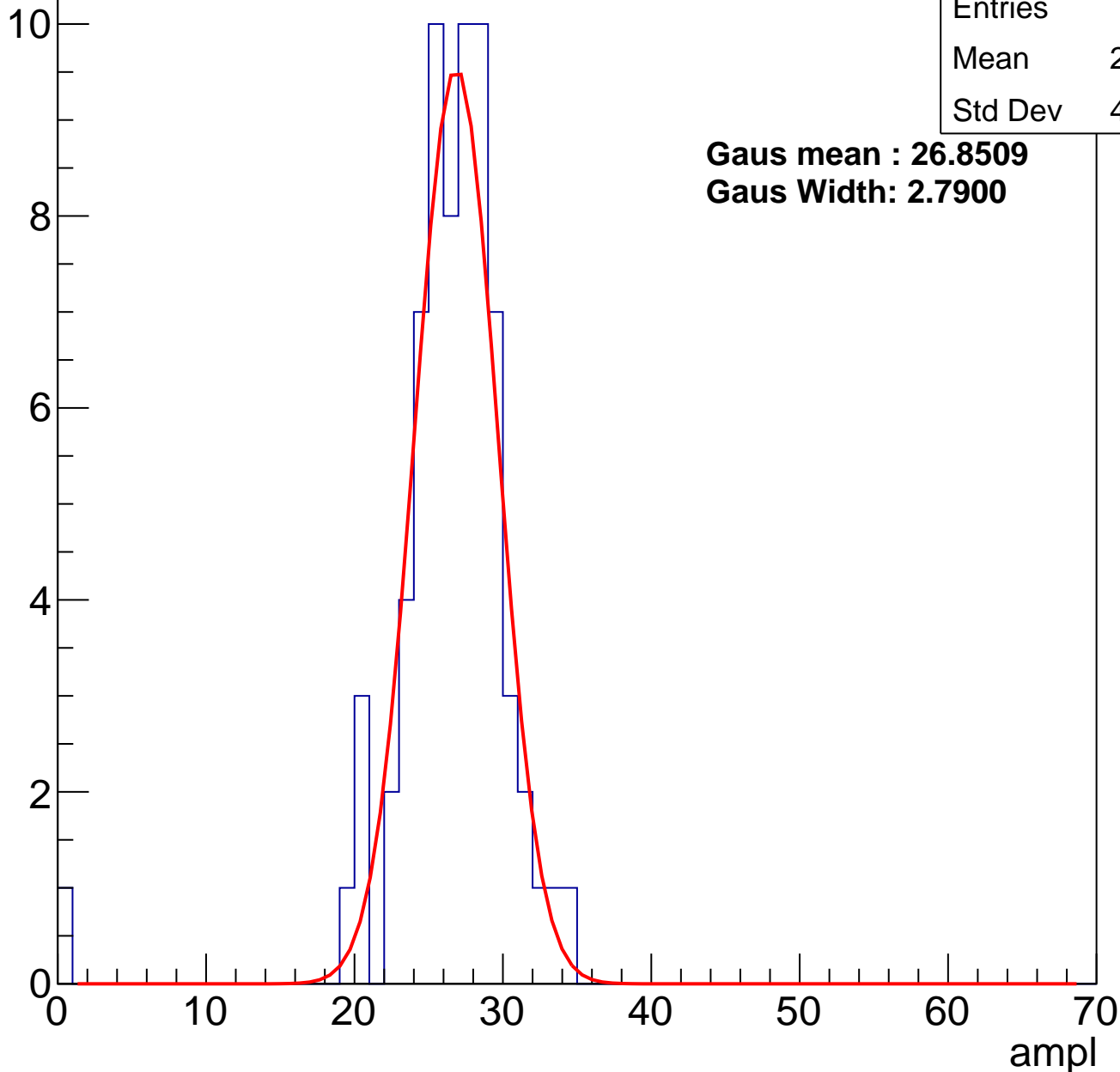
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	25.99
Std Dev	4.277

Gaus mean : 26.8509

Gaus Width: 2.7900

Entry



B1L103S, U6-ch86, adc1

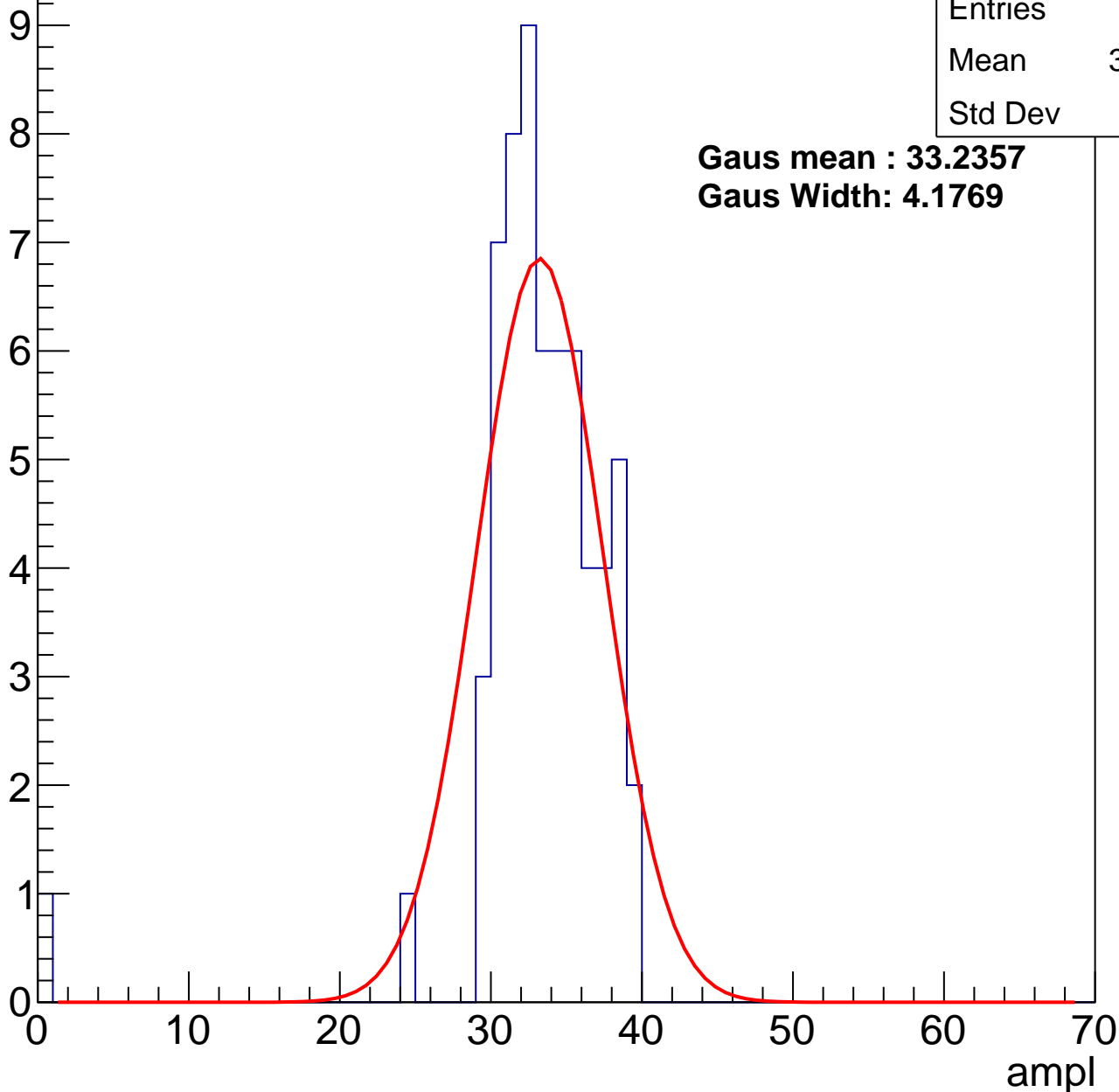
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.73
Std Dev	5.15

Gaus mean : 33.2357

Gaus Width: 4.1769



B1L103S, U6-ch86, adc2

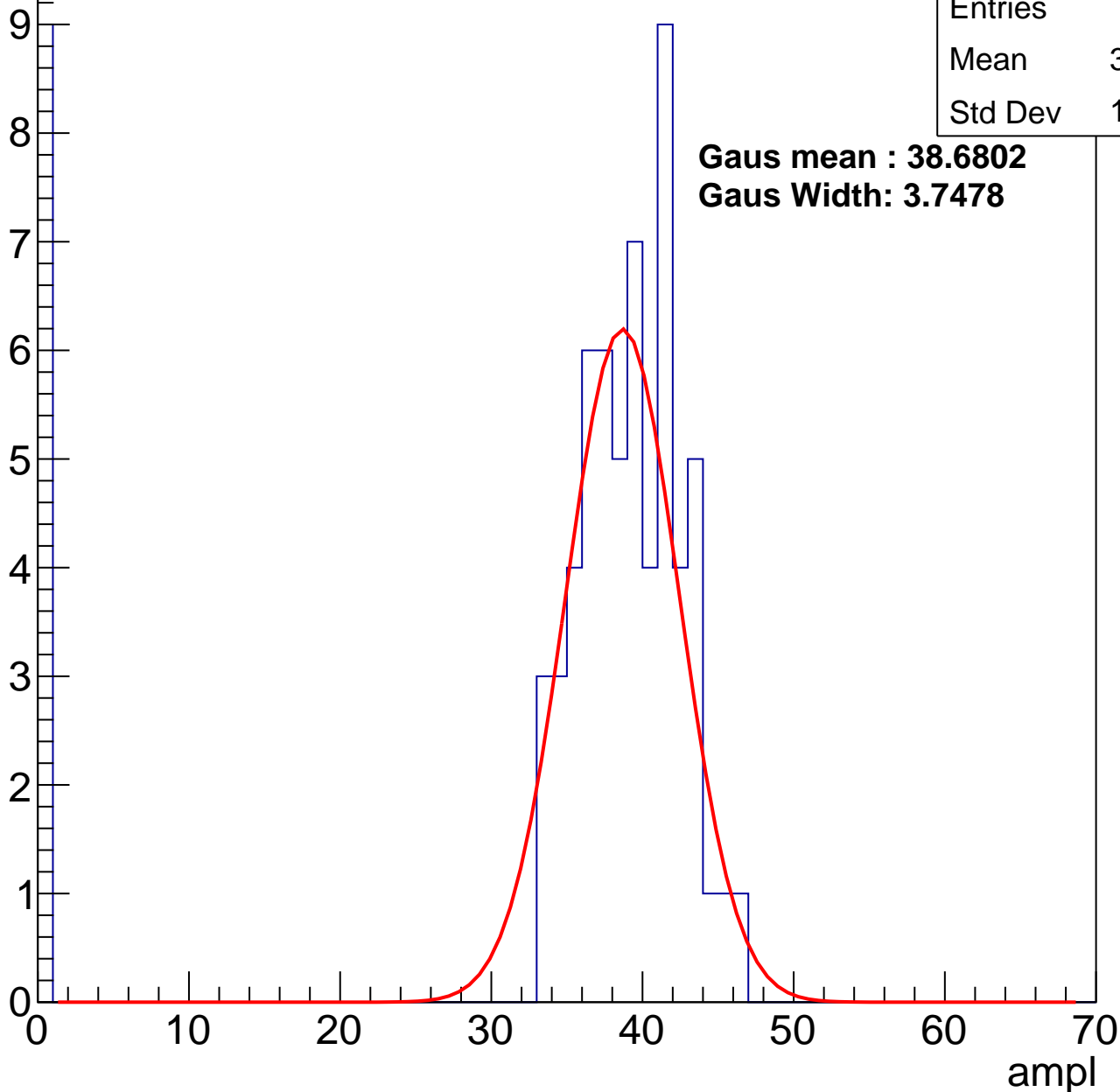
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.66
Std Dev	13.48

Gaus mean : 38.6802

Gaus Width: 3.7478

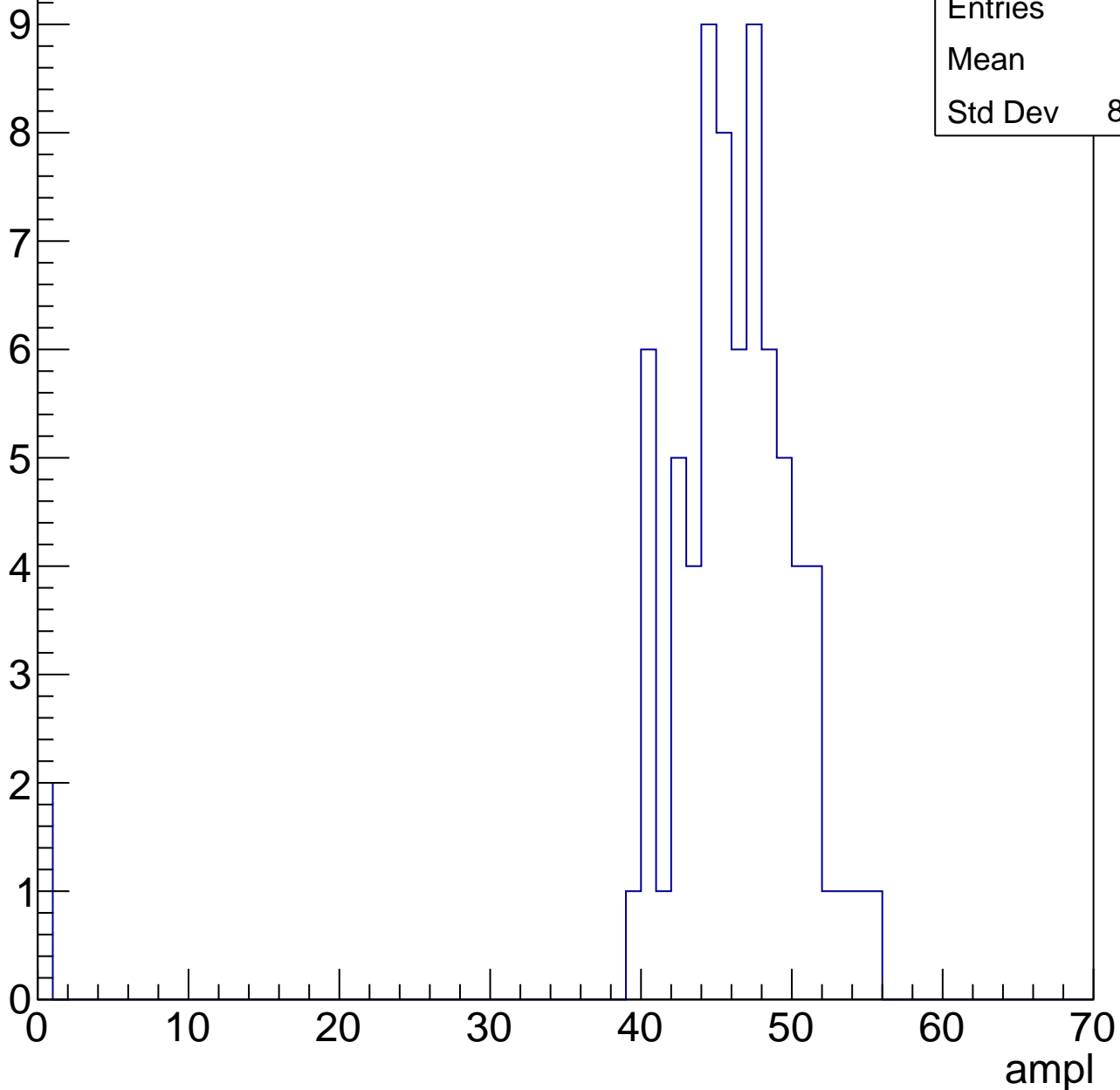


B1L103S, U6-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

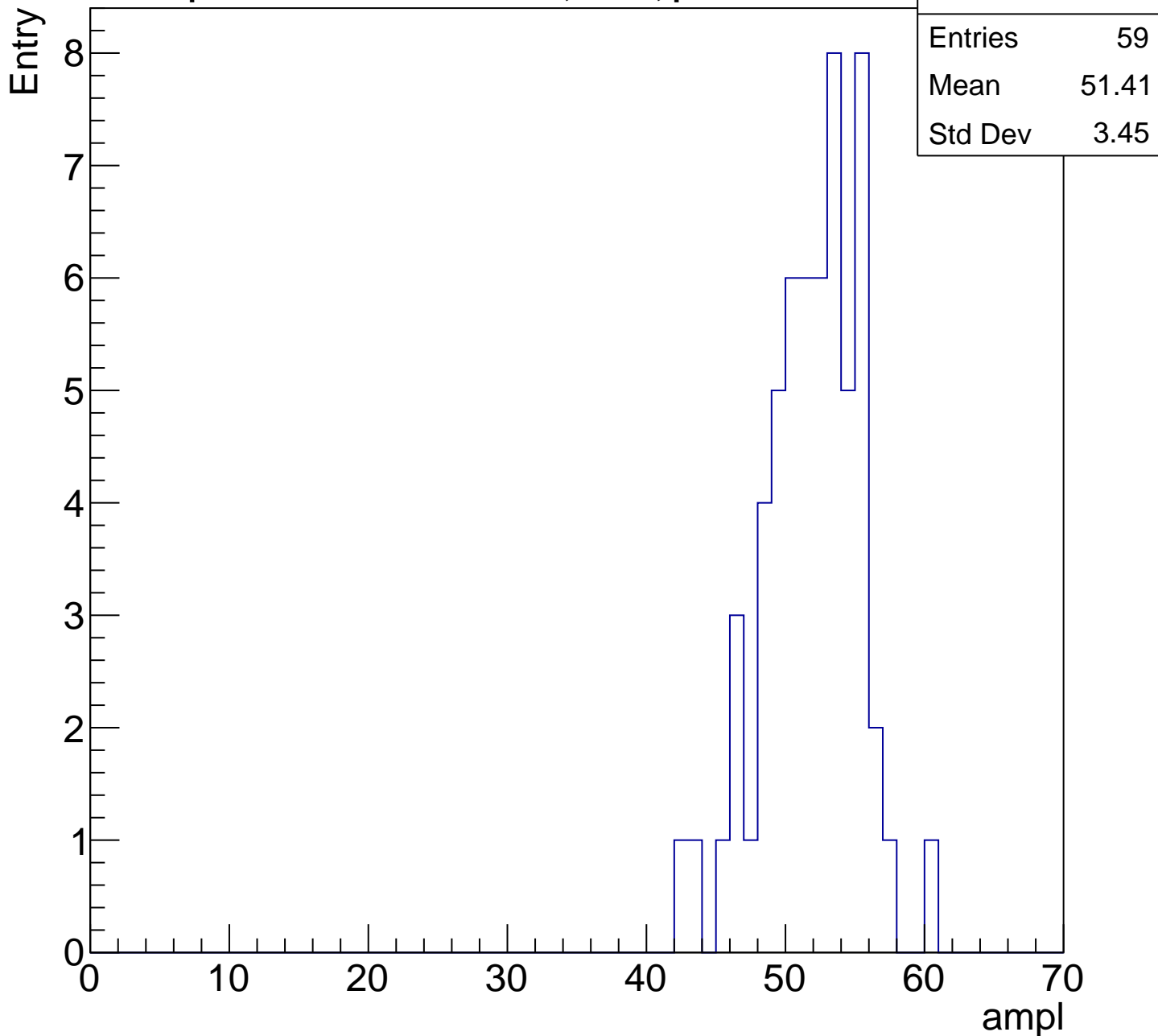
Entry

Entries	74
Mean	44.7
Std Dev	8.244



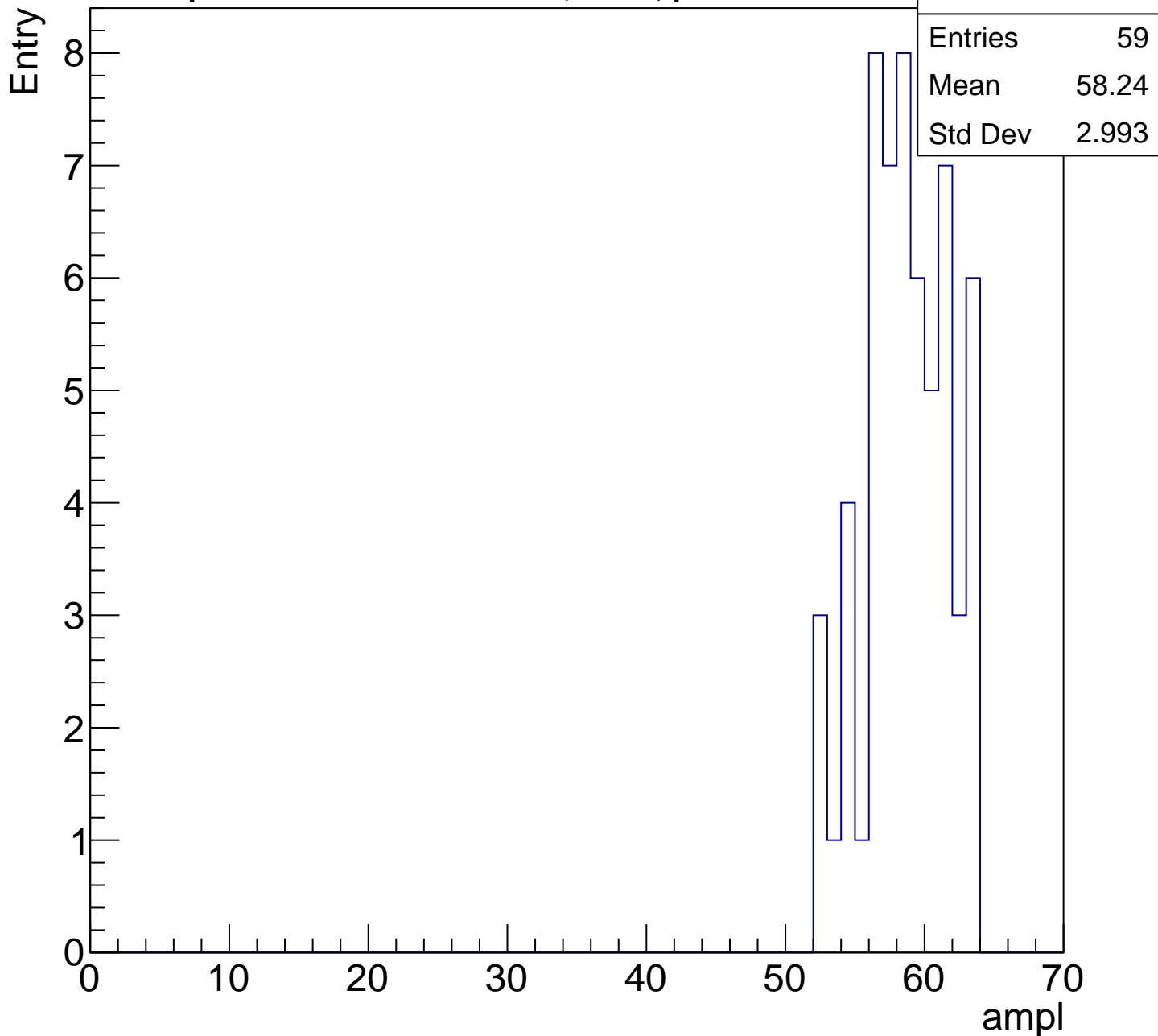
B1L103S, U6-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

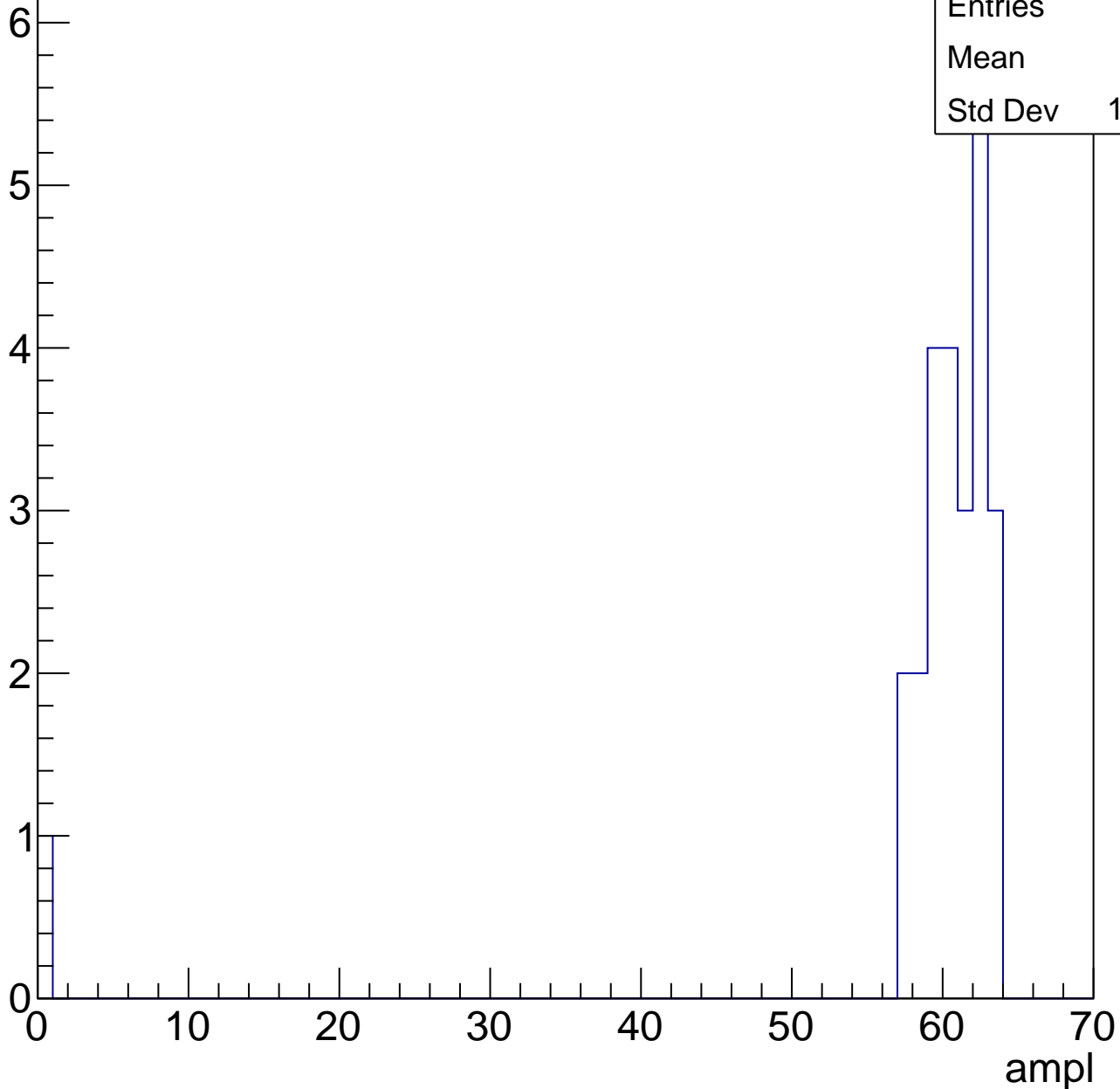


B1L103S, U6-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

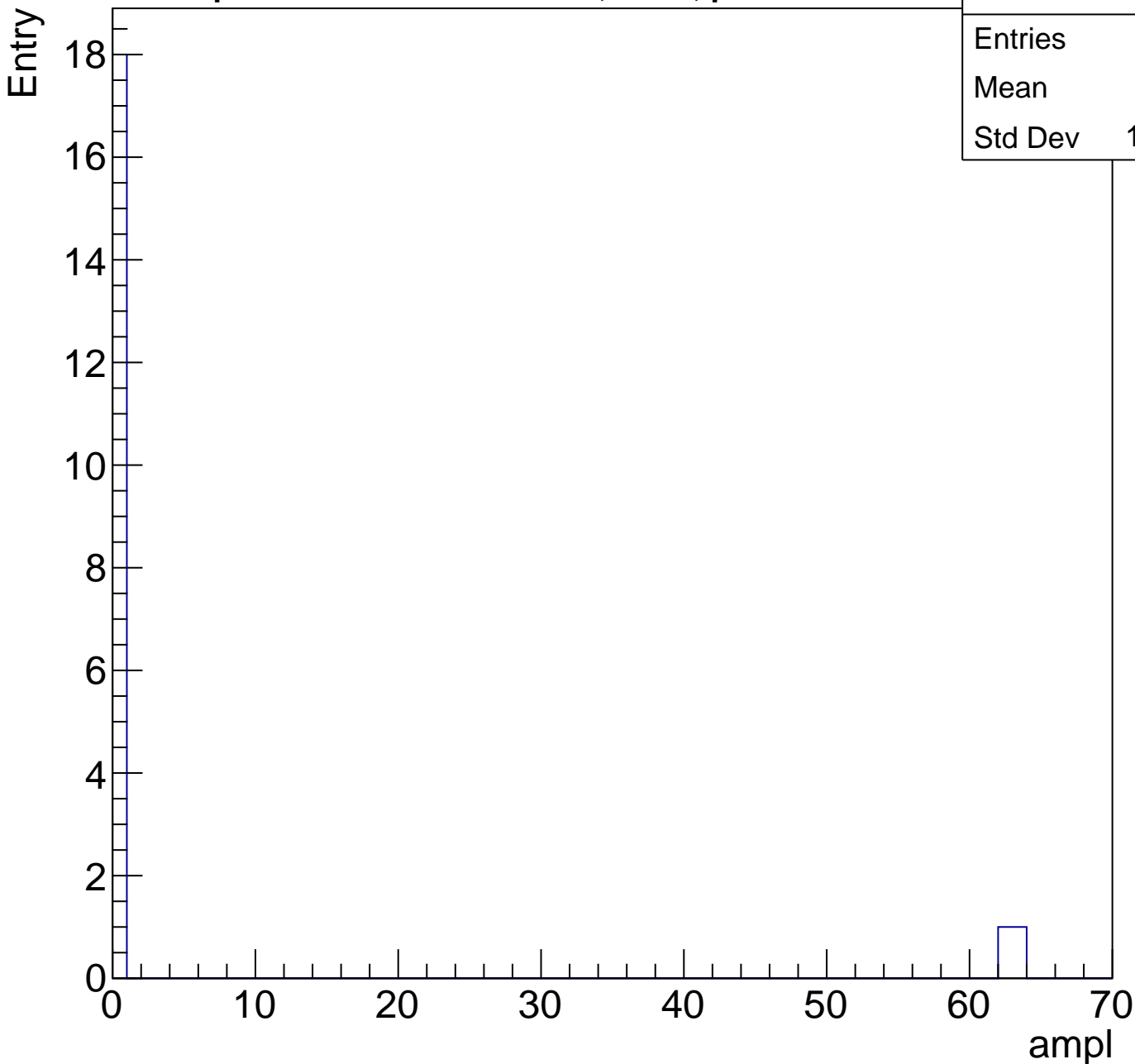
Entries	25
Mean	58
Std Dev	11.97



B1L103S, U6-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.25
Std Dev	18.75



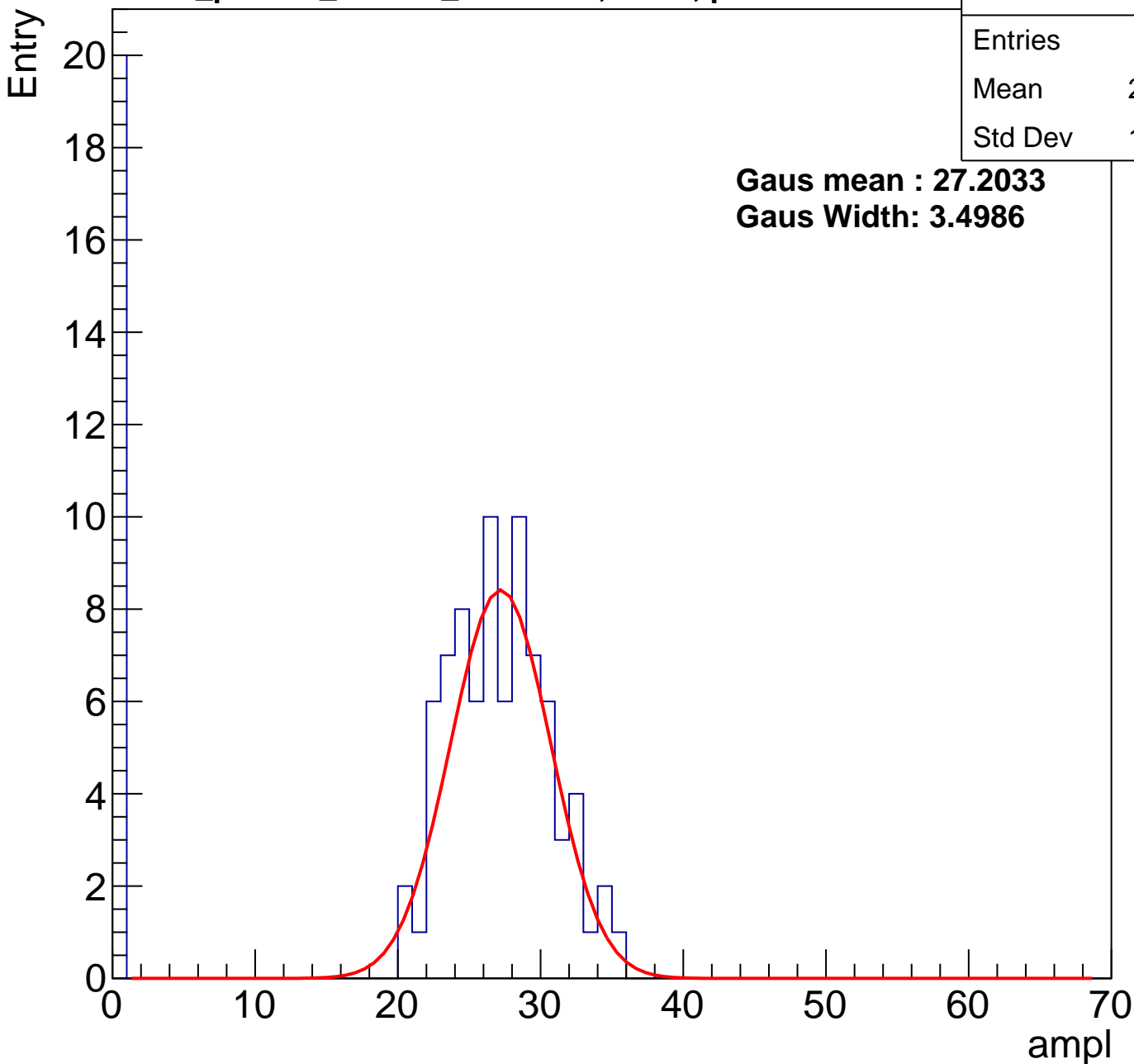
B1L103S, U6-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	21.38
Std Dev	11.12

Gaus mean : 27.2033

Gaus Width: 3.4986



B1L103S, U6-ch87, adc1

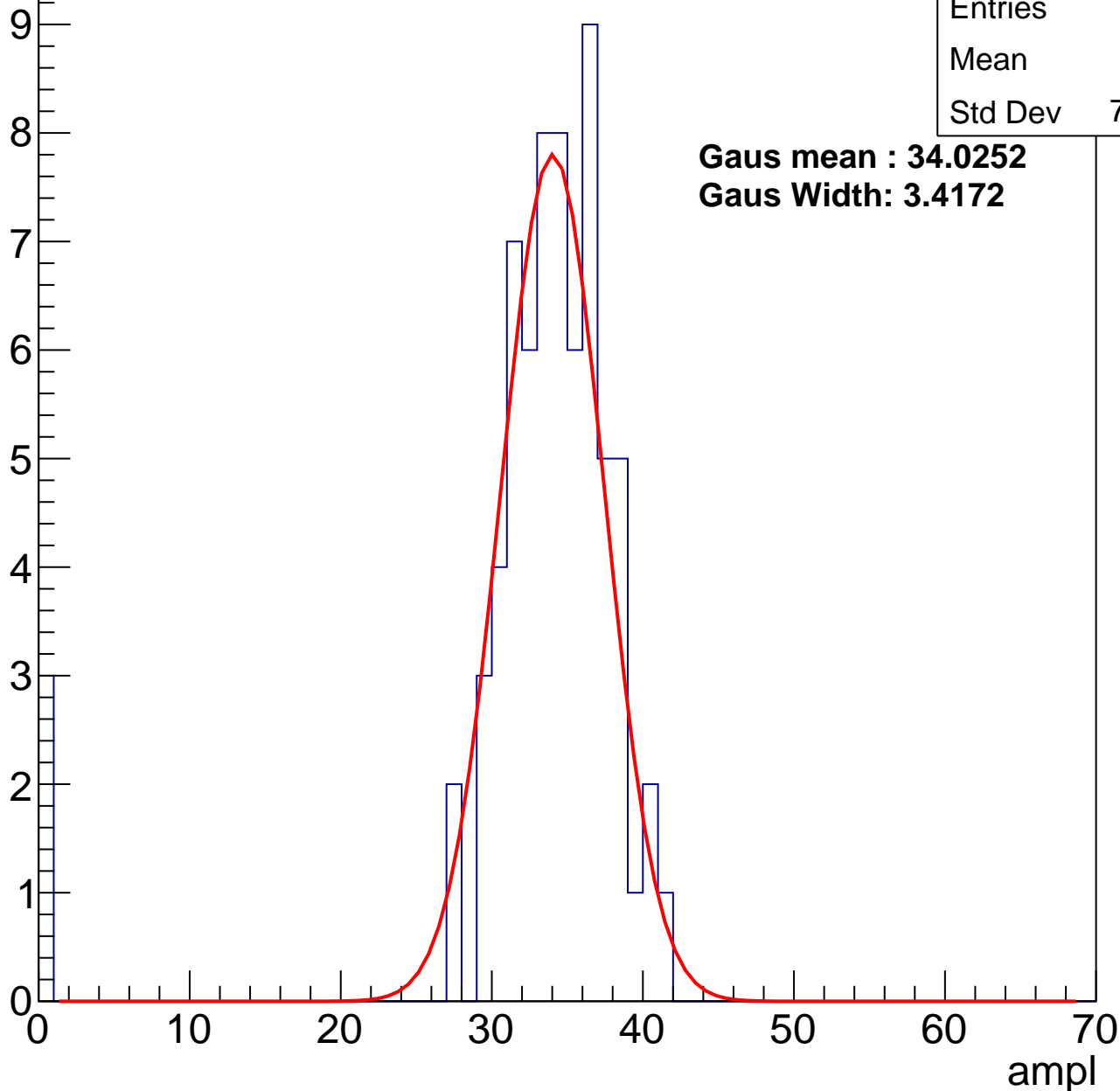
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.5
Std Dev	7.517

Gaus mean : 34.0252

Gaus Width: 3.4172



B1L103S, U6-ch87, adc2

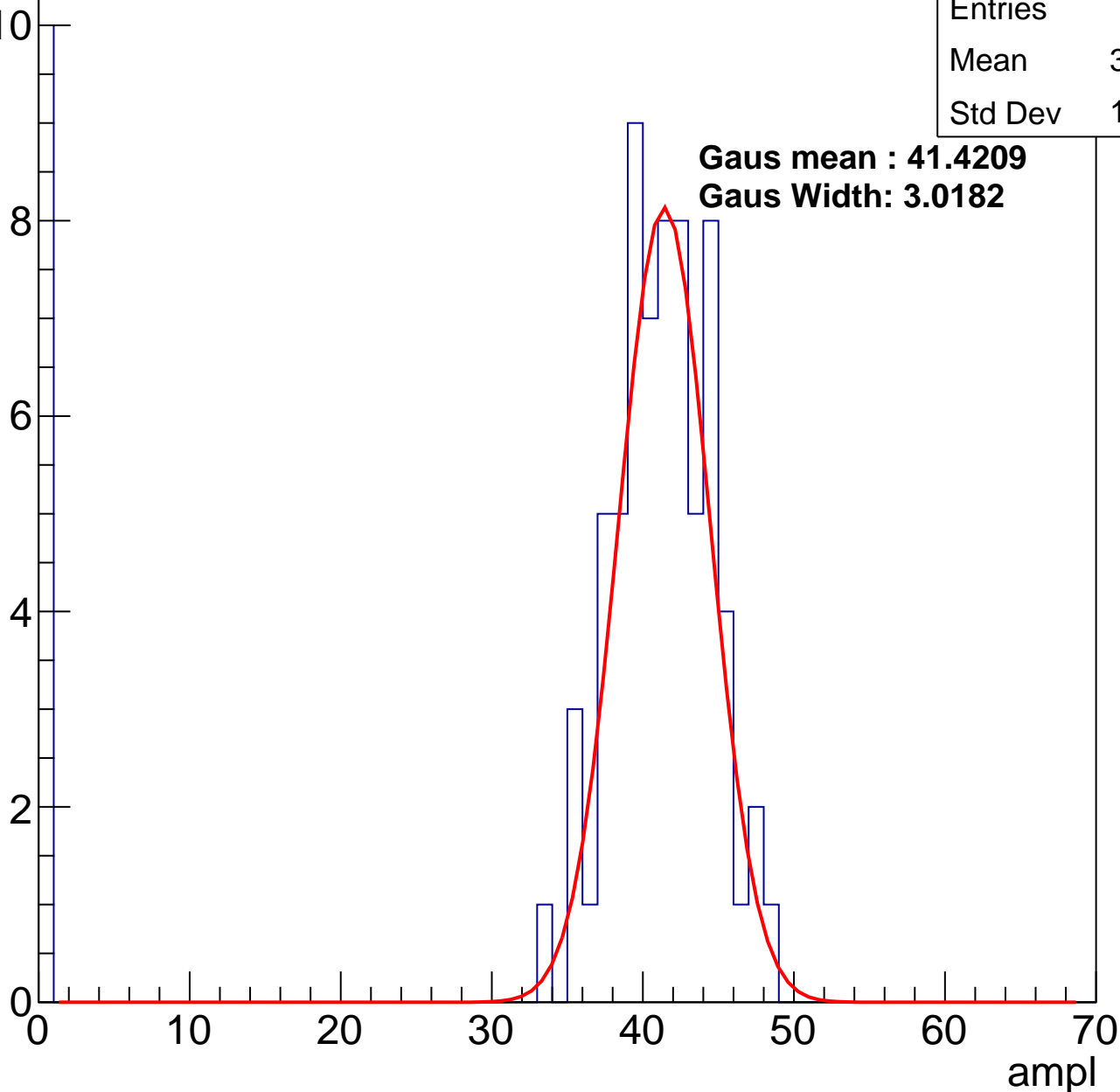
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	35.63
Std Dev	13.98

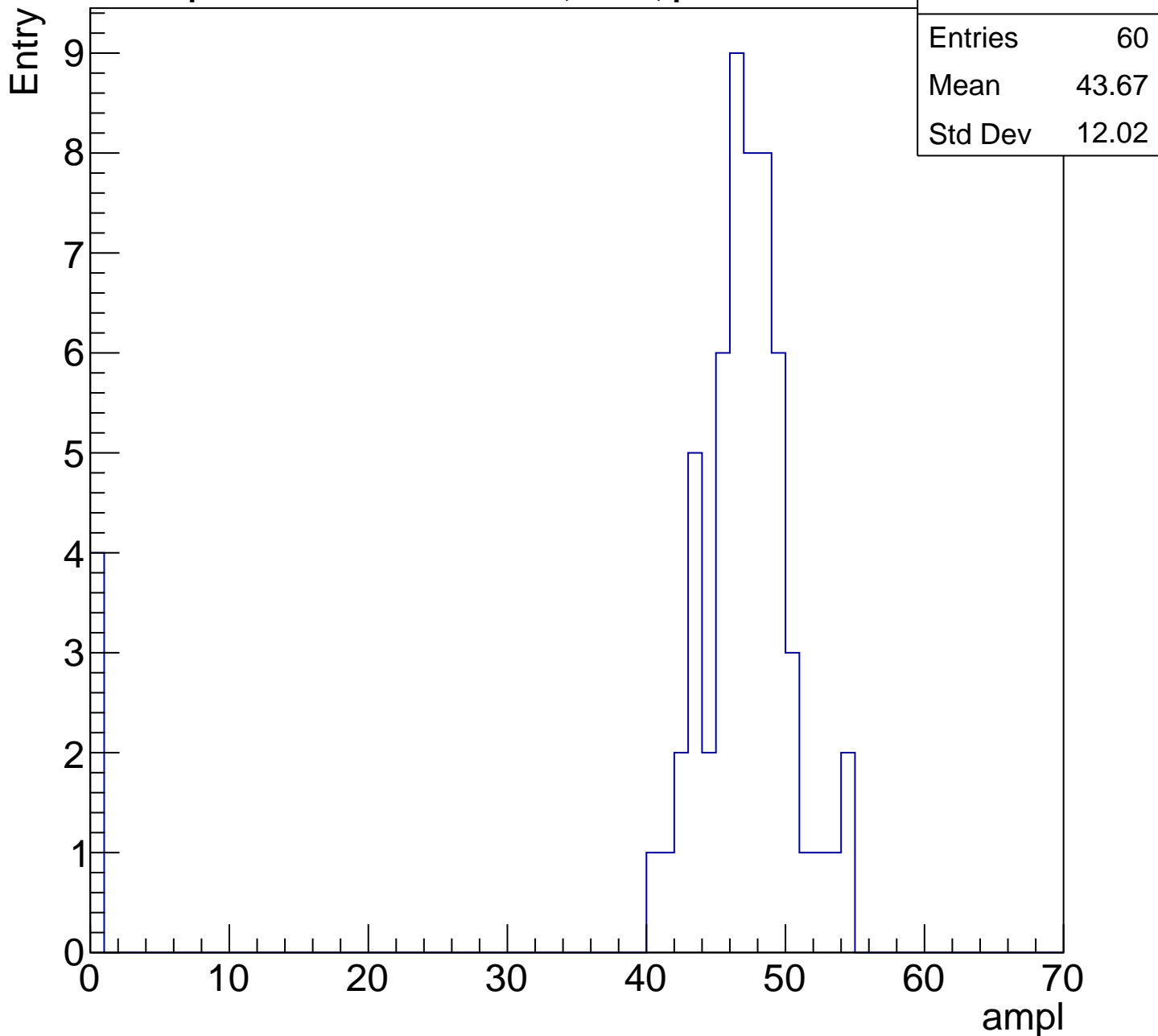
Gaus mean : 41.4209

Gaus Width: 3.0182



B1L103S, U6-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

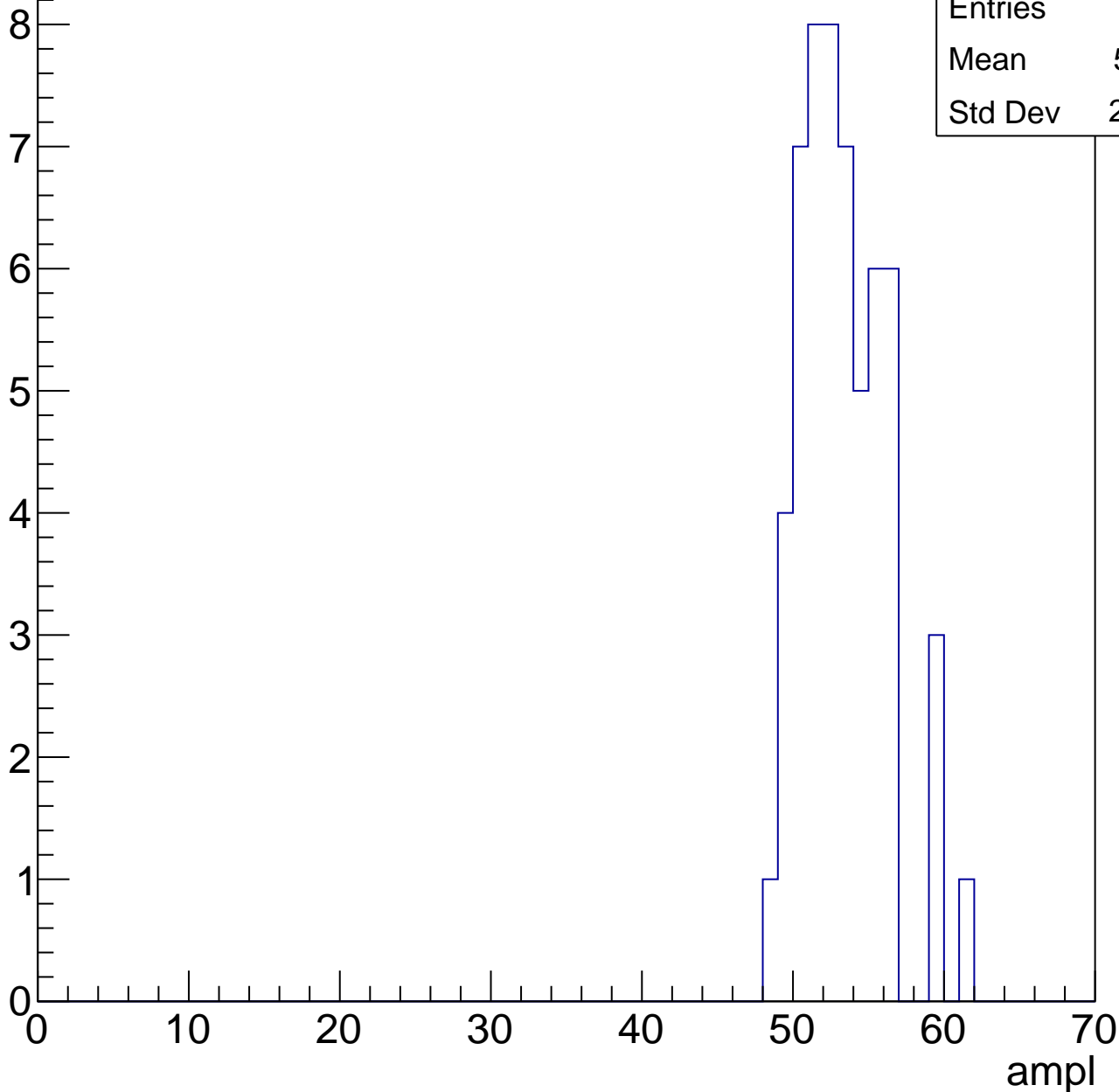


B1L103S, U6-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.91
Std Dev	2.824



B1L103S, U6-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 64

Mean 58.28

Std Dev 2.837

8

6

4

2

0

0

10

20

30

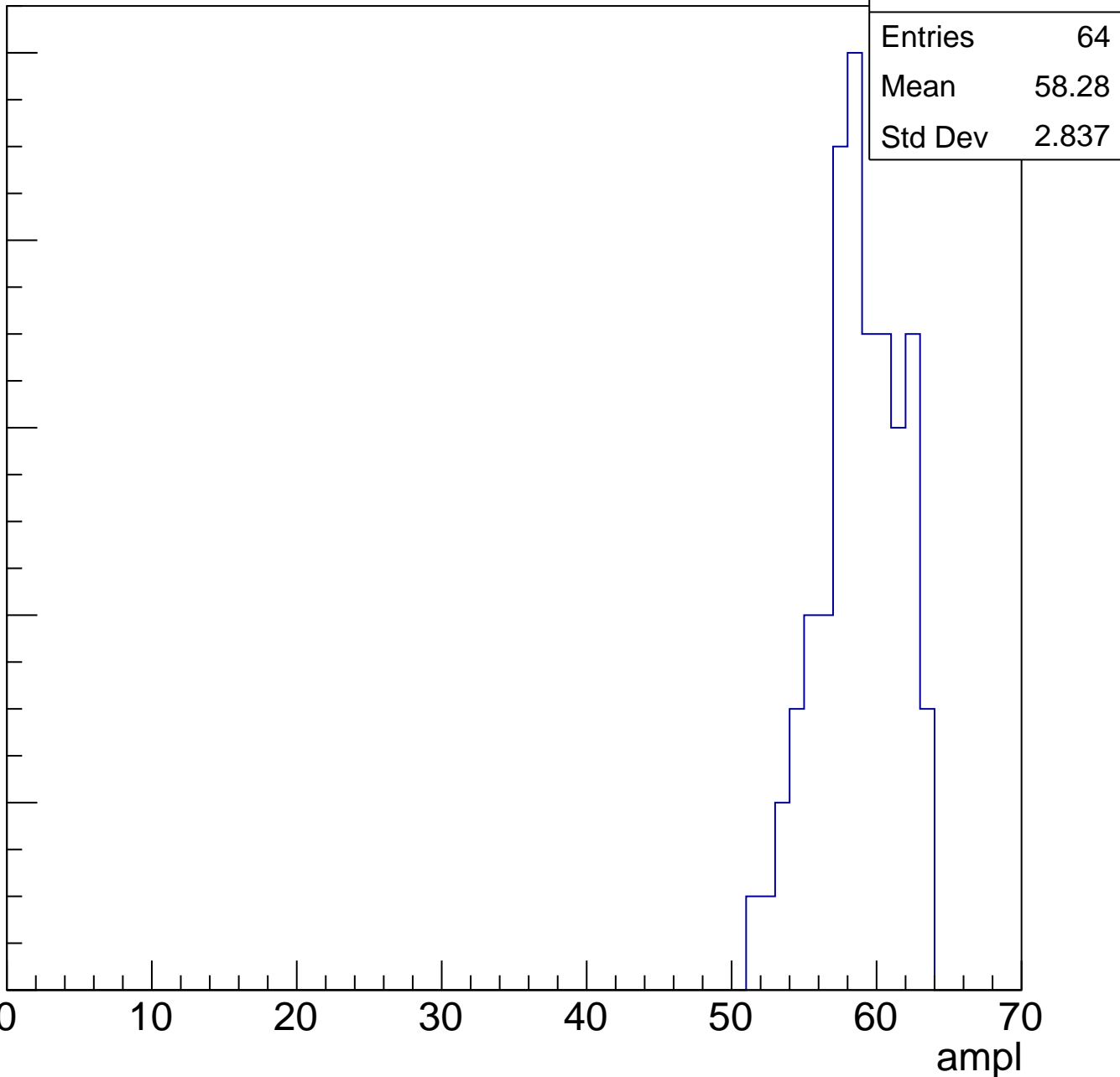
40

50

60

70

ampl

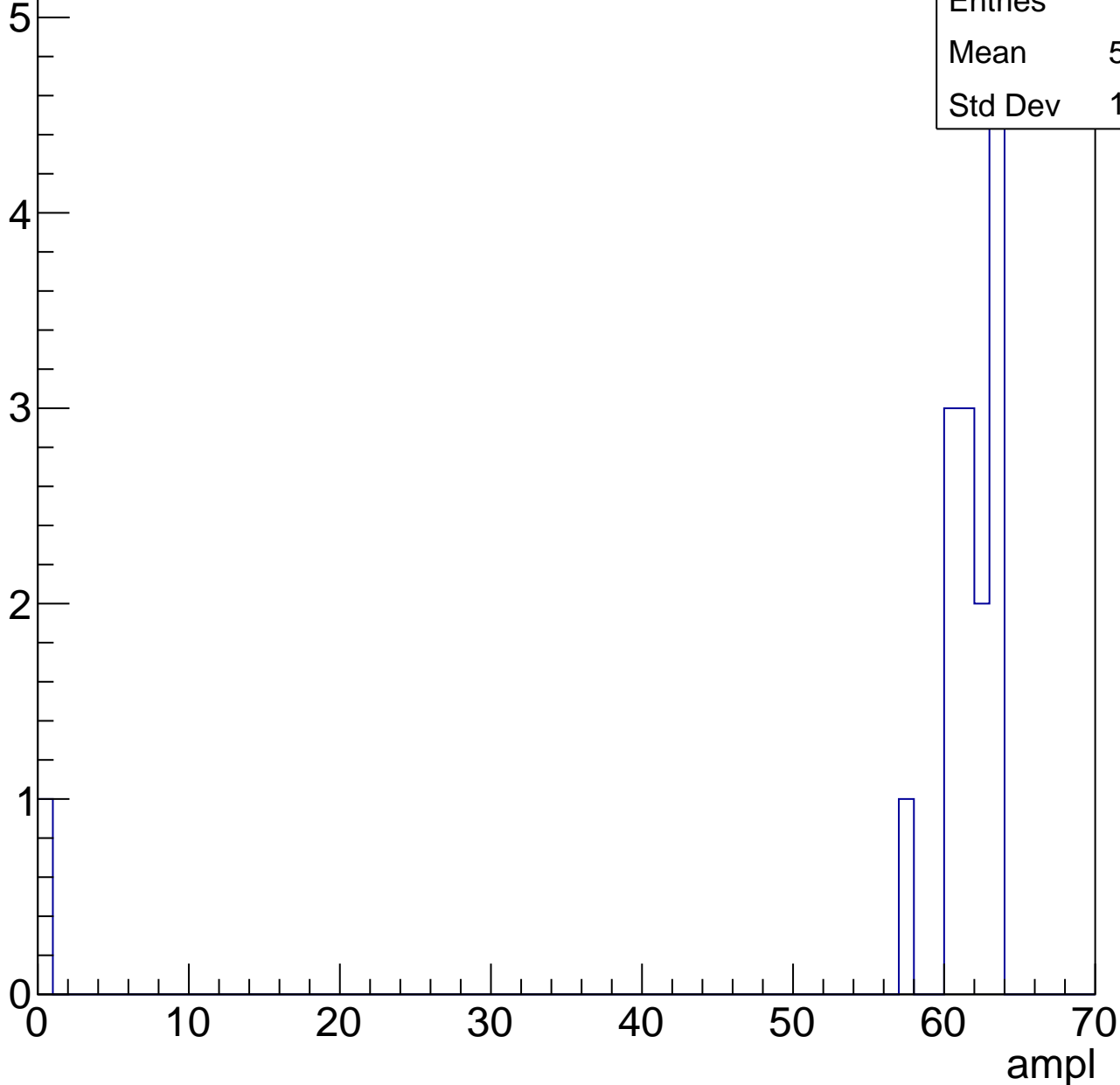


B1L103S, U6-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

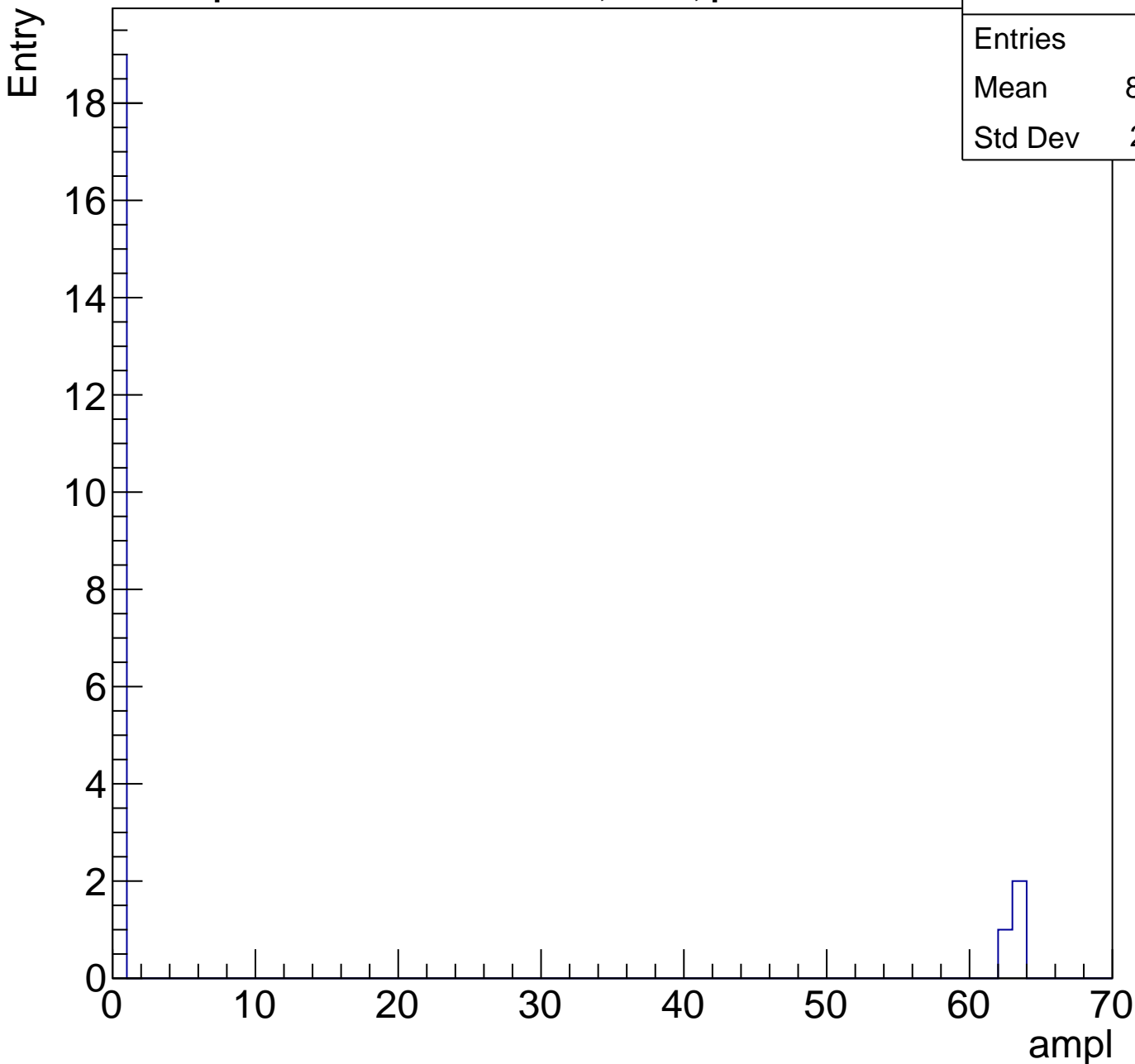
Entries	15
Mean	57.27
Std Dev	15.39



B1L103S, U6-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51



B1L103S, U6-ch88, adc0

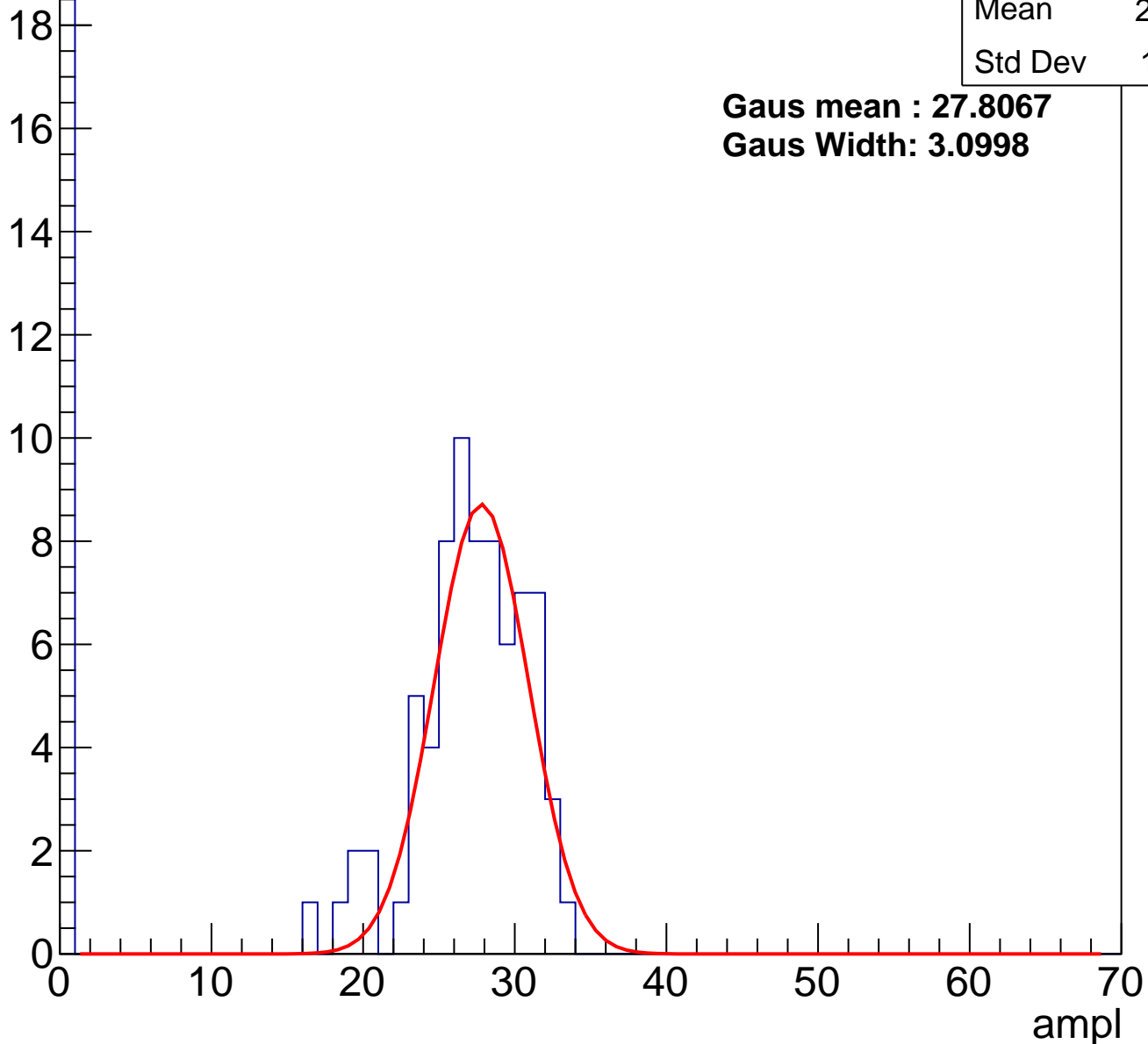
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	21.24
Std Dev	11.21

Gaus mean : 27.8067

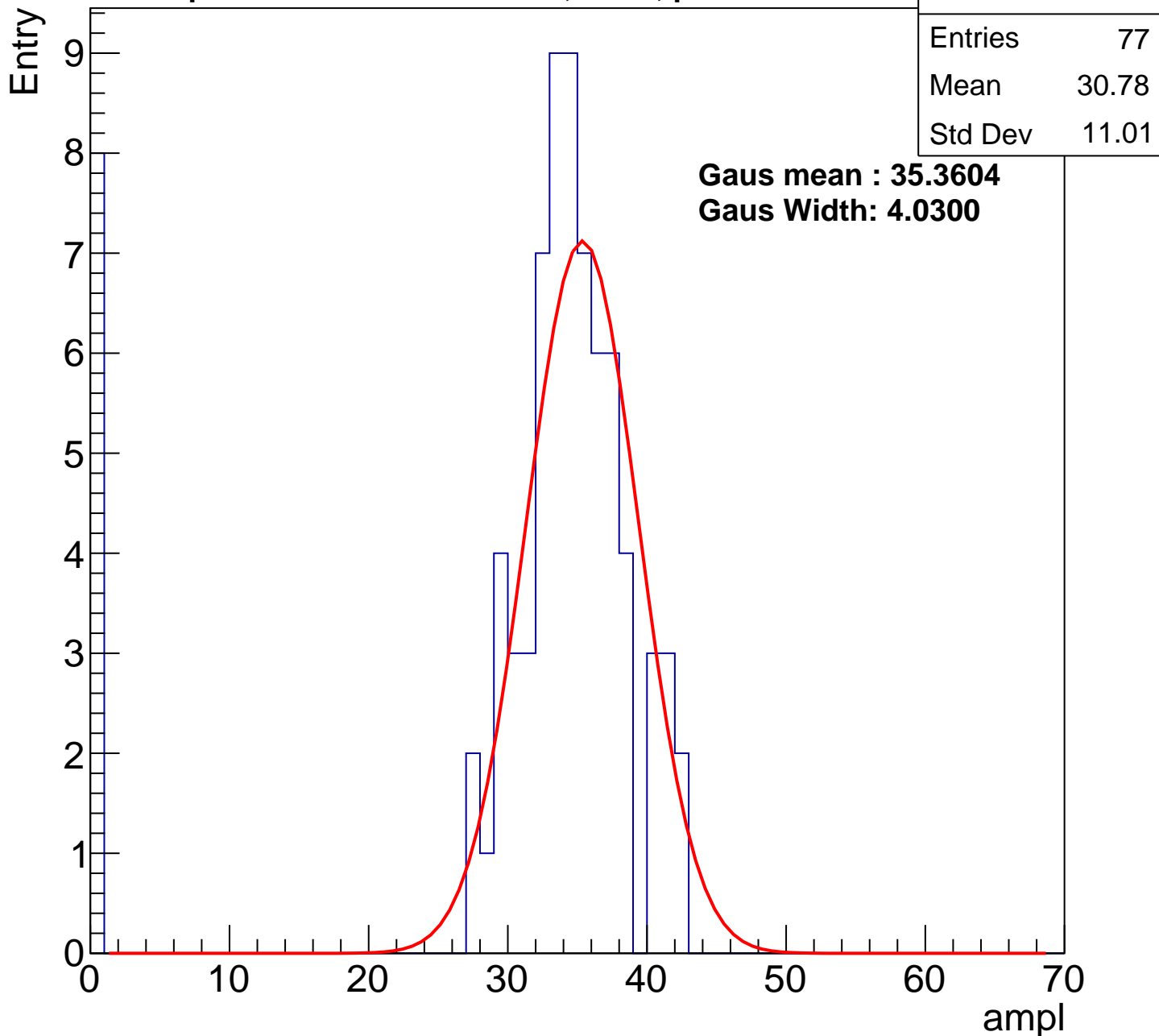
Gaus Width: 3.0998

Entry



B1L103S, U6-ch88, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch88, adc2

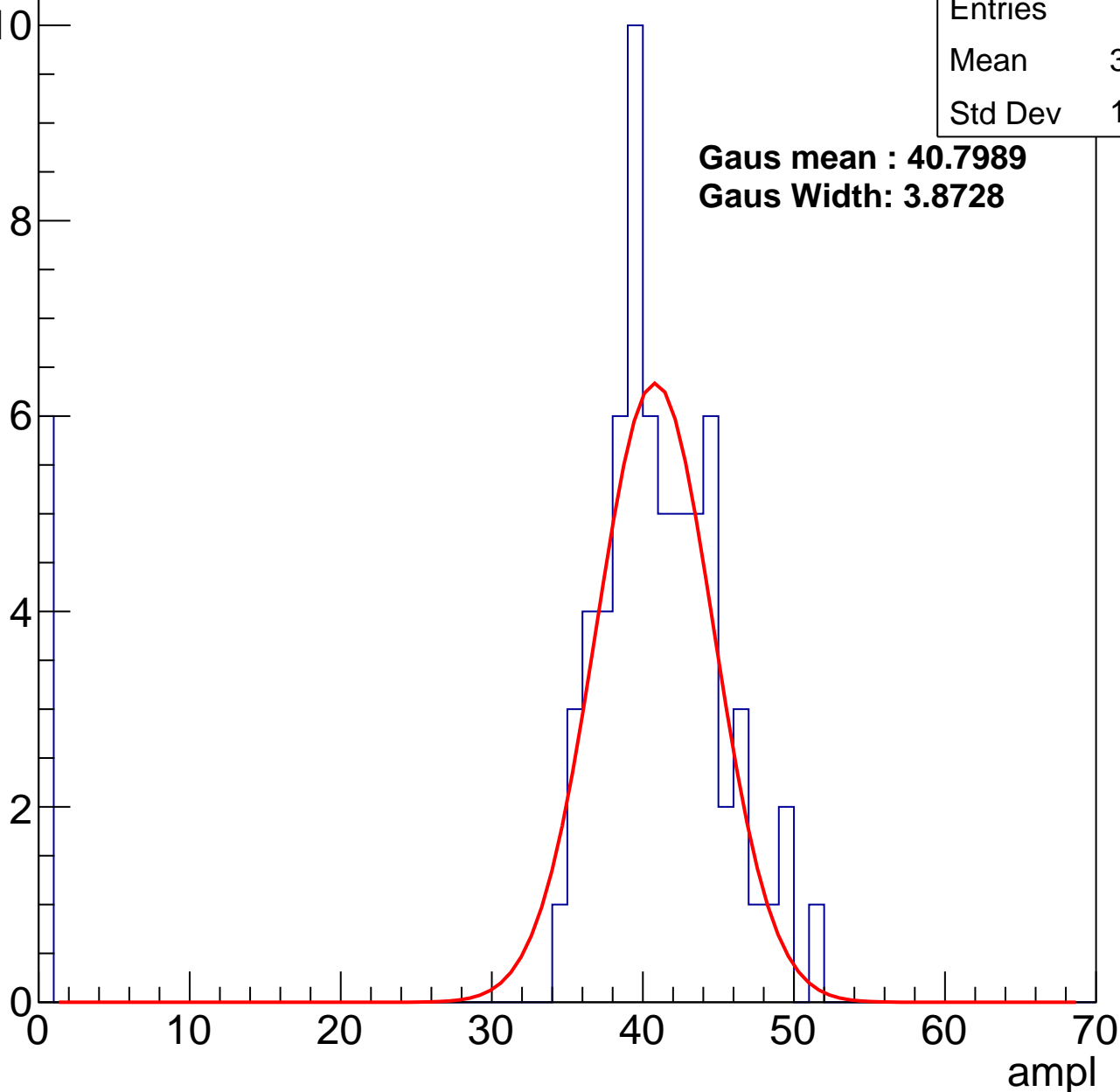
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	37.39
Std Dev	11.92

Gaus mean : 40.7989

Gaus Width: 3.8728

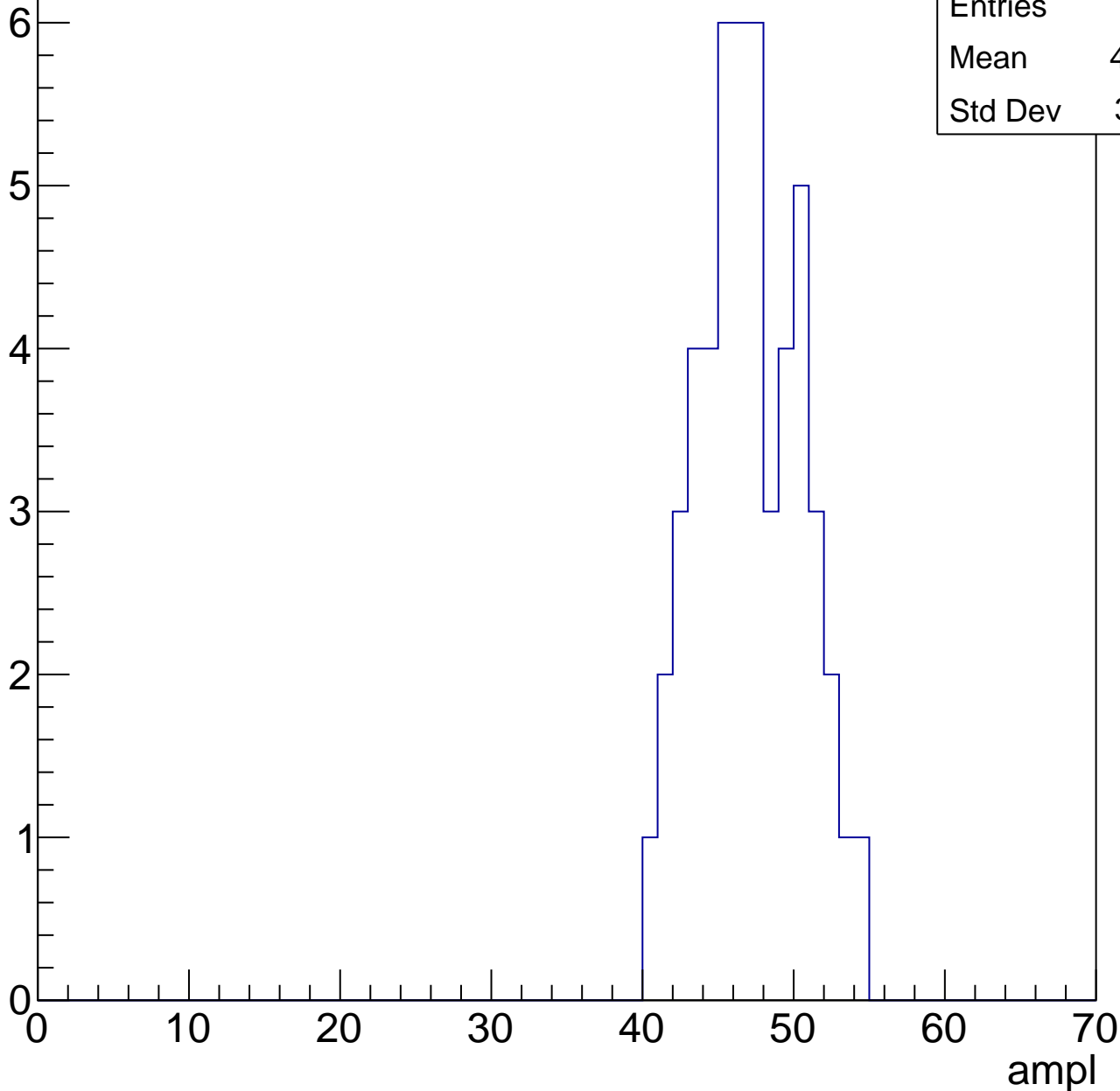


B1L103S, U6-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	46.63
Std Dev	3.331

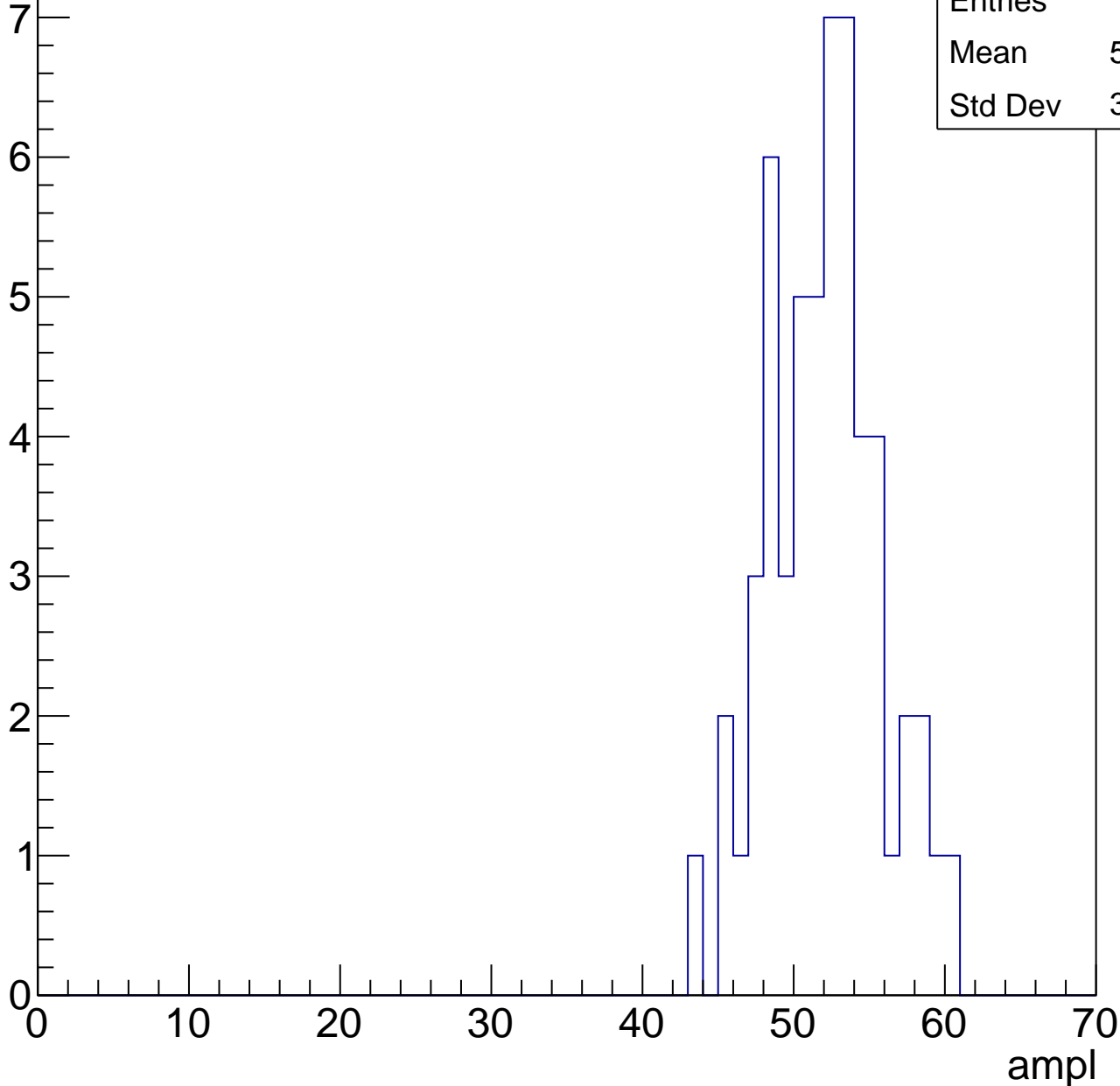


B1L103S, U6-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

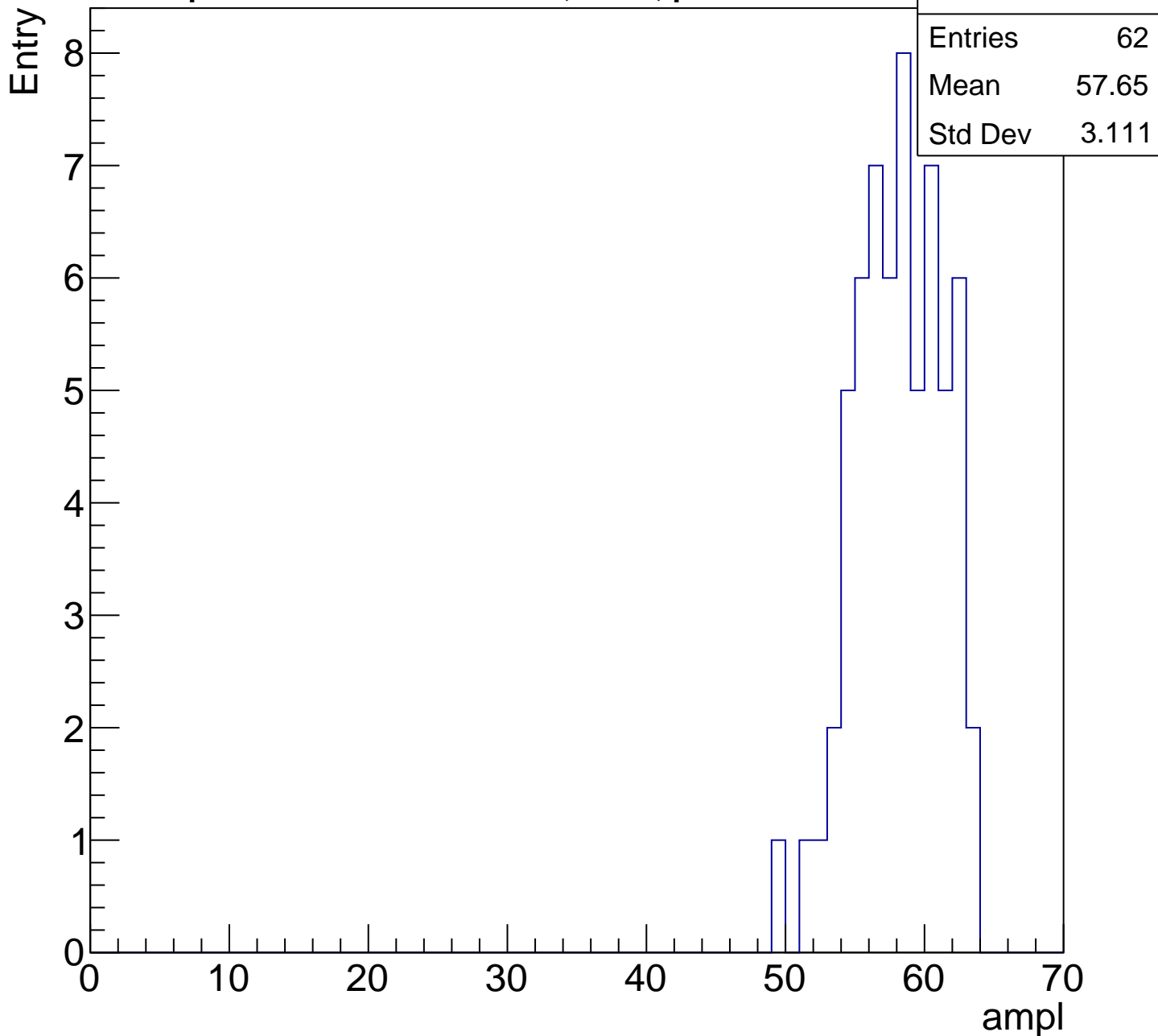
Entry

Entries	55
Mean	51.56
Std Dev	3.662



B1L103S, U6-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

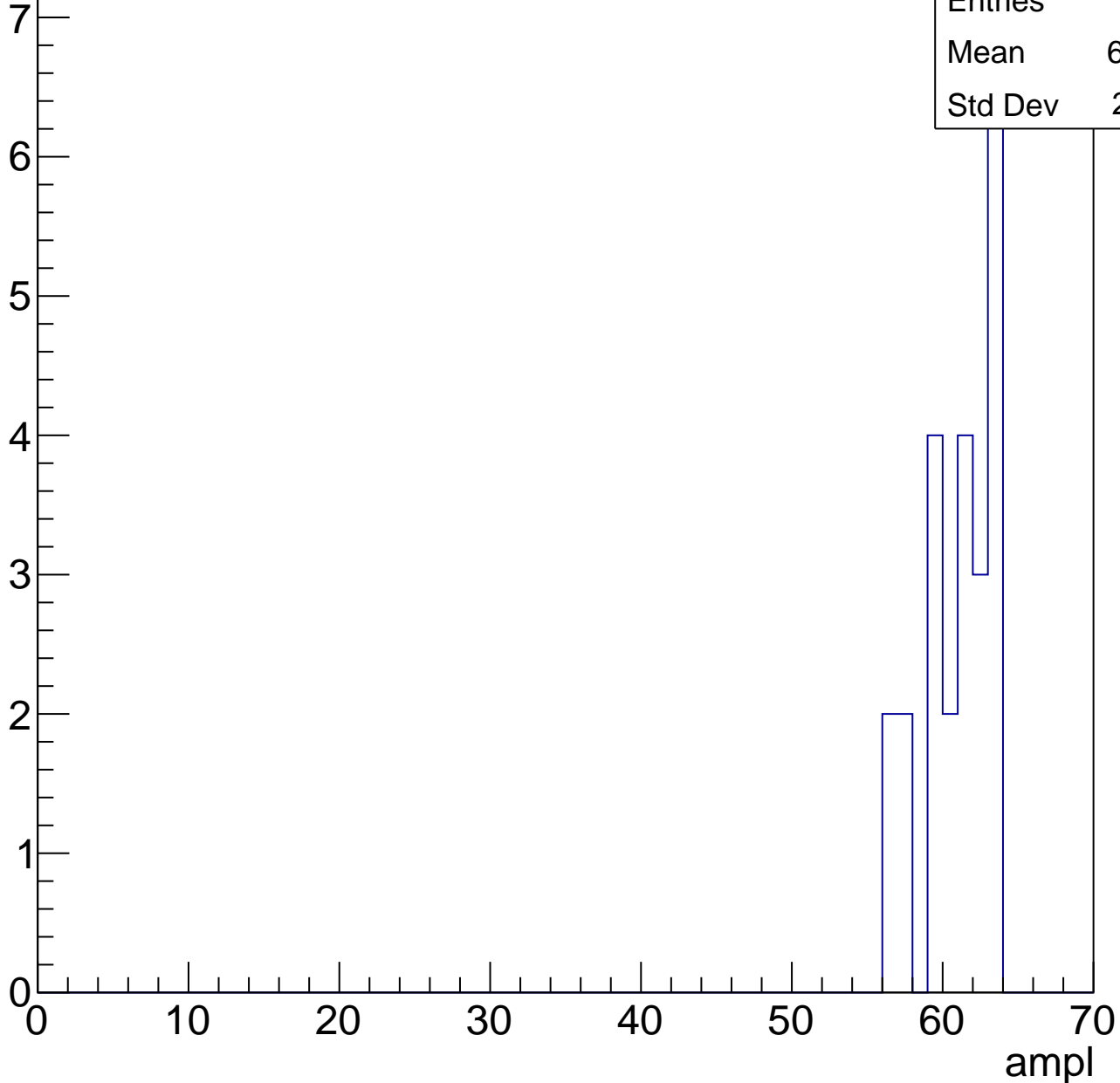


B1L103S, U6-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

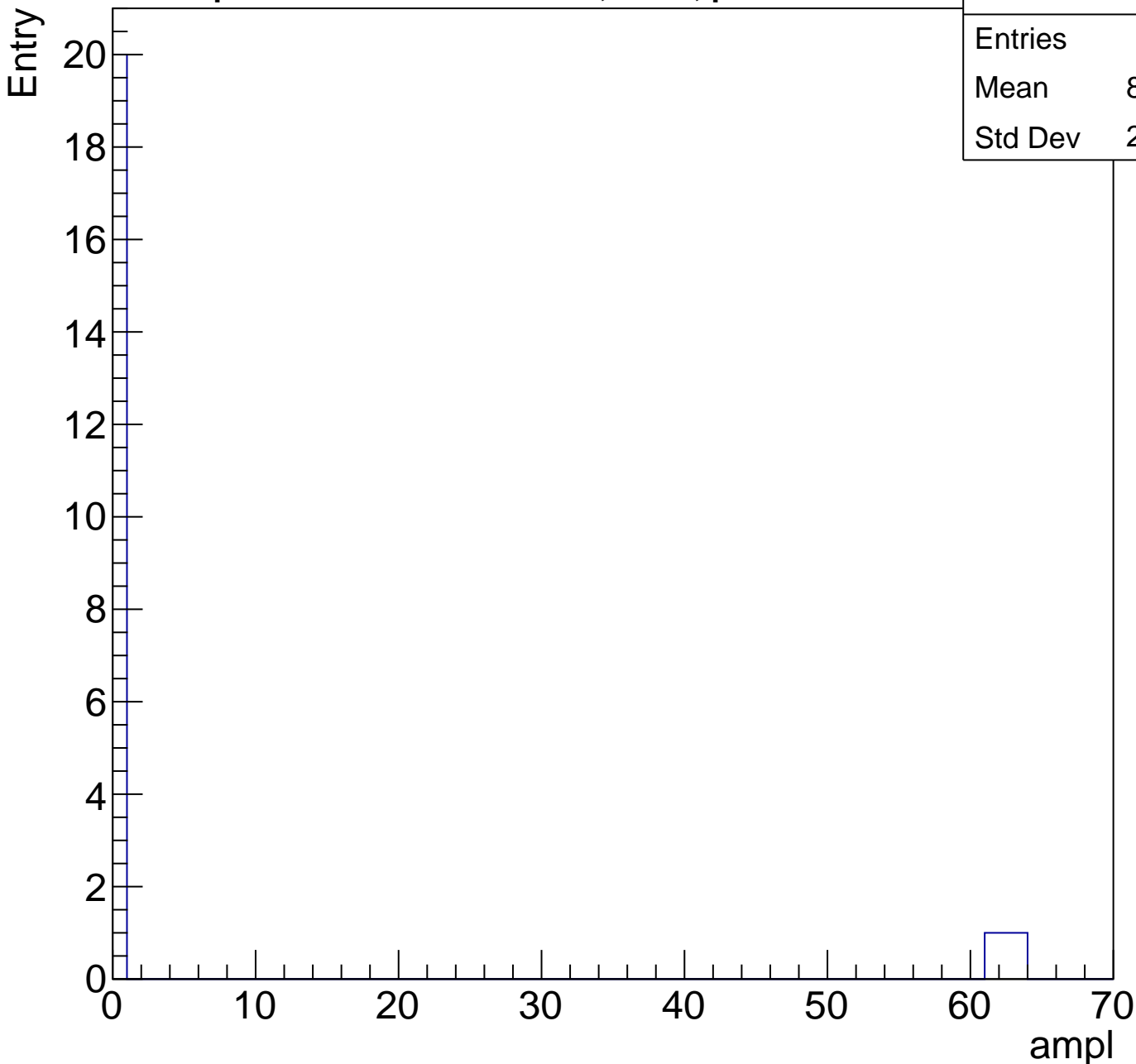
Entries	24
Mean	60.54
Std Dev	2.291



B1L103S, U6-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.087
Std Dev	20.88



B1L103S, U6-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	24.46
Std Dev	10.85

Gaus mean : 29.4221

Gaus Width: 3.2701

Entry

10

8

6

4

2

0

0

10

20

30

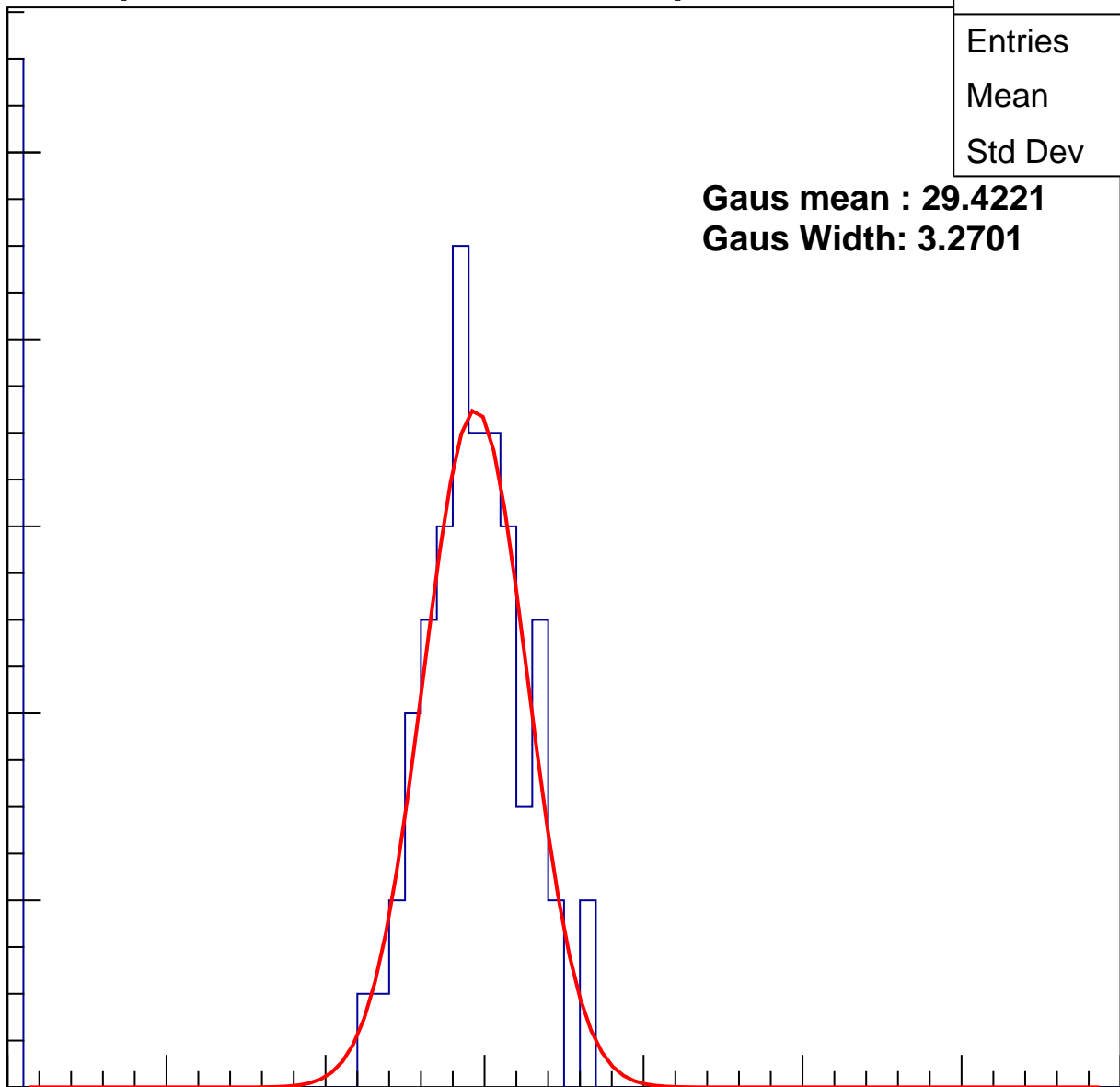
40

50

60

70

ampl



B1L103S, U6-ch89, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	28.92
Std Dev	13.7

Gaus mean : 35.1039

Gaus Width: 3.4495

Entry

10

8

6

4

2

0

0

10

20

30

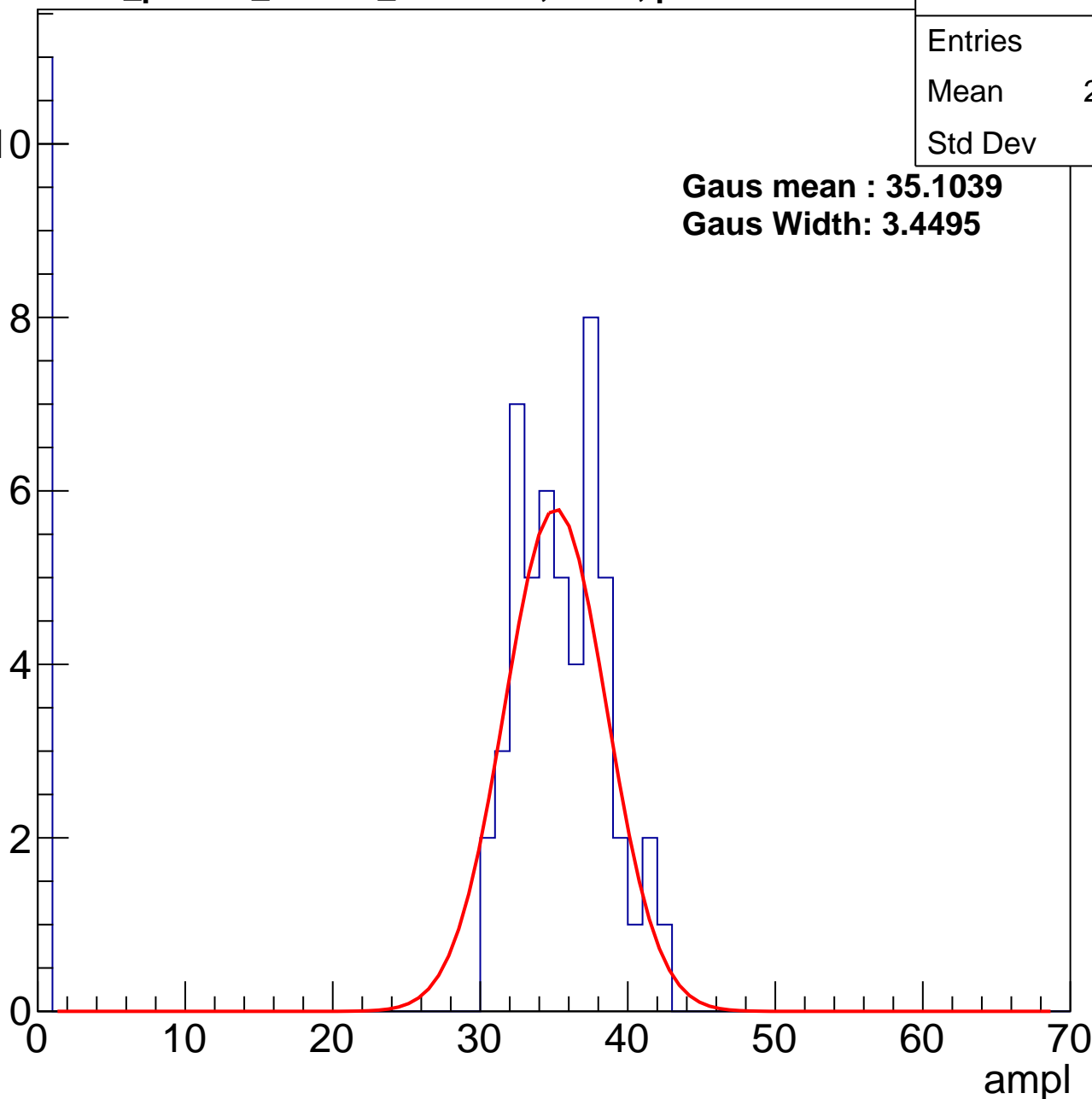
40

50

60

70

ampl



B1L103S, U6-ch89, adc2

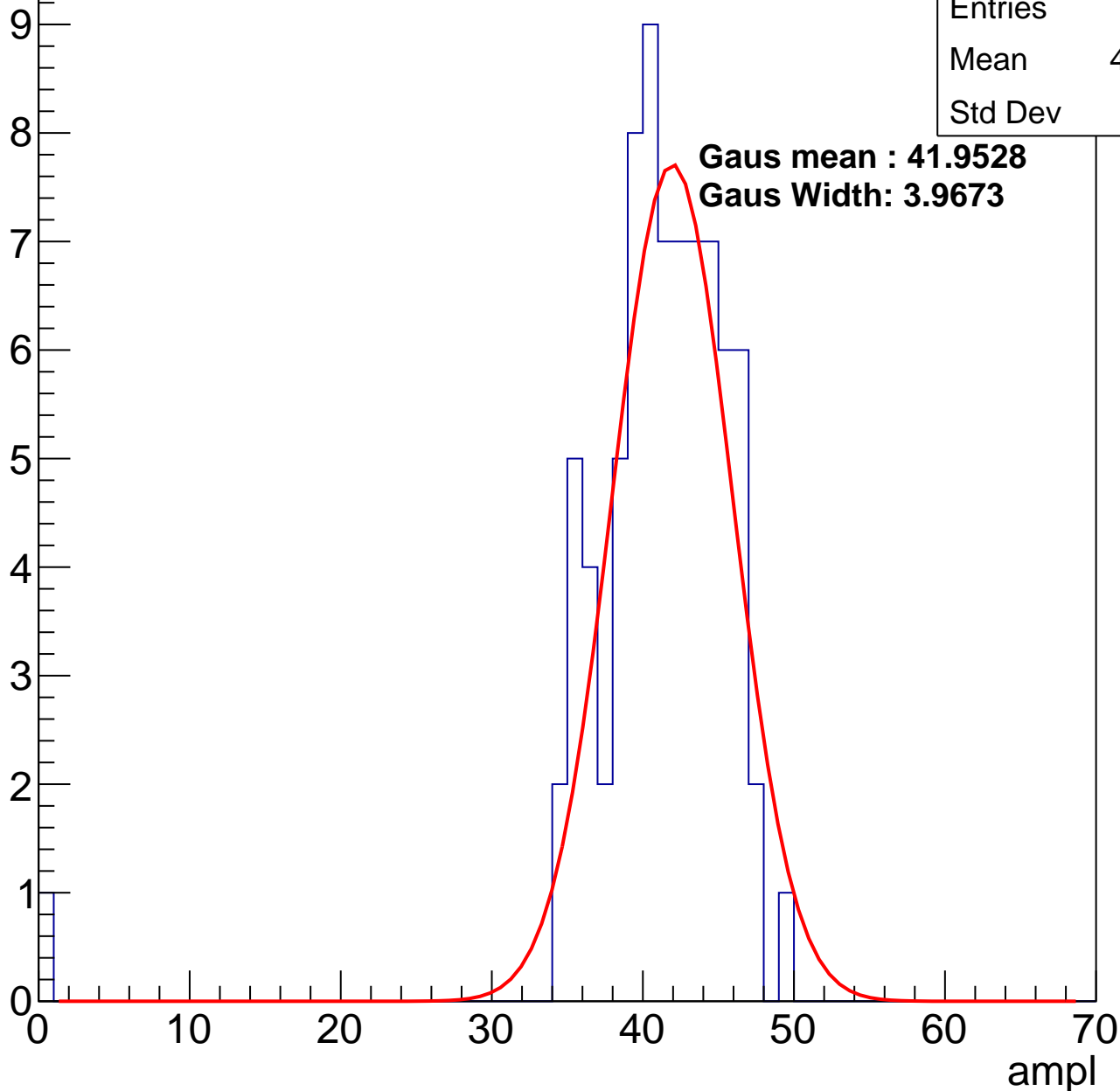
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	40.53
Std Dev	5.78

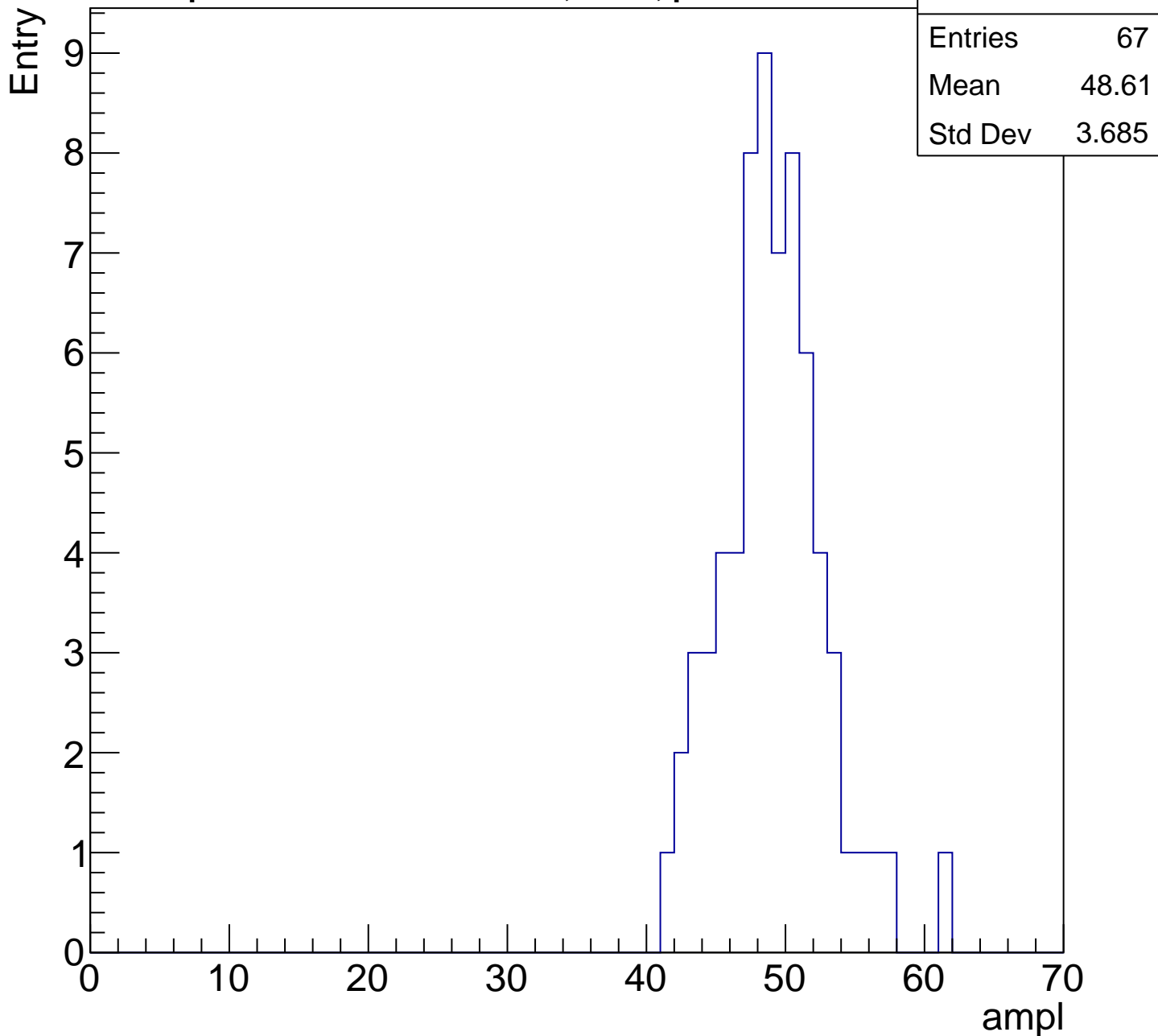
Gaus mean : 41.9528

Gaus Width: 3.9673



B1L103S, U6-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

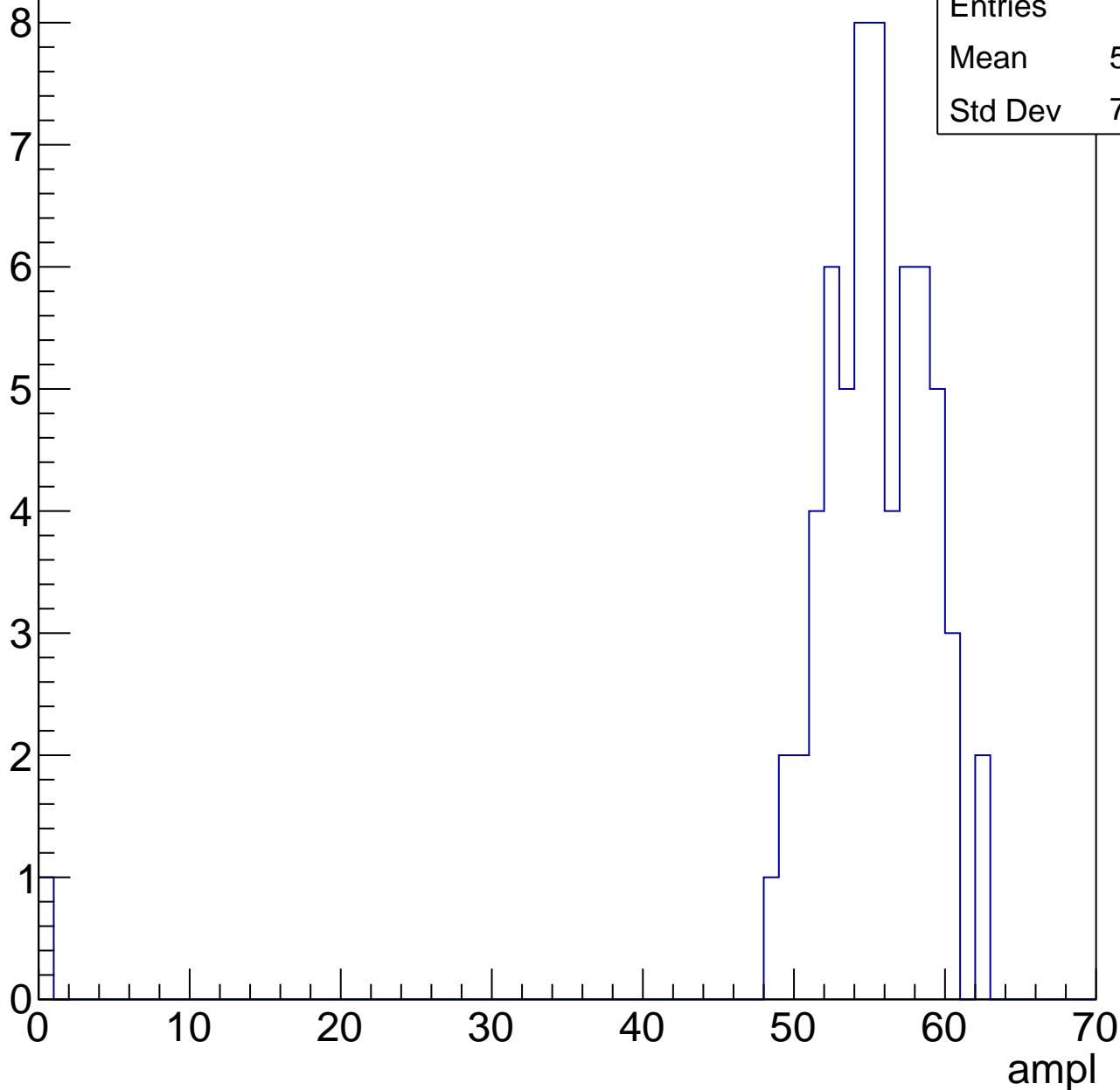


B1L103S, U6-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.16
Std Dev	7.593



B1L103S, U6-ch89, adc5

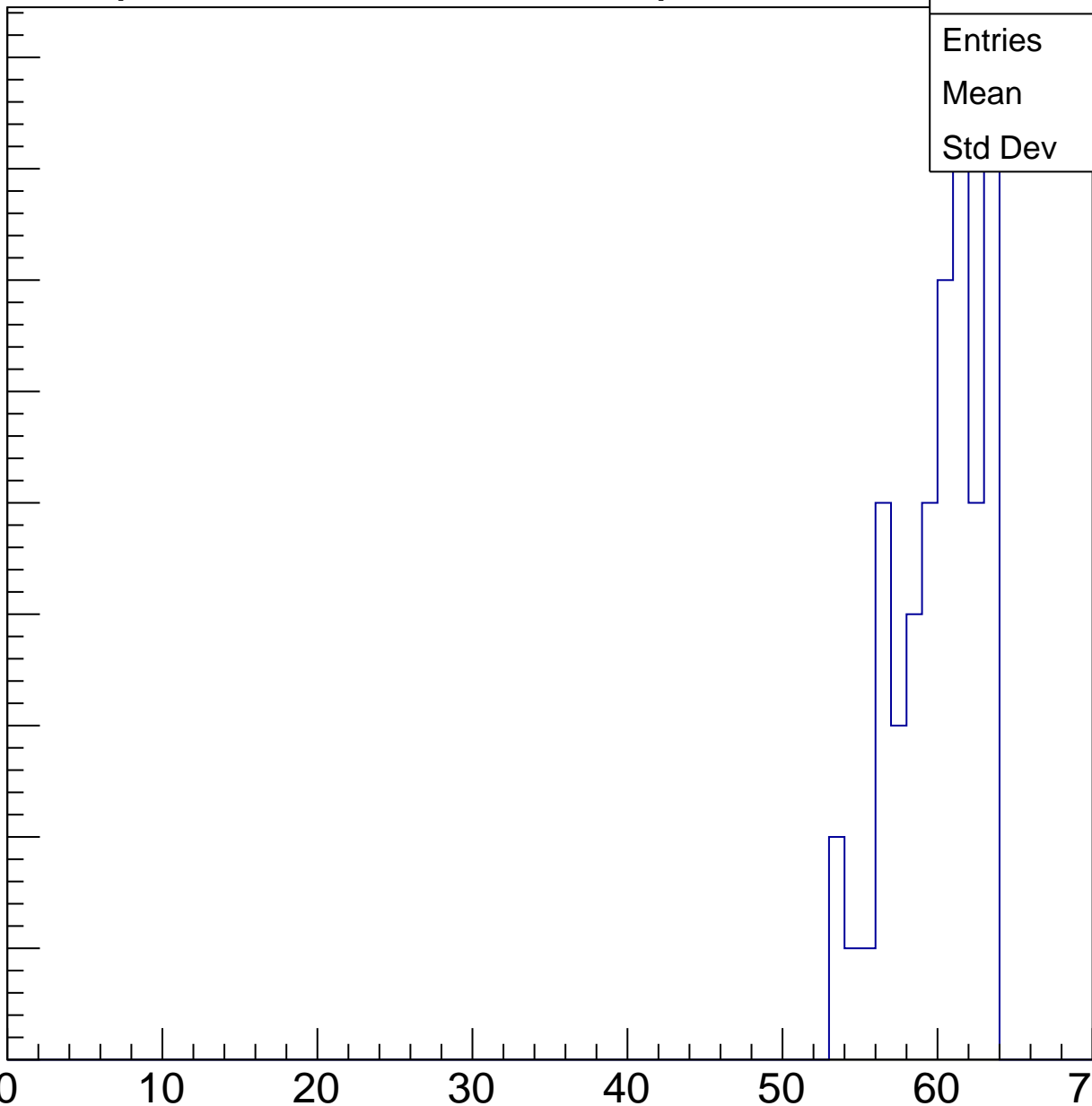
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.59
Std Dev	2.759

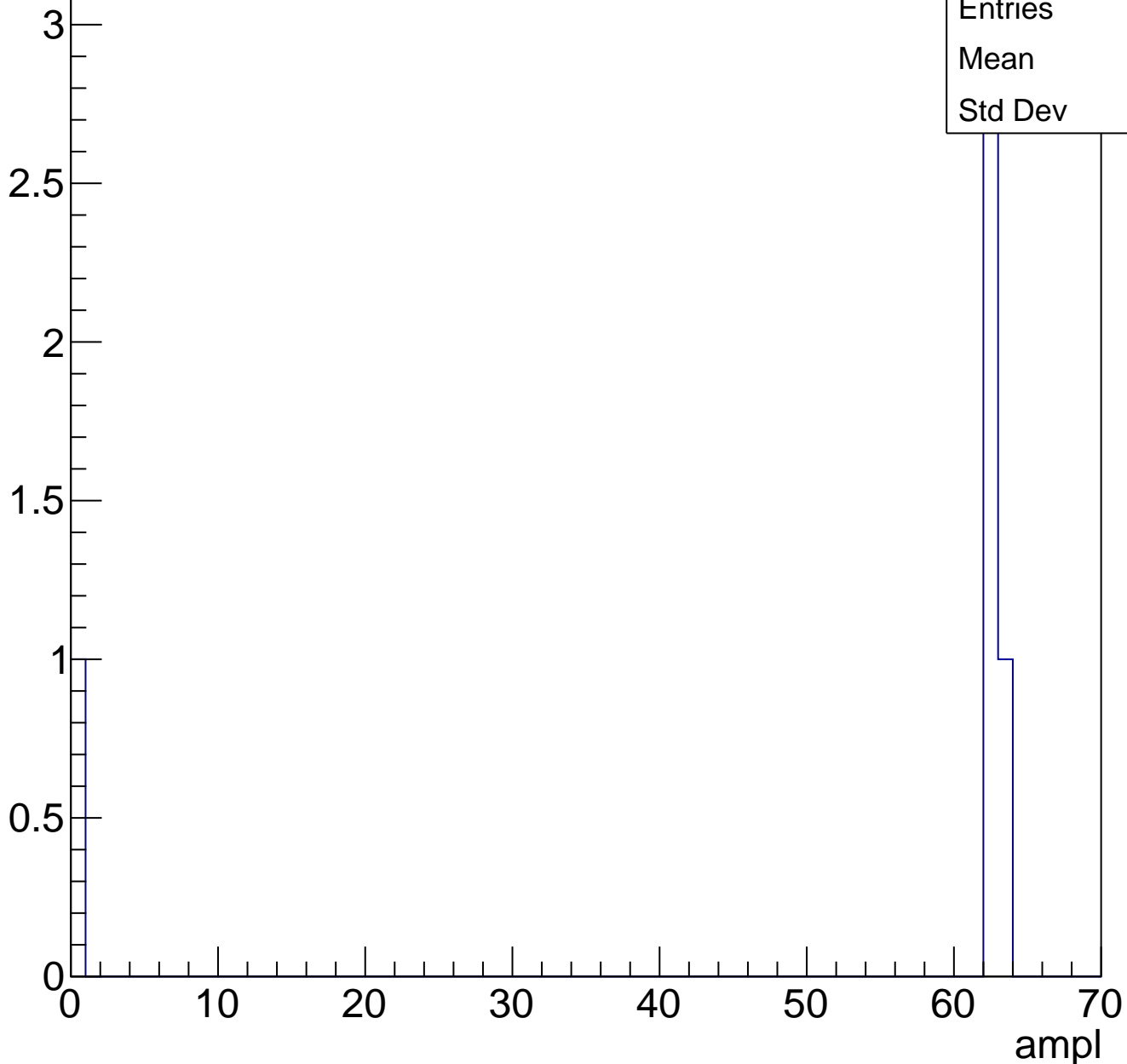
ampl



B1L103S, U6-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch90, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	23.24
Std Dev	10.07

Gaus mean : 27.7839

Gaus Width: 3.3652

Entry

10

8

6

4

2

0

0

10

20

30

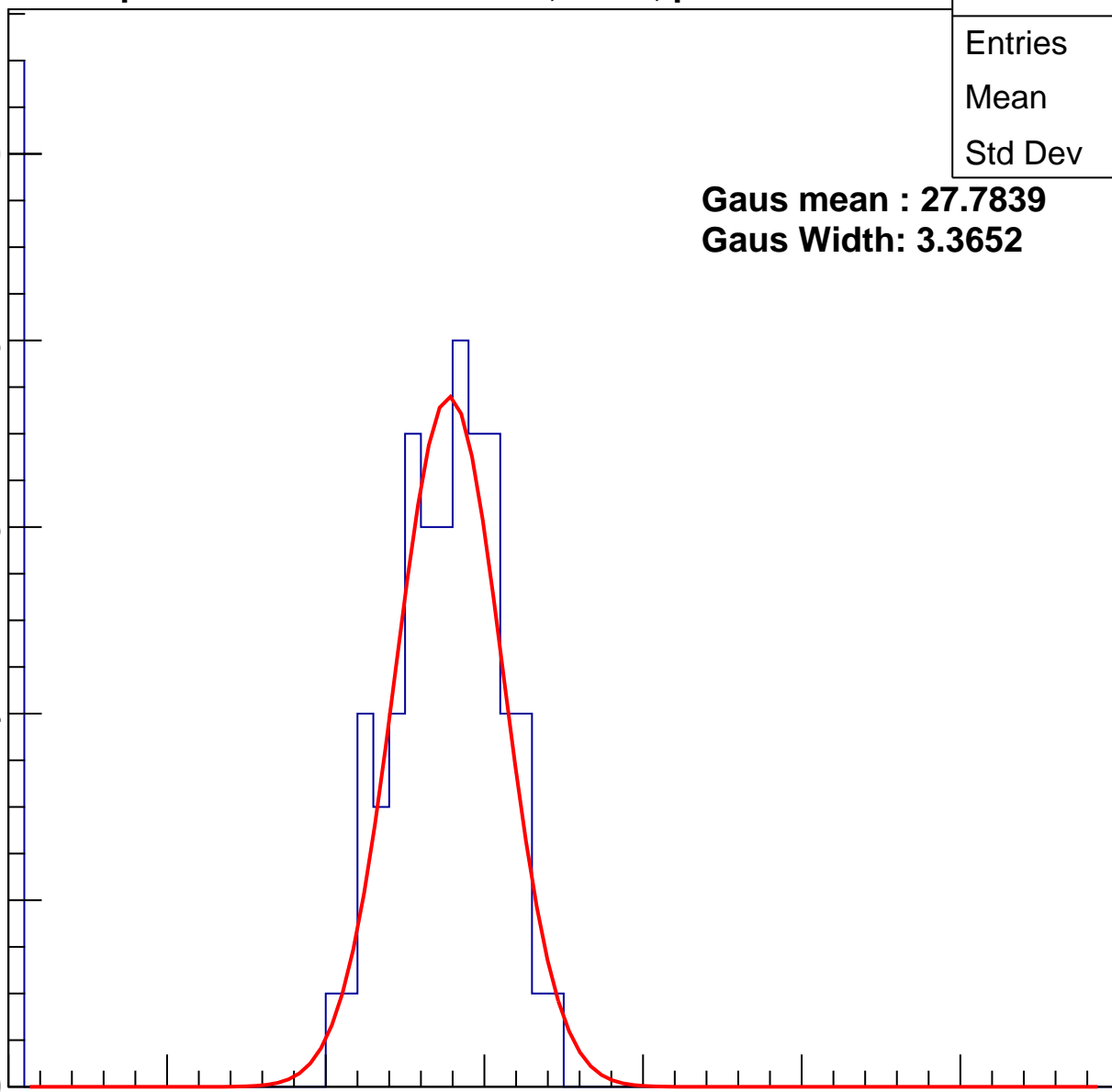
40

50

60

70

ampl



B1L103S, U6-ch90, adc1

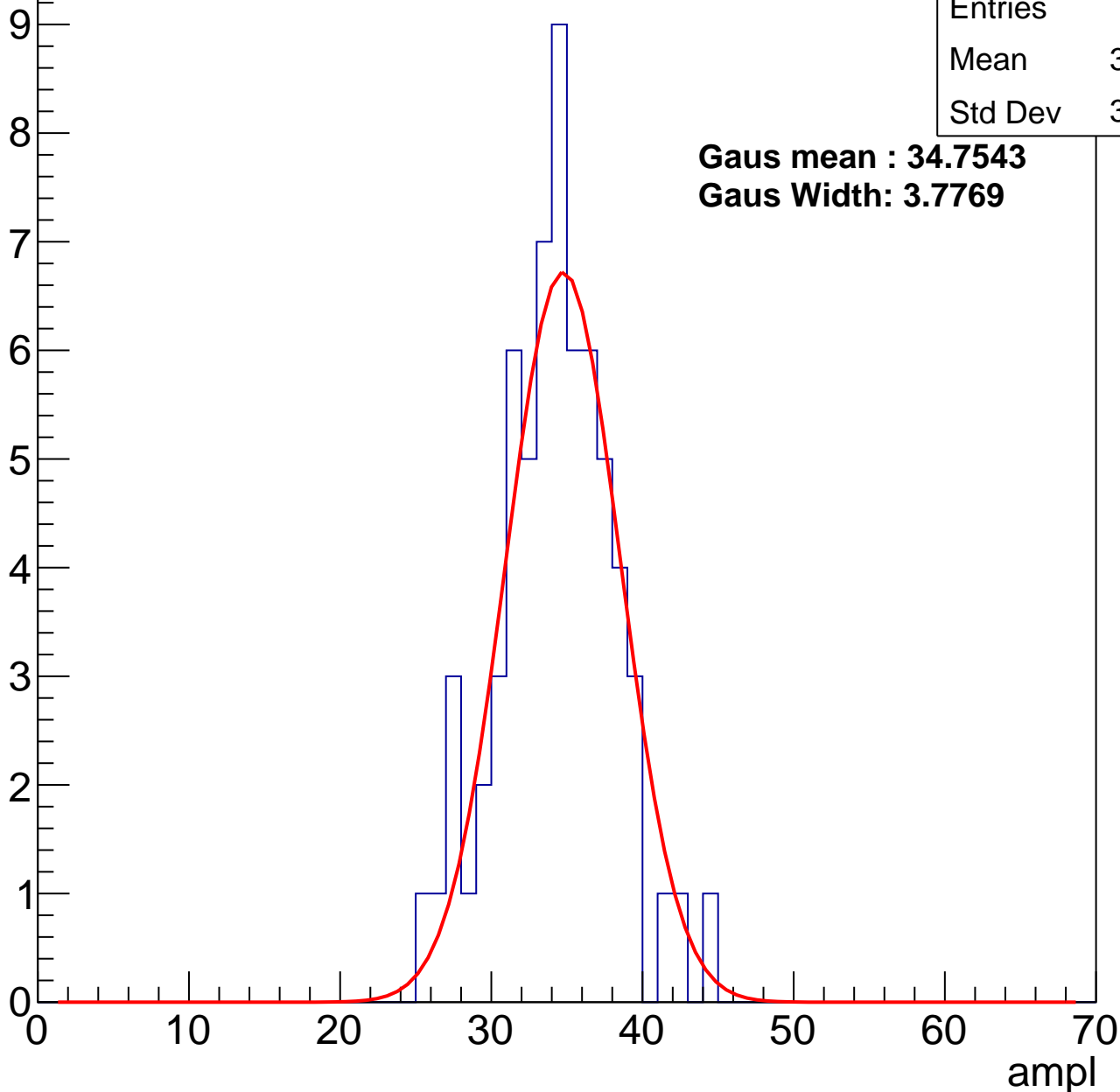
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	33.82
Std Dev	3.786

Gaus mean : 34.7543

Gaus Width: 3.7769



B1L103S, U6-ch90, adc2

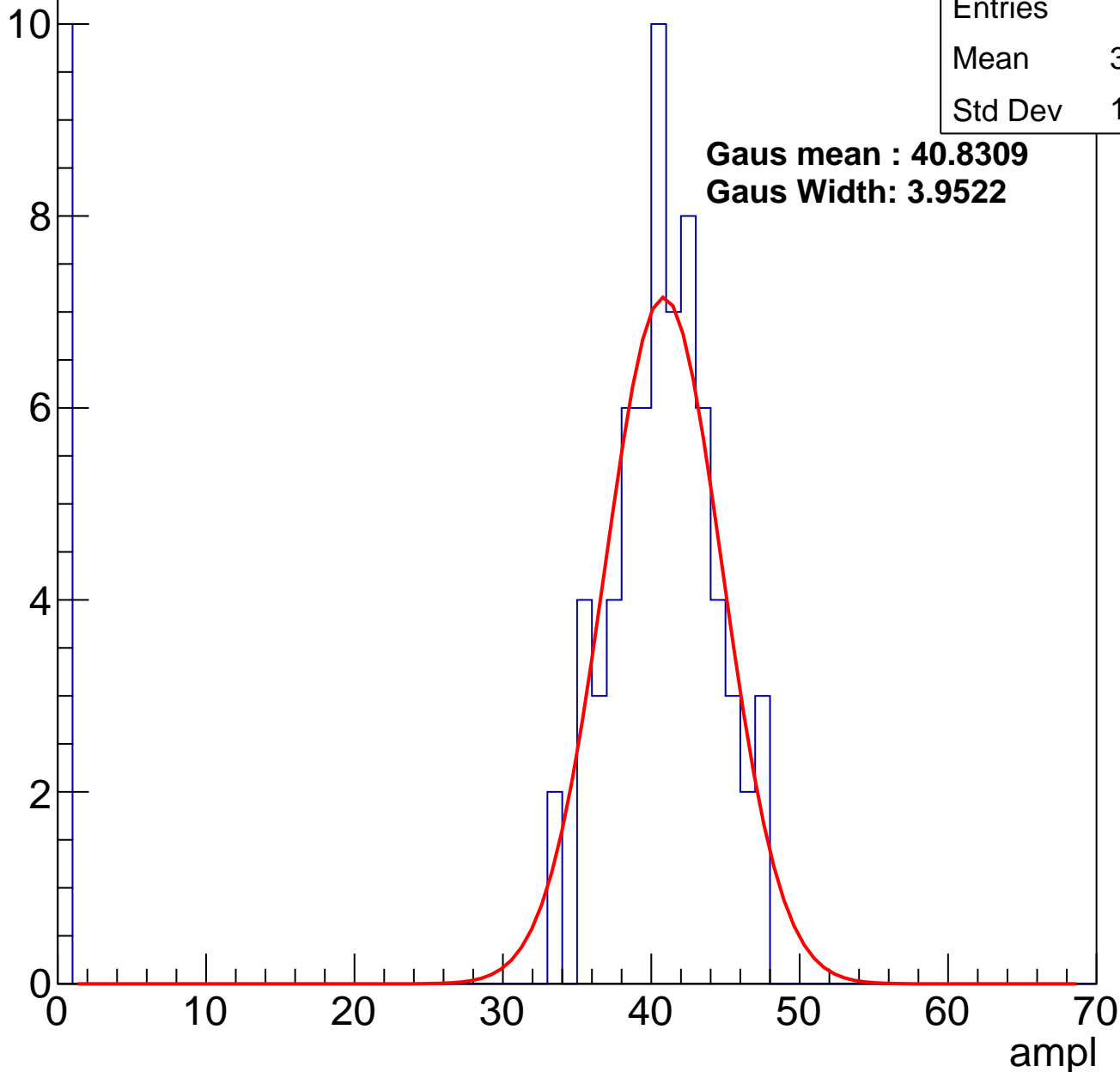
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	35.24
Std Dev	13.87

Gaus mean : 40.8309

Gaus Width: 3.9522

Entry

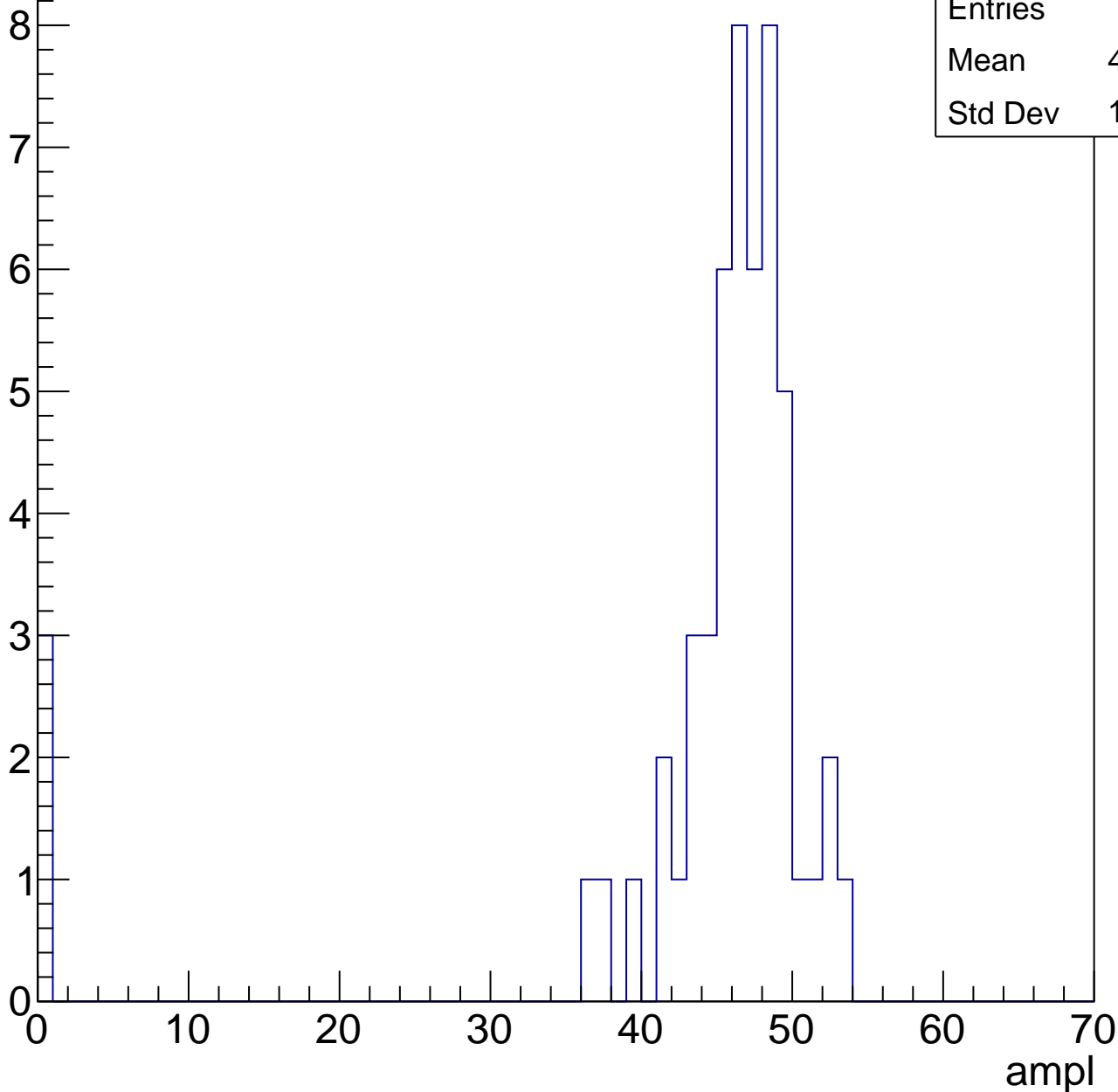


B1L103S, U6-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

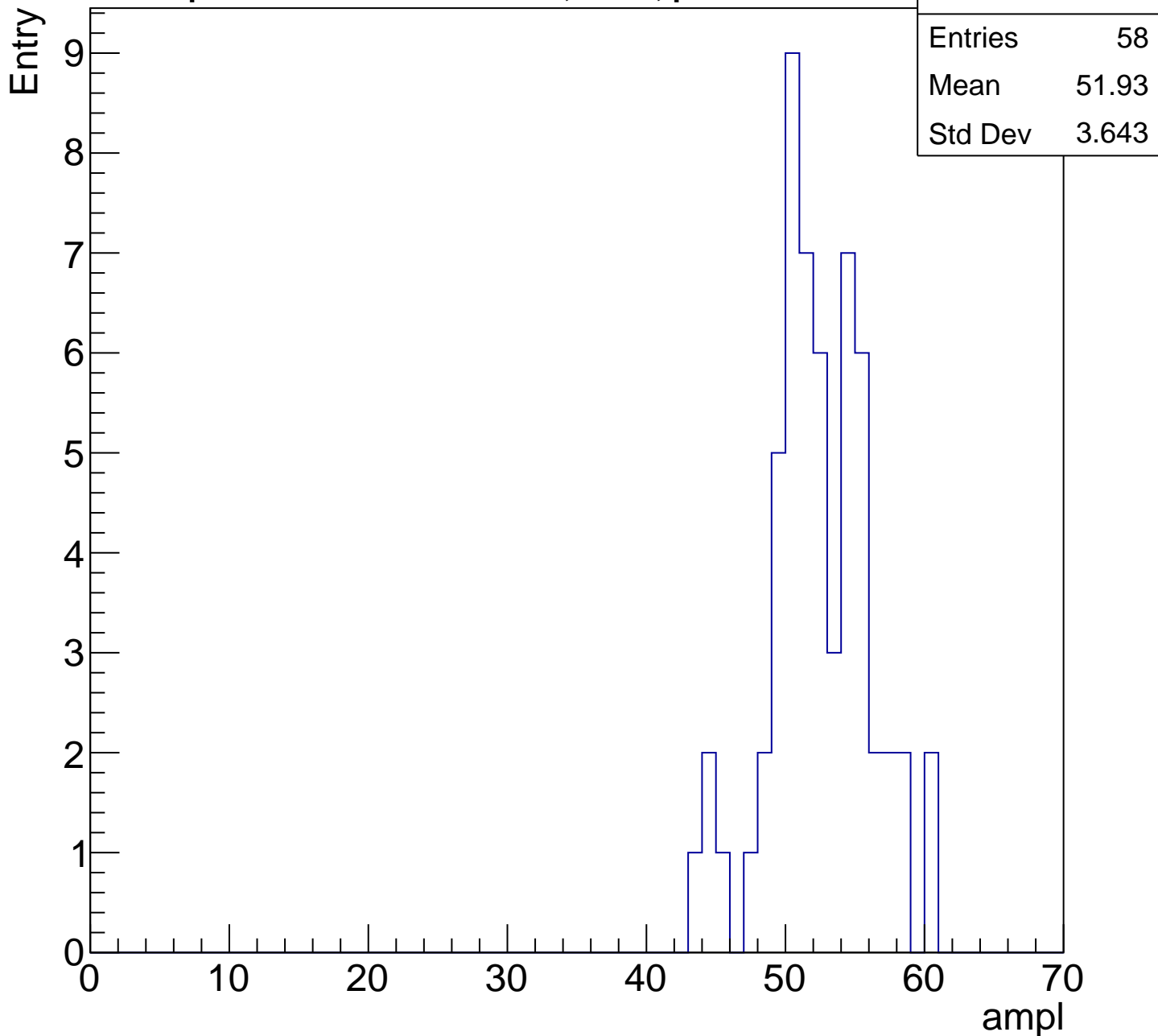
Entry

Entries	53
Mean	43.47
Std Dev	11.16



B1L103S, U6-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

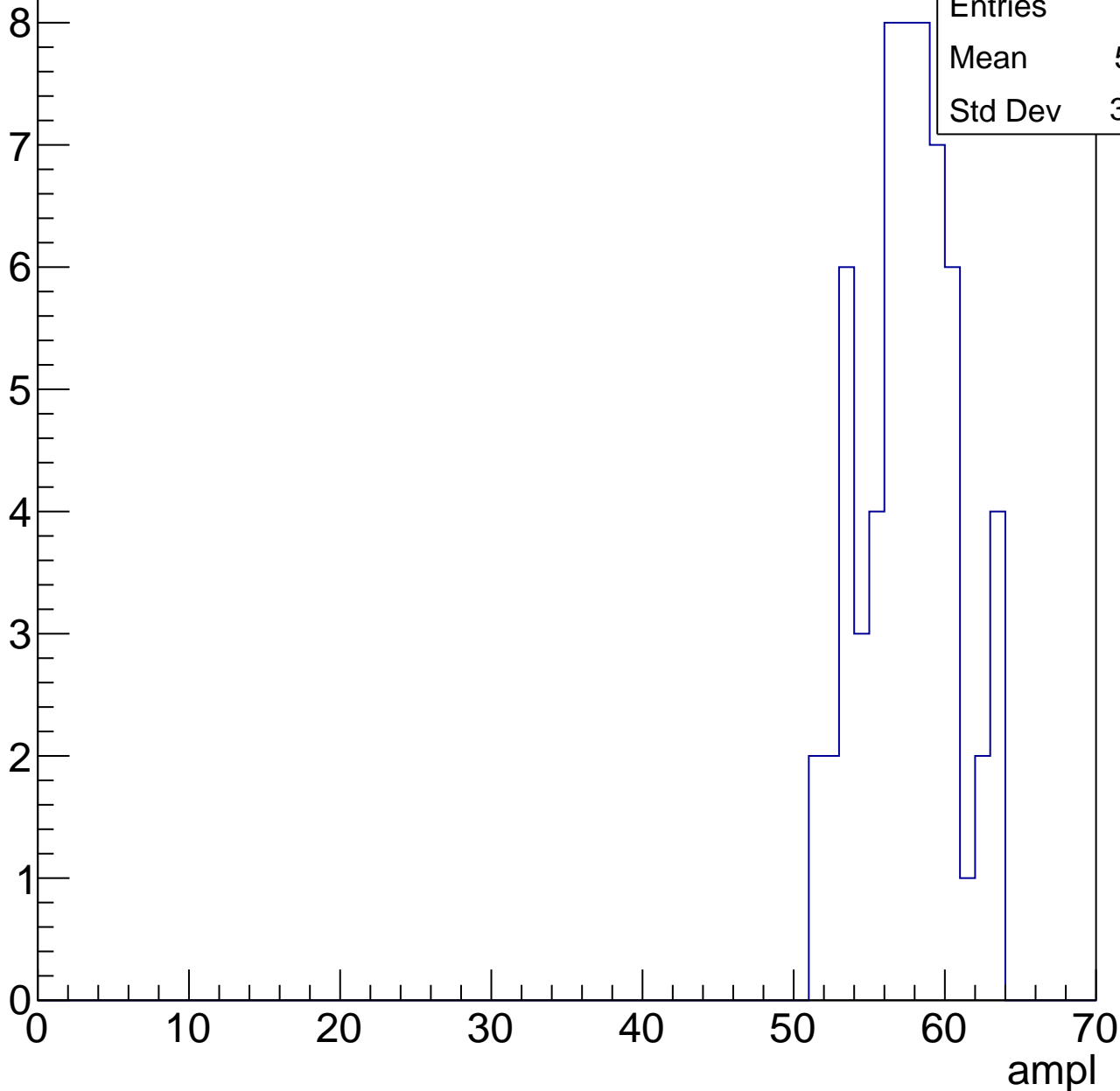


B1L103S, U6-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.11
Std Dev	3.052

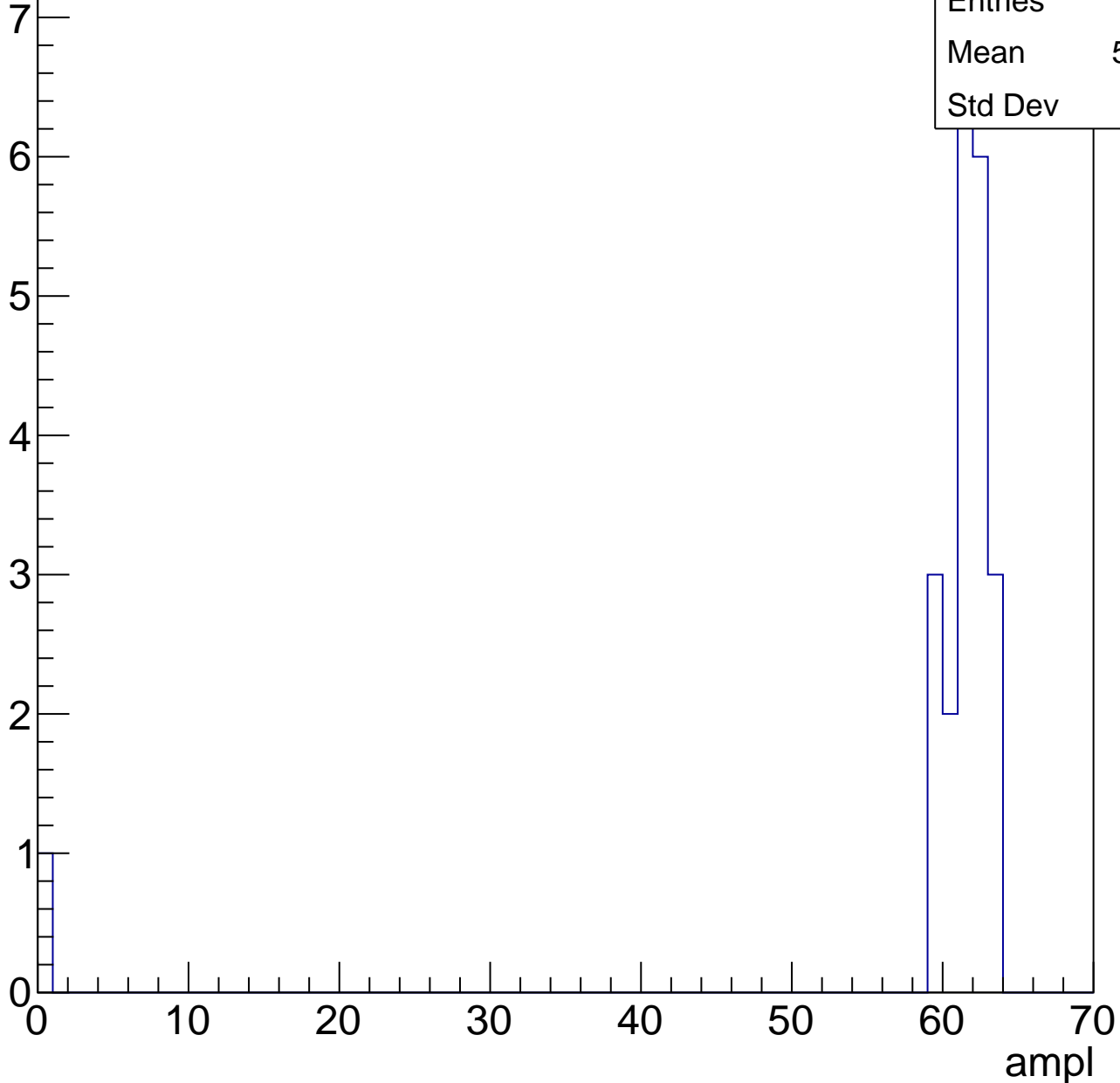


B1L103S, U6-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.41
Std Dev	12.8



B1L103S, U6-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	26
Mean	16.73
Std Dev	27.57

ampl

0 10 20 30 40 50 60 70

B1L103S, U6-ch91, adc0

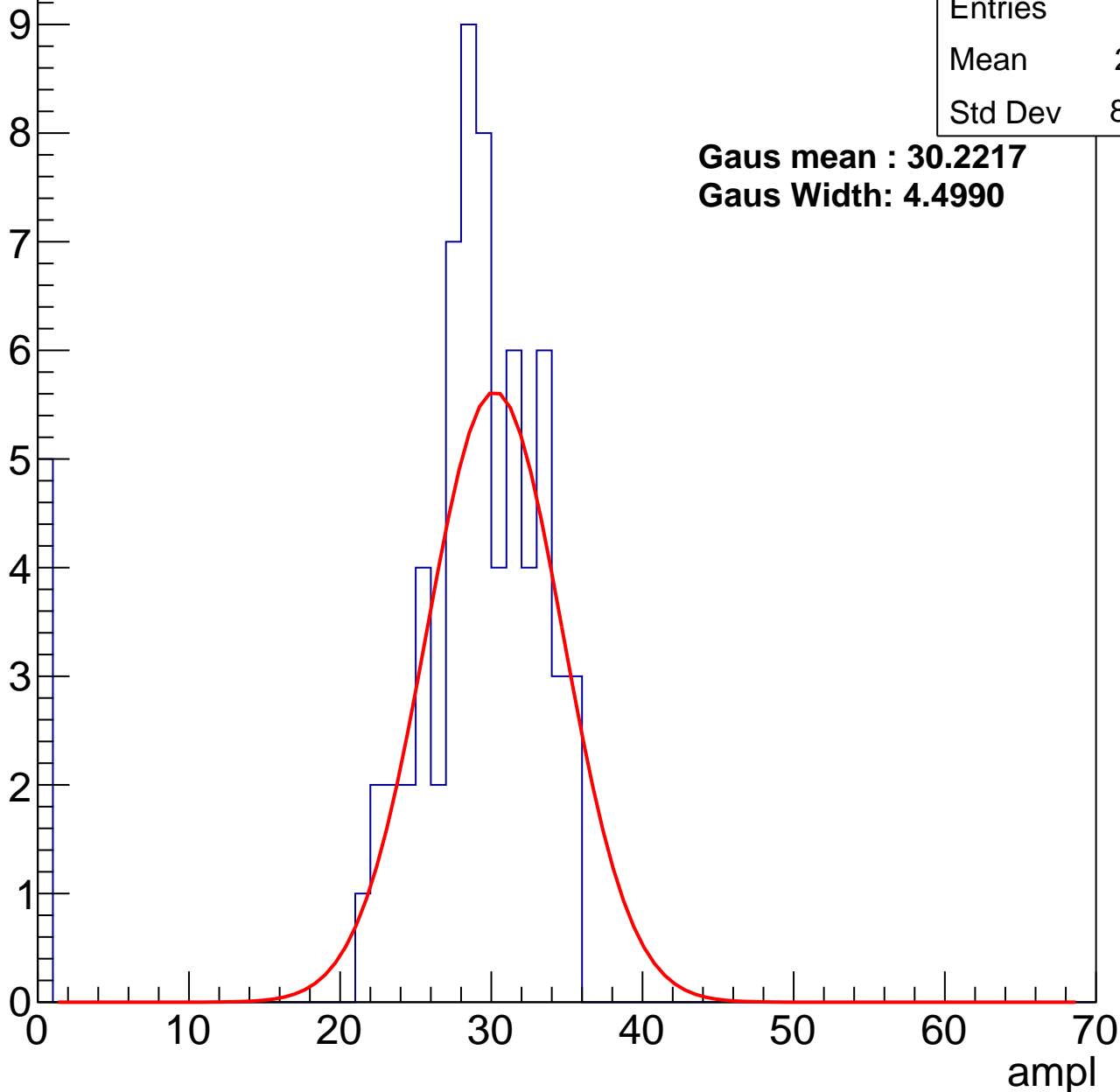
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	26.81
Std Dev	8.247

Gaus mean : 30.2217

Gaus Width: 4.4990



B1L103S, U6-ch91, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	35.12
Std Dev	6.575

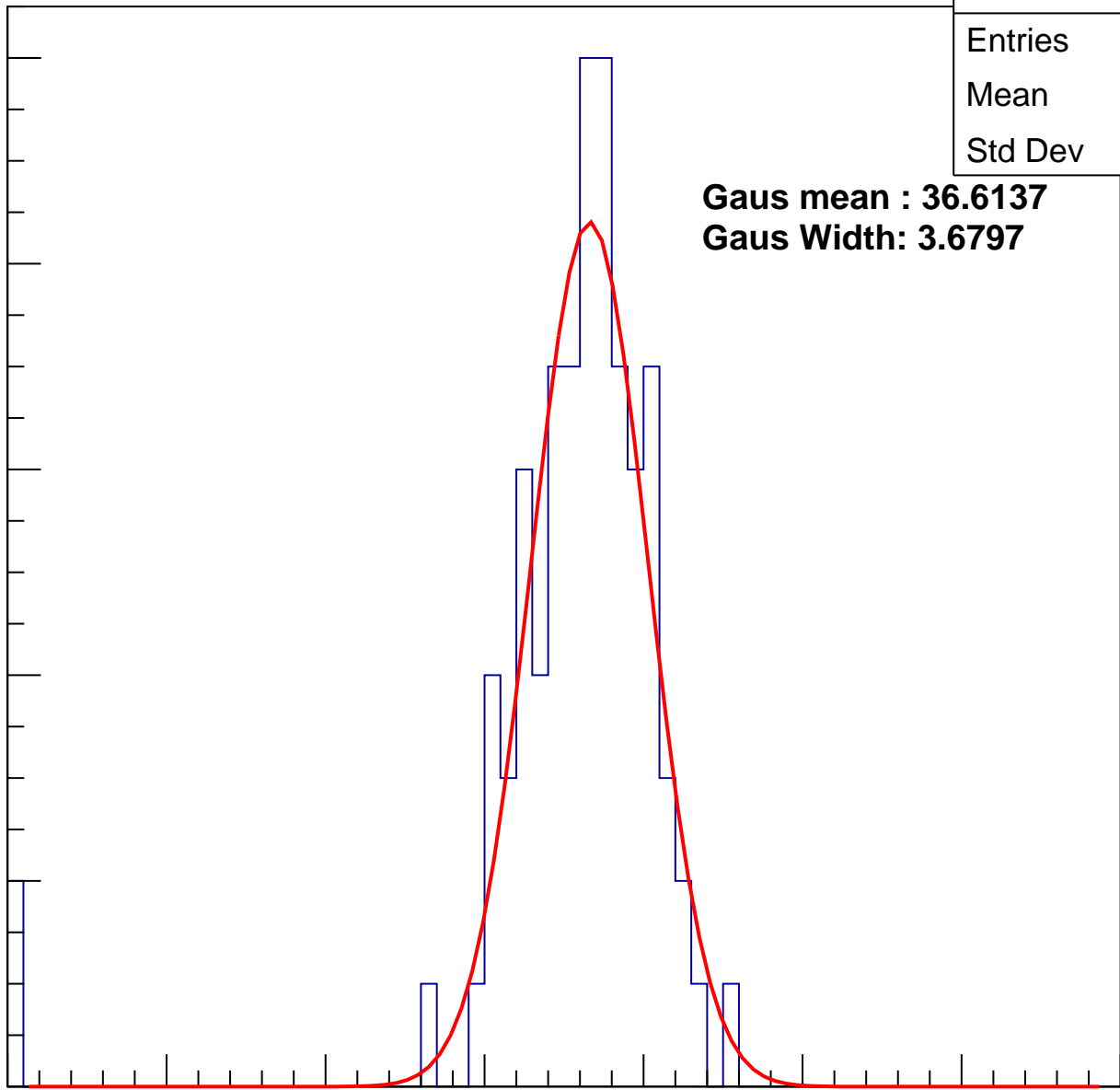
Gaus mean : 36.6137
Gaus Width: 3.6797

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch91, adc2

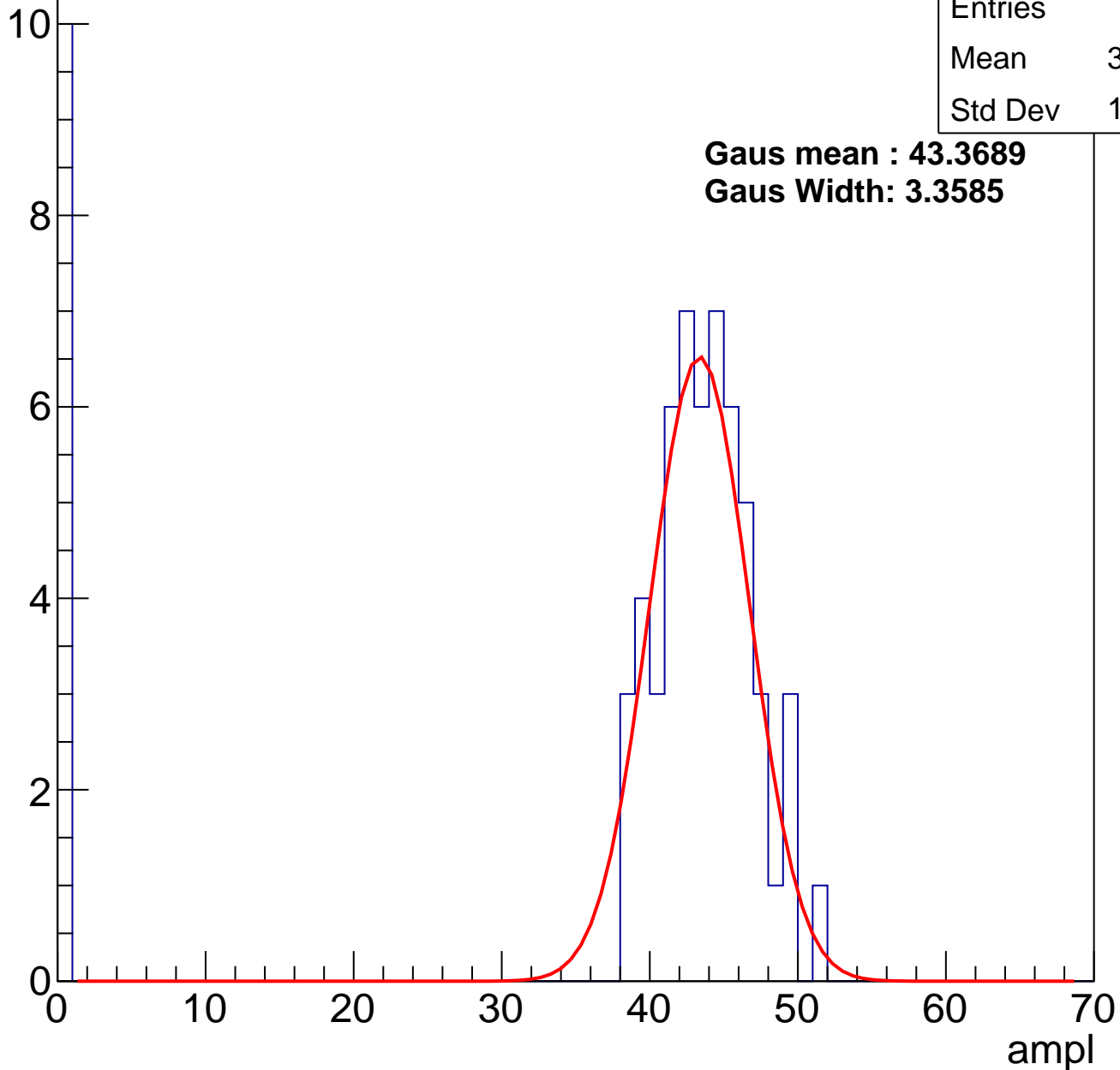
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	36.66
Std Dev	15.88

Gaus mean : 43.3689

Gaus Width: 3.3585

Entry

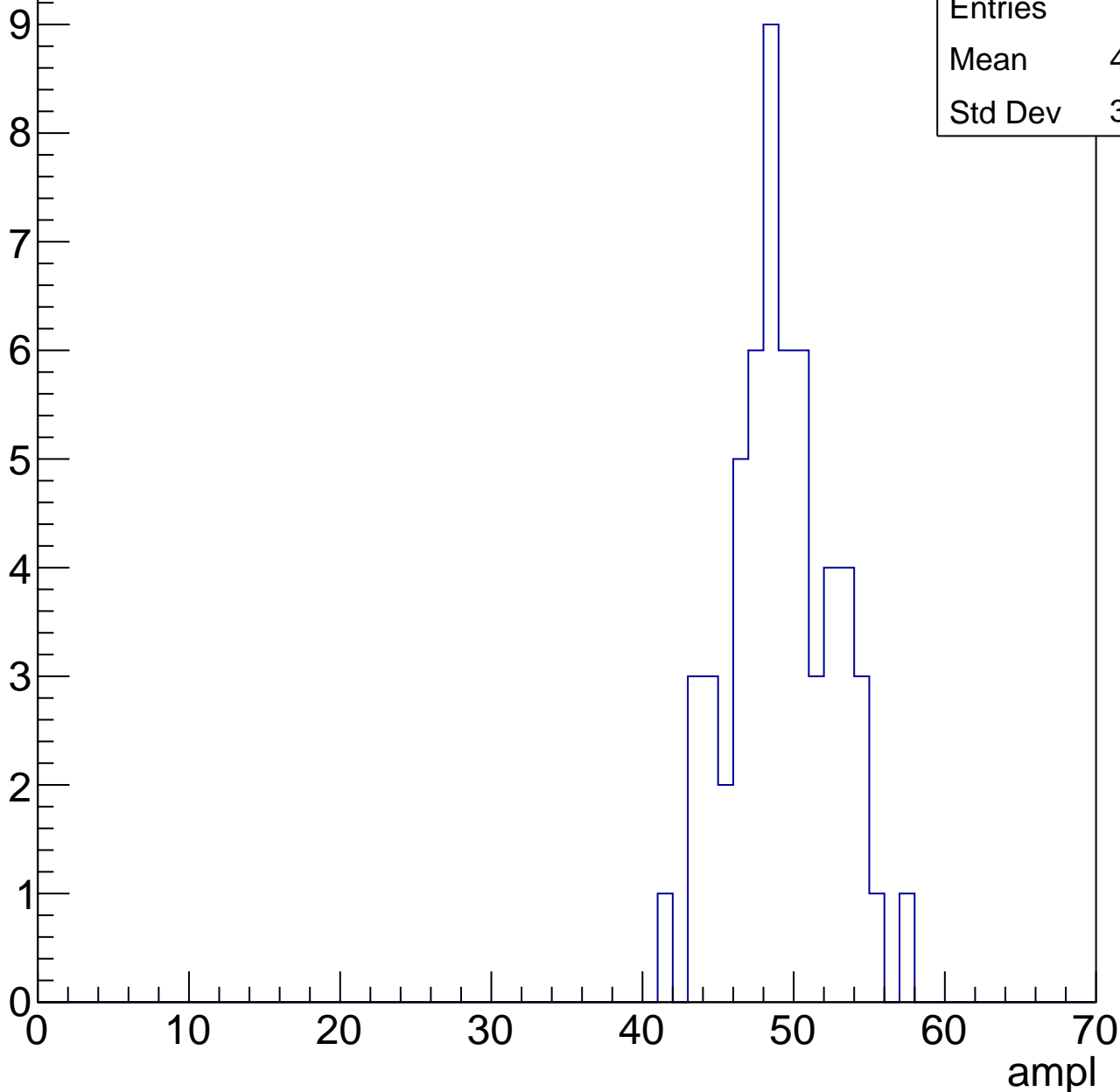


B1L103S, U6-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

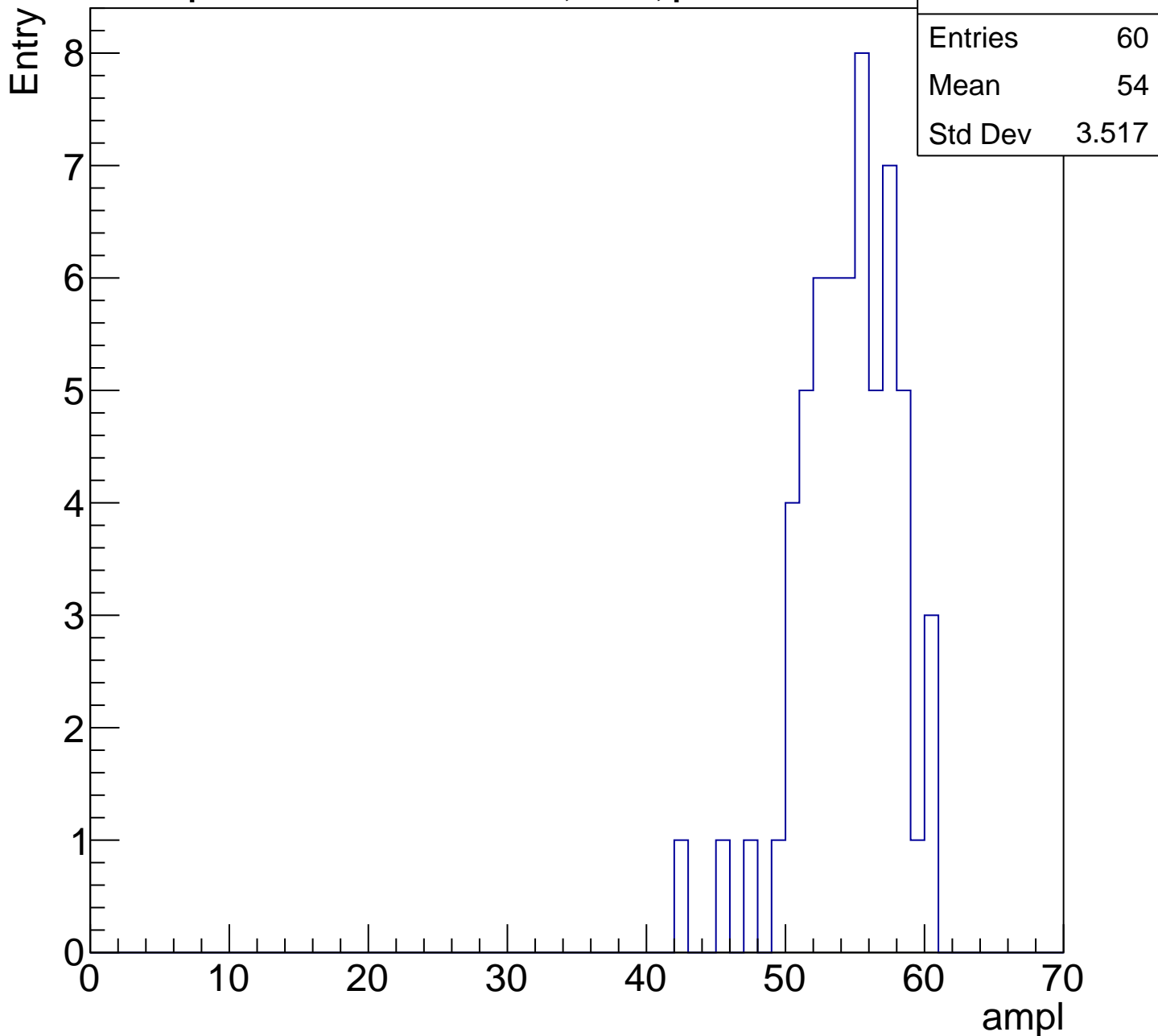
Entry

Entries	57
Mean	48.72
Std Dev	3.365



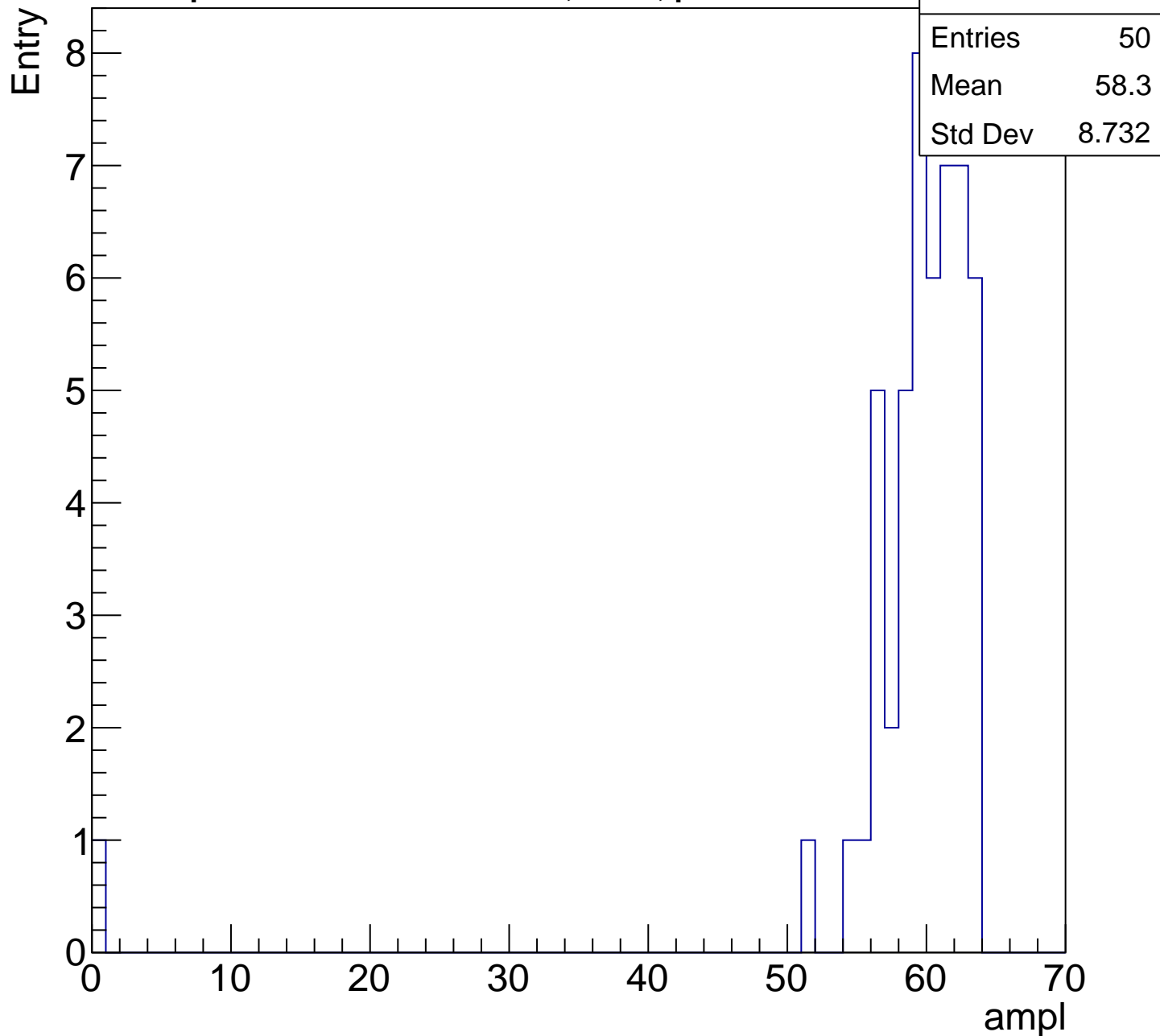
B1L103S, U6-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch91, adc5

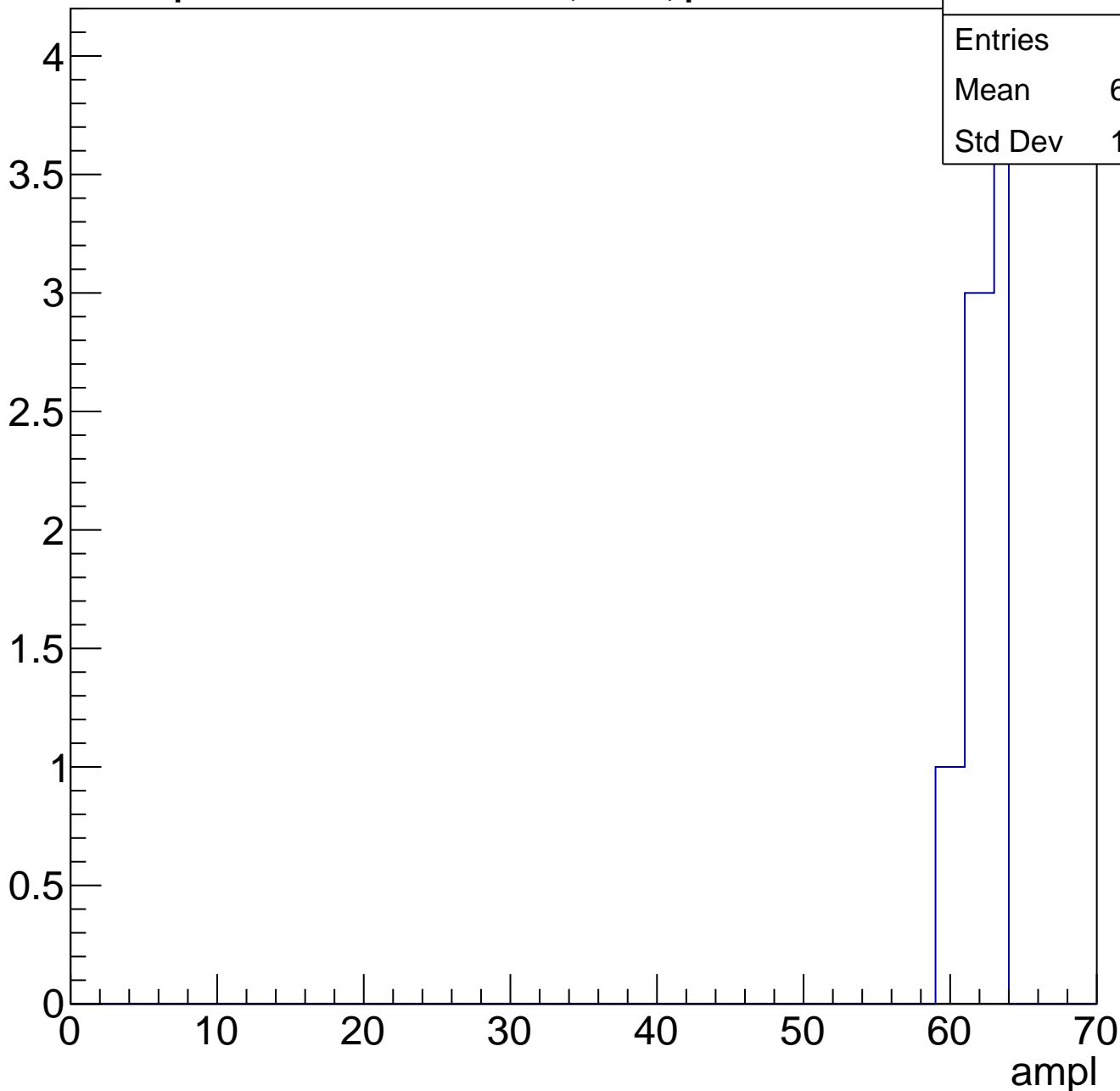
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

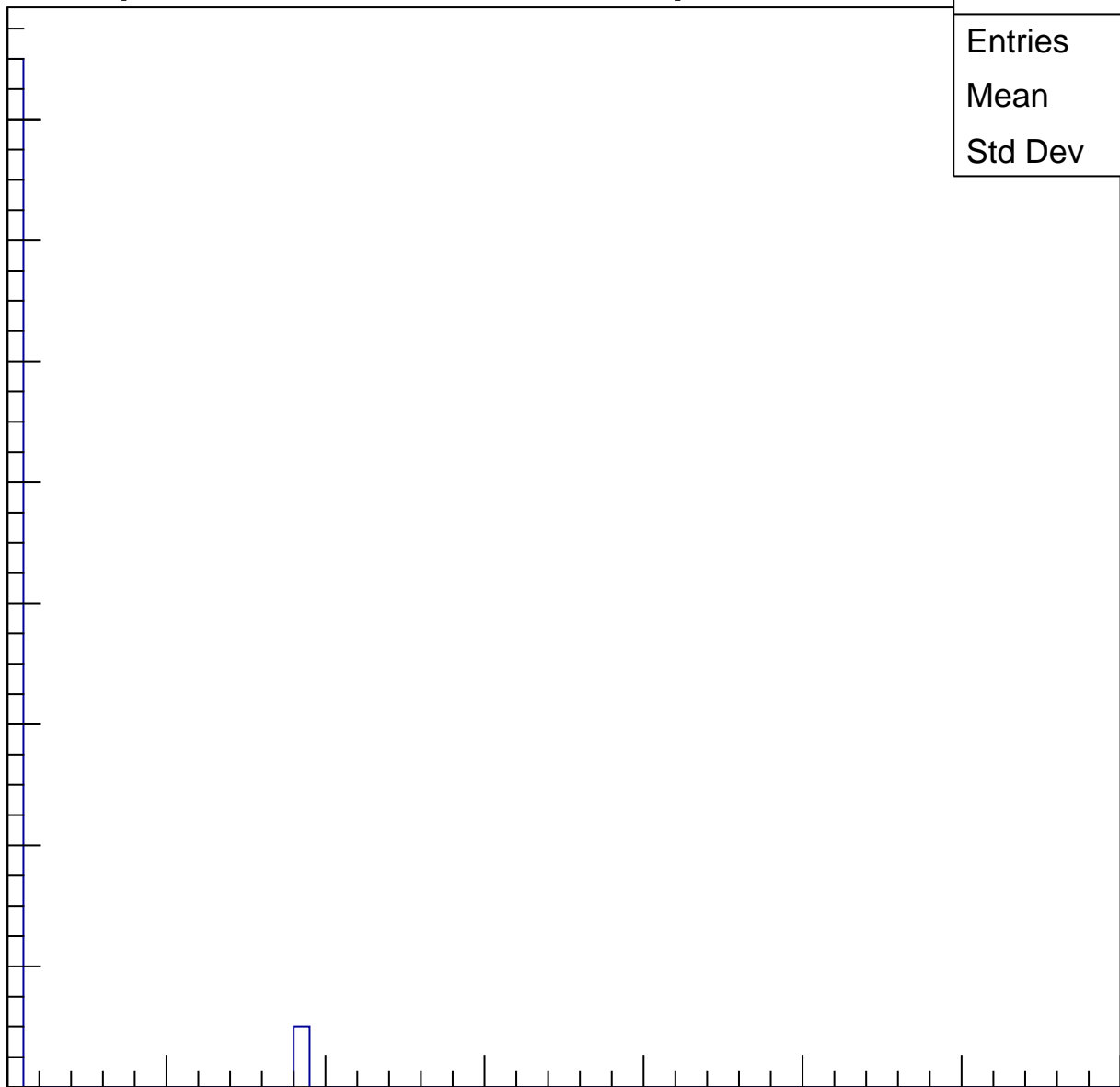
Entries	18
Mean	1
Std Dev	4.123

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	21.53
Std Dev	10.35

Gaus mean : 26.2282

Gaus Width: 3.0201

Entry

14

12

10

8

6

4

2

0

0

10

20

30

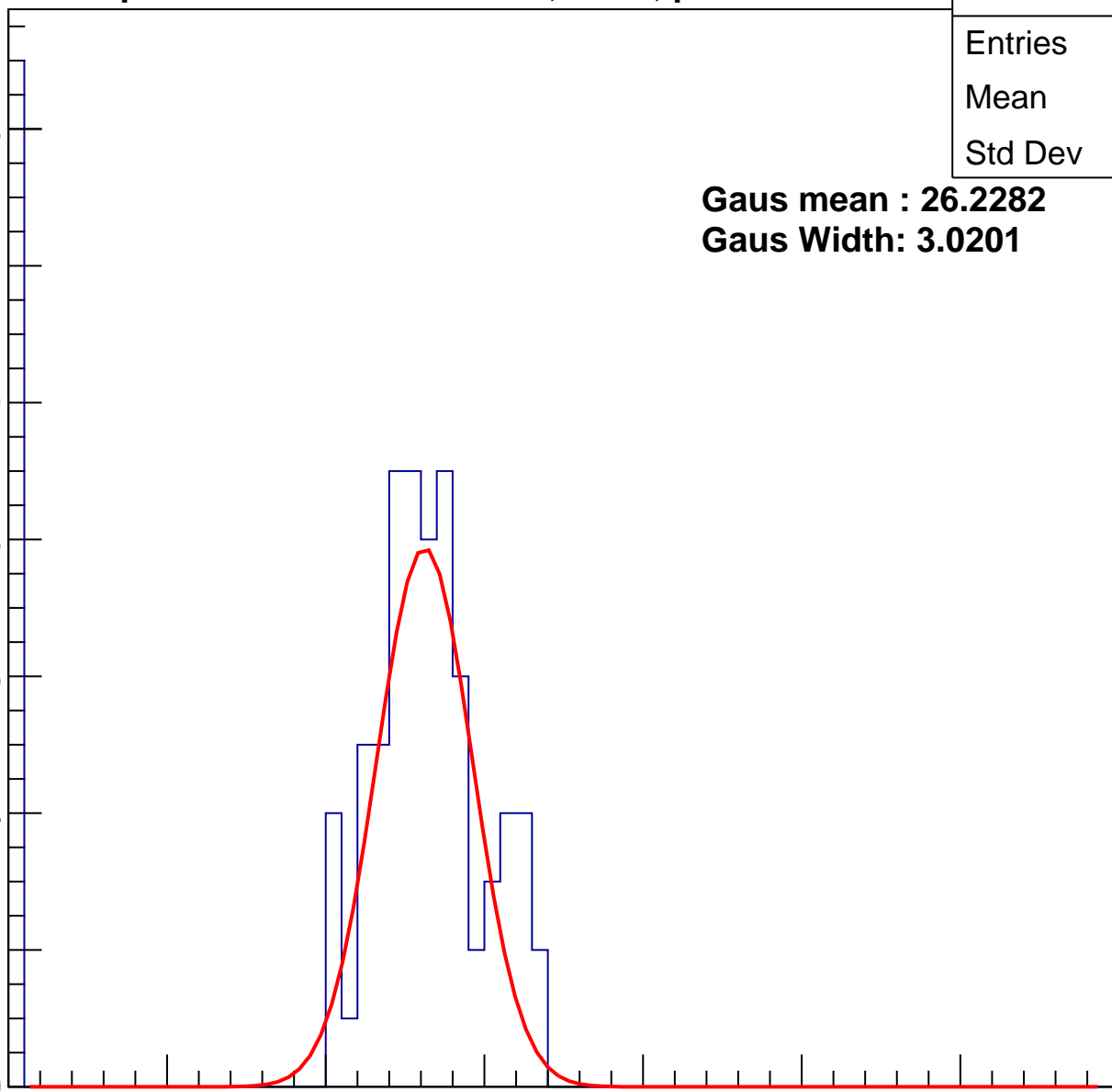
40

50

60

70

ampl



B1L103S, U6-ch92, adc1

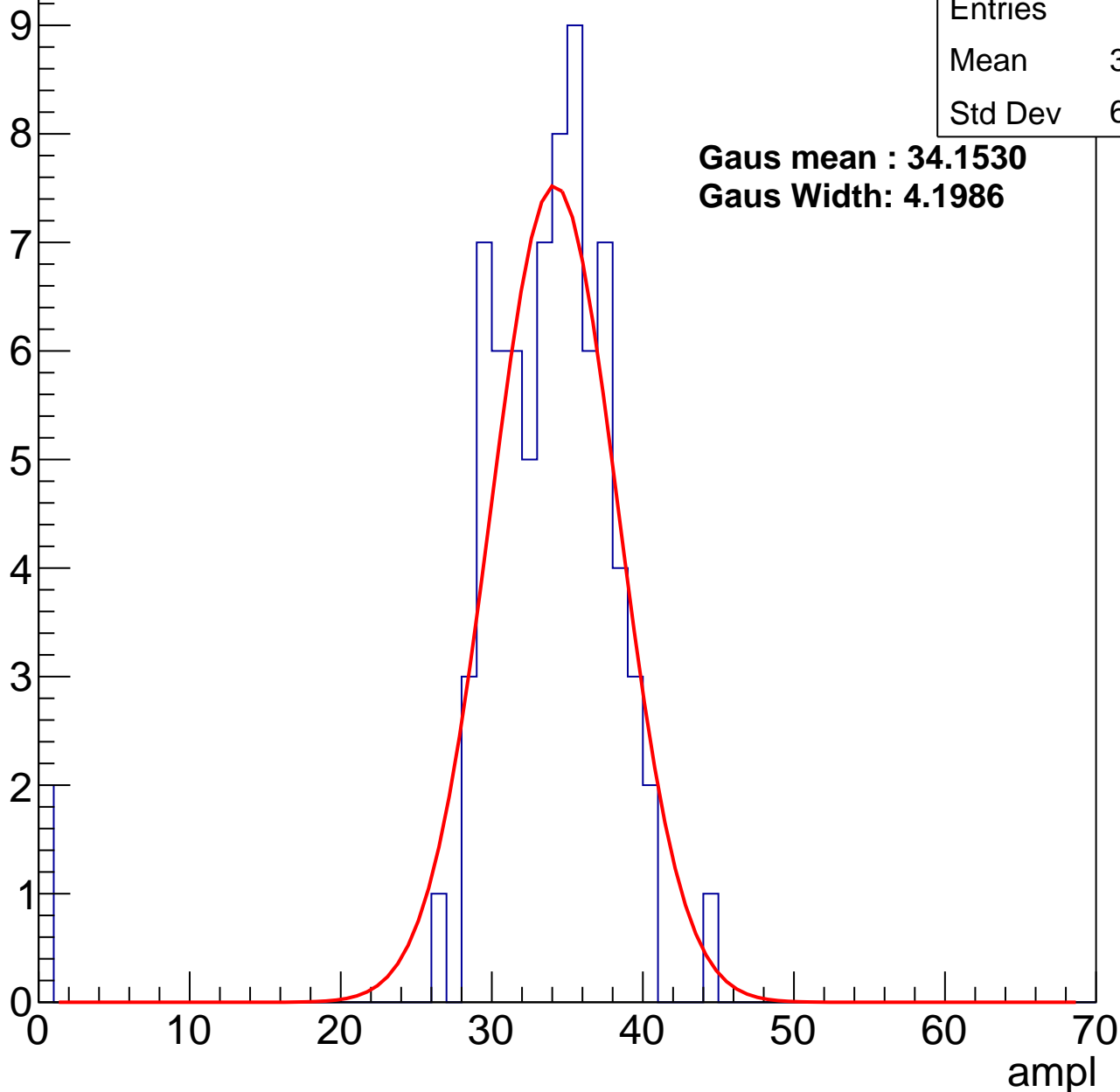
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	32.79
Std Dev	6.376

Gaus mean : 34.1530

Gaus Width: 4.1986



B1L103S, U6-ch92, adc2

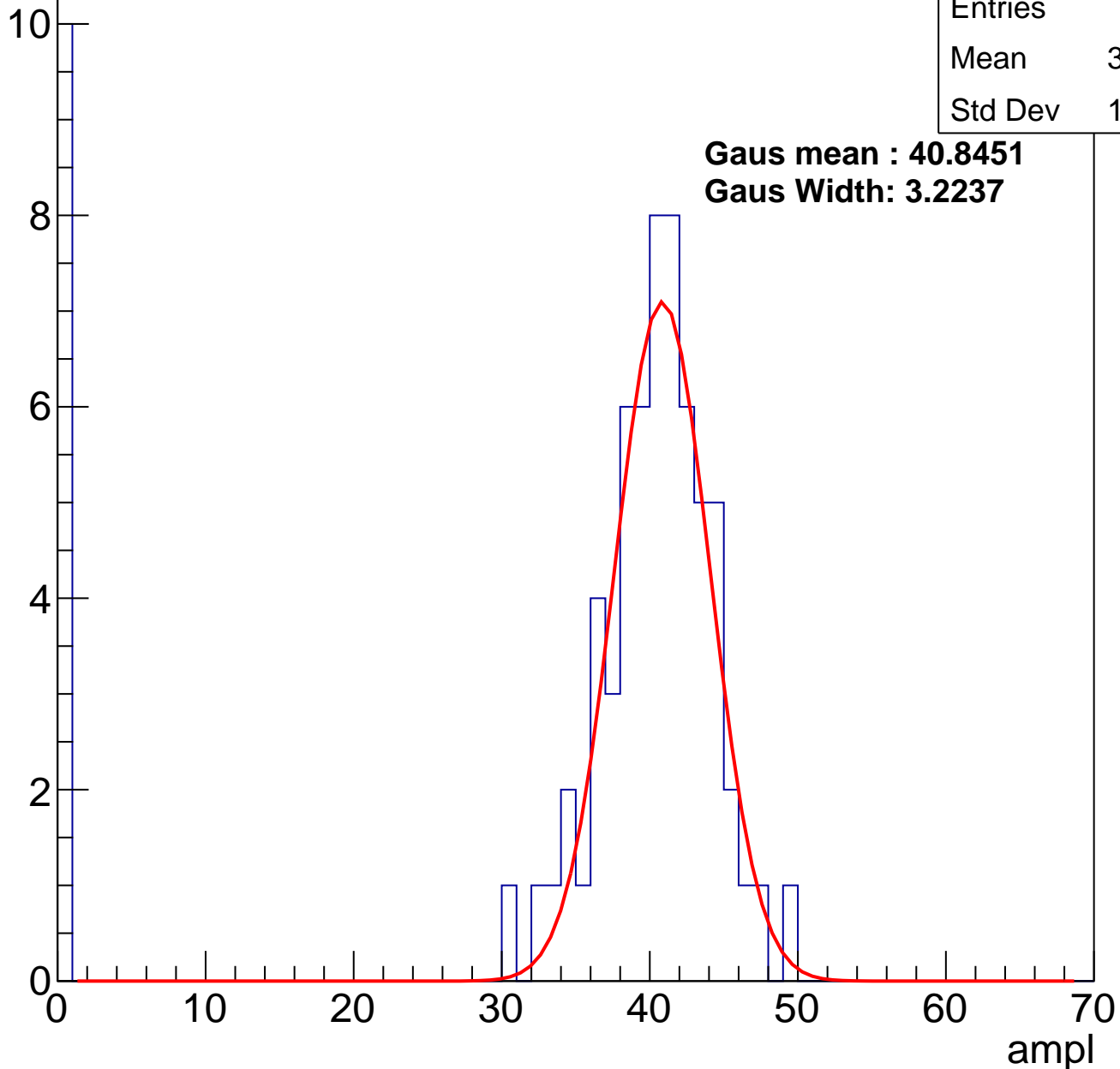
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.47
Std Dev	14.24

Gaus mean : 40.8451

Gaus Width: 3.2237

Entry

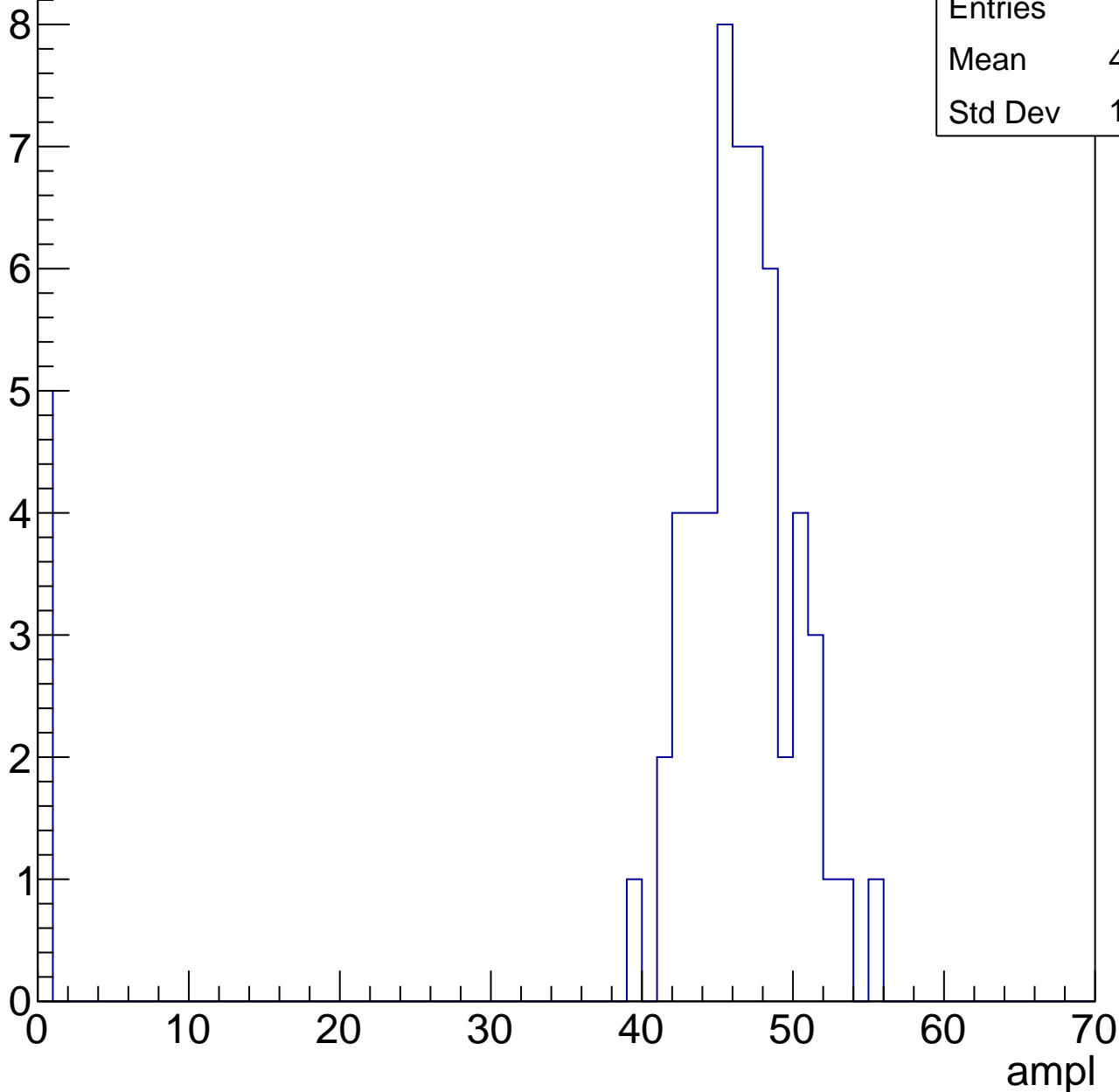


B1L103S, U6-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	42.45
Std Dev	13.17

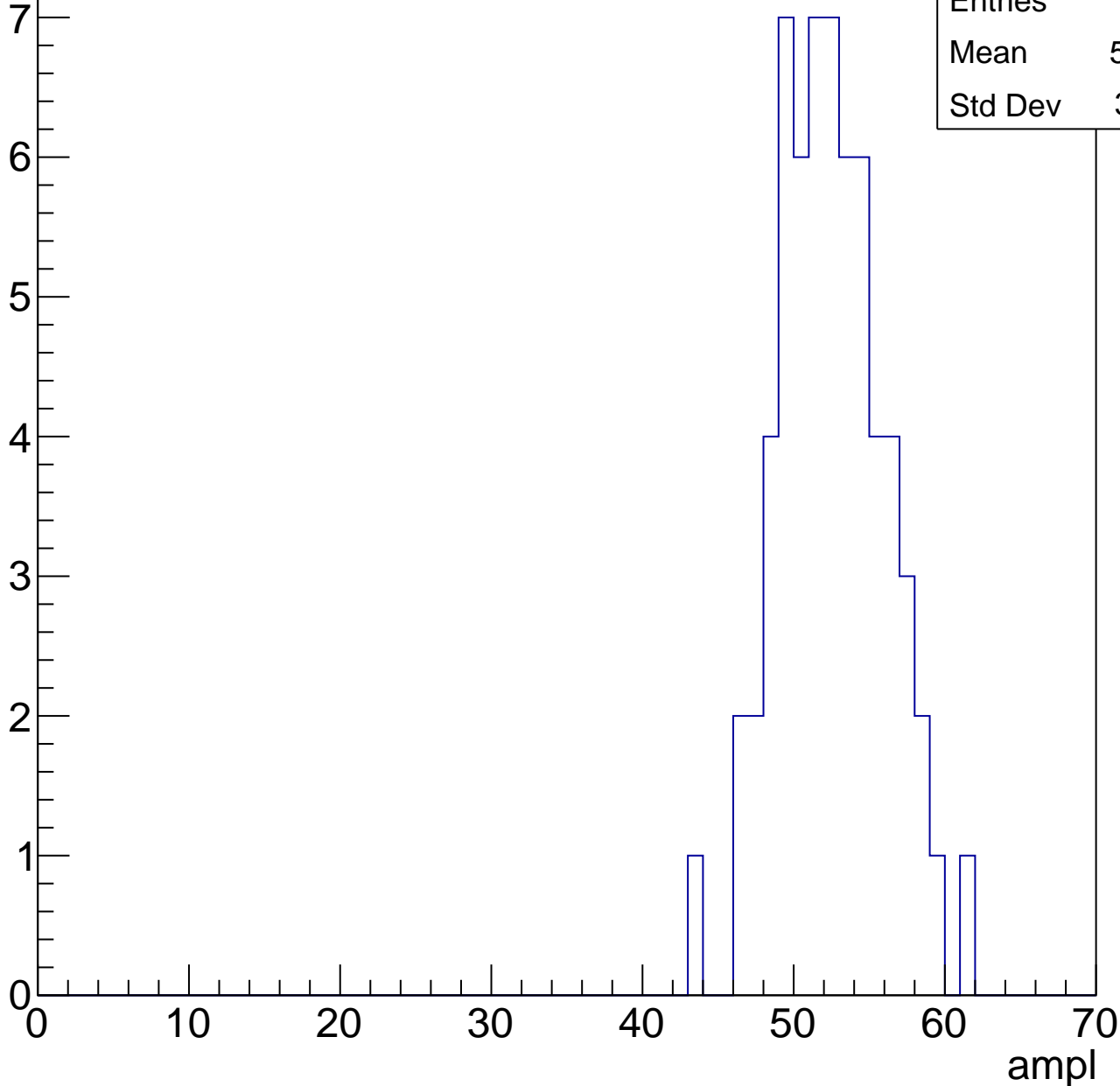


B1L103S, U6-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

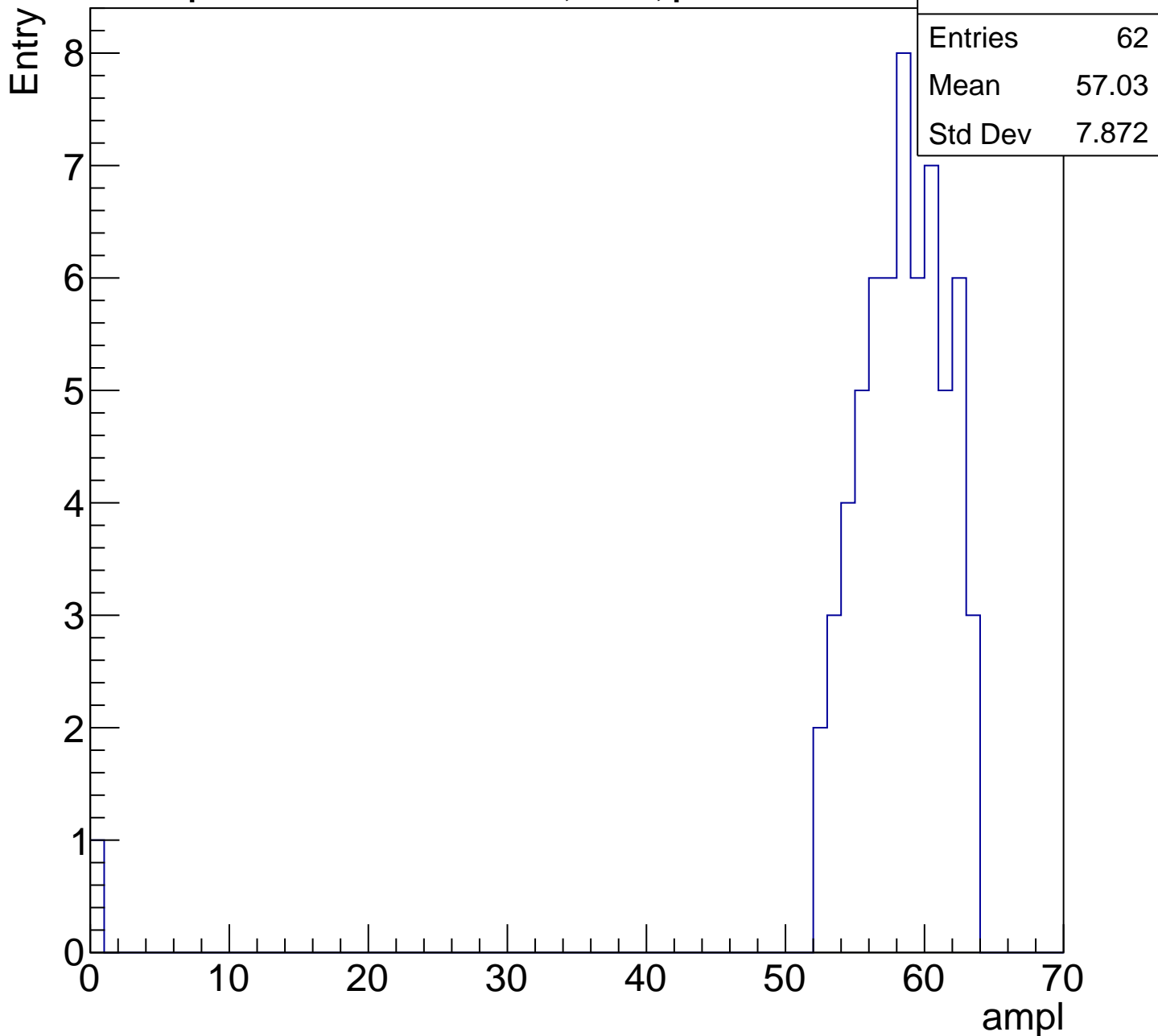
Entry

Entries	63
Mean	52.03
Std Dev	3.491



B1L103S, U6-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

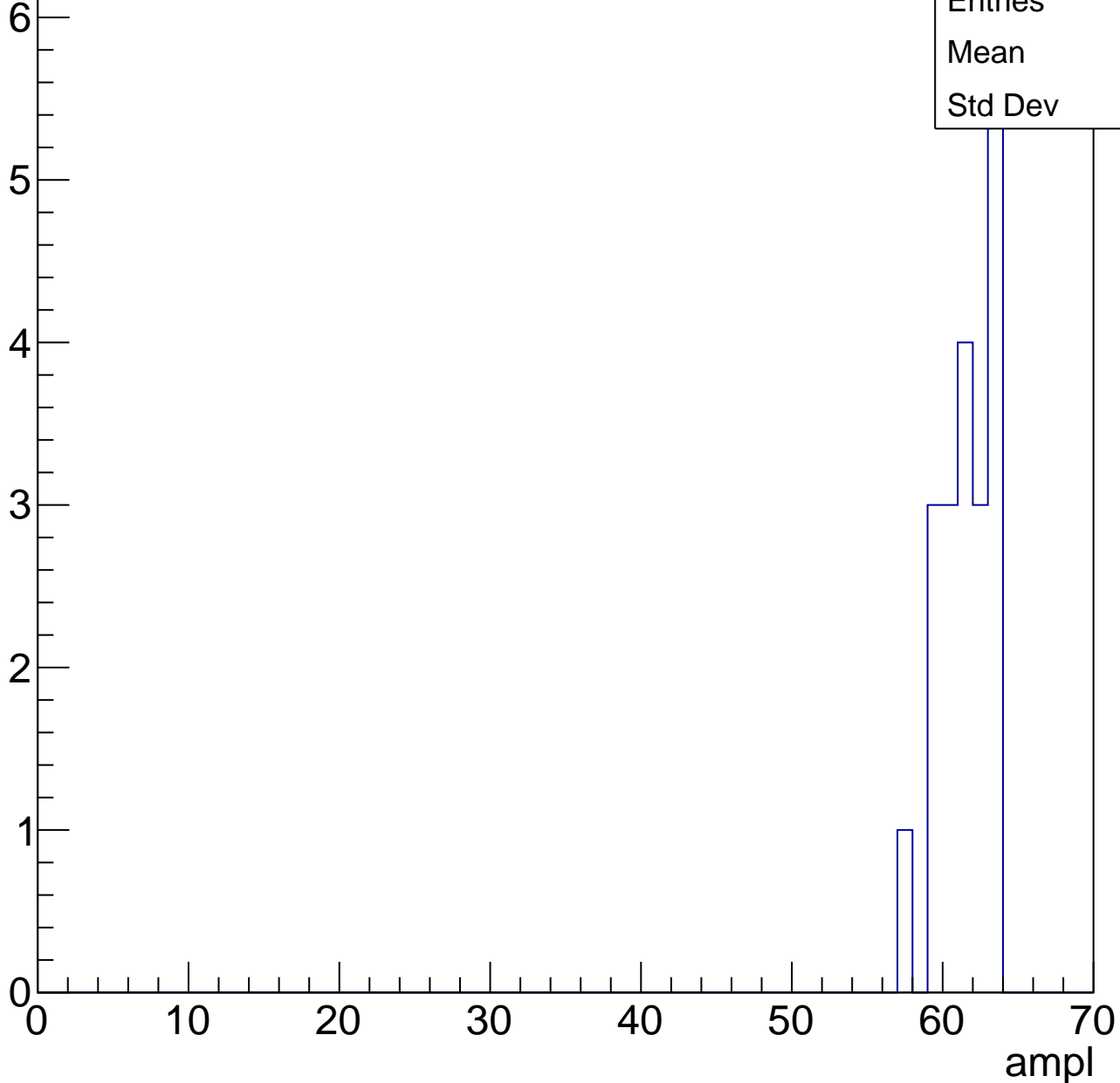


B1L103S, U6-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

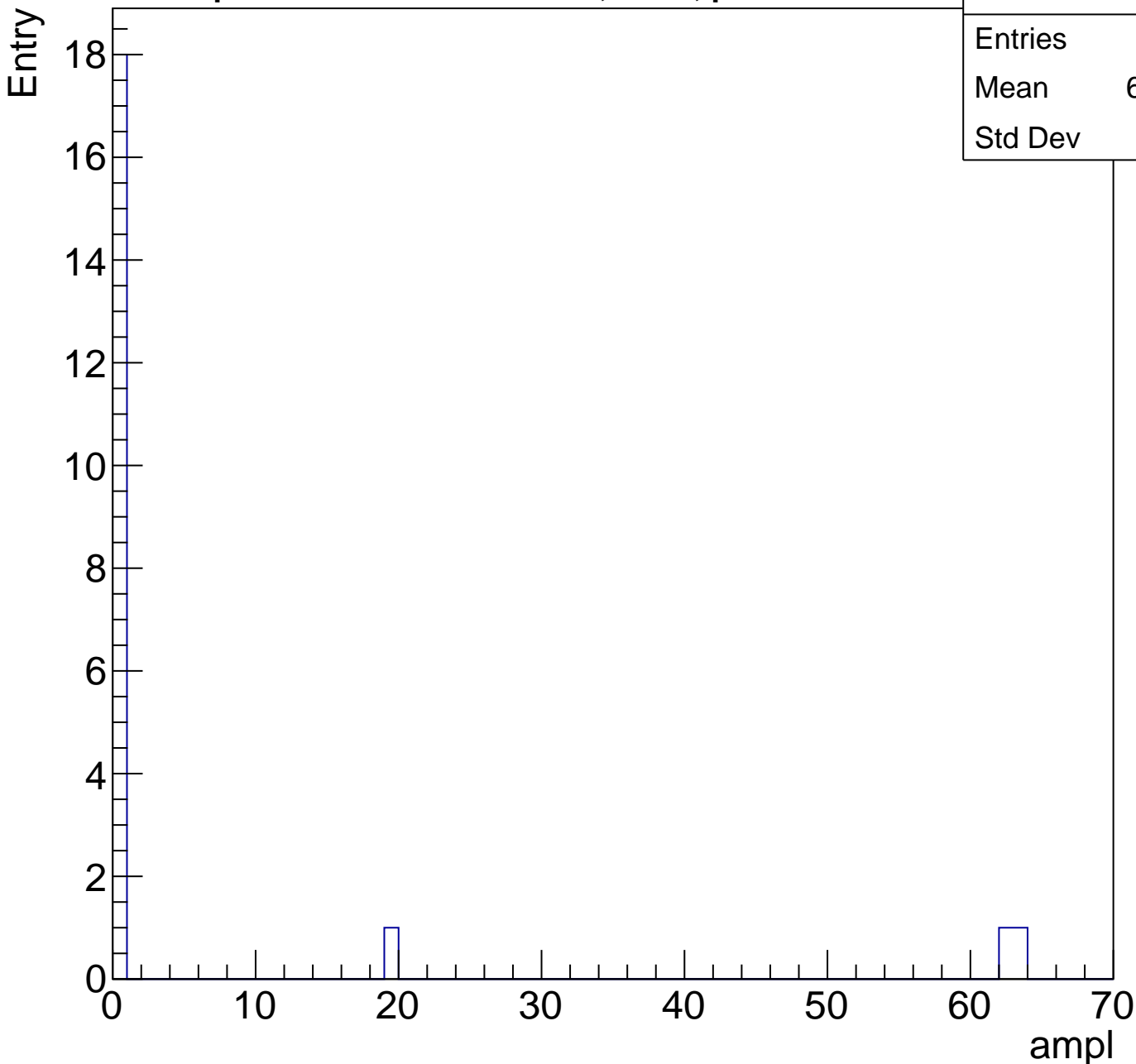
Entries	20
Mean	61.1
Std Dev	1.7



B1L103S, U6-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.857
Std Dev	18.5



B1L103S, U6-ch93, adc0

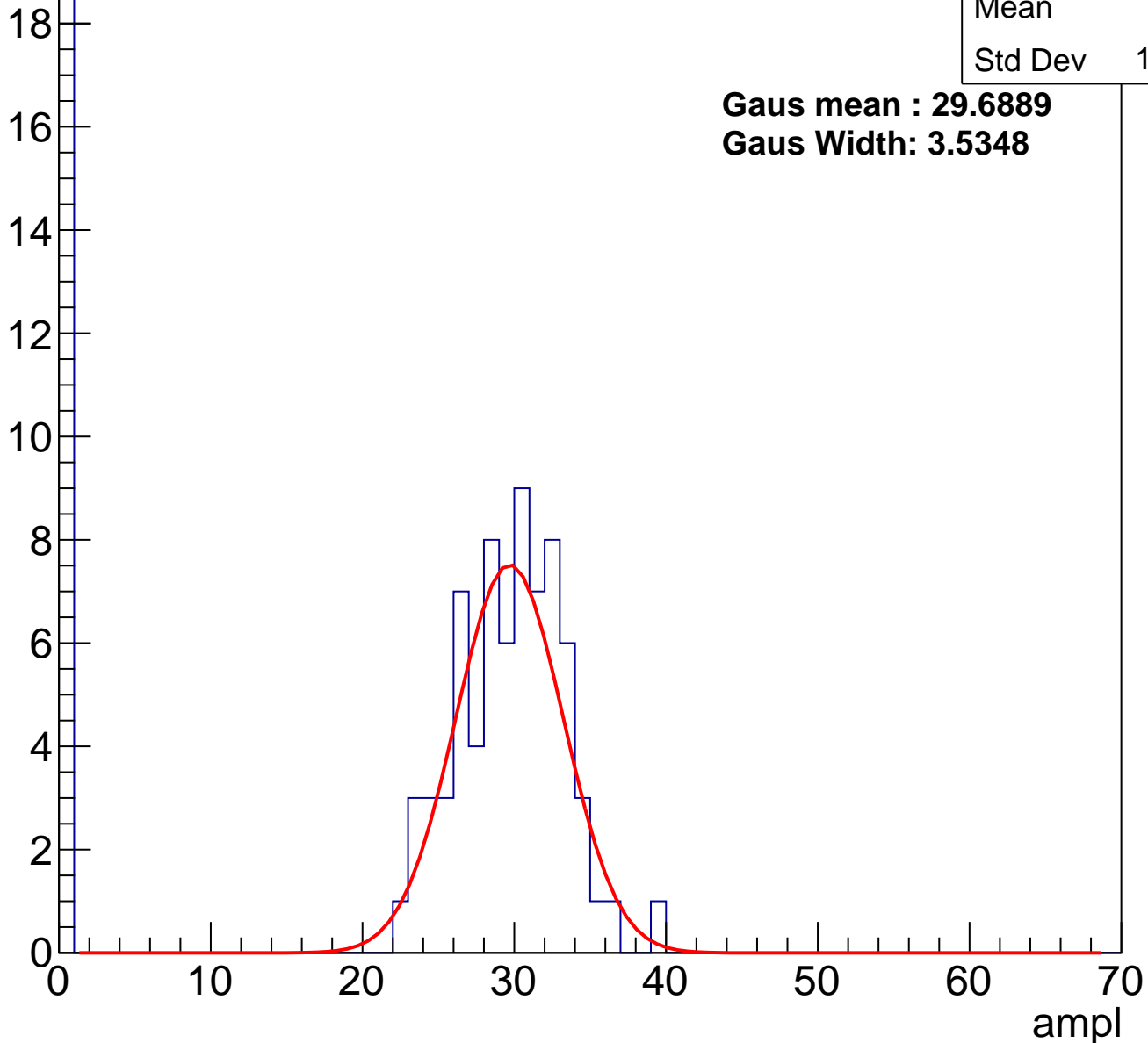
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	23.1
Std Dev	12.33

Gaus mean : 29.6889

Gaus Width: 3.5348

Entry



B1L103S, U6-ch93, adc1

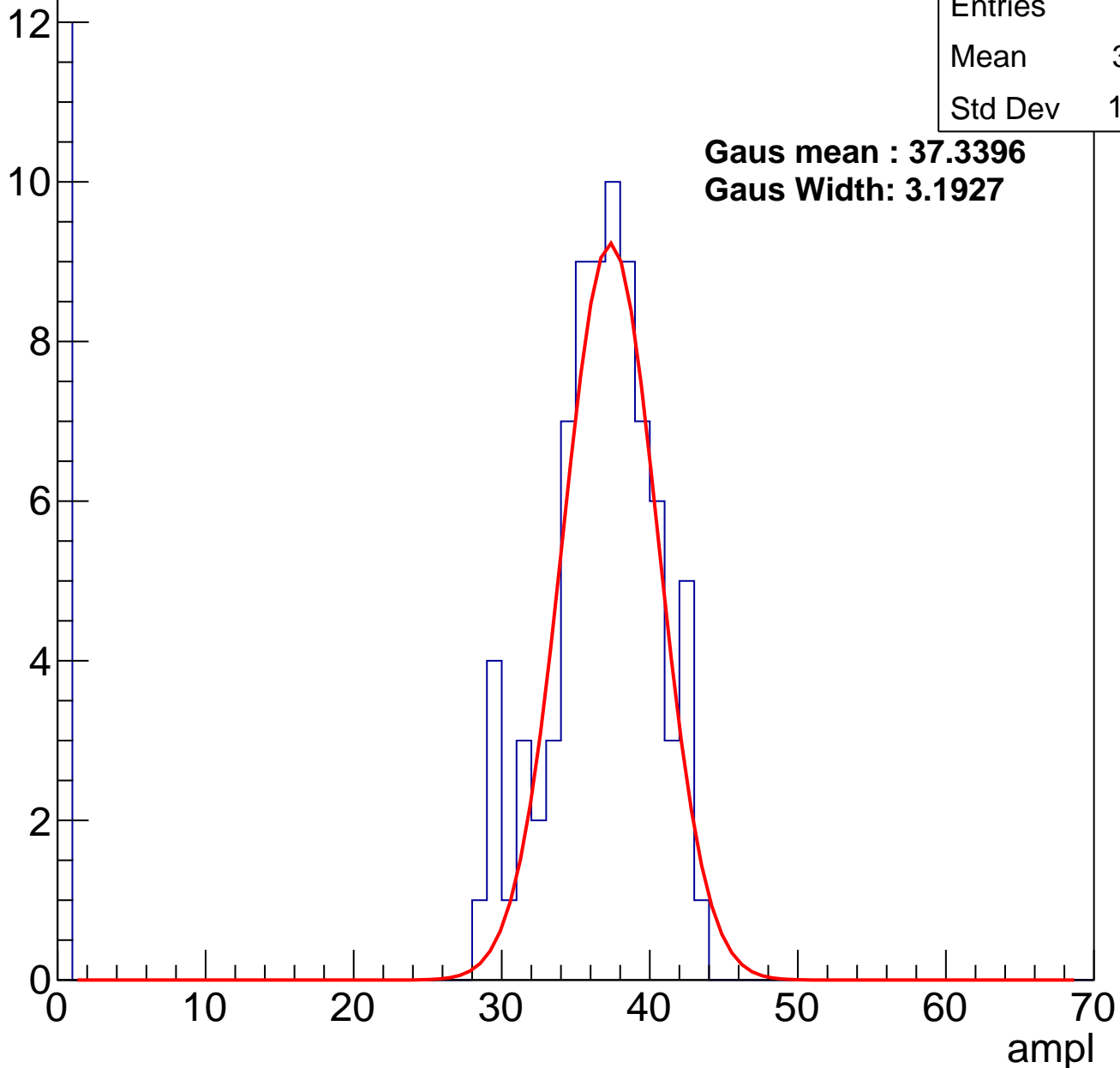
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	31.61
Std Dev	12.66

Gaus mean : 37.3396

Gaus Width: 3.1927

Entry



B1L103S, U6-ch93, adc2

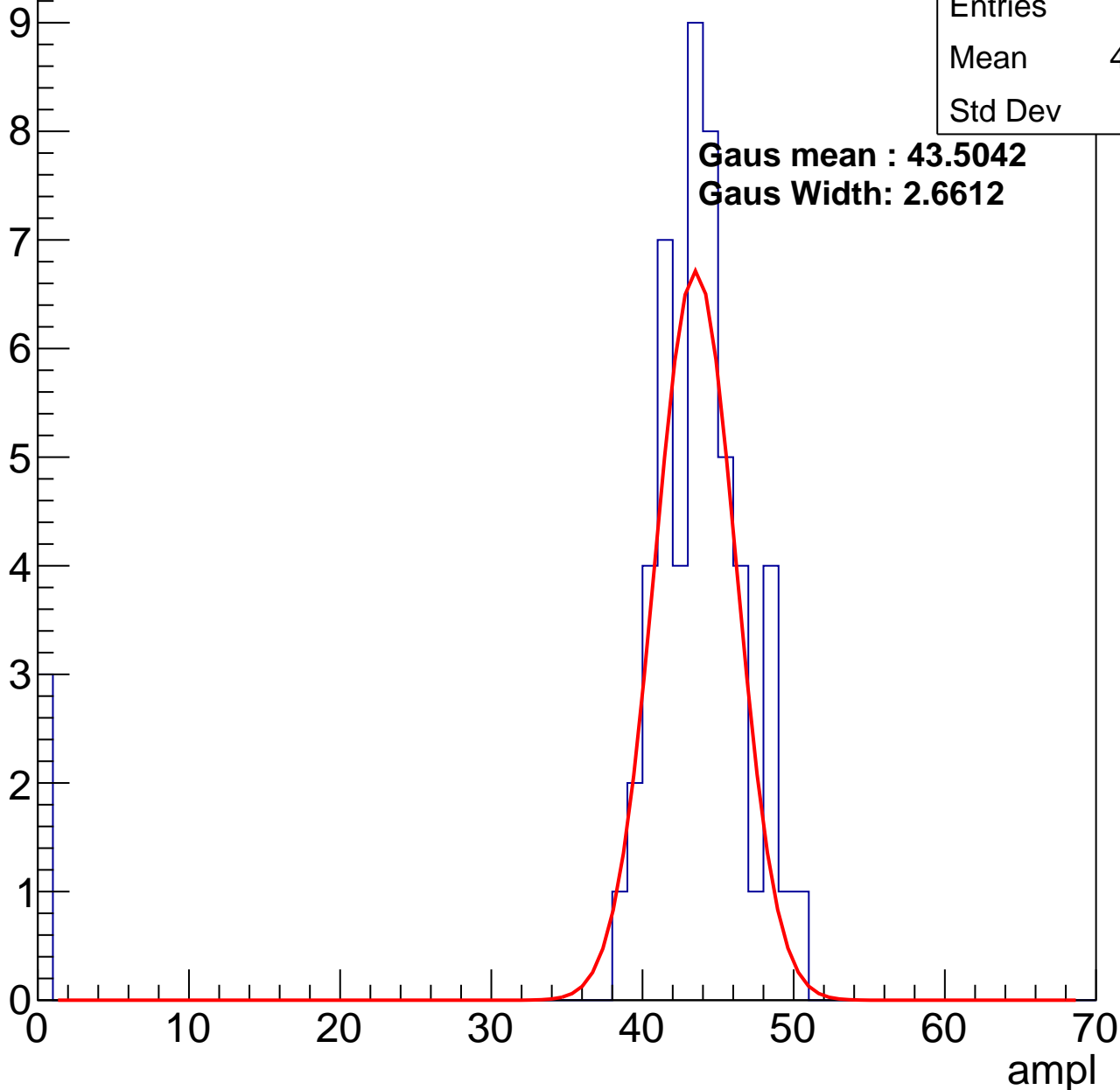
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	41.06
Std Dev	10.3

Gaus mean : 43.5042

Gaus Width: 2.6612

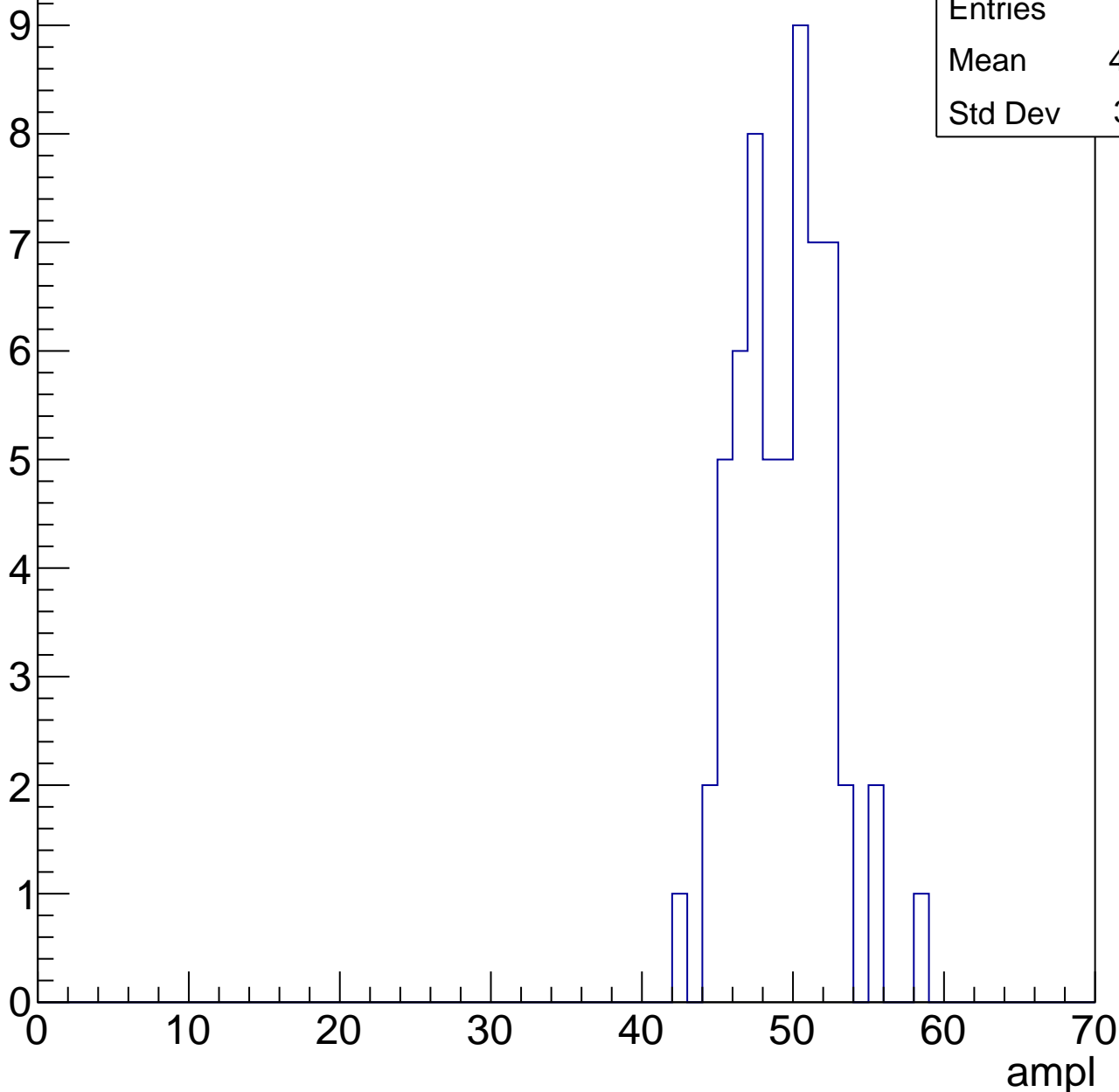


B1L103S, U6-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.95
Std Dev	3.041

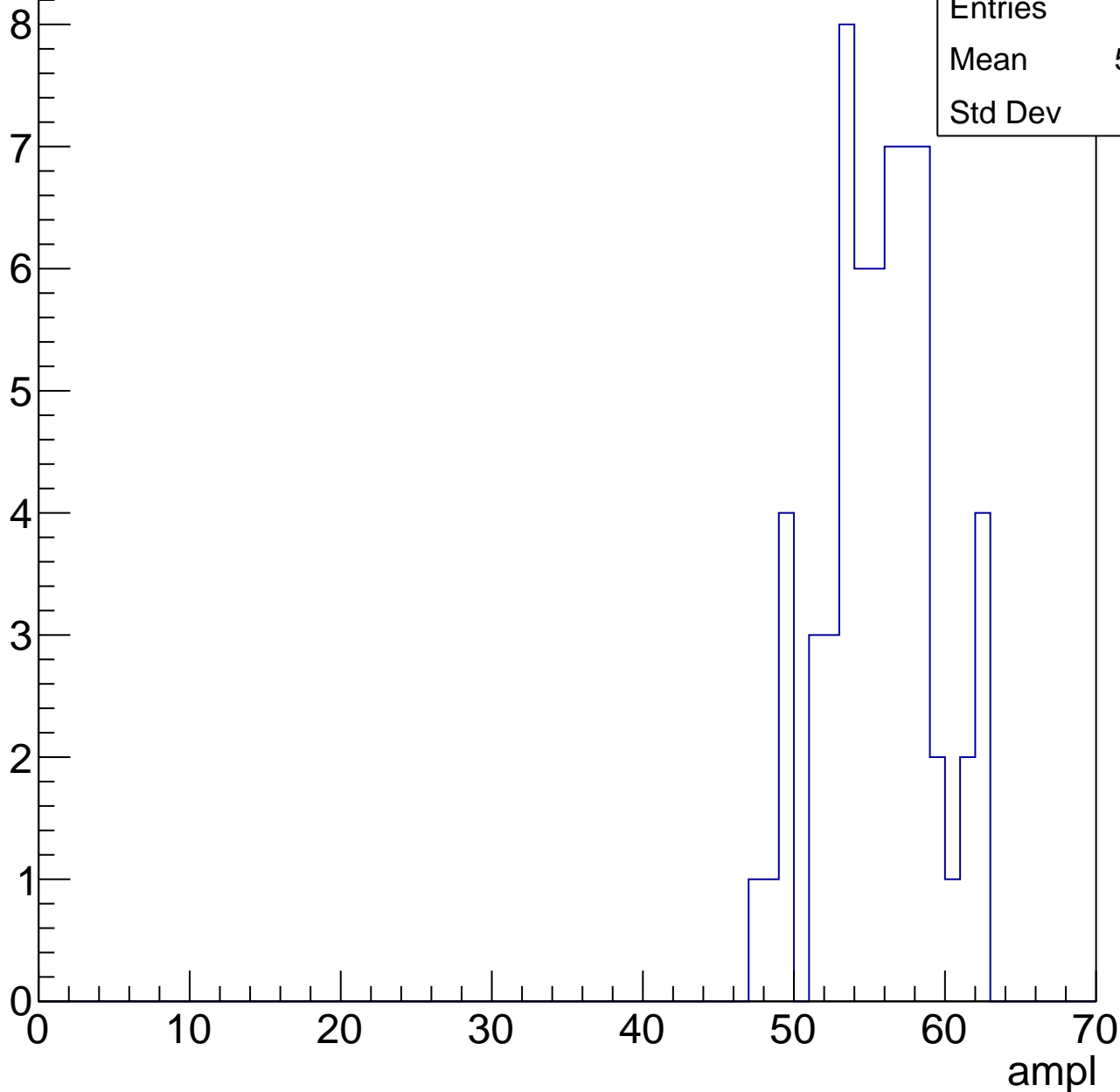


B1L103S, U6-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.21
Std Dev	3.57

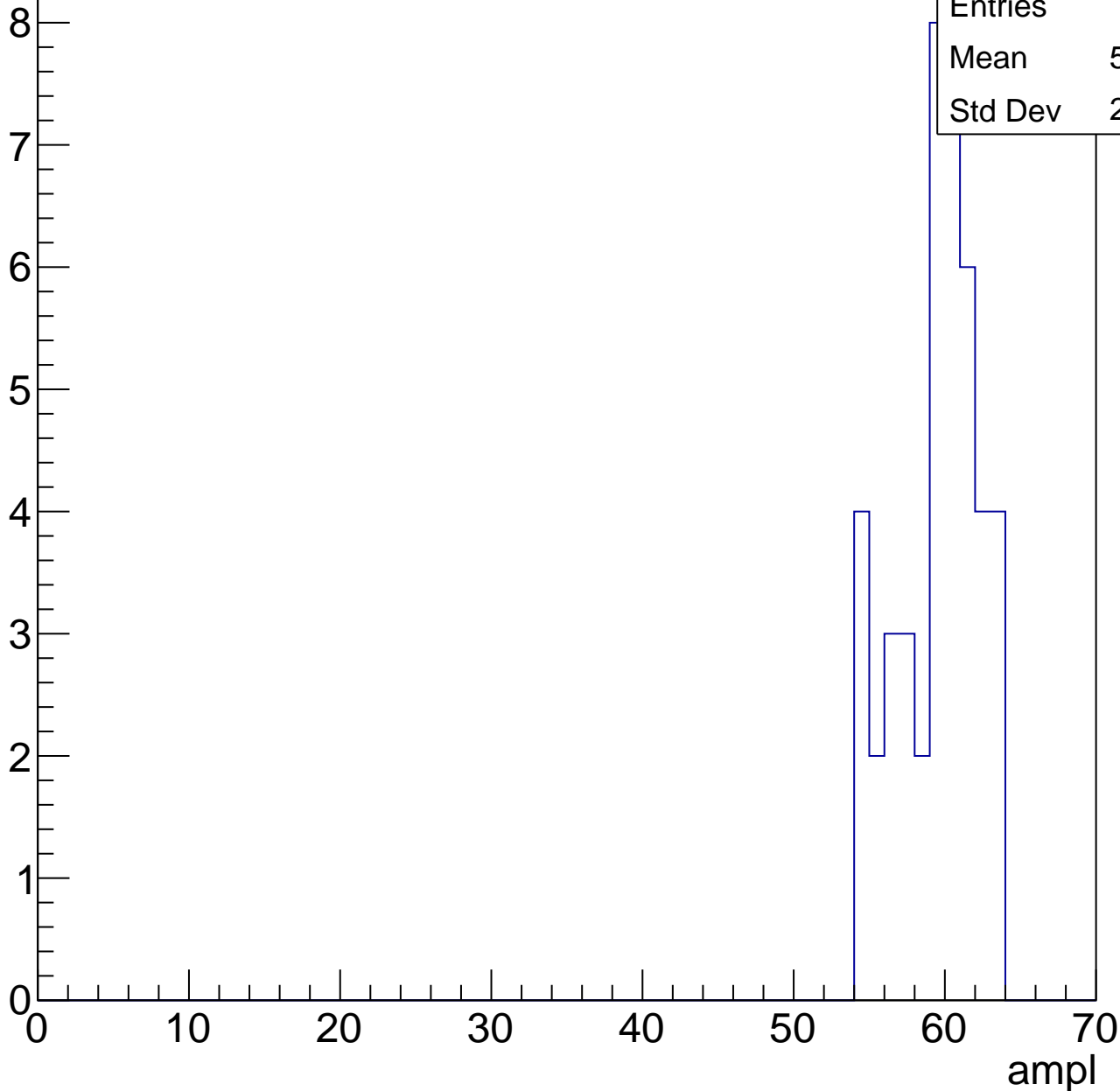


B1L103S, U6-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	59.07
Std Dev	2.632

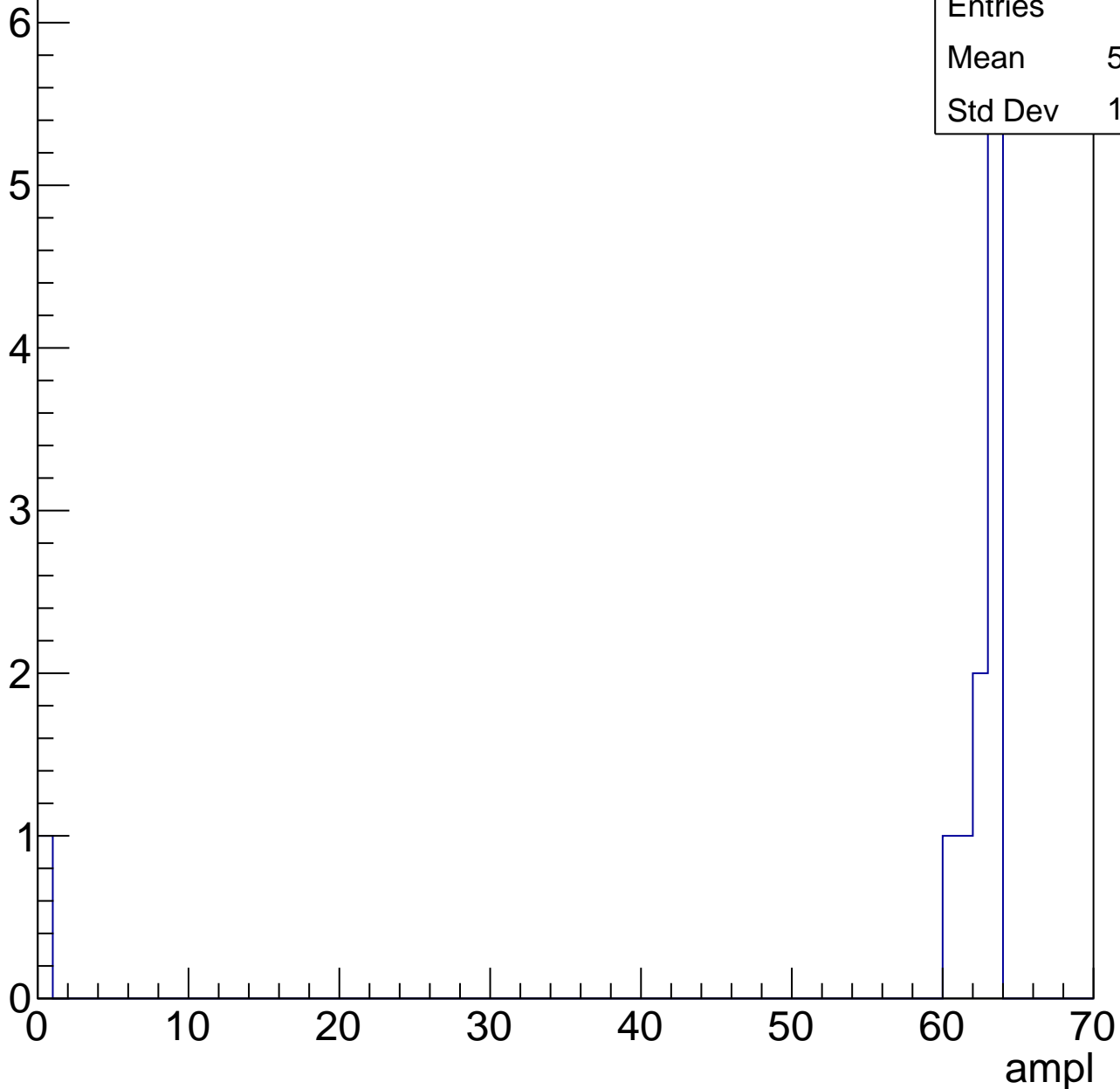


B1L103S, U6-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

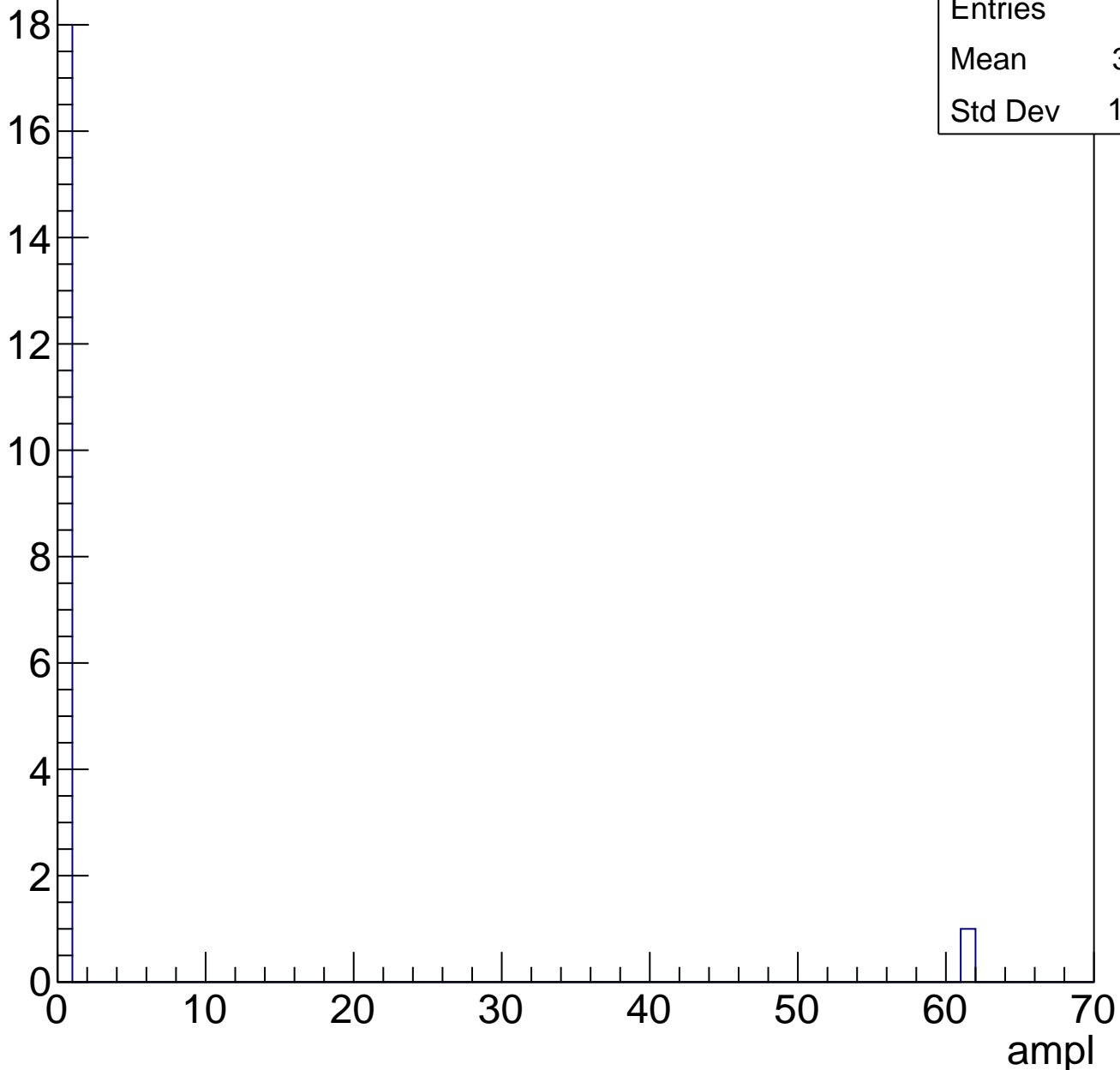
Entries	11
Mean	56.64
Std Dev	17.94



B1L103S, U6-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	3.211
Std Dev	13.62

B1L103S, U6-ch94, adc0

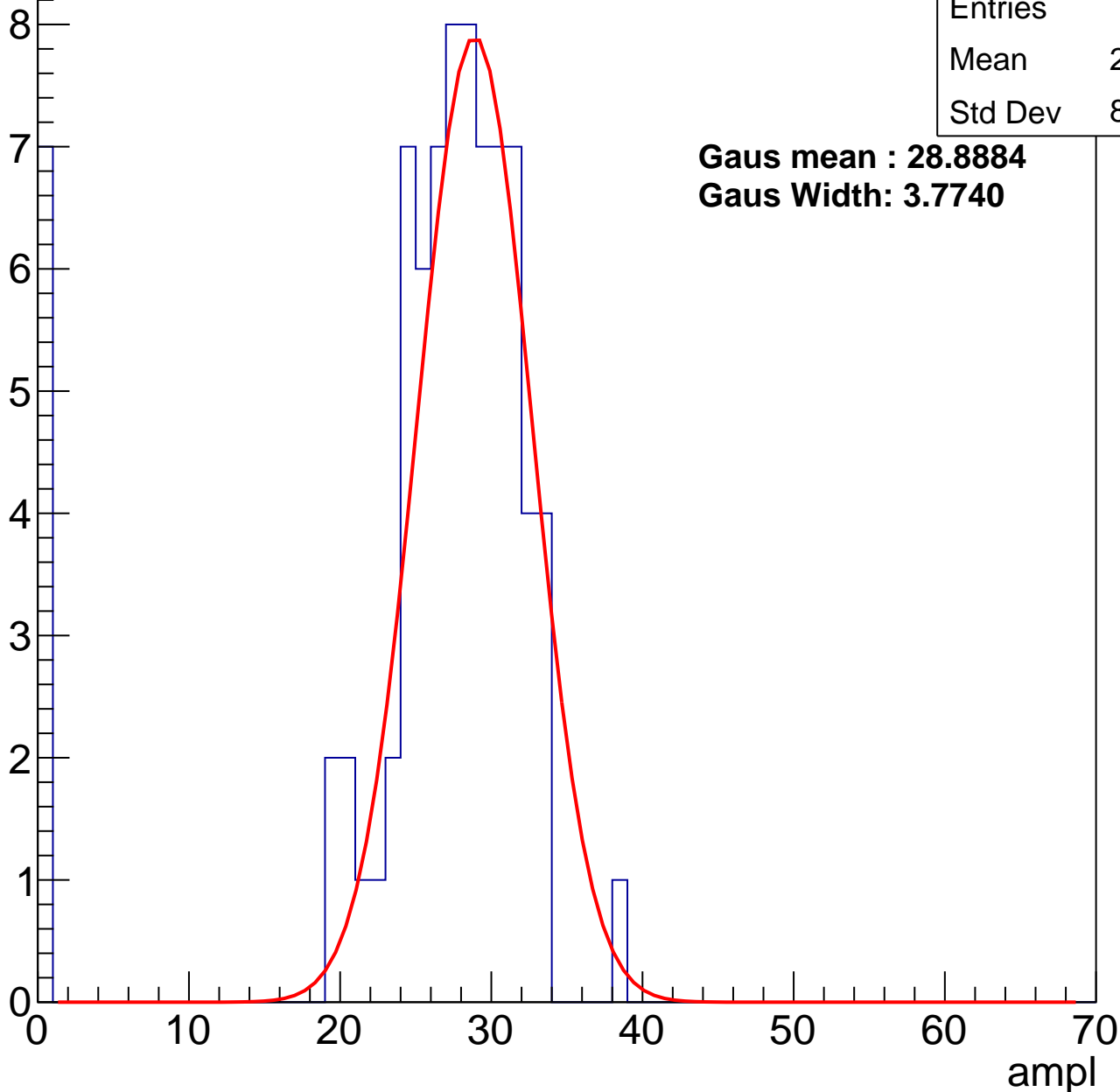
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	25.12
Std Dev	8.467

Gaus mean : 28.8884

Gaus Width: 3.7740



B1L103S, U6-ch94, adc1

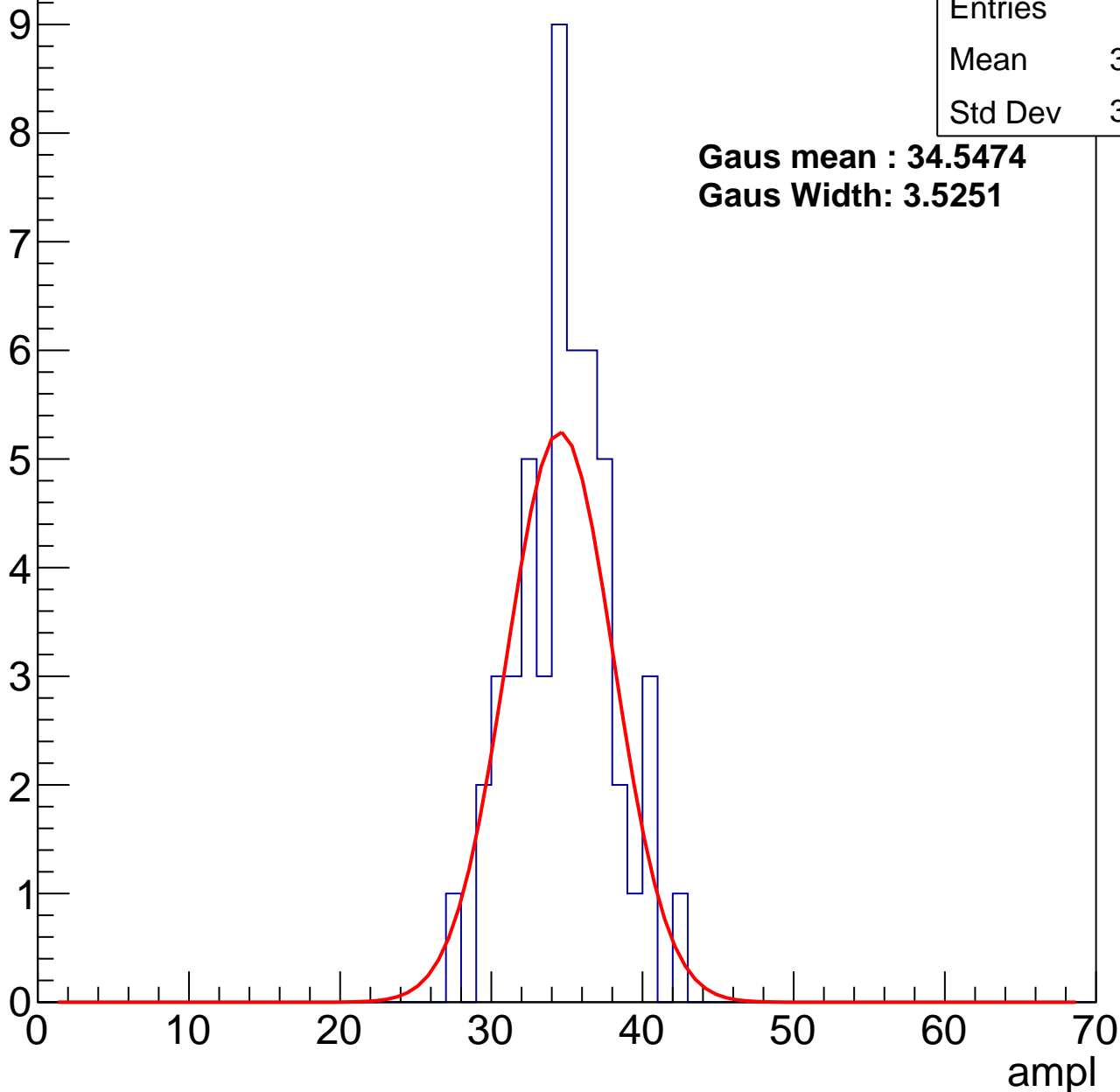
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	34.42
Std Dev	3.137

Gaus mean : 34.5474

Gaus Width: 3.5251



B1L103S, U6-ch94, adc2

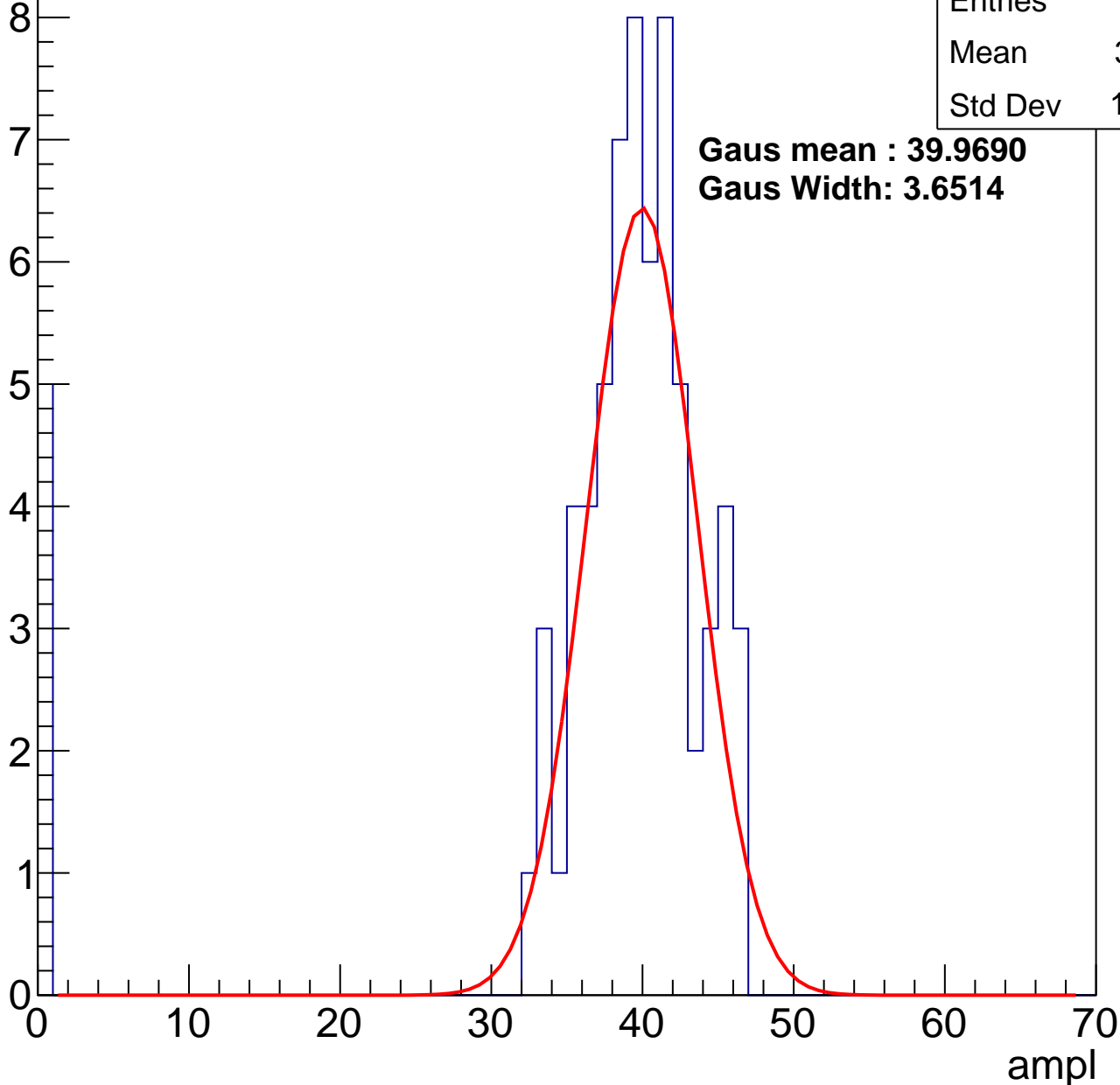
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.61
Std Dev	10.77

Gaus mean : 39.9690

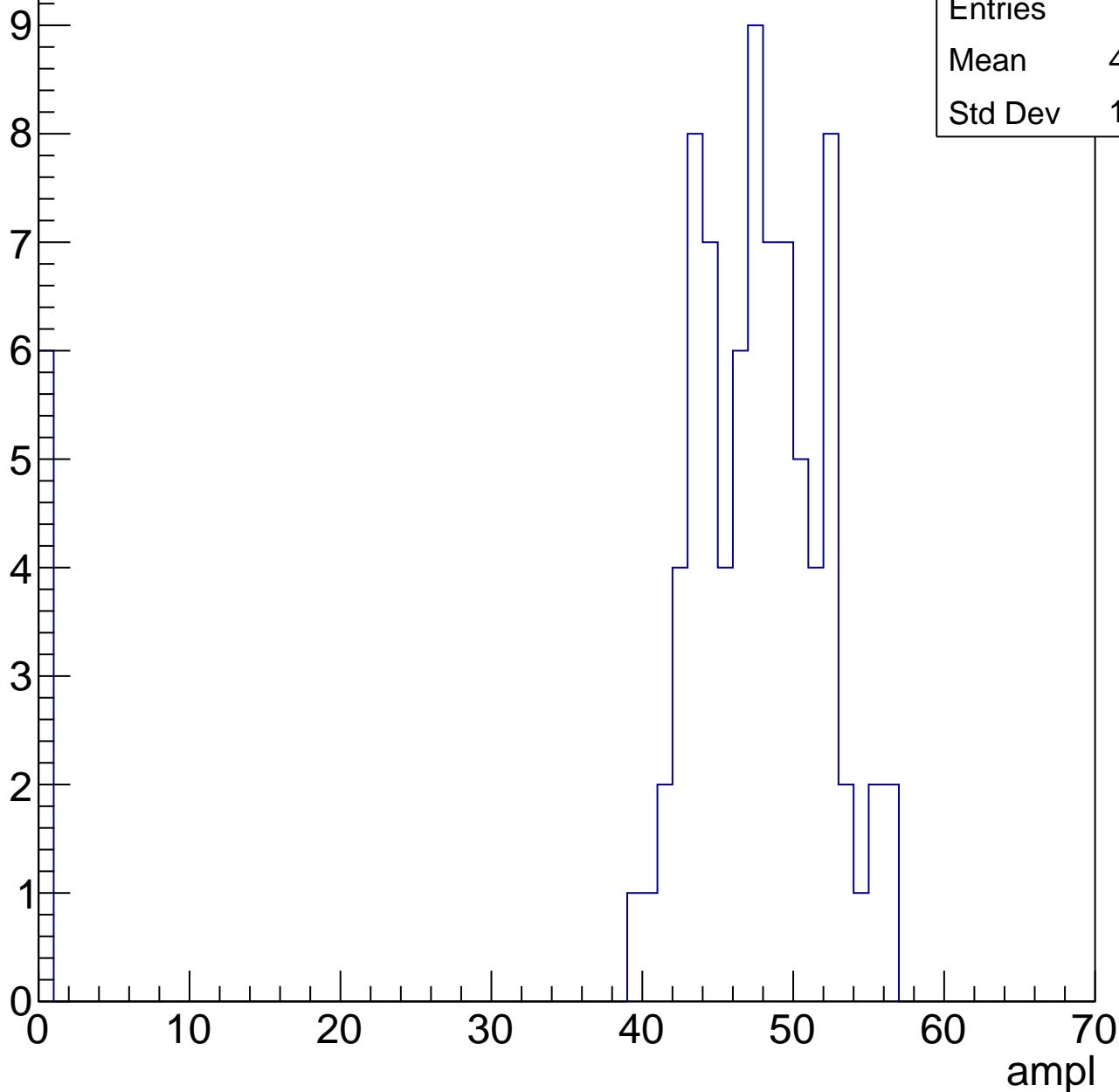
Gaus Width: 3.6514



B1L103S, U6-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



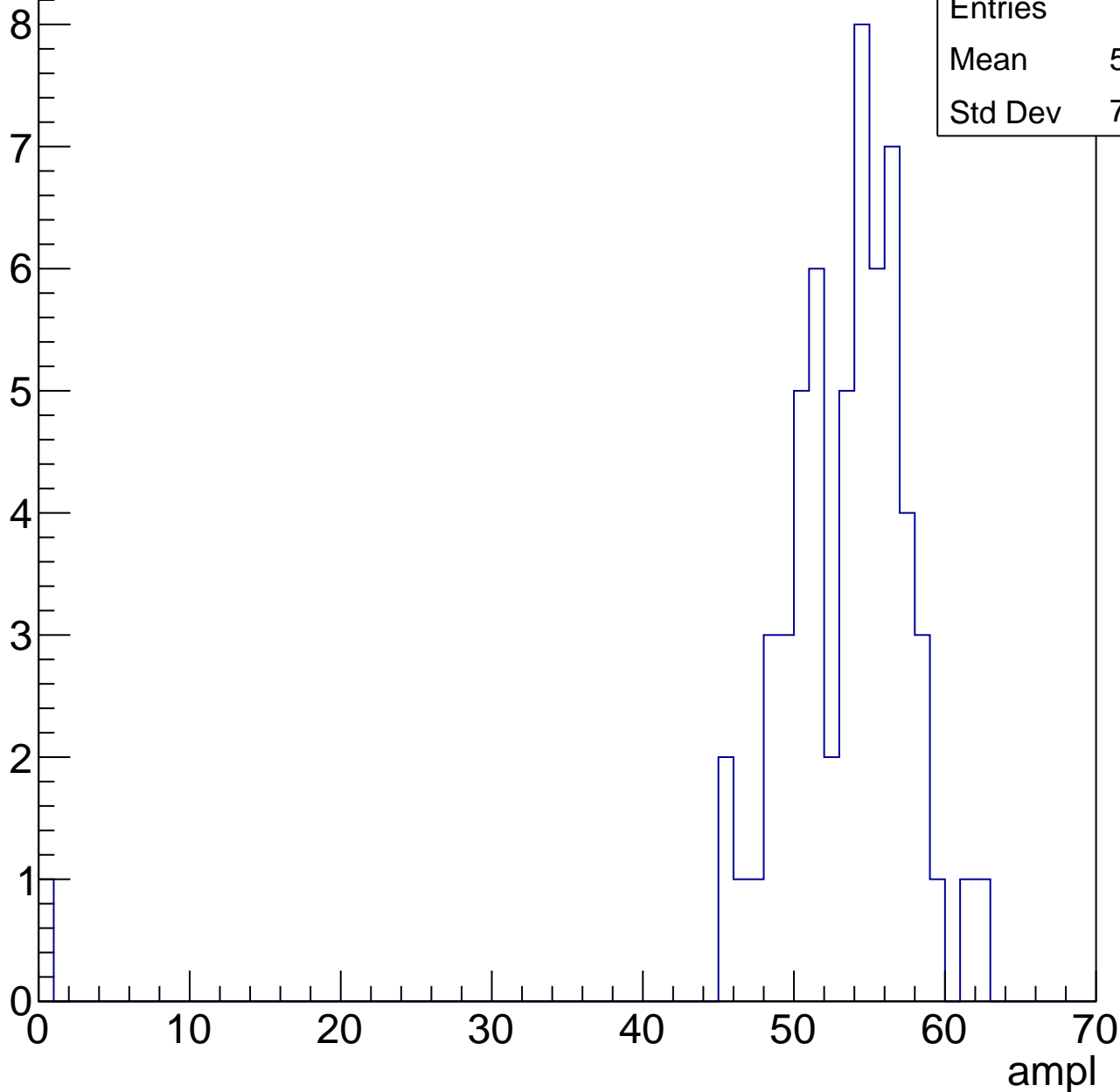
Entries	86
Mean	44.08
Std Dev	12.66

B1L103S, U6-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	52.28
Std Dev	7.746

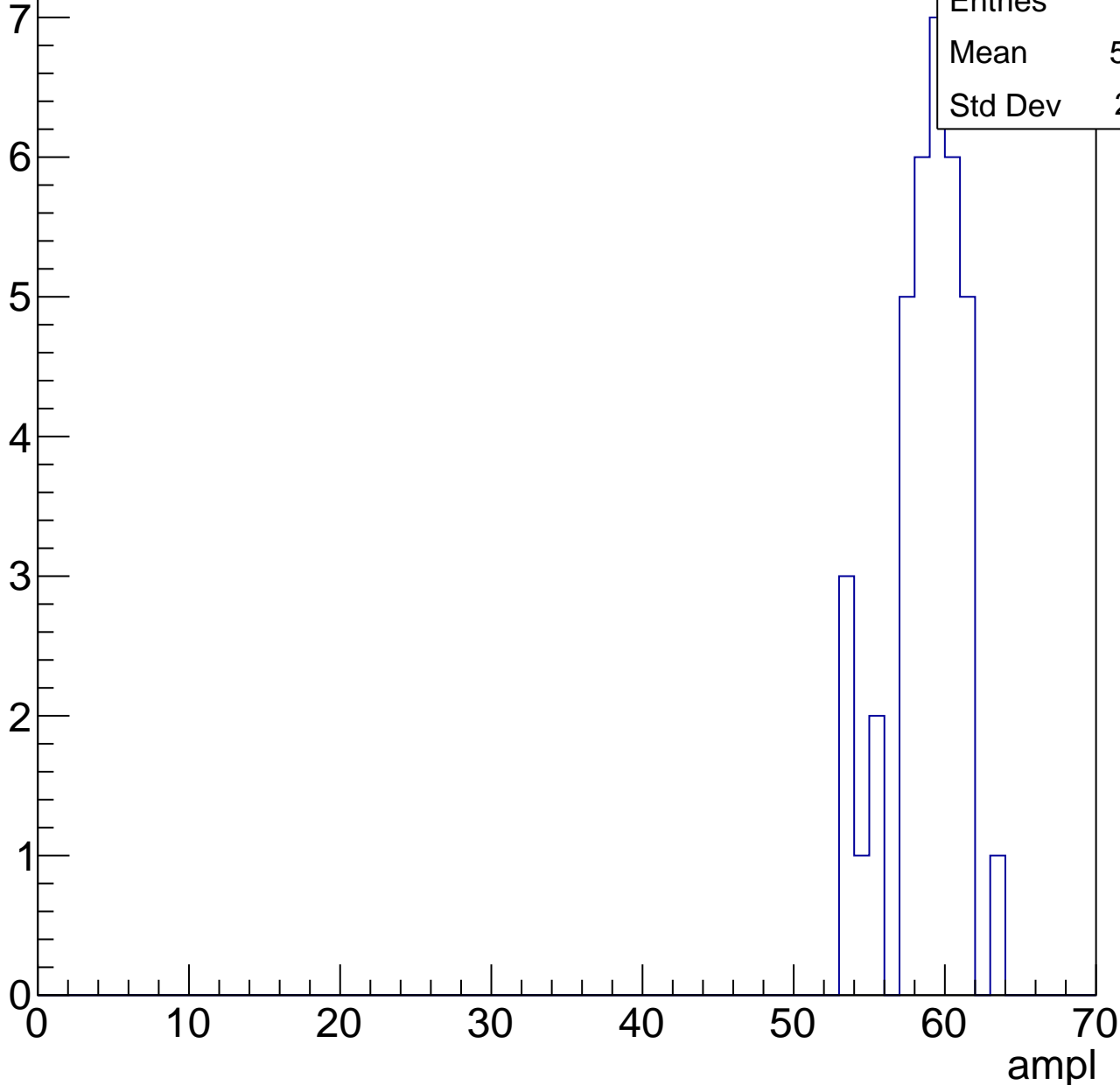


B1L103S, U6-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.25
Std Dev	2.431

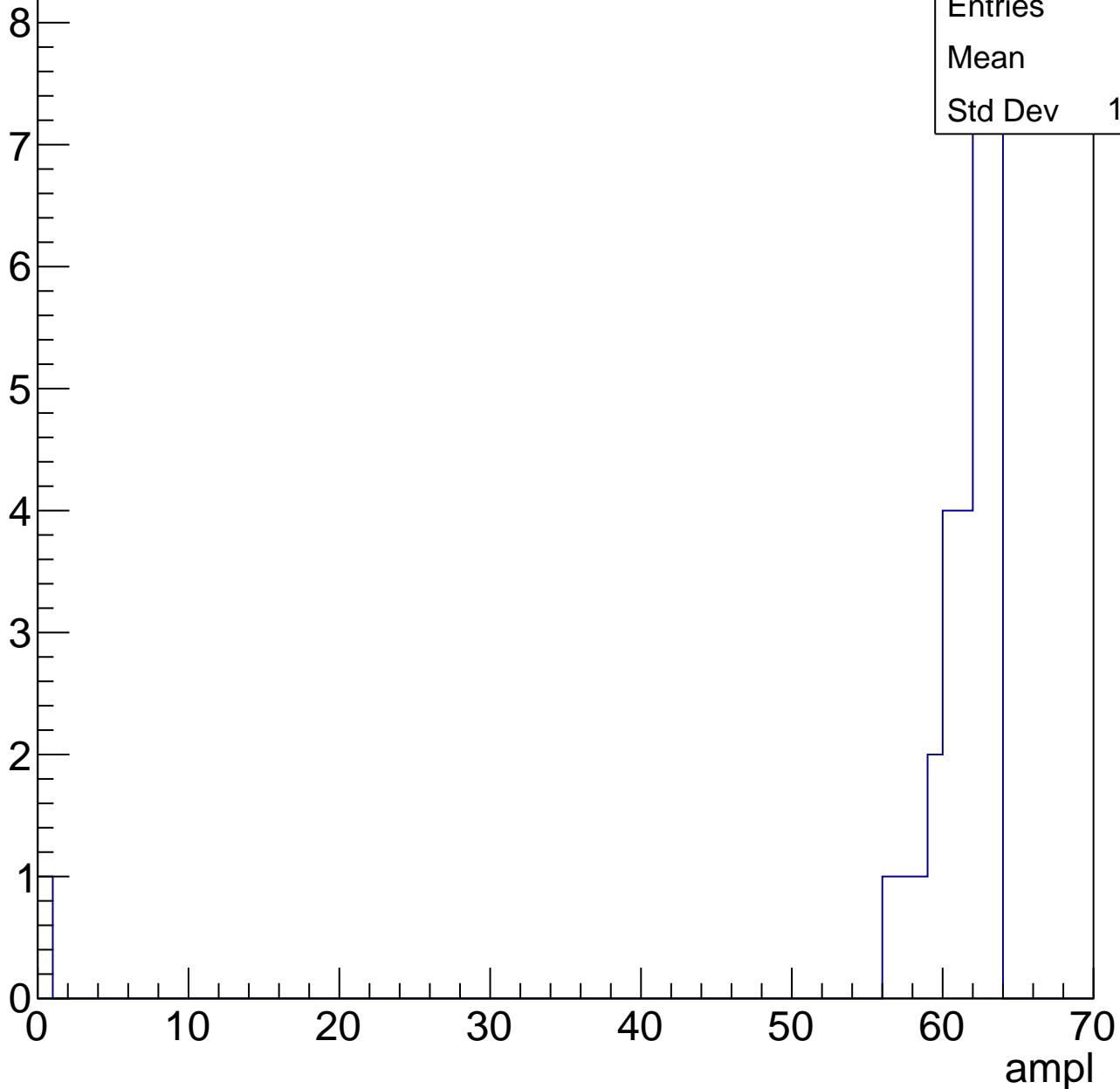


B1L103S, U6-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	59.1
Std Dev	11.13

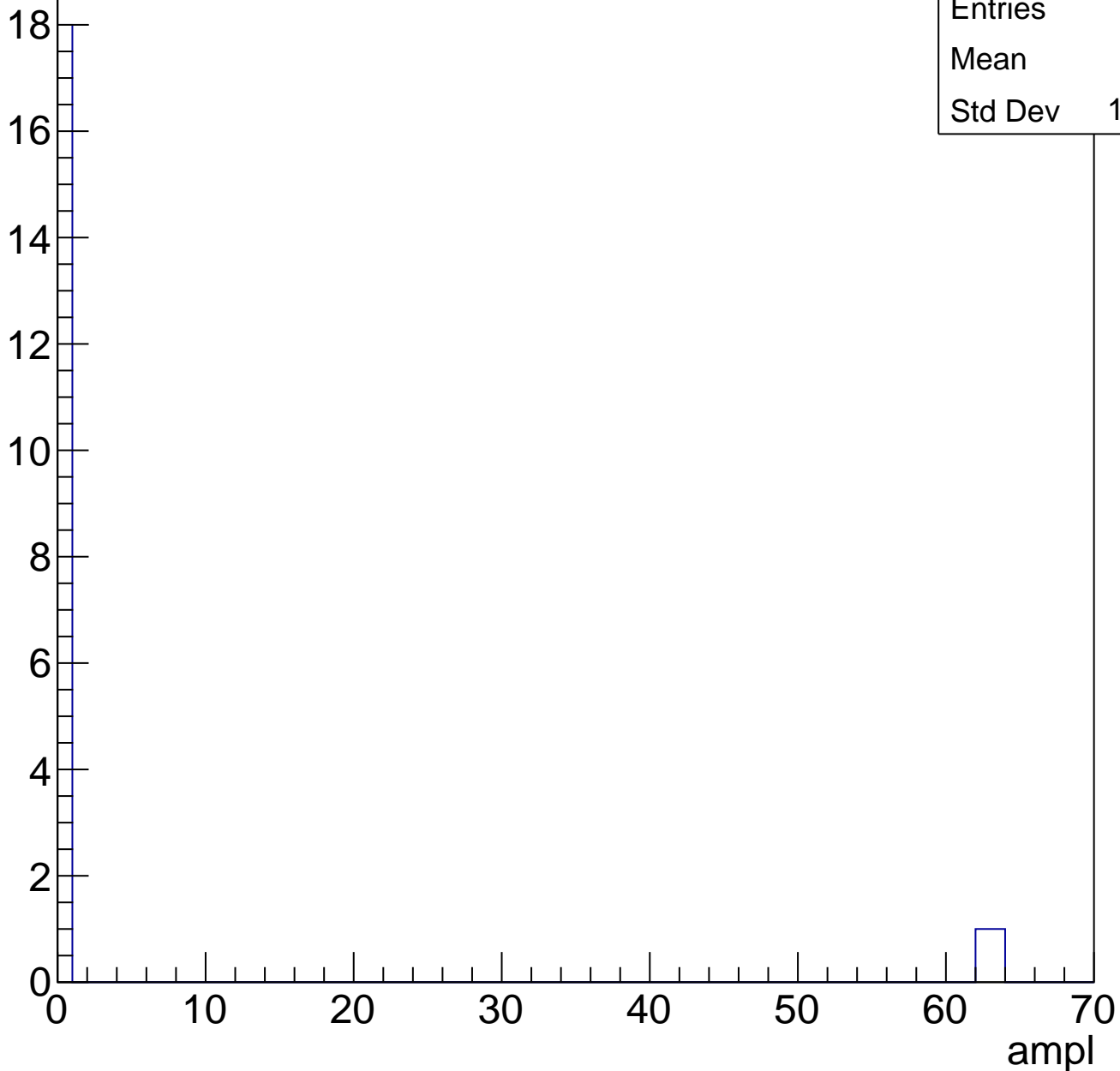


B1L103S, U6-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.25
Std Dev	18.75

Entry



B1L103S, U6-ch95, adc0

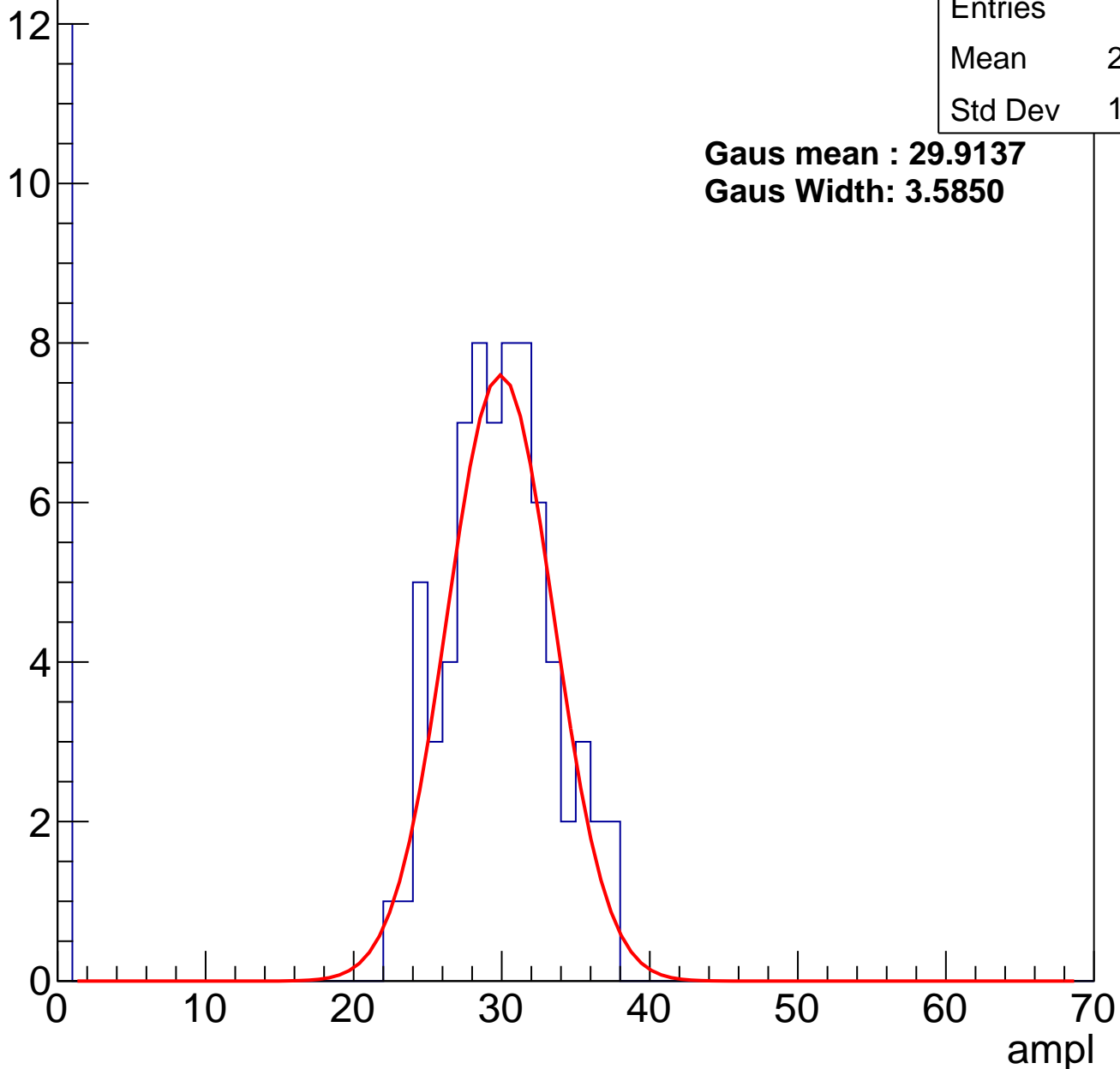
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	25.19
Std Dev	10.84

Gaus mean : 29.9137

Gaus Width: 3.5850

Entry



B1L103S, U6-ch95, adc1

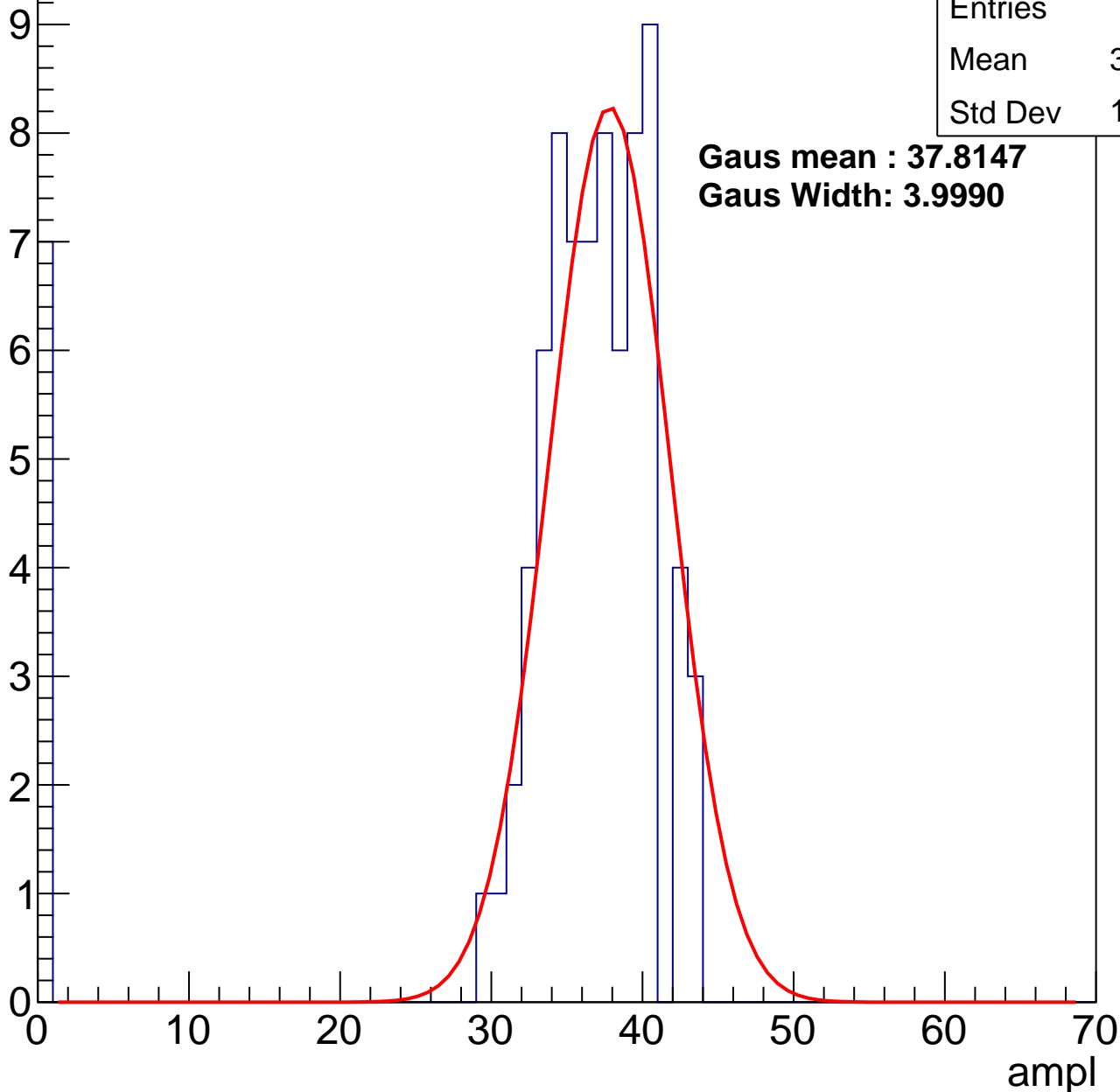
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	33.44
Std Dev	10.76

Gaus mean : 37.8147

Gaus Width: 3.9990



B1L103S, U6-ch95, adc2

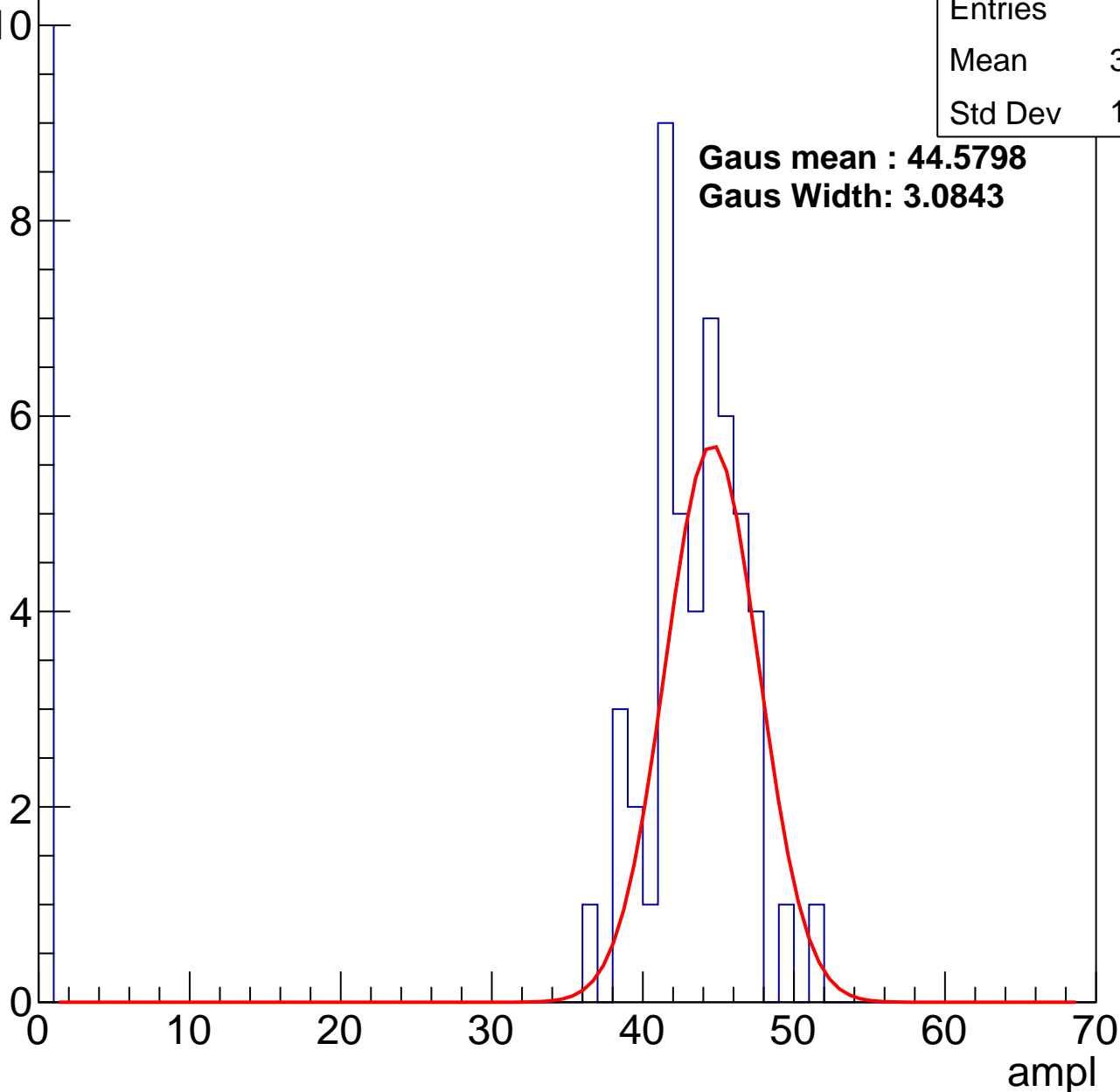
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.85
Std Dev	16.43

Gaus mean : 44.5798

Gaus Width: 3.0843



B1L103S, U6-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

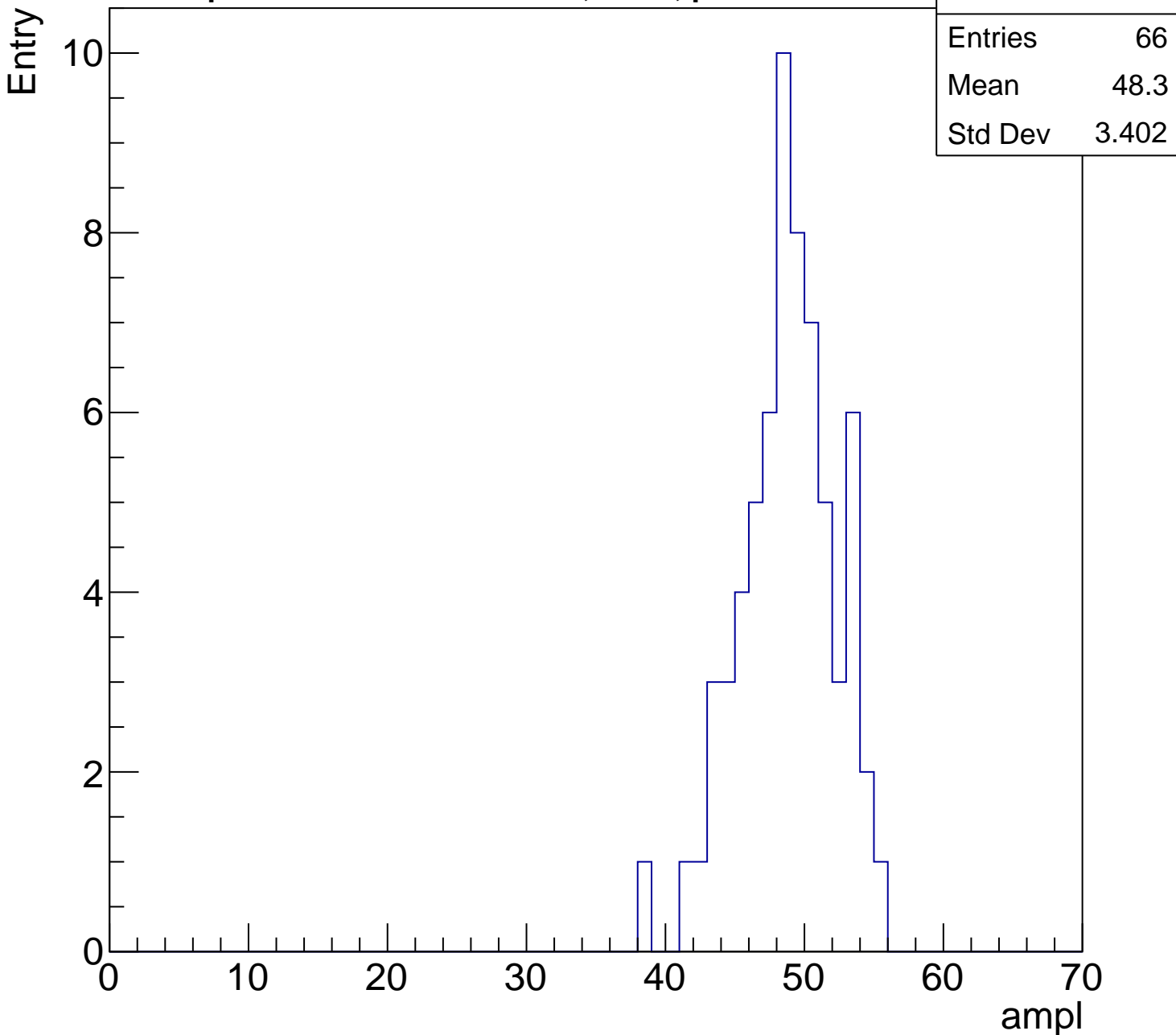
Entries	66
Mean	48.3
Std Dev	3.402

Entry

10
8
6
4
2
0

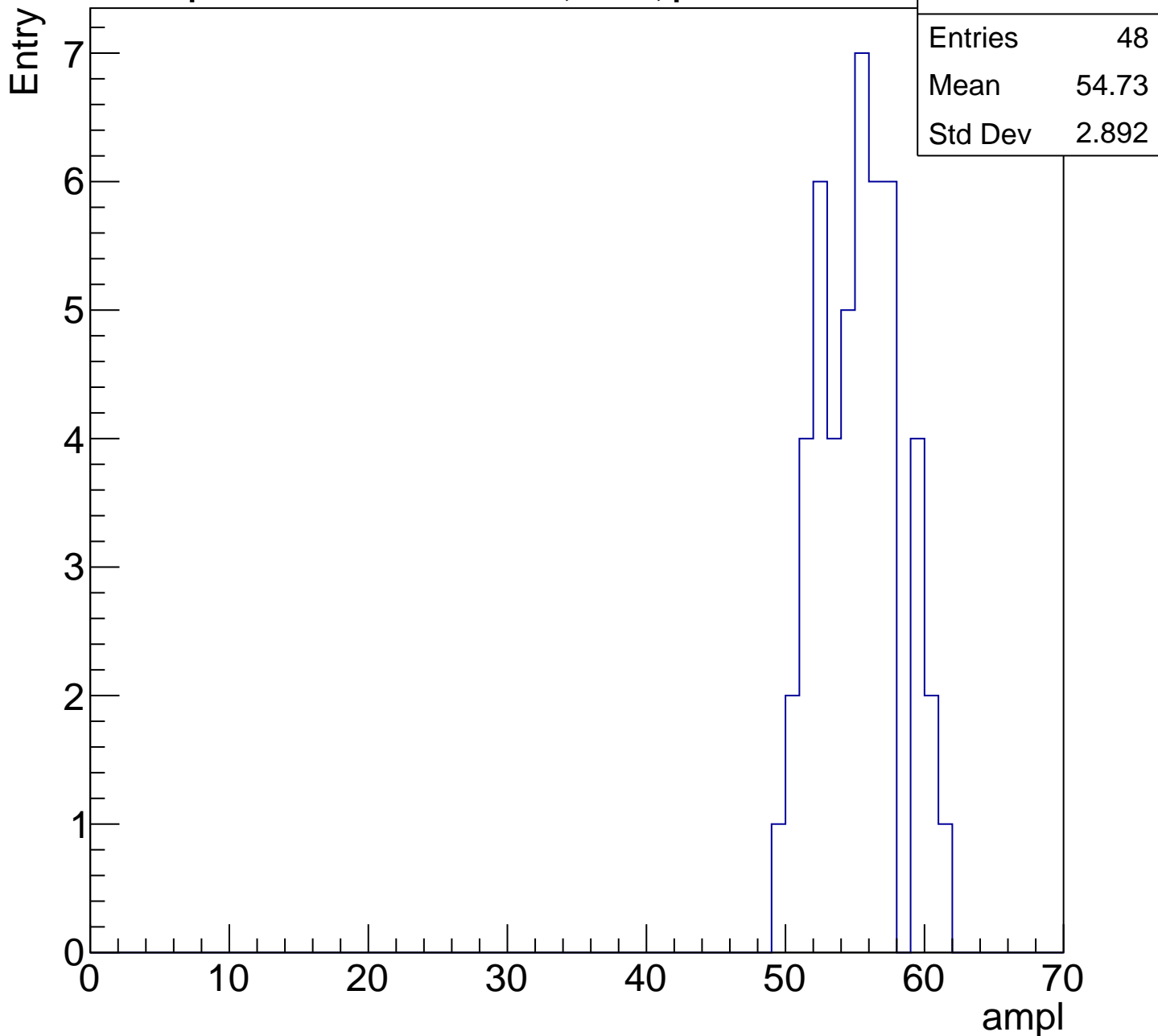
0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

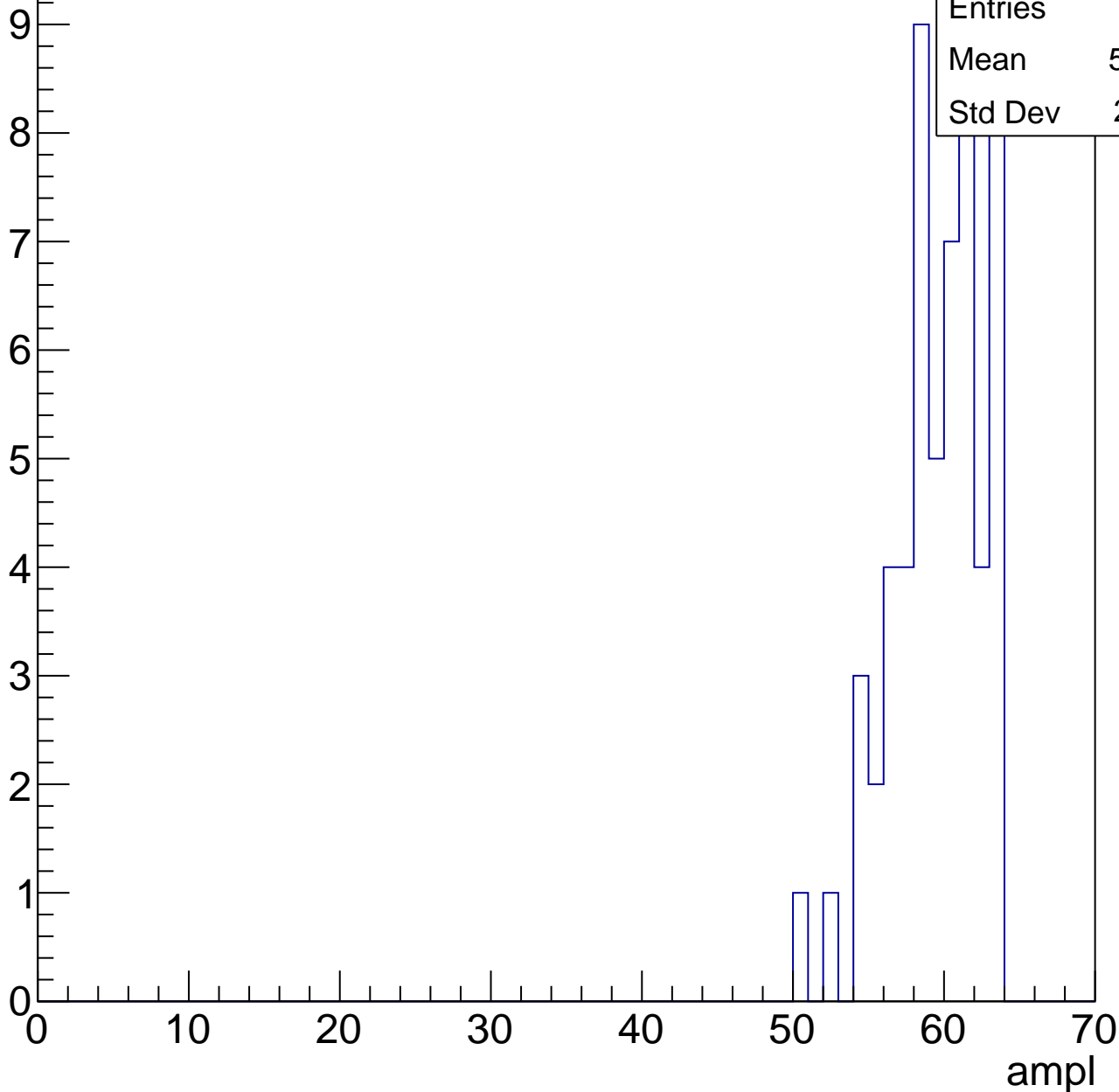


B1L103S, U6-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.98
Std Dev	2.991

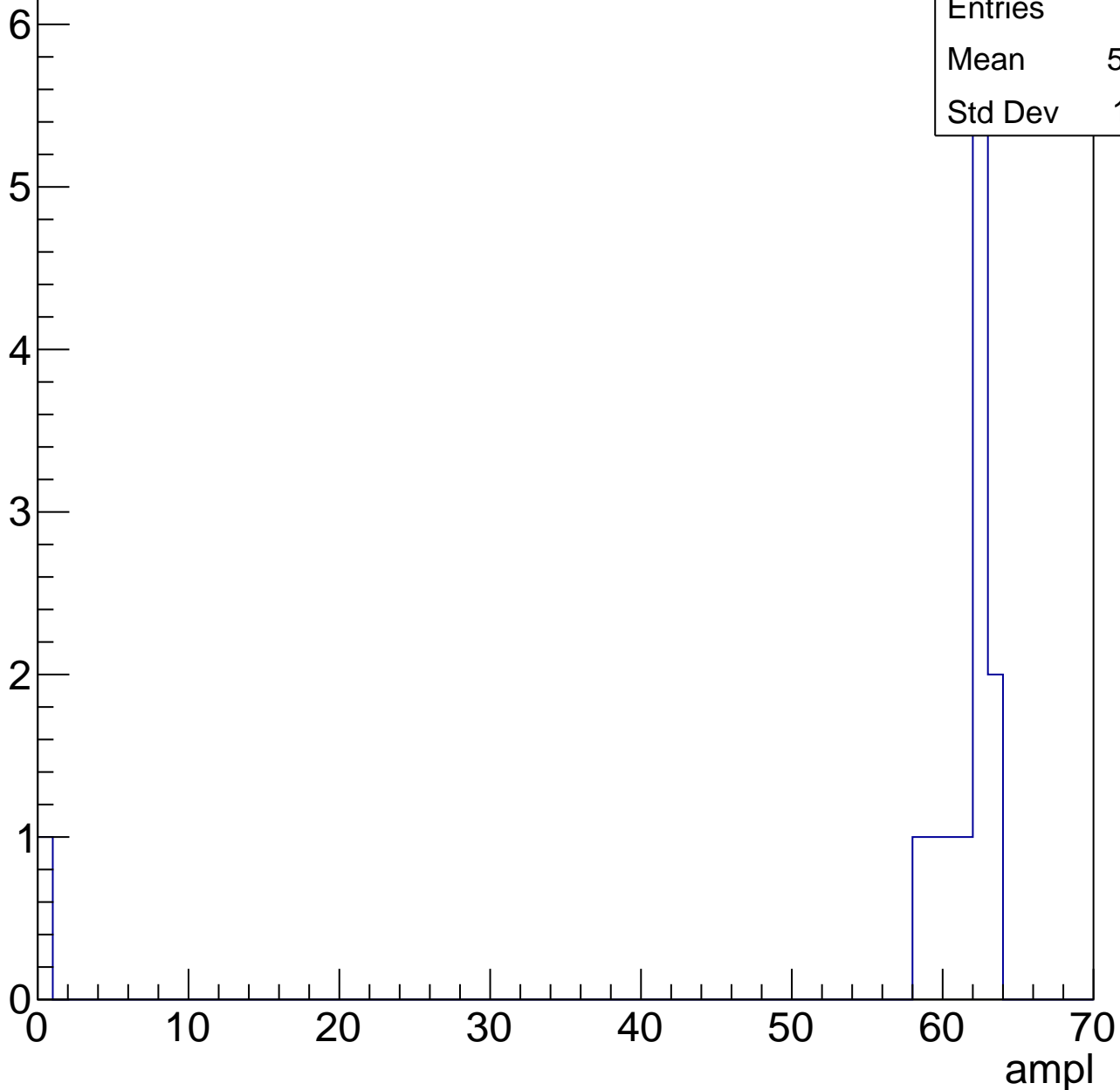


B1L103S, U6-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	56.62
Std Dev	16.41



B1L103S, U6-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U6-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	21.36
Std Dev	9.814

Gaus mean : 25.6303

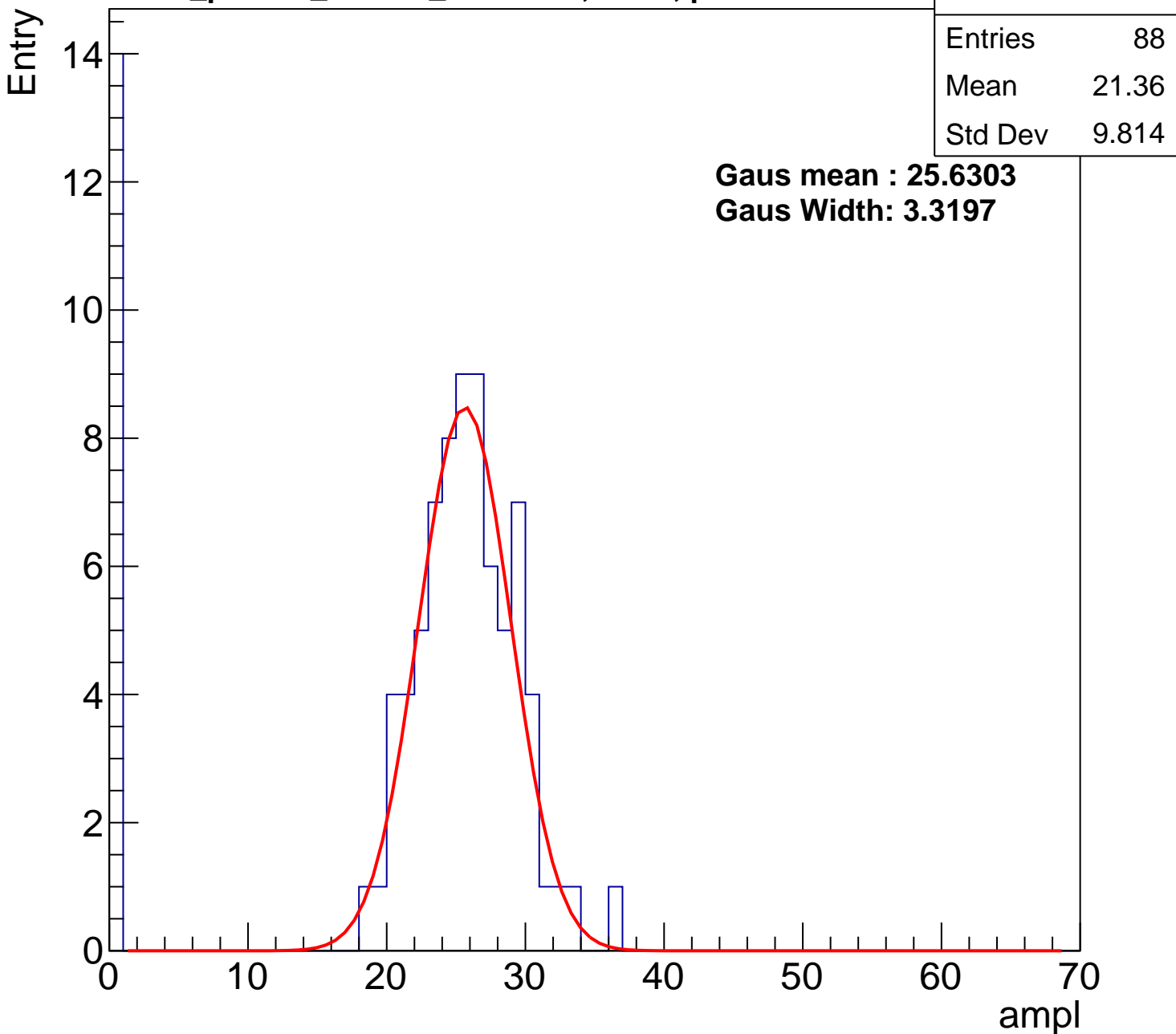
Gaus Width: 3.3197

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch96, adc1

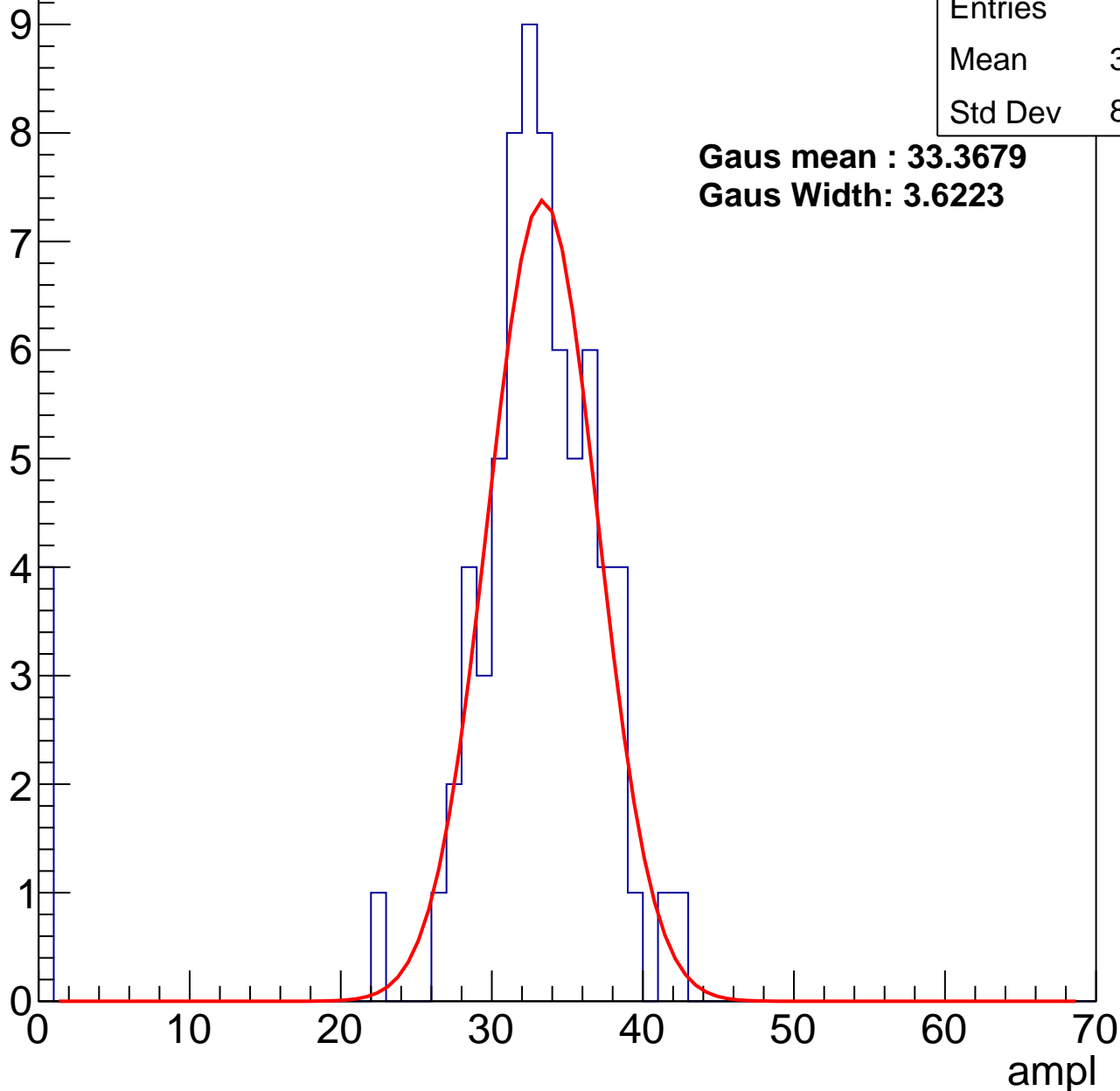
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	31.07
Std Dev	8.263

Gaus mean : 33.3679

Gaus Width: 3.6223



B1L103S, U6-ch96, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.14
Std Dev	13.3

Gaus mean : 40.0862

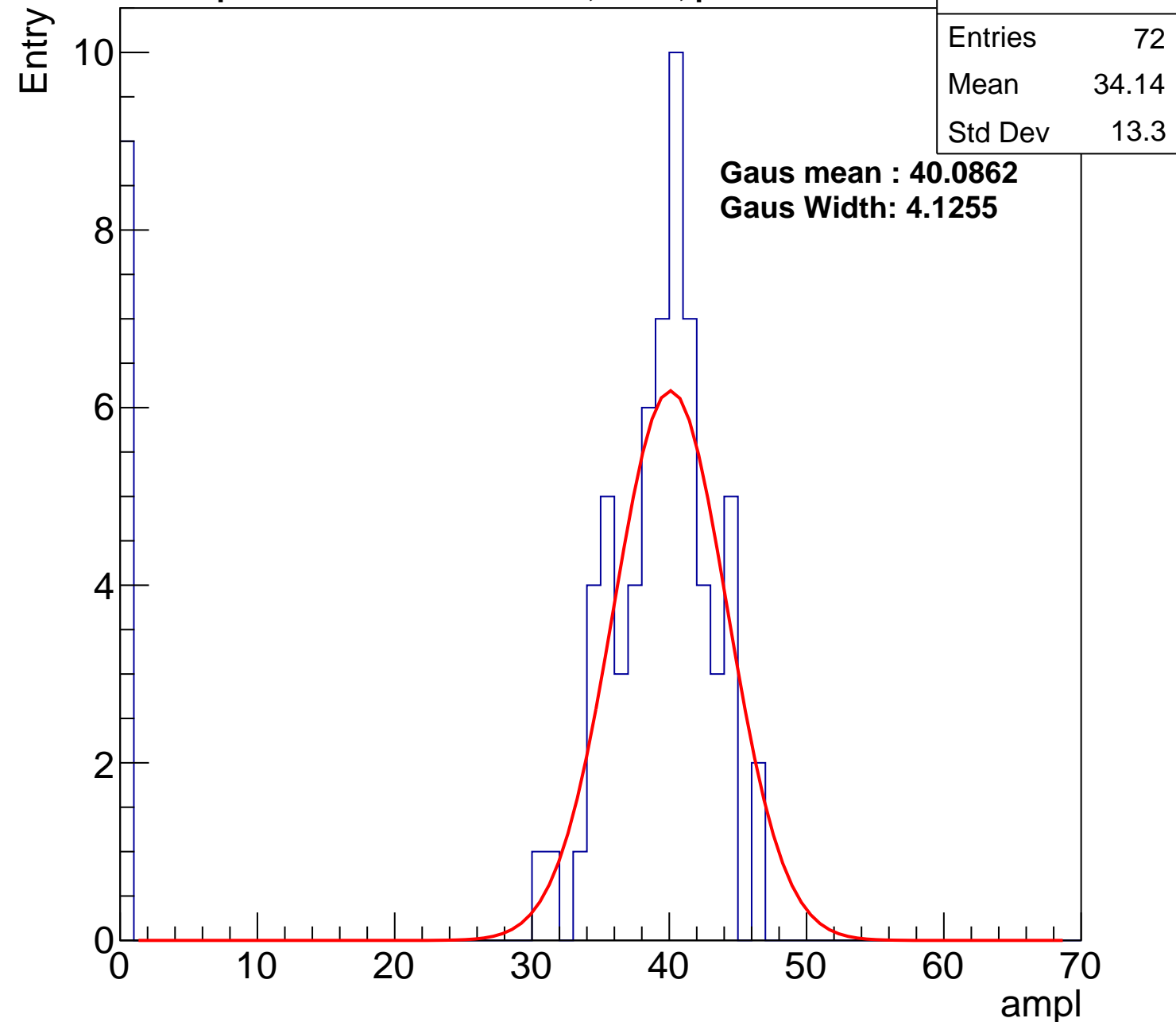
Gaus Width: 4.1255

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

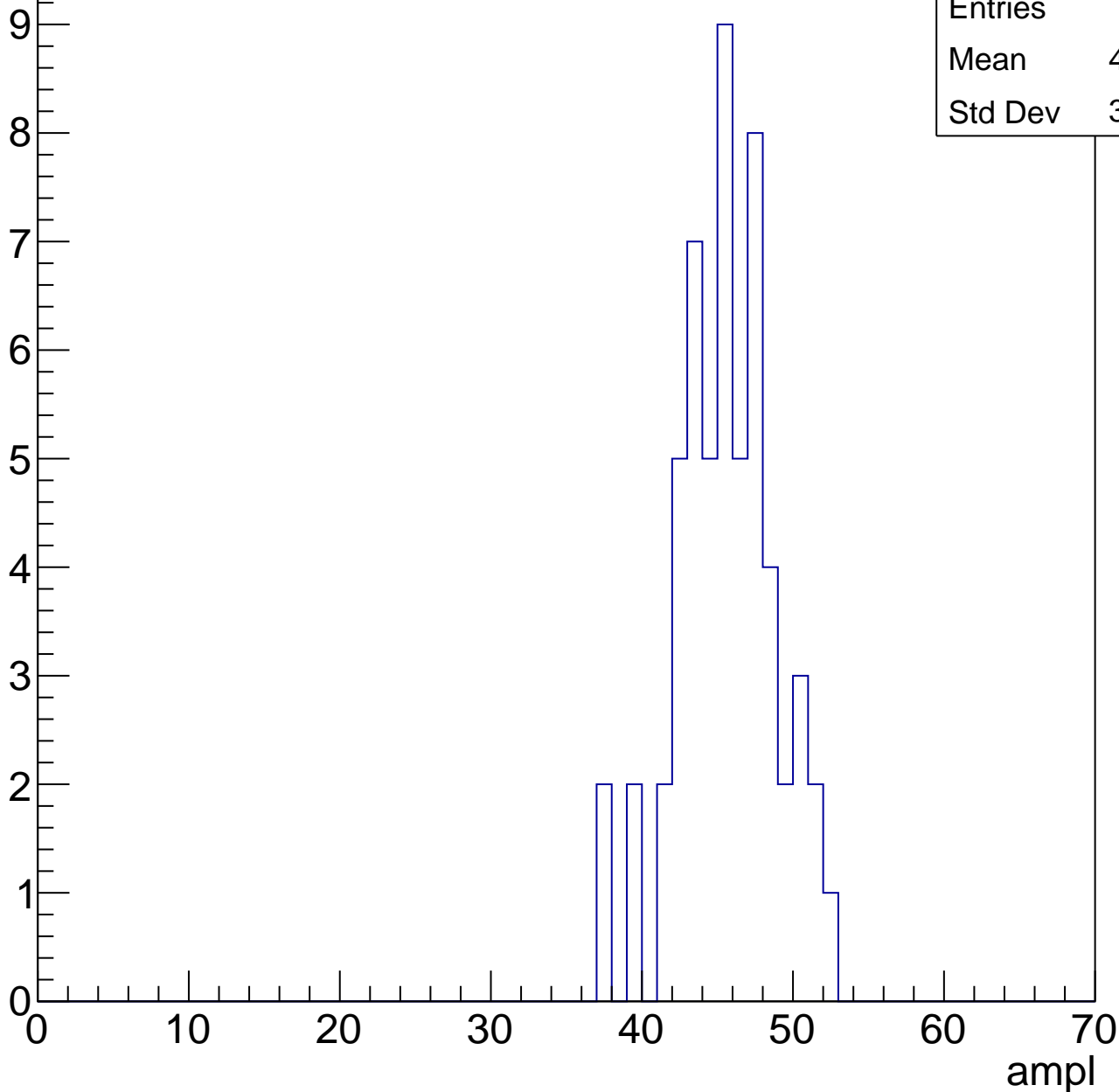


B1L103S, U6-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	45.09
Std Dev	3.273

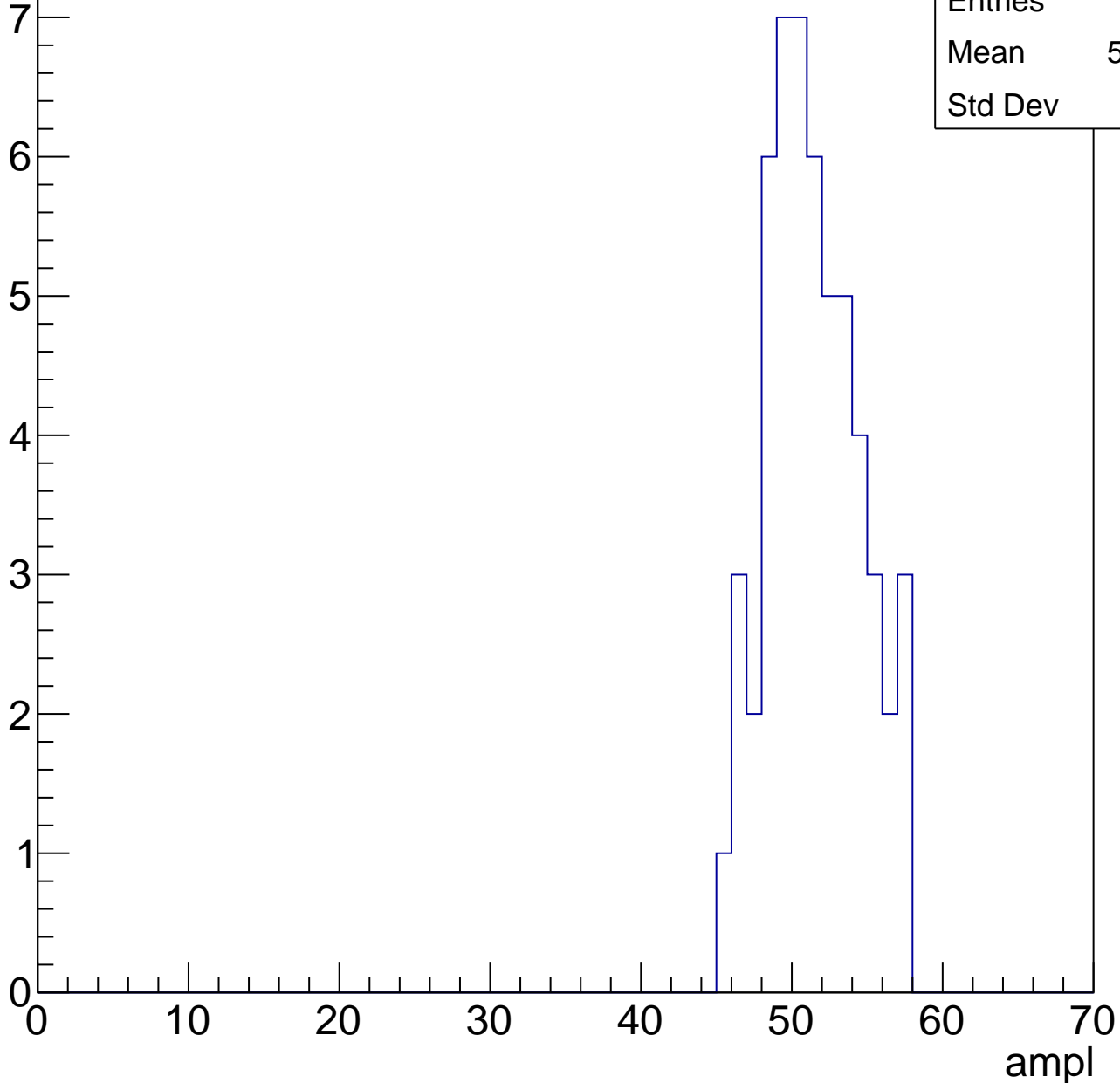


B1L103S, U6-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	50.98
Std Dev	3.04

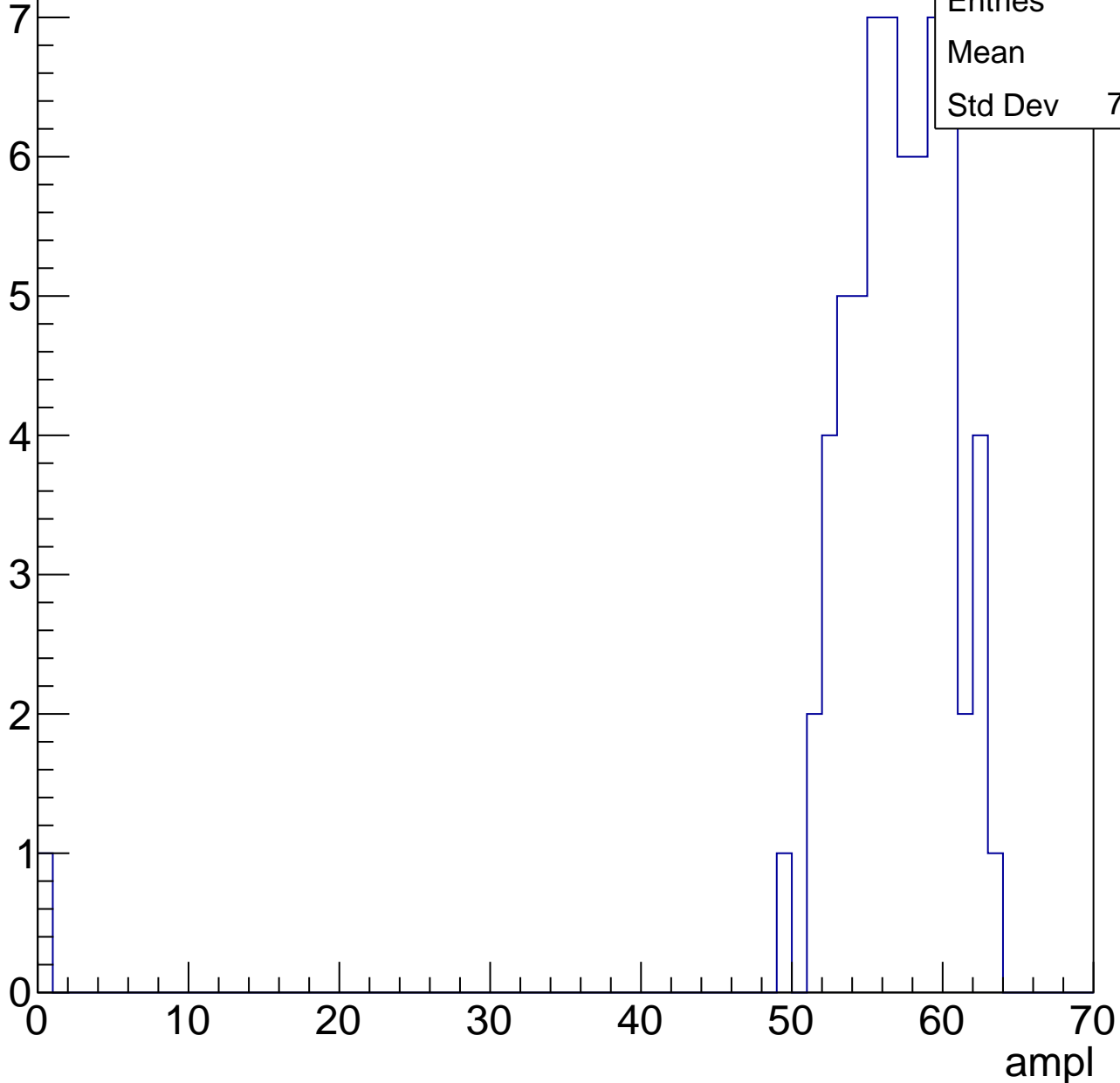


B1L103S, U6-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.8
Std Dev	7.662

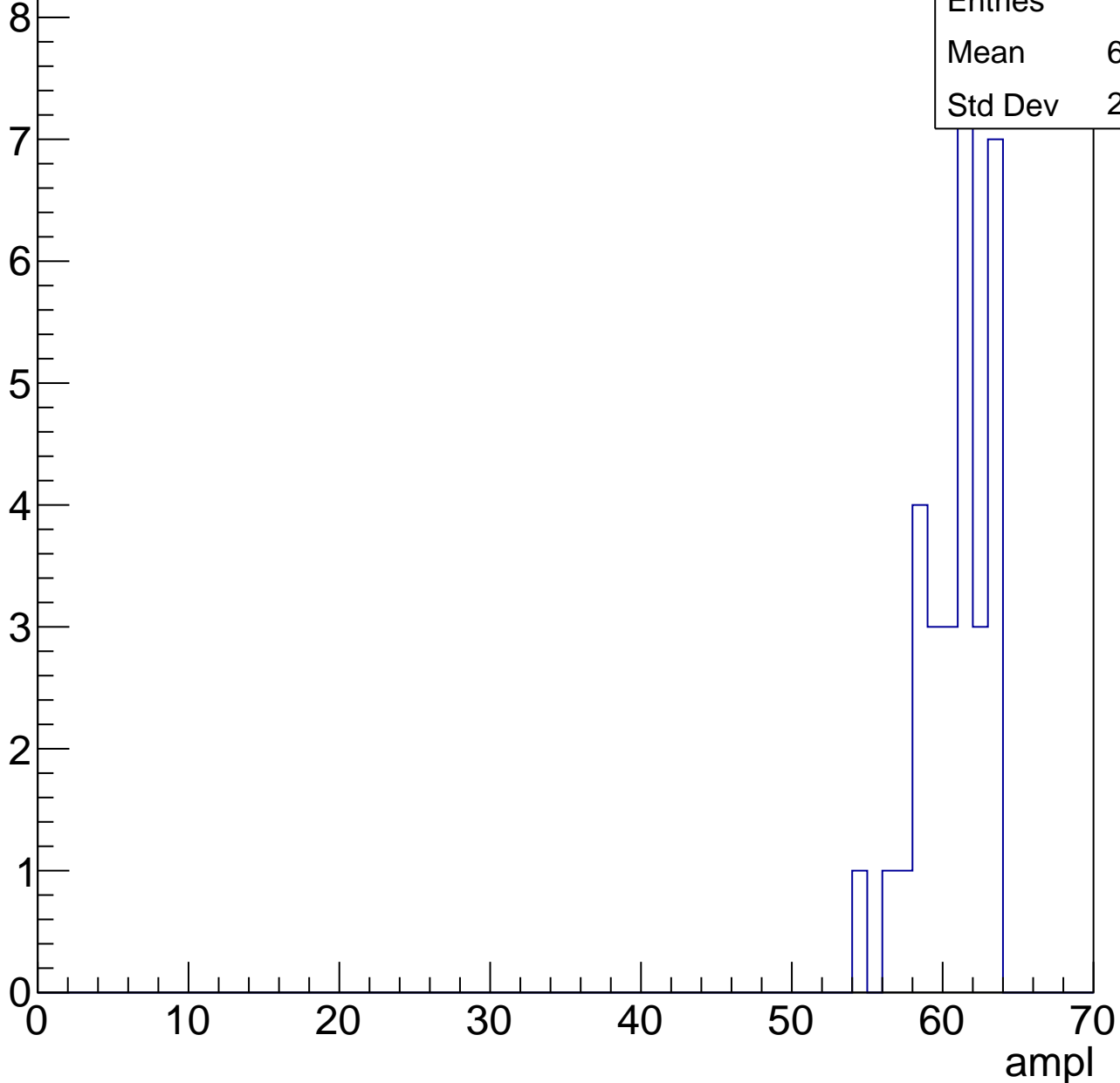


B1L103S, U6-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	60.35
Std Dev	2.265

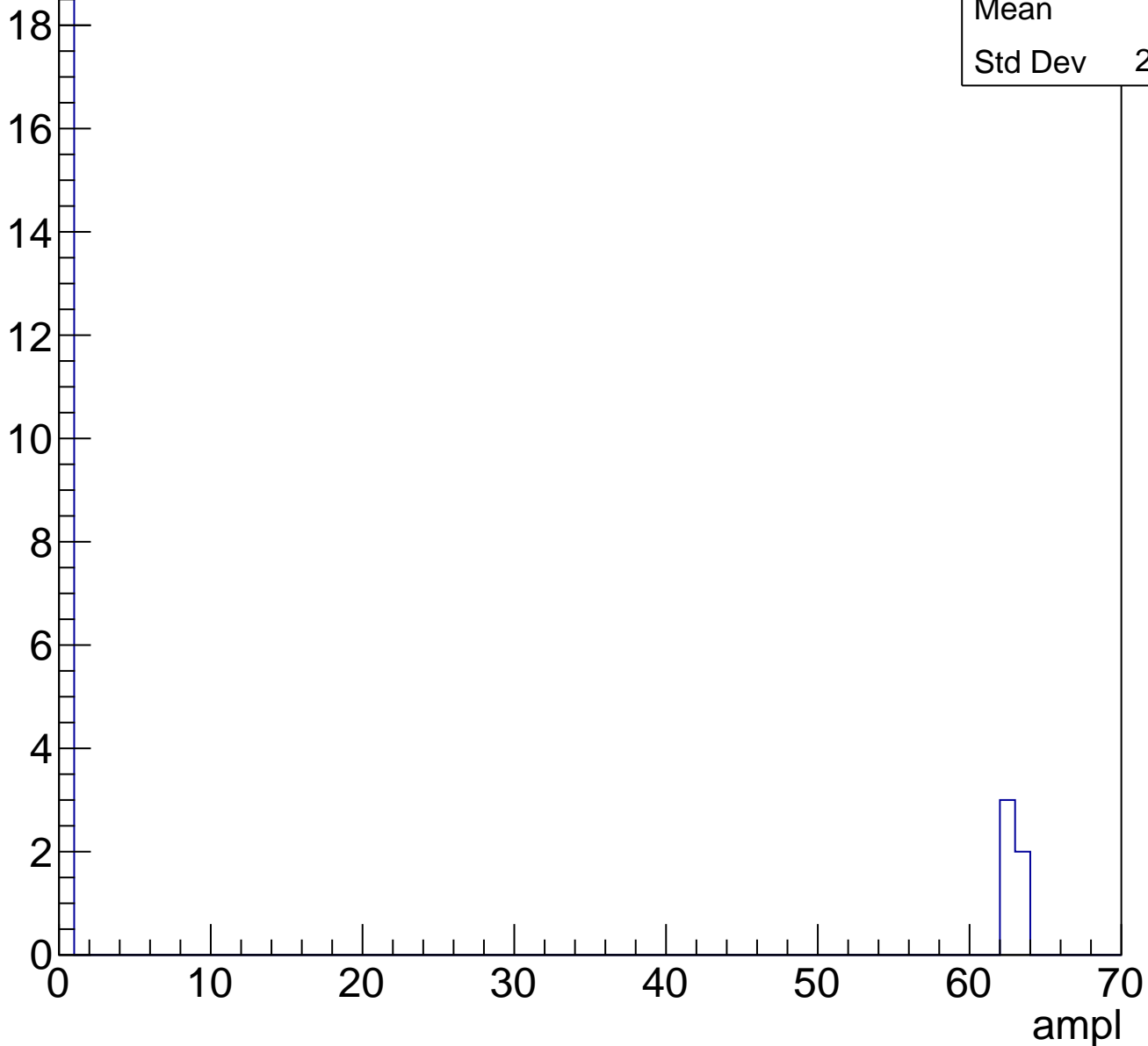


B1L103S, U6-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	13
Std Dev	25.34

Entry



B1L103S, U6-ch97, adc0

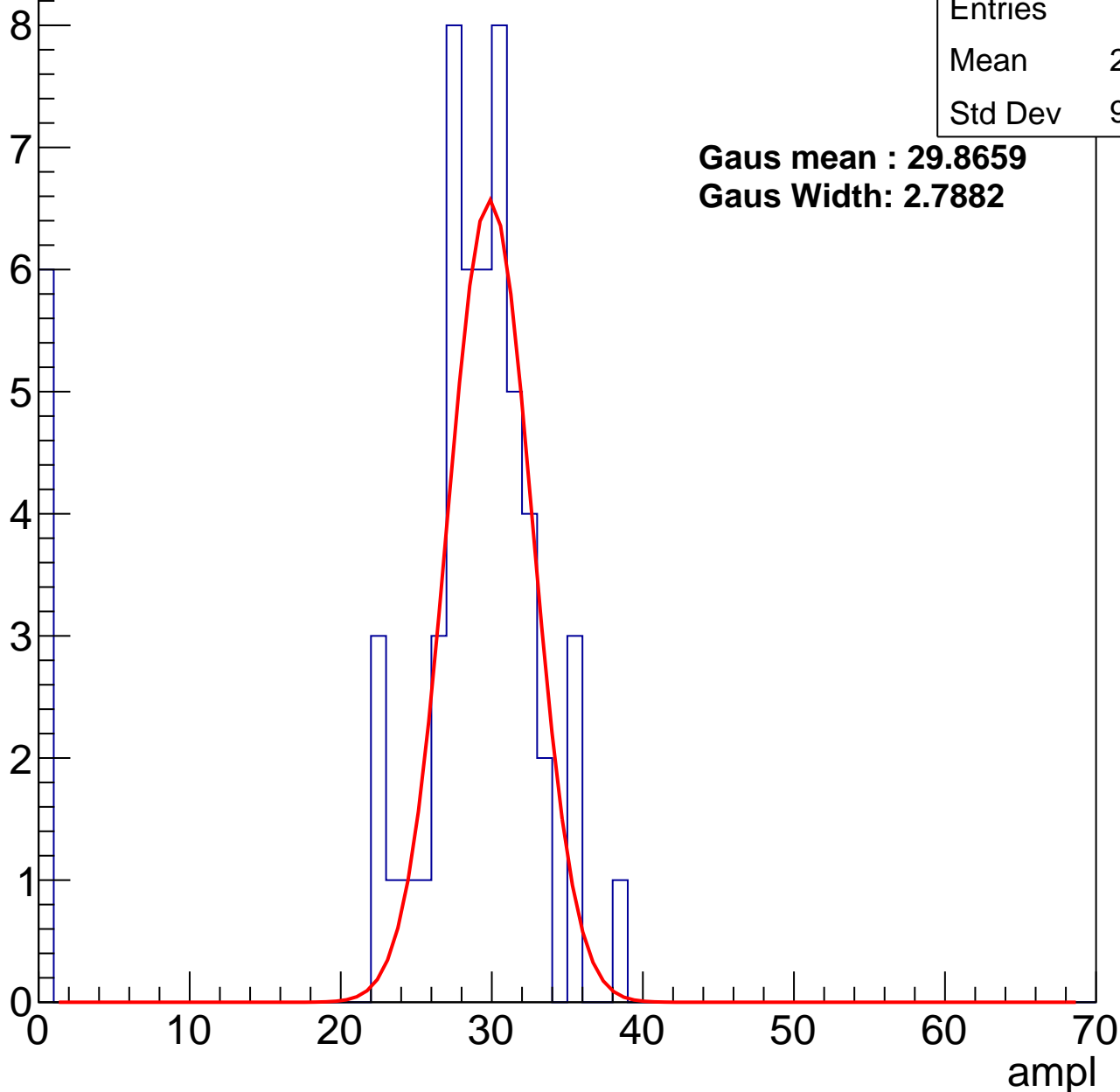
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	25.97
Std Dev	9.364

Gaus mean : 29.8659

Gaus Width: 2.7882



B1L103S, U6-ch97, adc1

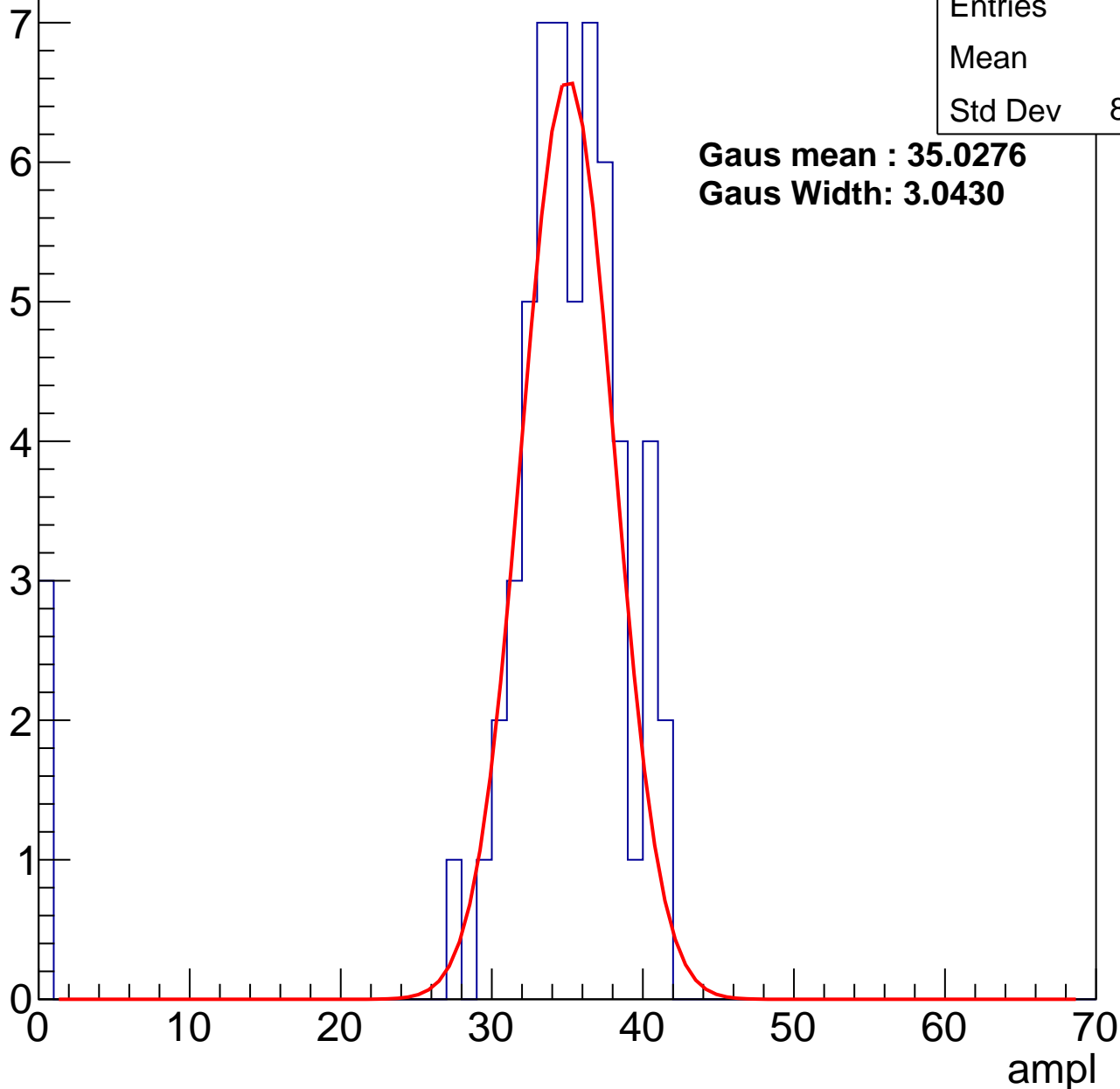
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.1
Std Dev	8.304

Gaus mean : 35.0276

Gaus Width: 3.0430



B1L103S, U6-ch97, adc2

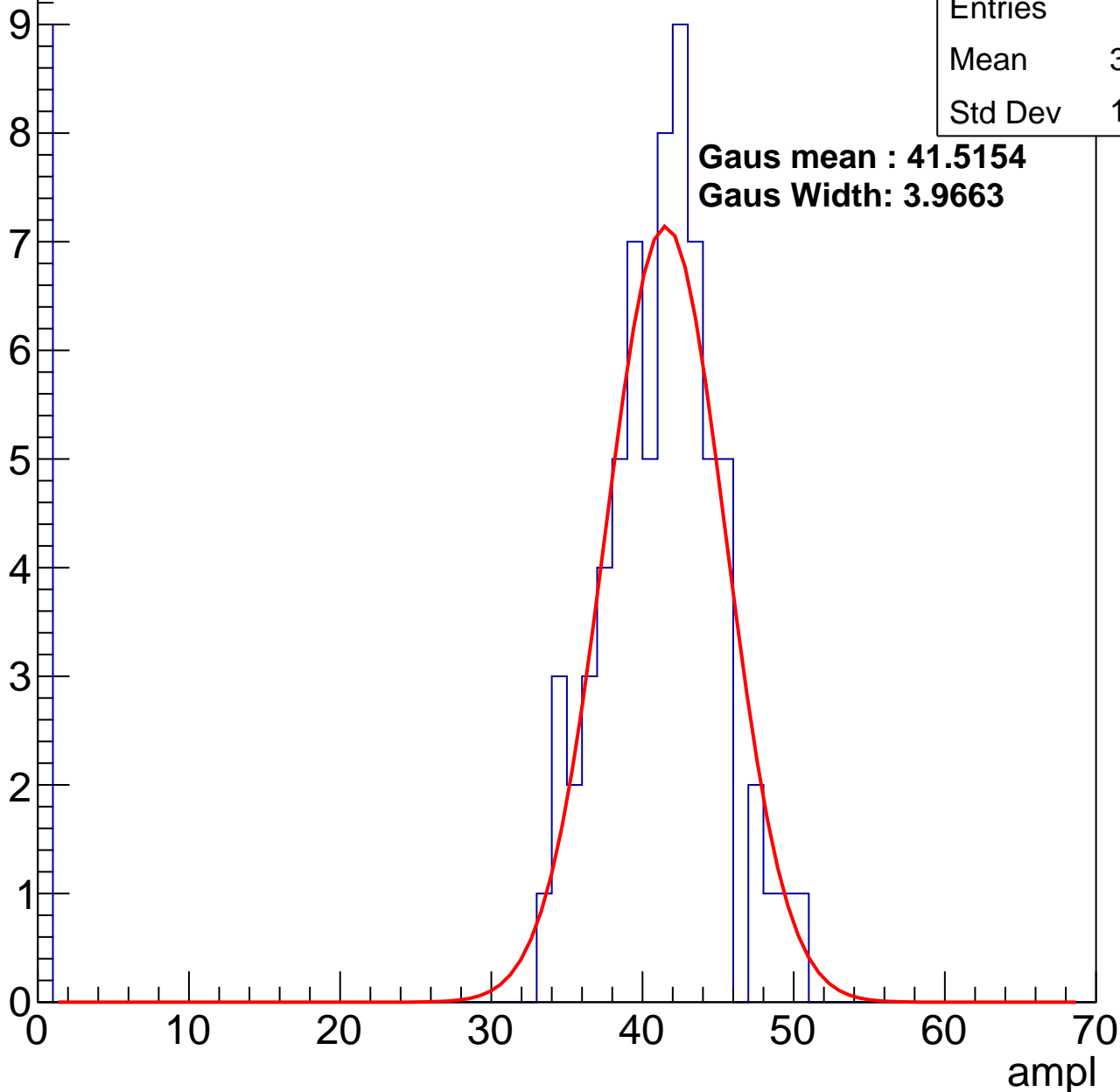
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	36.12
Std Dev	13.49

Gaus mean : 41.5154

Gaus Width: 3.9663

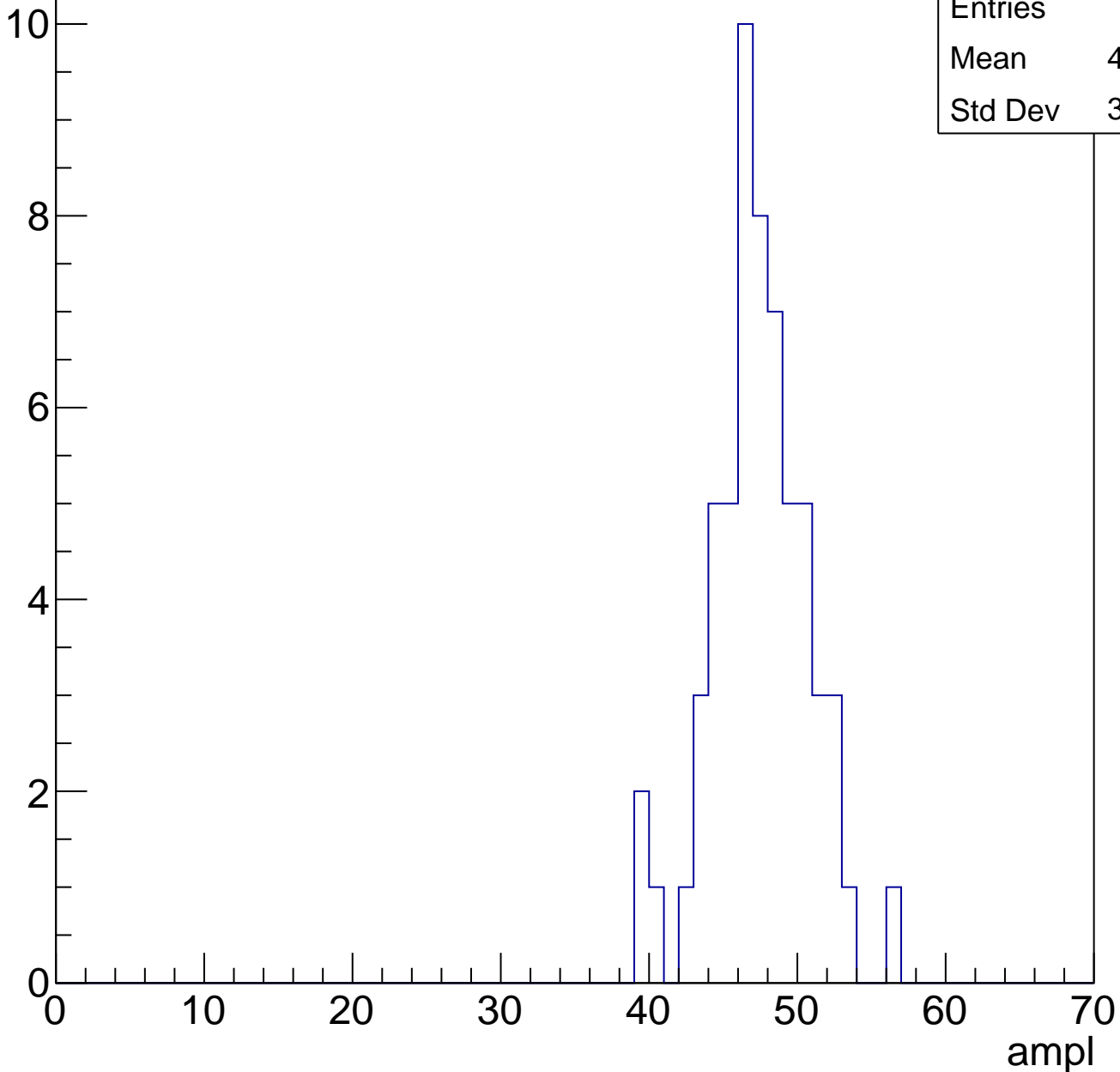


B1L103S, U6-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	46.98
Std Dev	3.258

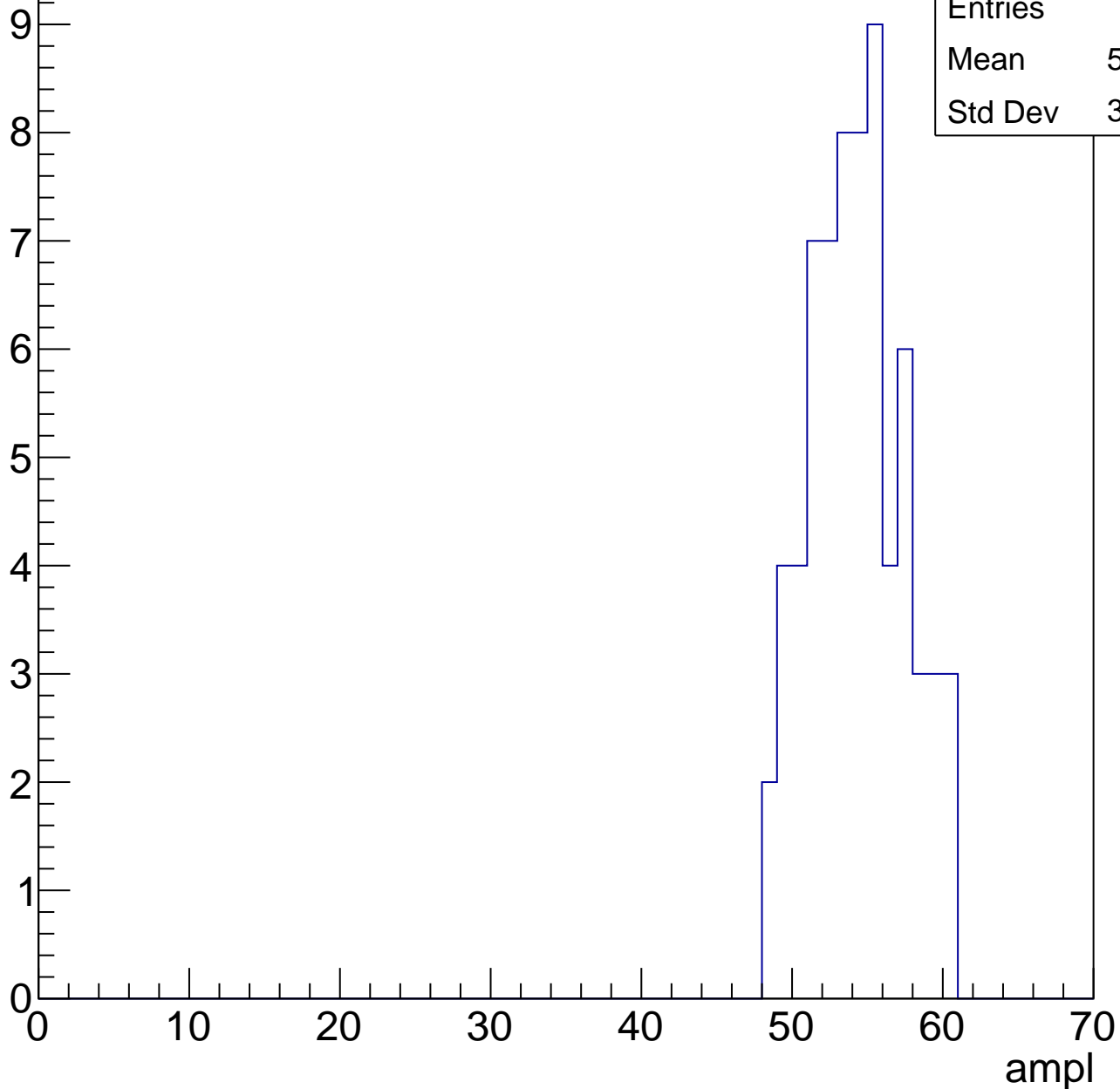
Entry



B1L103S, U6-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



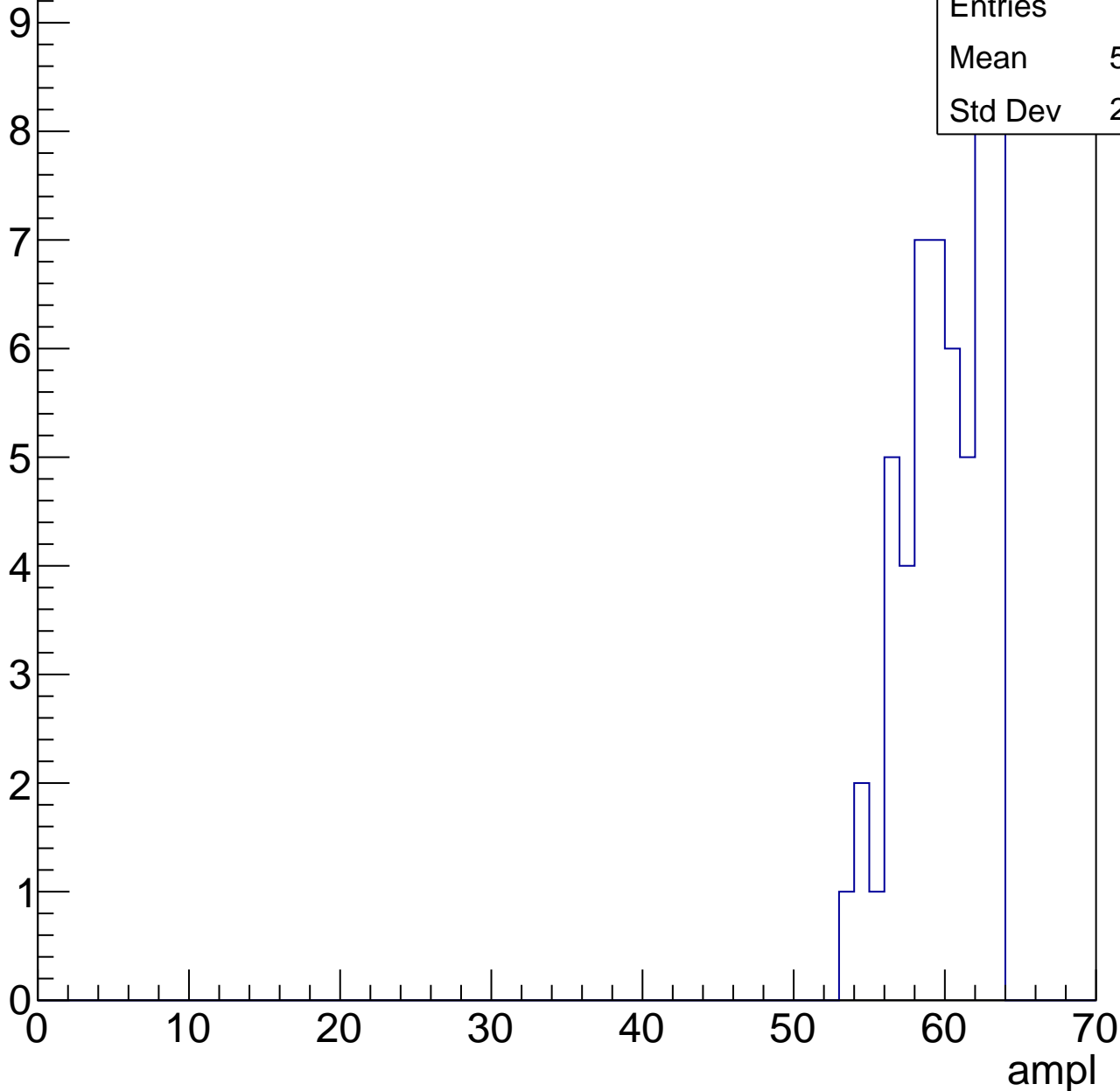
Entries	68
Mean	53.84
Std Dev	3.076

B1L103S, U6-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

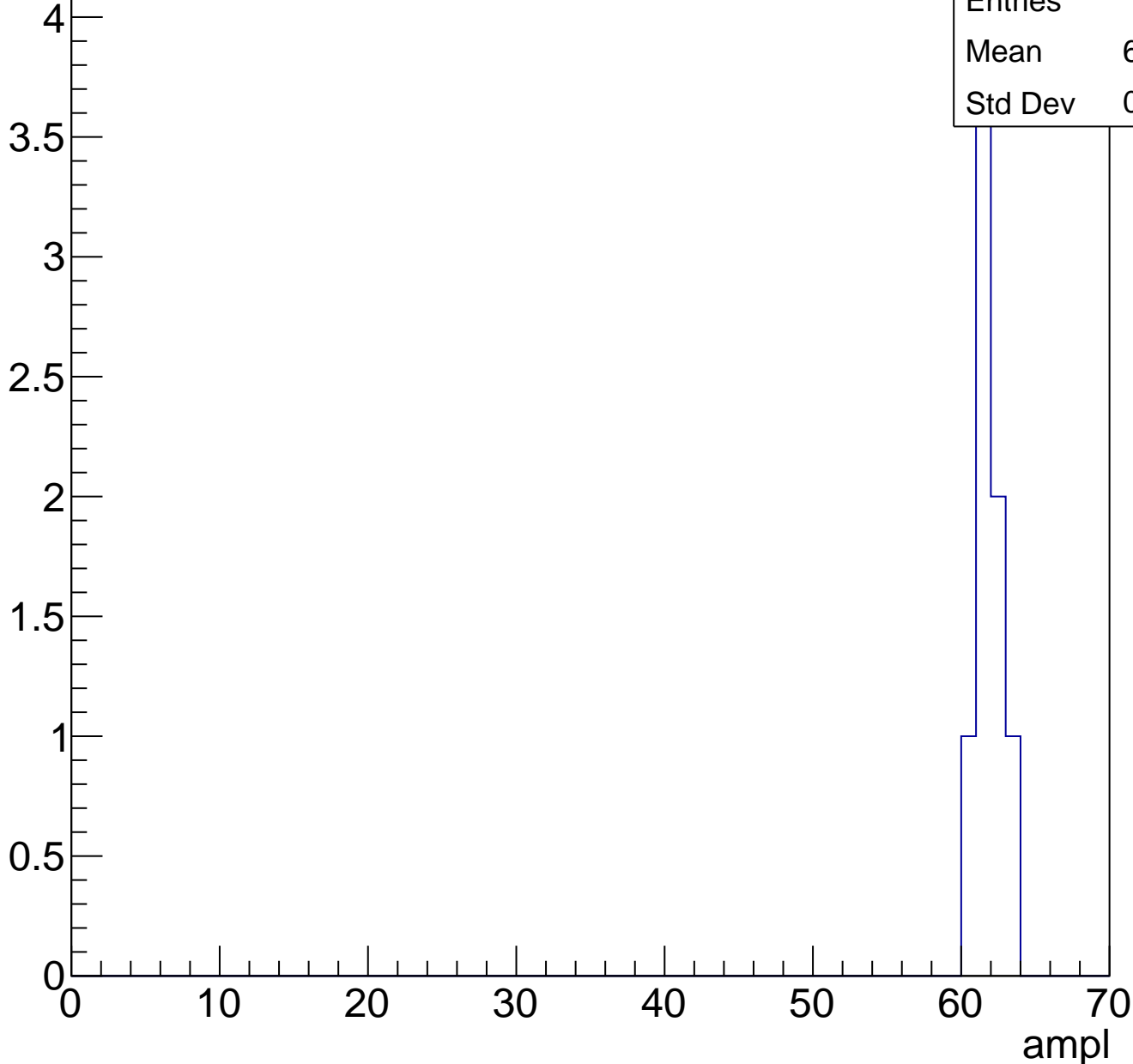
Entries	55
Mean	59.47
Std Dev	2.696



B1L103S, U6-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

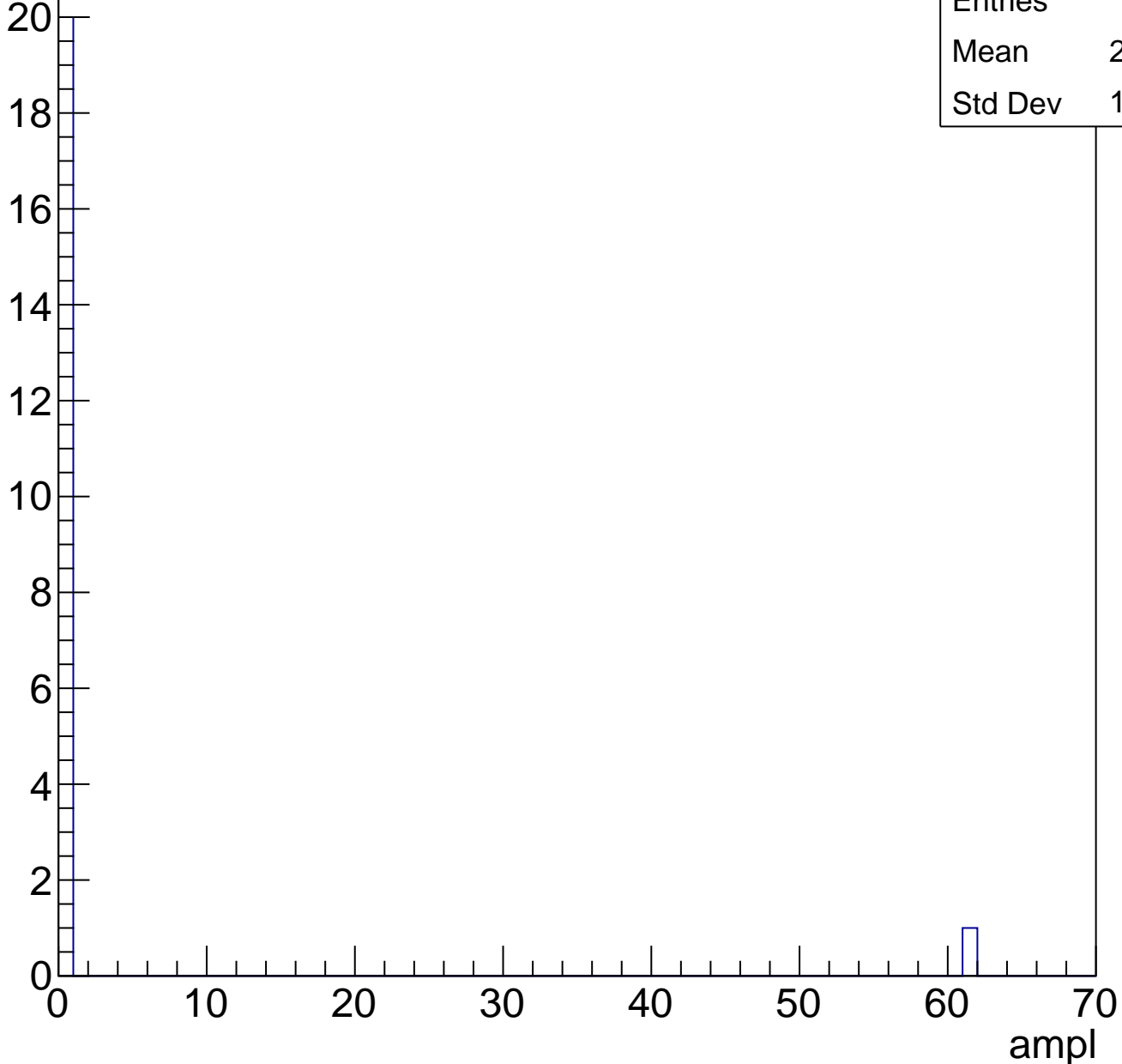


B1L103S, U6-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	2.905
Std Dev	12.99

Entry



B1L103S, U6-ch98, adc0

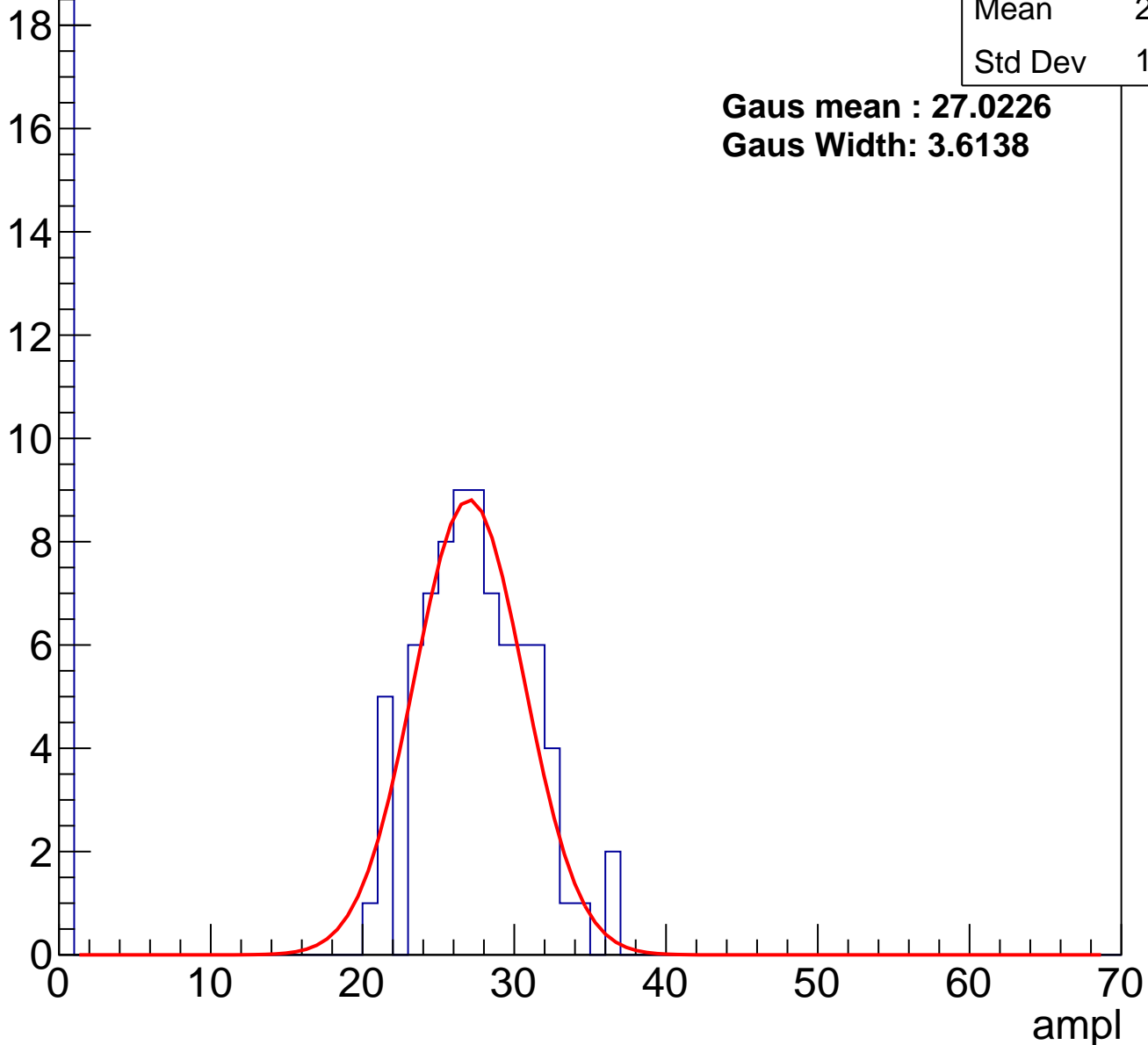
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	21.76
Std Dev	11.19

Gaus mean : 27.0226

Gaus Width: 3.6138

Entry



B1L103S, U6-ch98, adc1

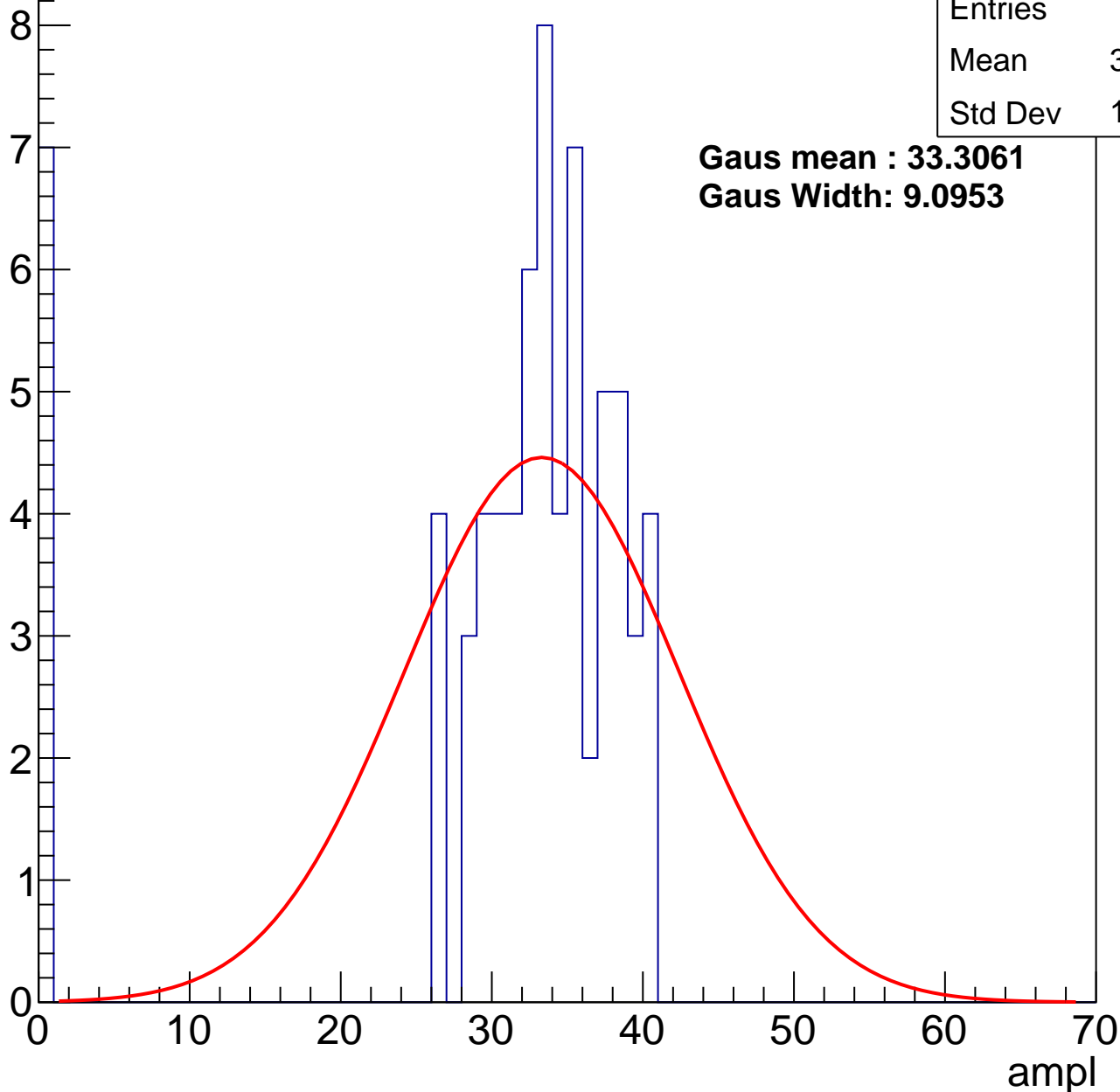
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	30.13
Std Dev	10.69

Gaus mean : 33.3061

Gaus Width: 9.0953



B1L103S, U6-ch98, adc2

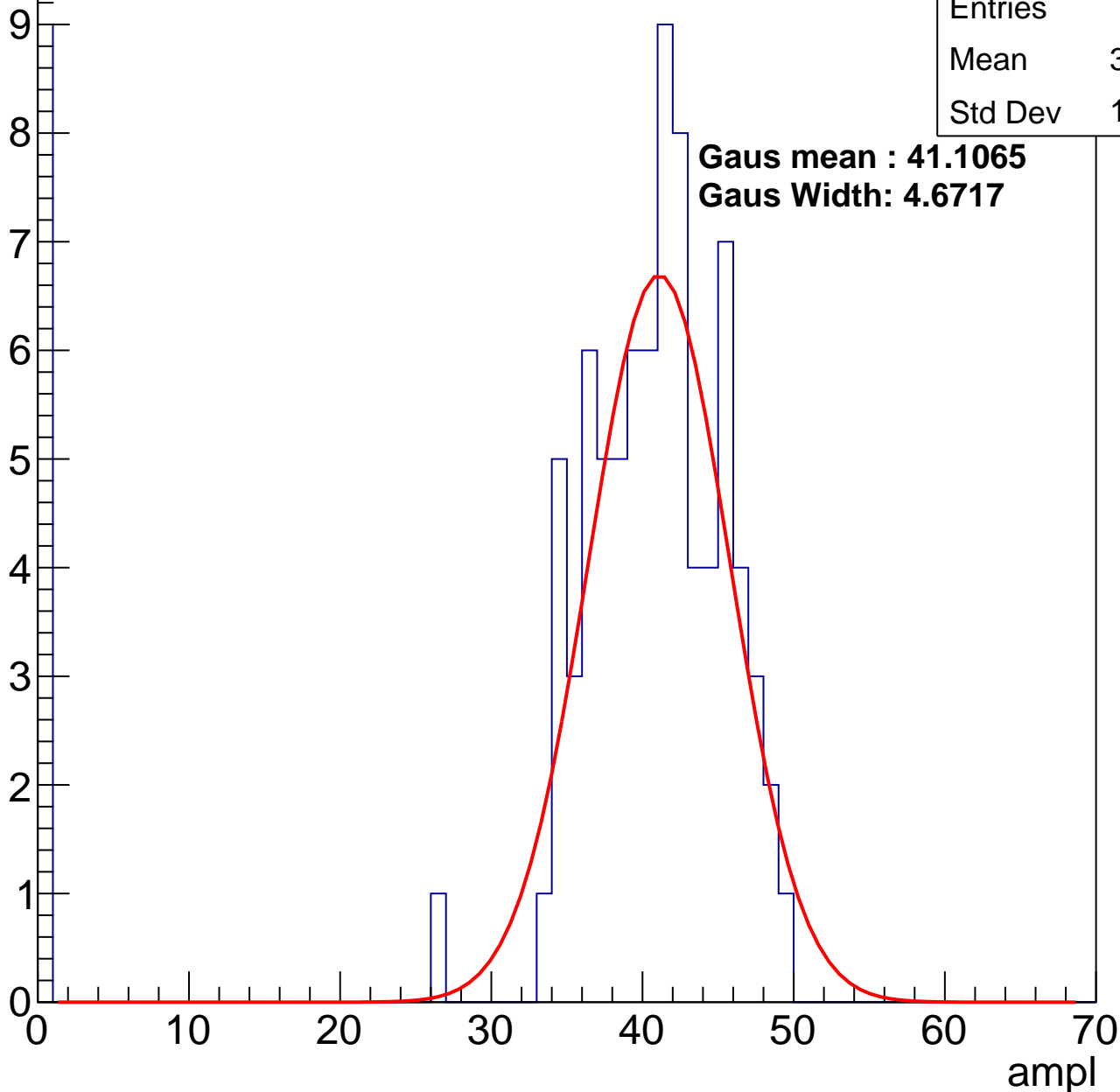
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	36.37
Std Dev	12.86

Gaus mean : 41.1065

Gaus Width: 4.6717

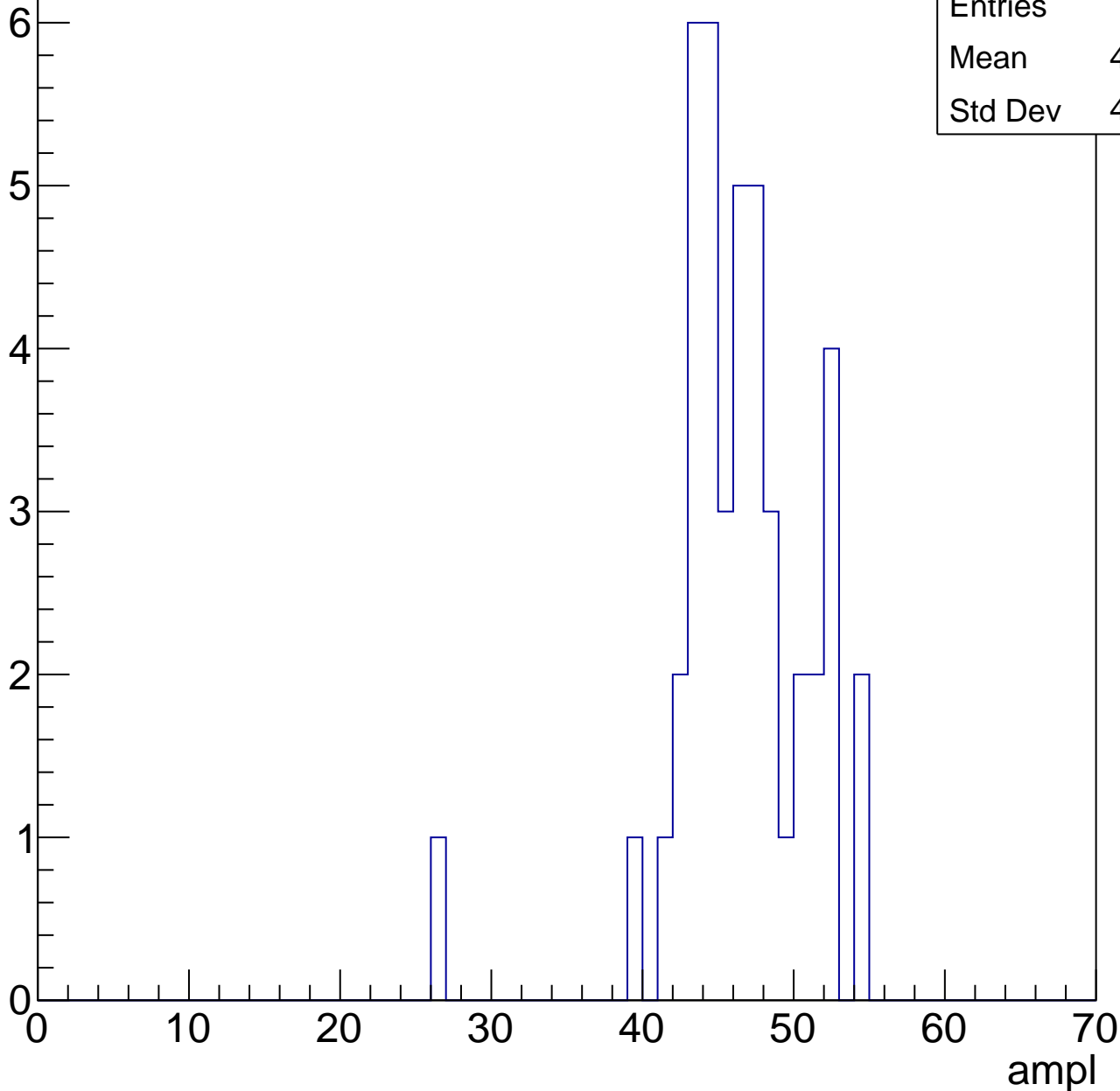


B1L103S, U6-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	45.98
Std Dev	4.688

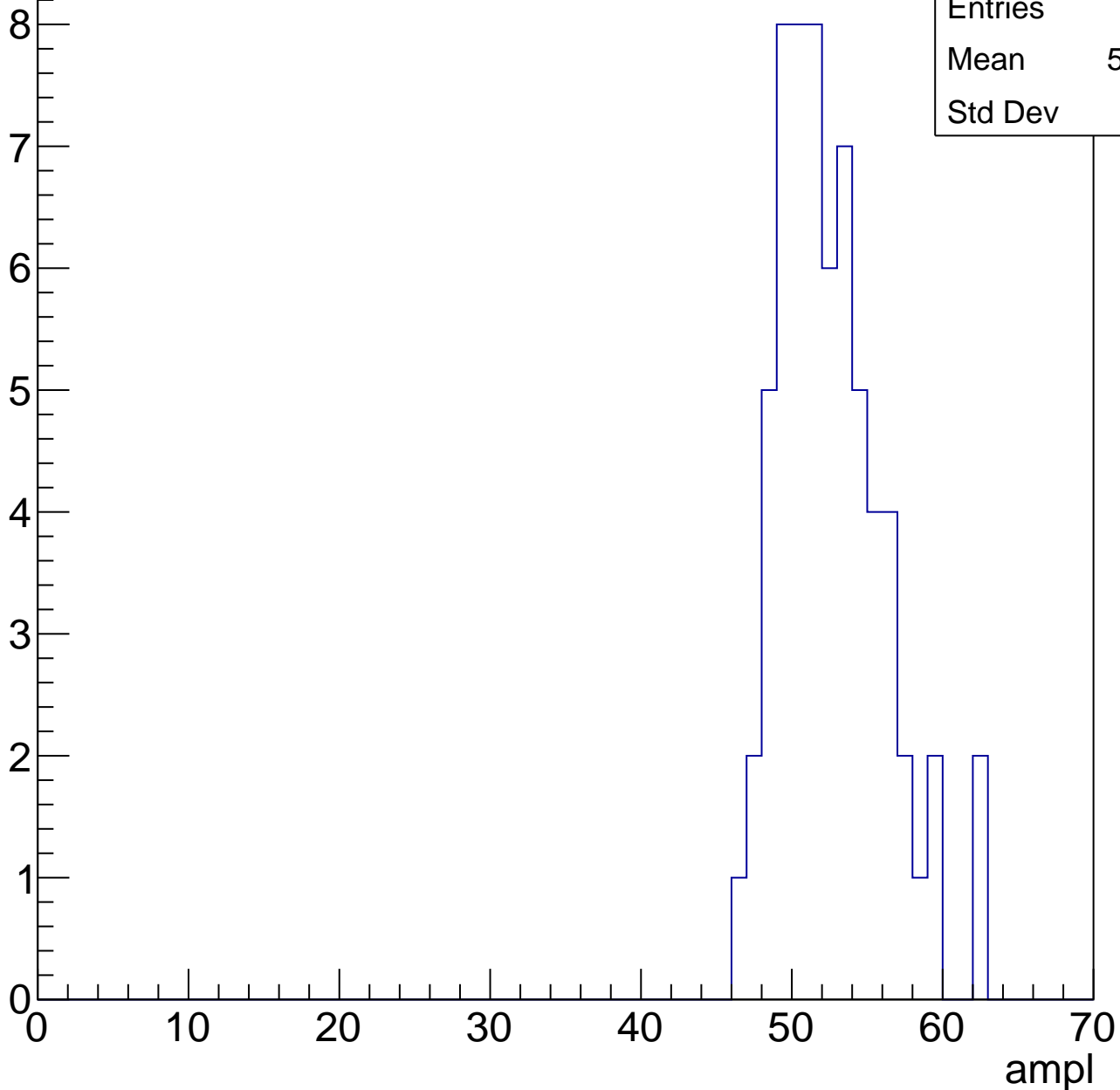


B1L103S, U6-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	52.17
Std Dev	3.48

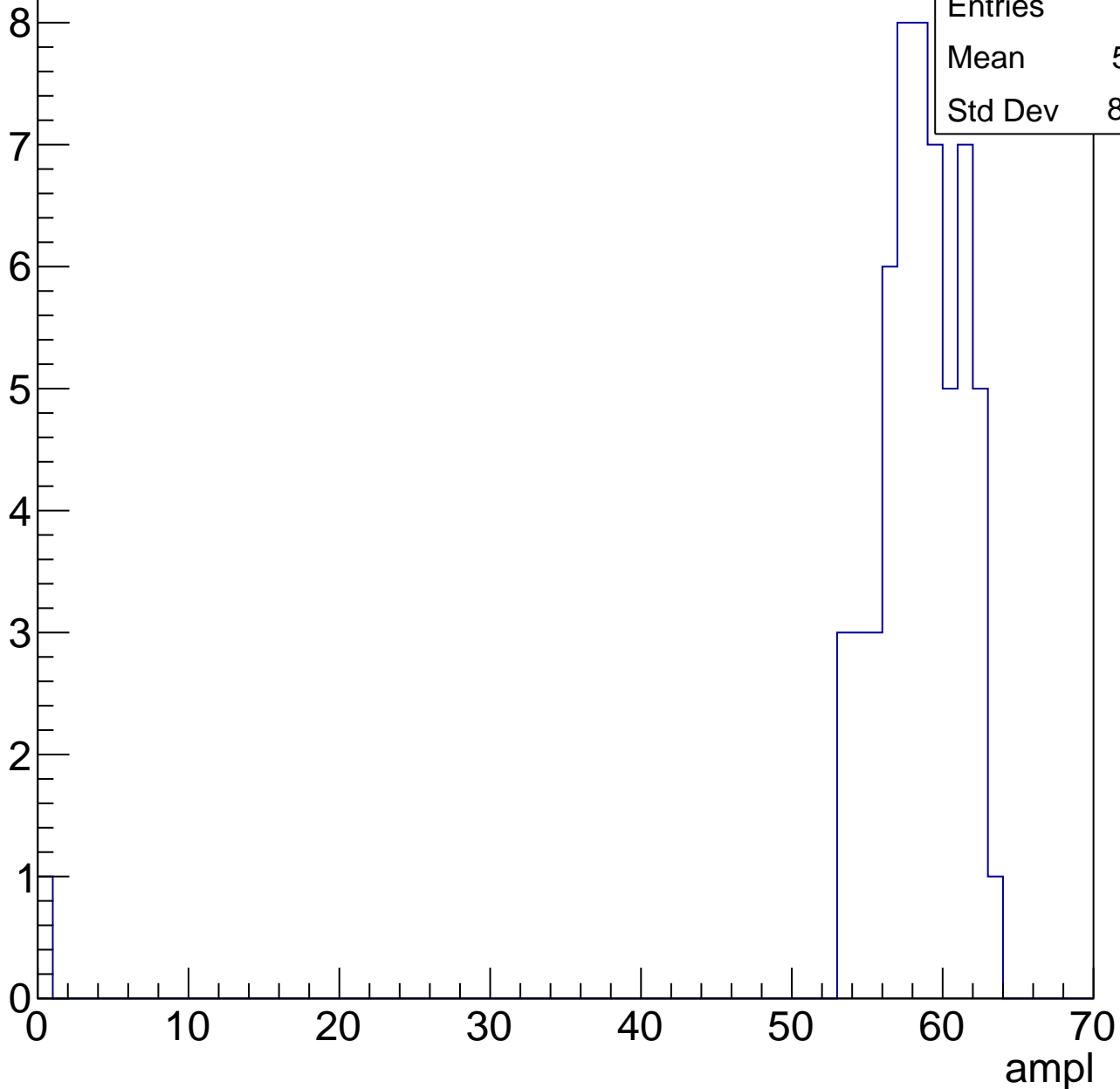


B1L103S, U6-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	57.11
Std Dev	8.052

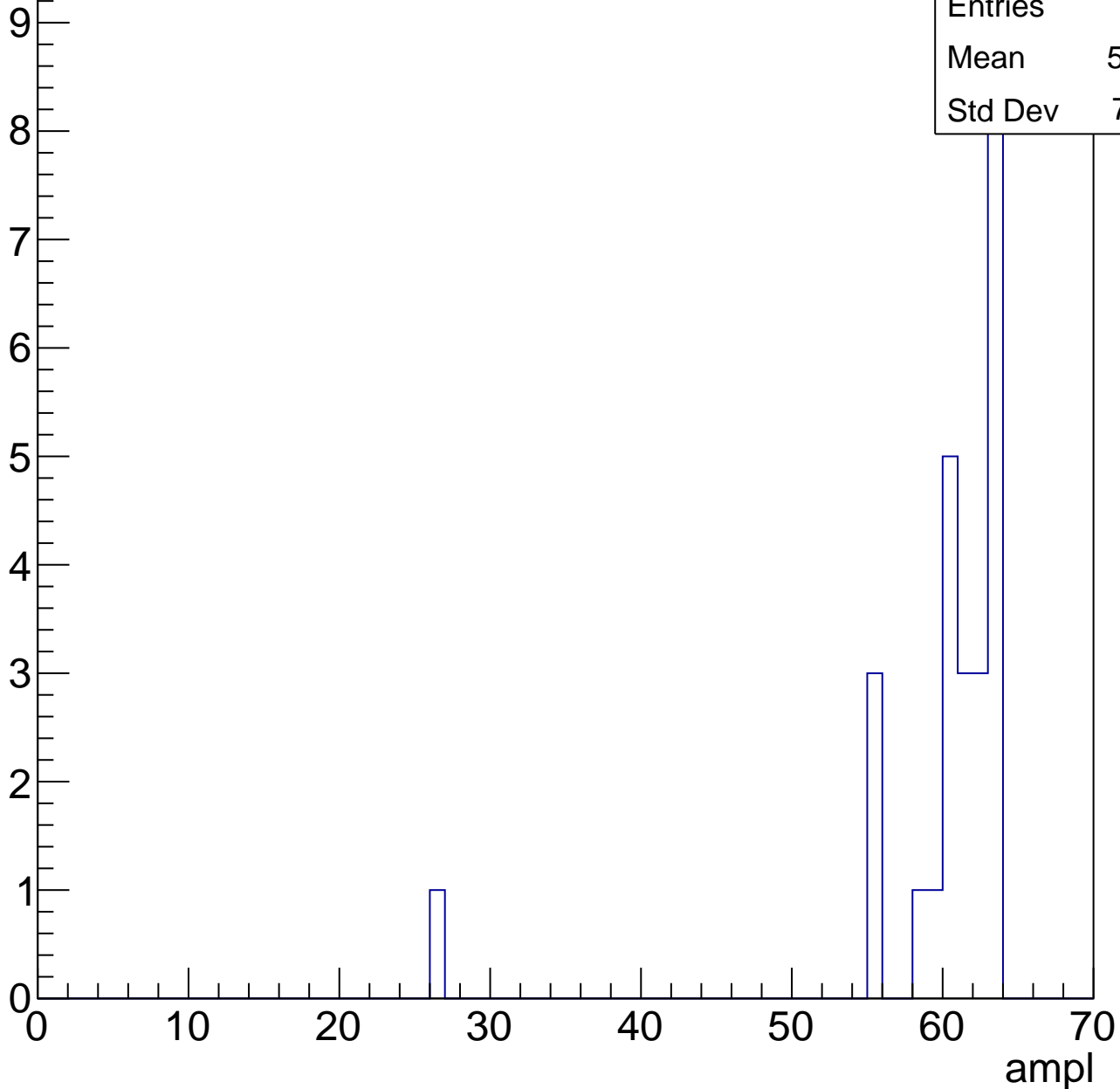


B1L103S, U6-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

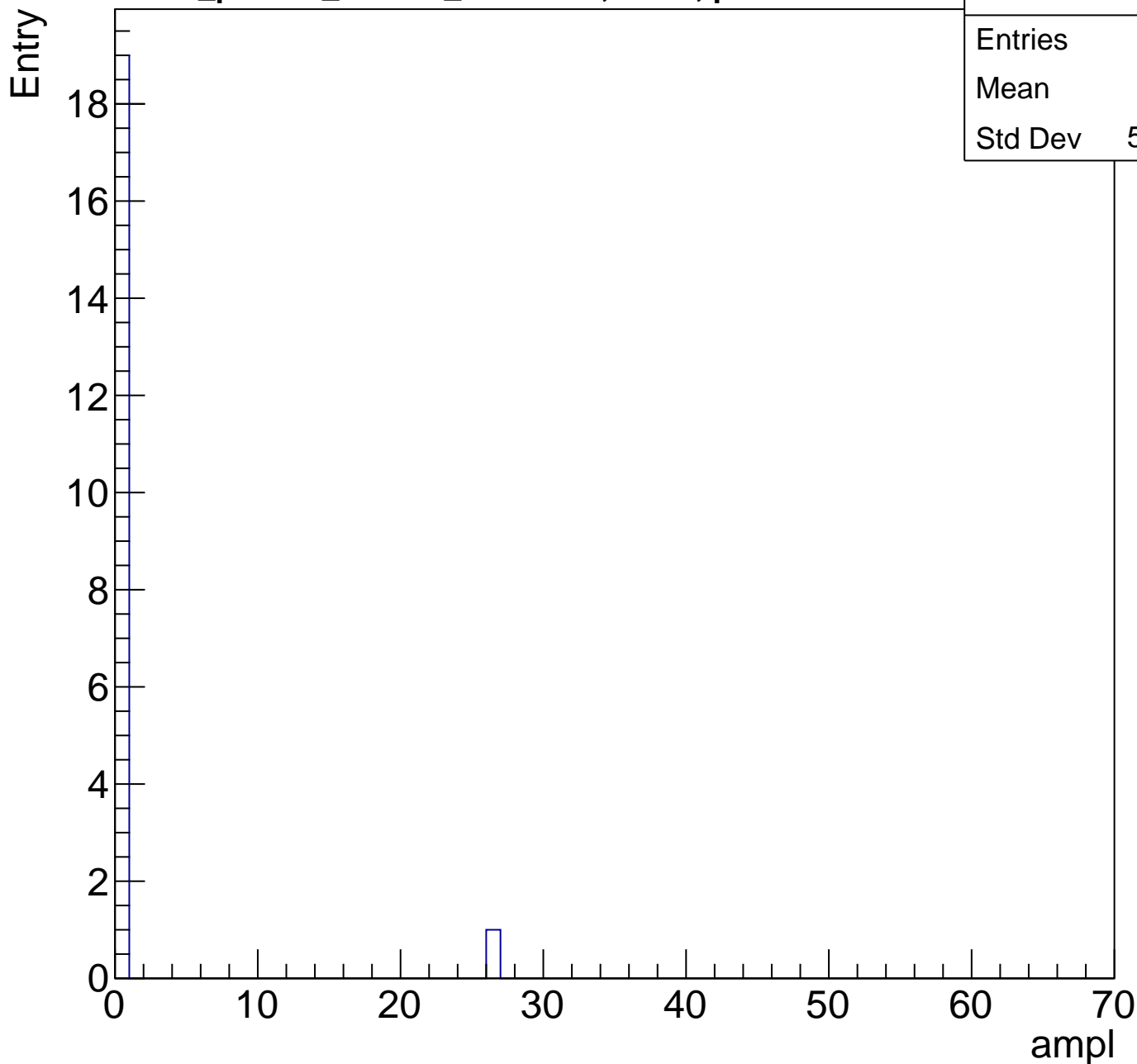
Entries	26
Mean	59.38
Std Dev	7.131



B1L103S, U6-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	1.3
Std Dev	5.667



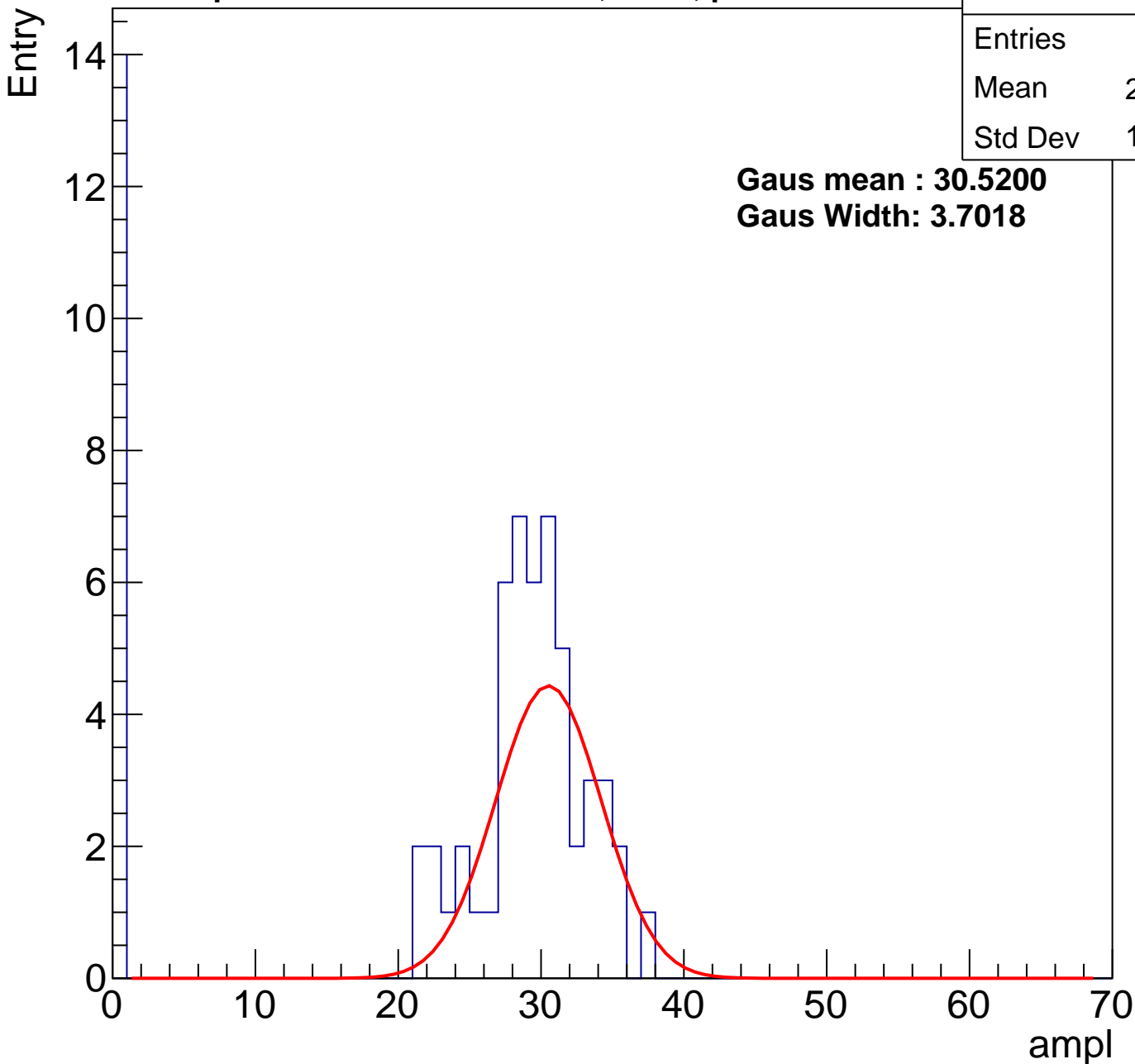
B1L103S, U6-ch99, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	22.72
Std Dev	12.33

Gaus mean : 30.5200

Gaus Width: 3.7018



B1L103S, U6-ch99, adc1

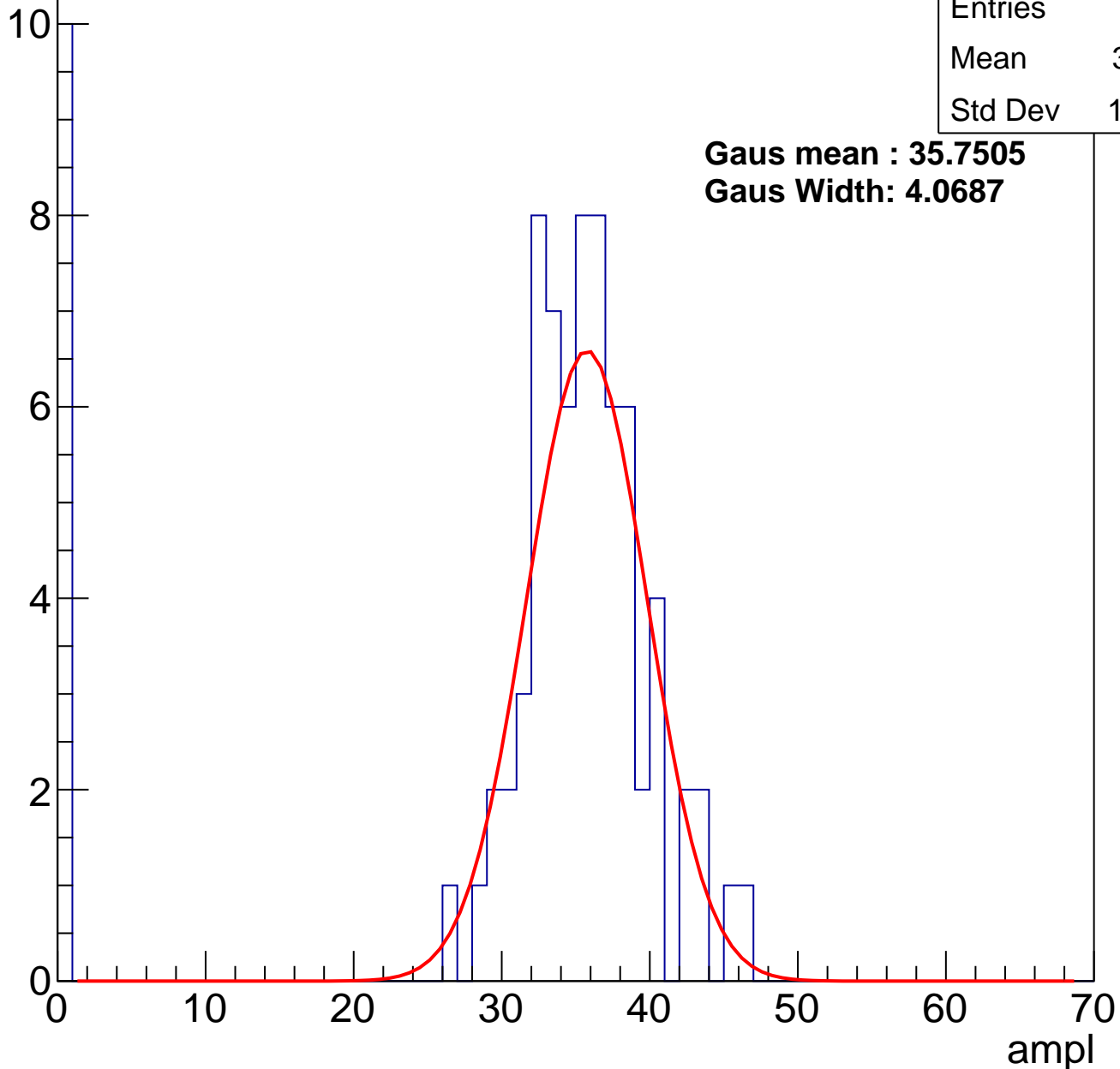
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	30.91
Std Dev	12.24

Gaus mean : 35.7505

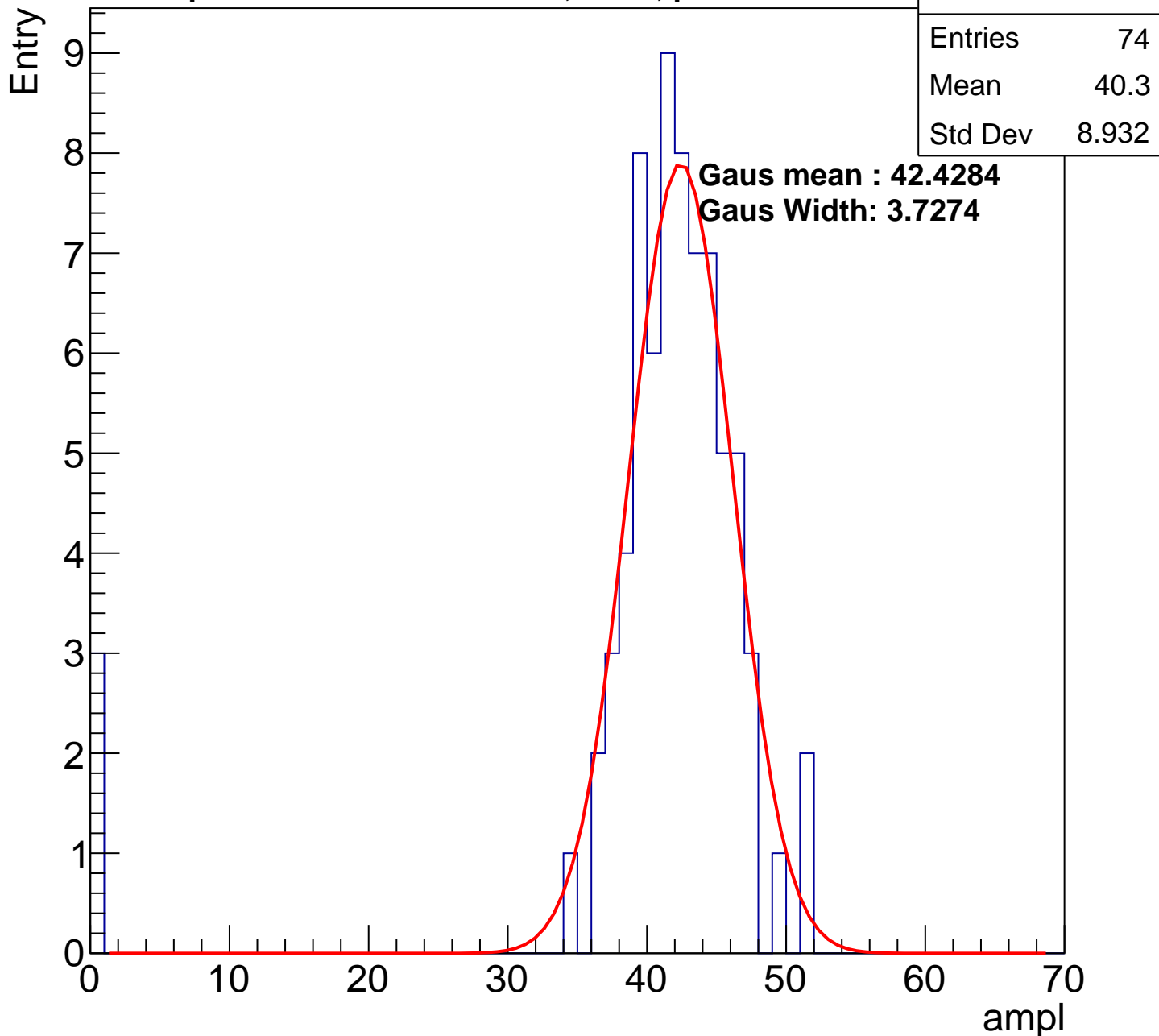
Gaus Width: 4.0687

Entry



B1L103S, U6-ch99, adc2

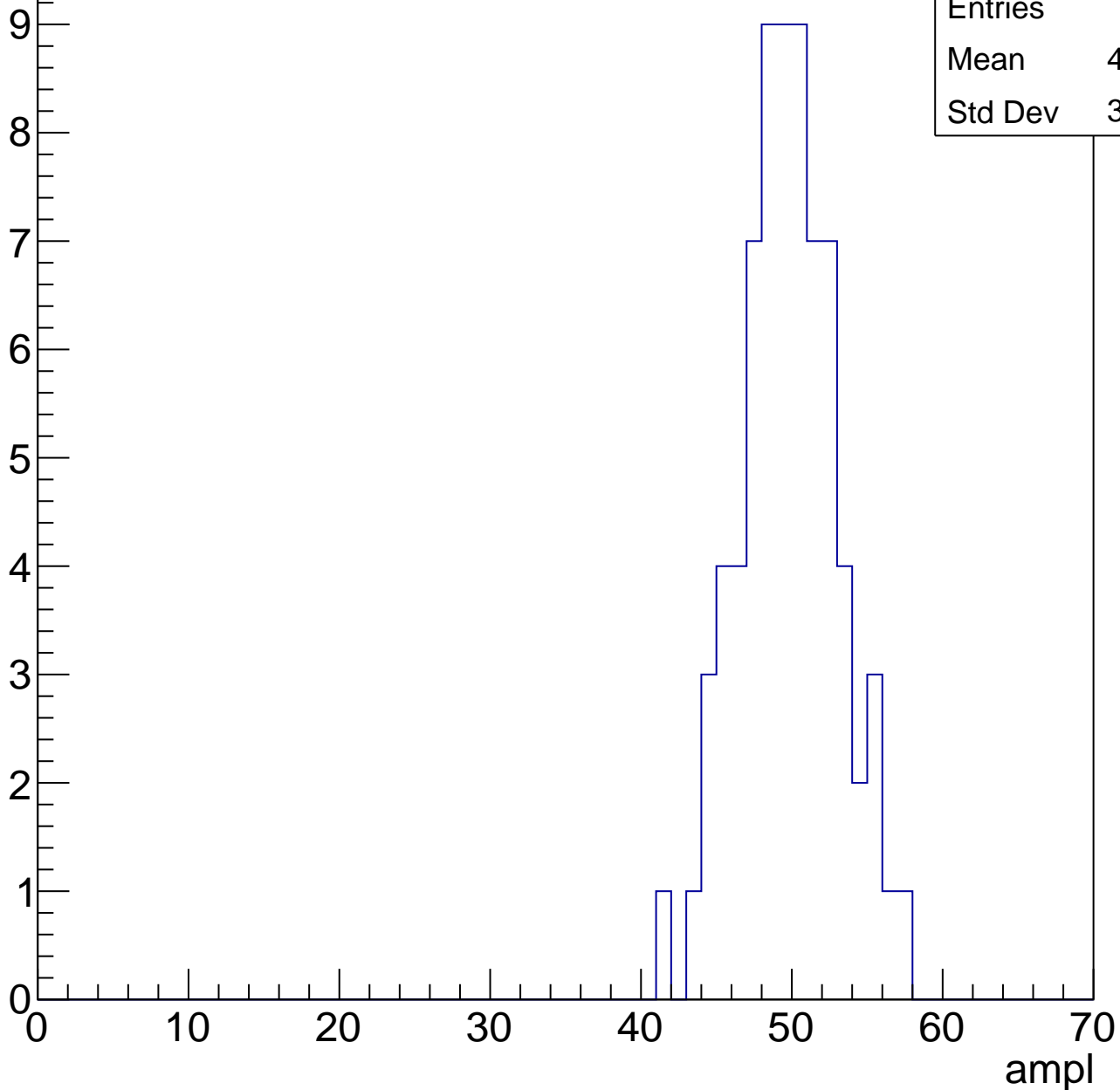
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



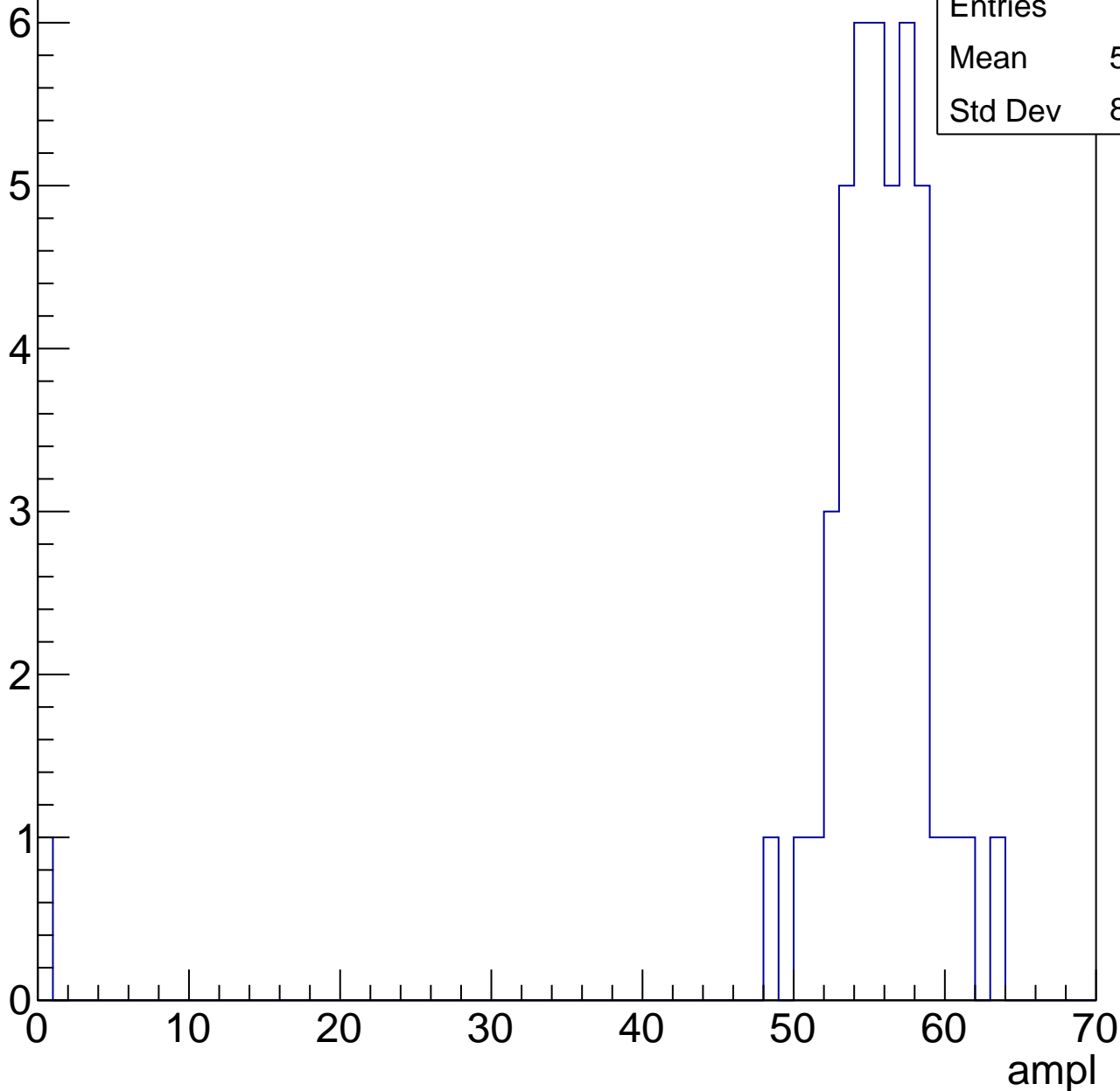
Entries	72
Mean	49.32
Std Dev	3.205

B1L103S, U6-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	54.07
Std Dev	8.719

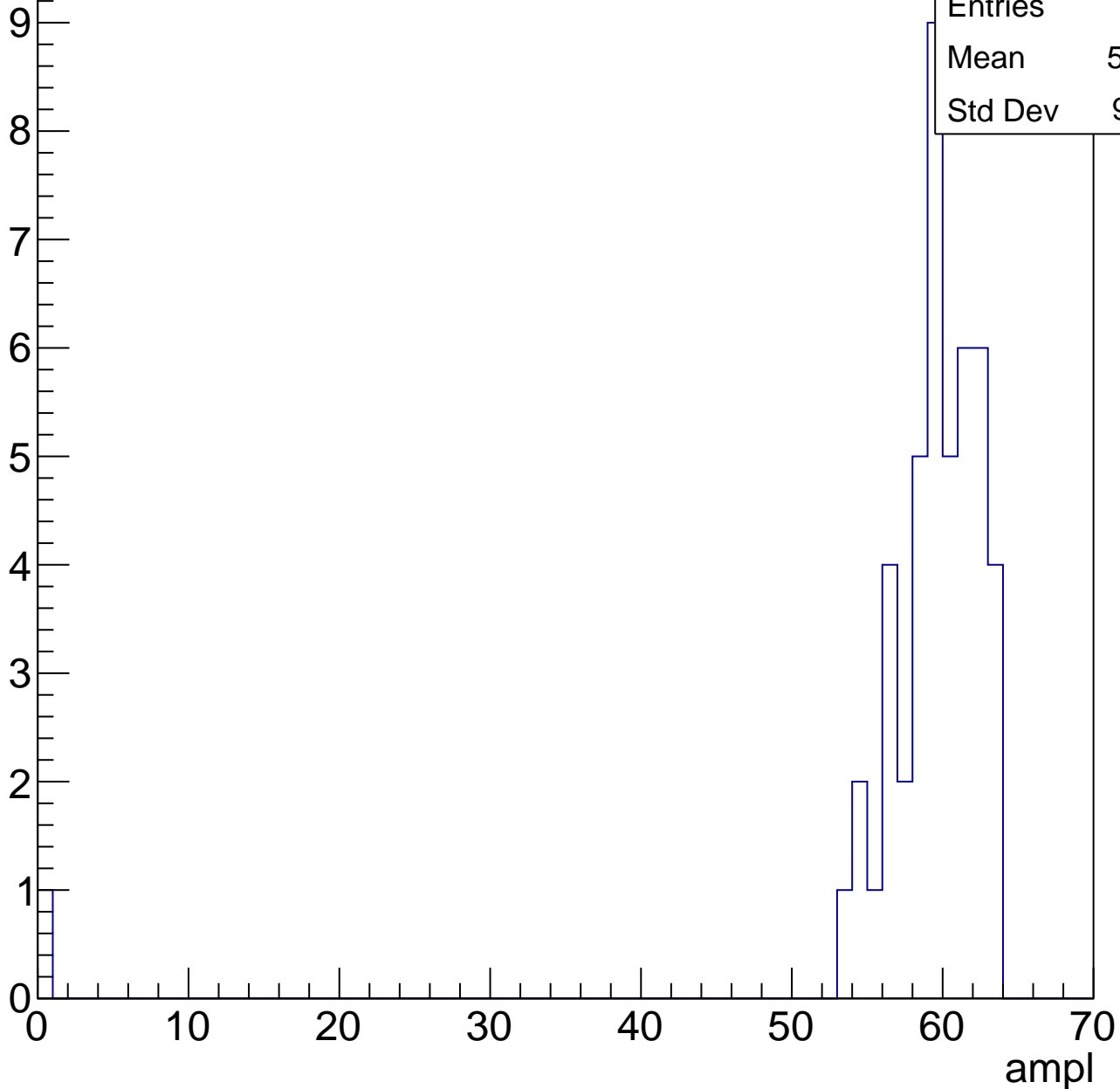


B1L103S, U6-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	57.93
Std Dev	9.001

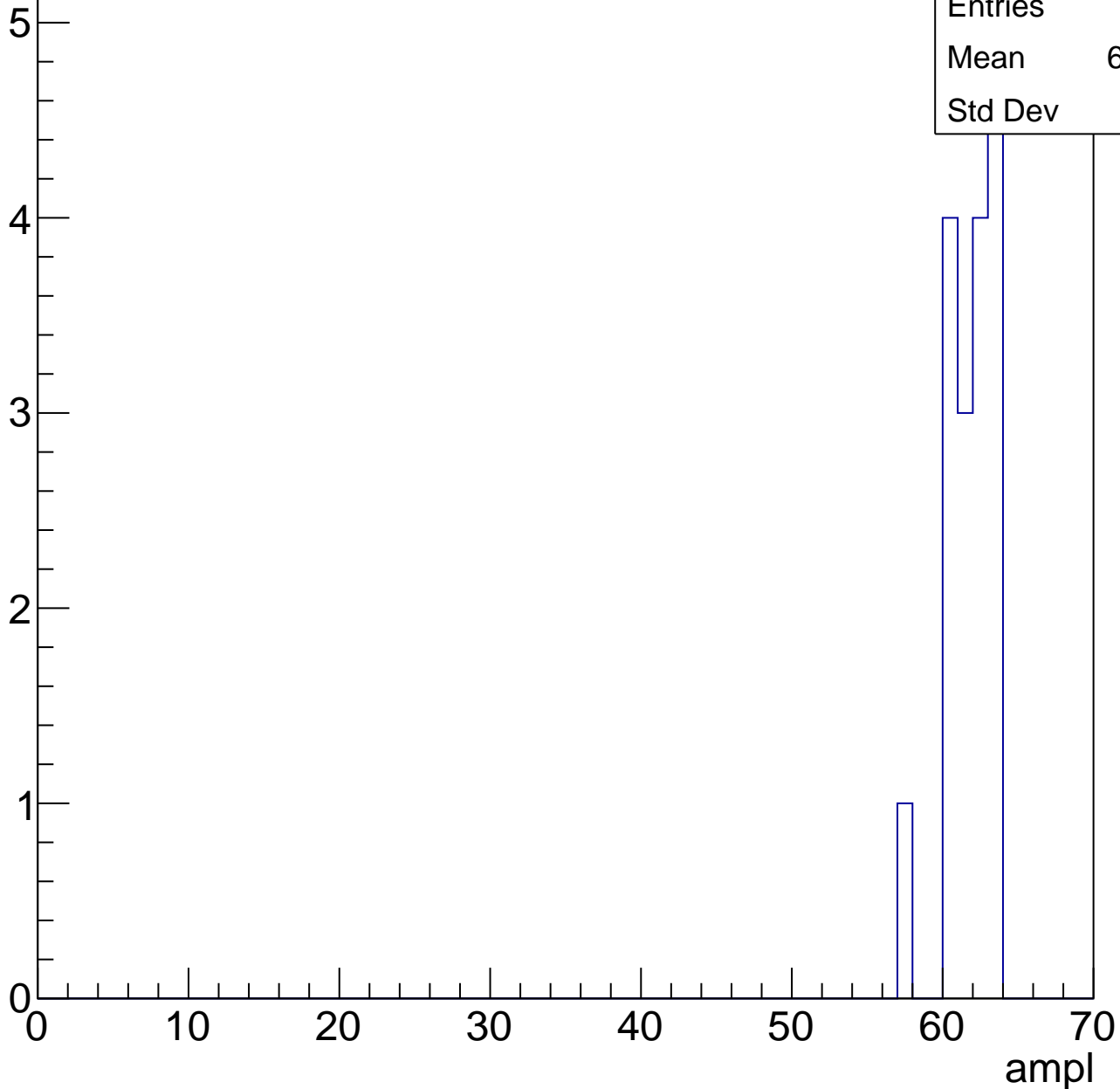


B1L103S, U6-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.57



B1L103S, U6-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U6-ch100, adc0

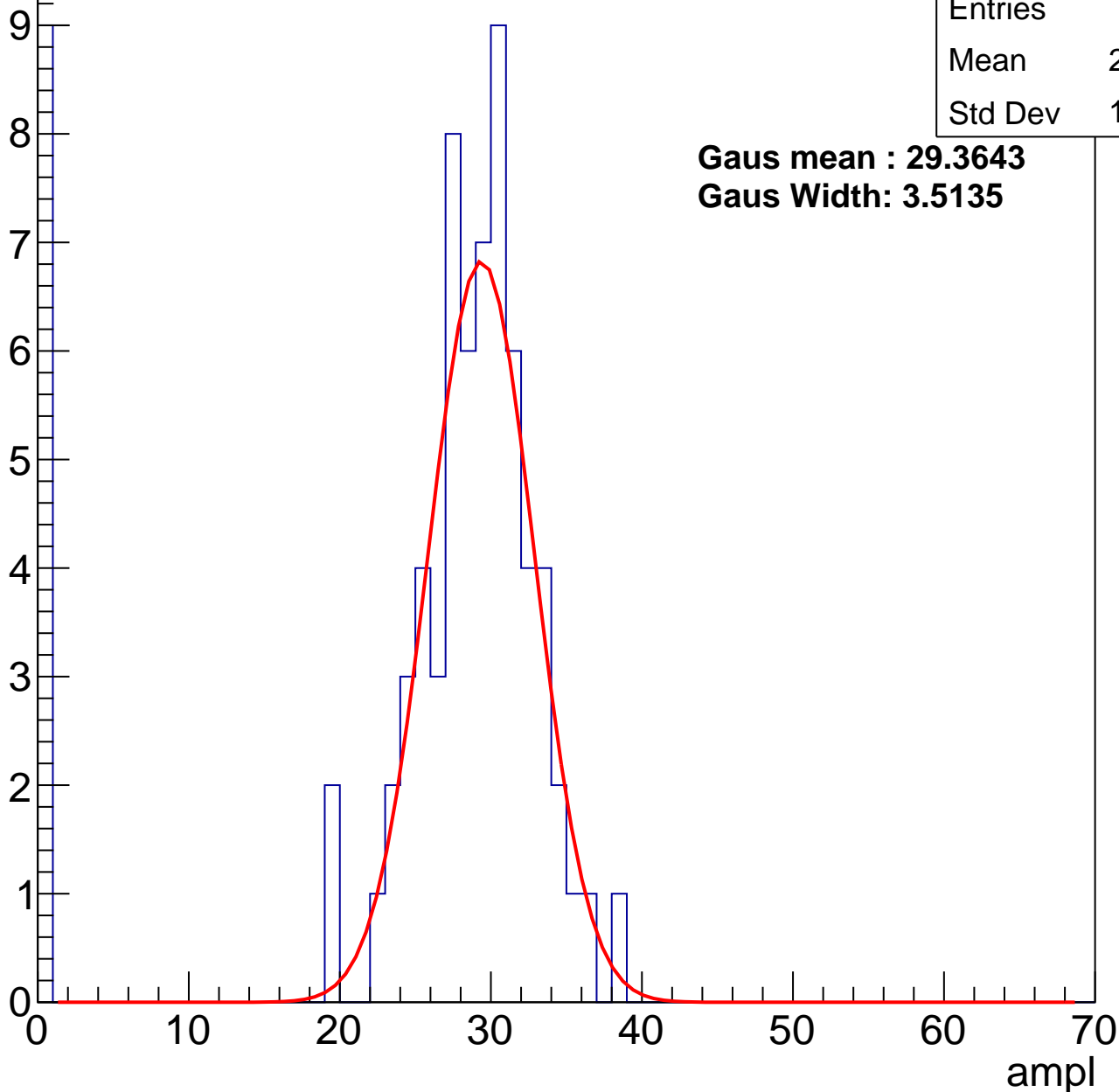
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	25.15
Std Dev	10.04

Gaus mean : 29.3643

Gaus Width: 3.5135



B1L103S, U6-ch100, adc1

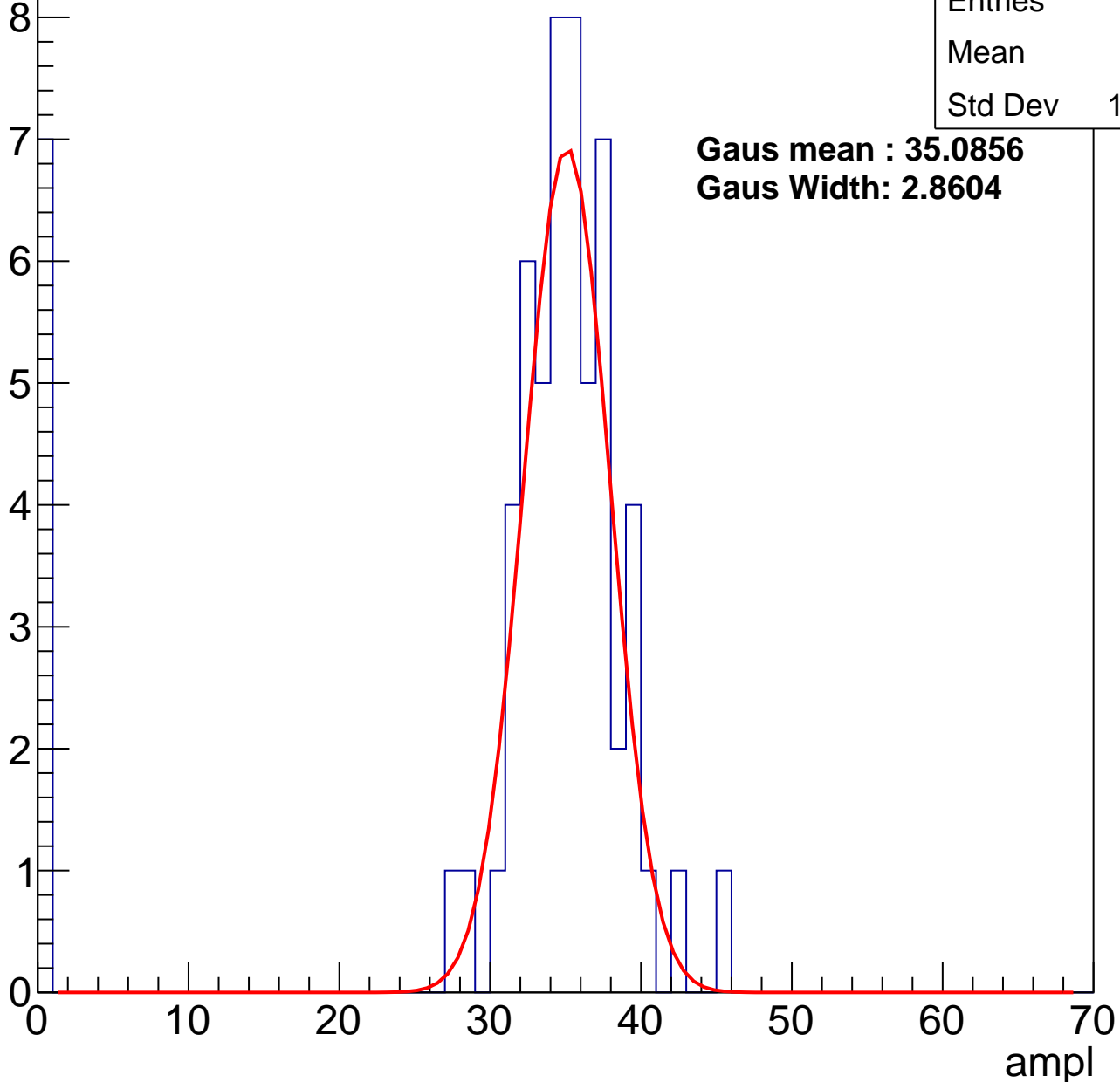
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	30.9
Std Dev	11.44

Gaus mean : 35.0856

Gaus Width: 2.8604



B1L103S, U6-ch100, adc2

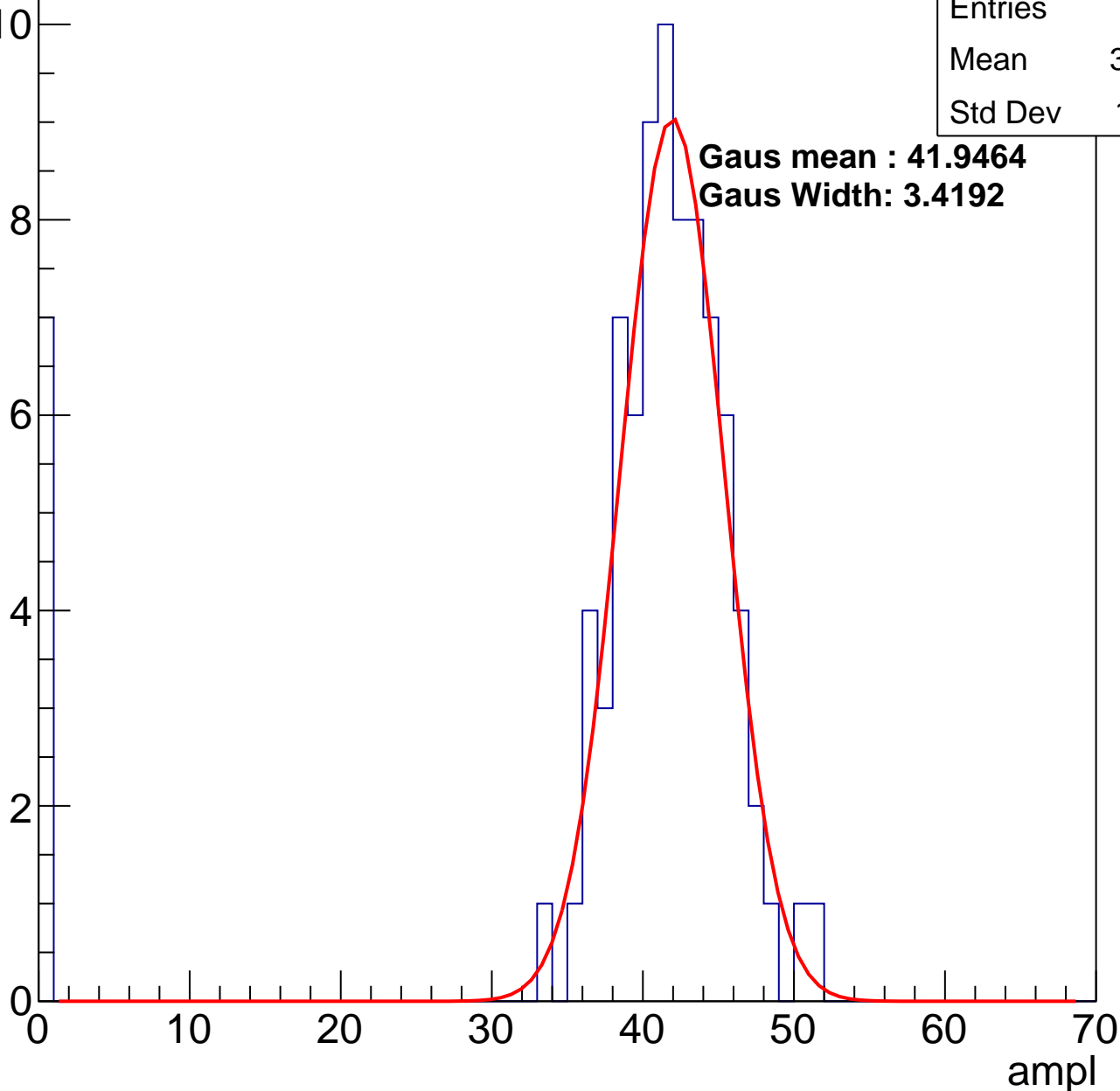
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	38.12
Std Dev	11.81

Gaus mean : 41.9464

Gaus Width: 3.4192

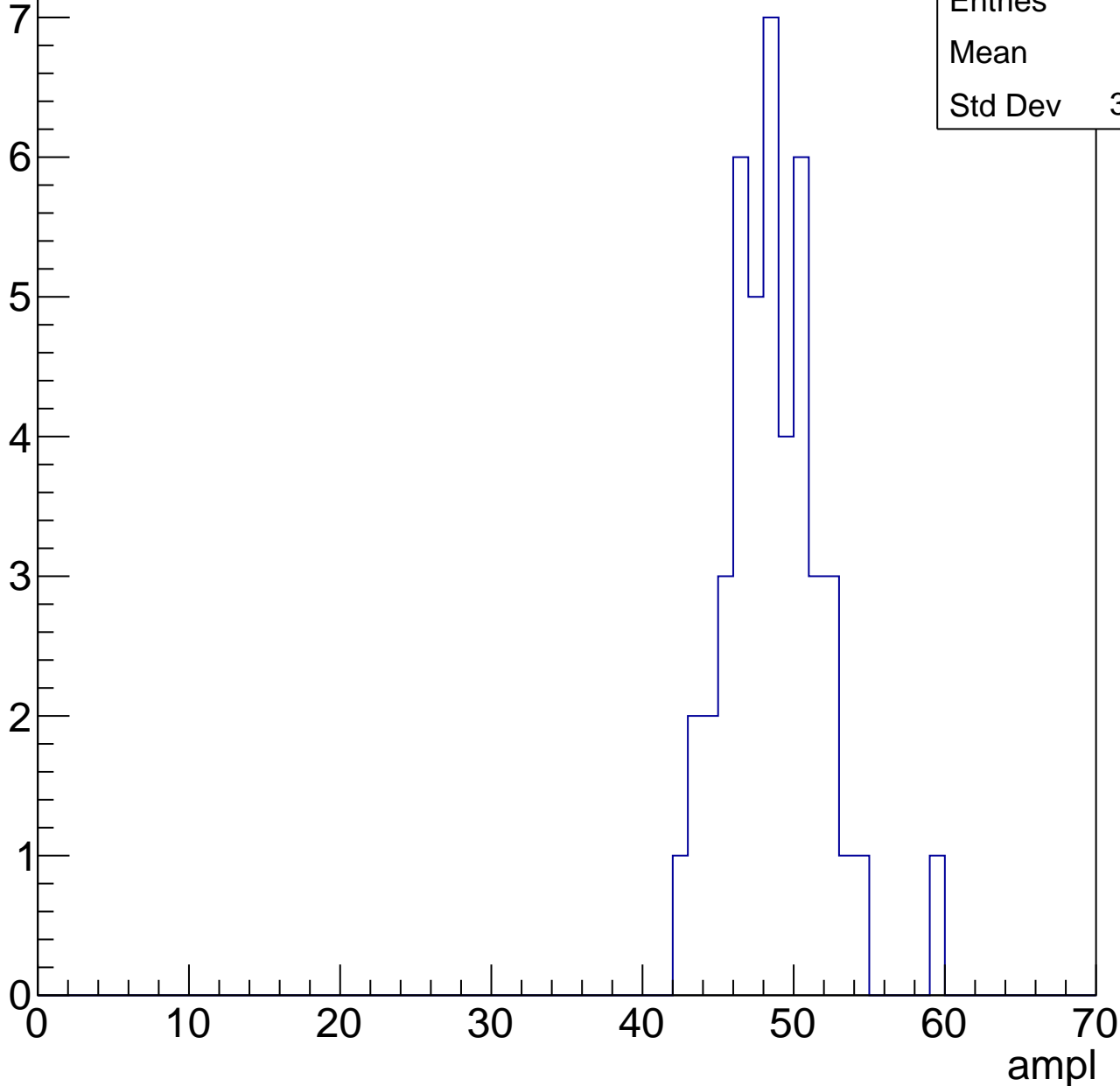


B1L103S, U6-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	48.2
Std Dev	3.187

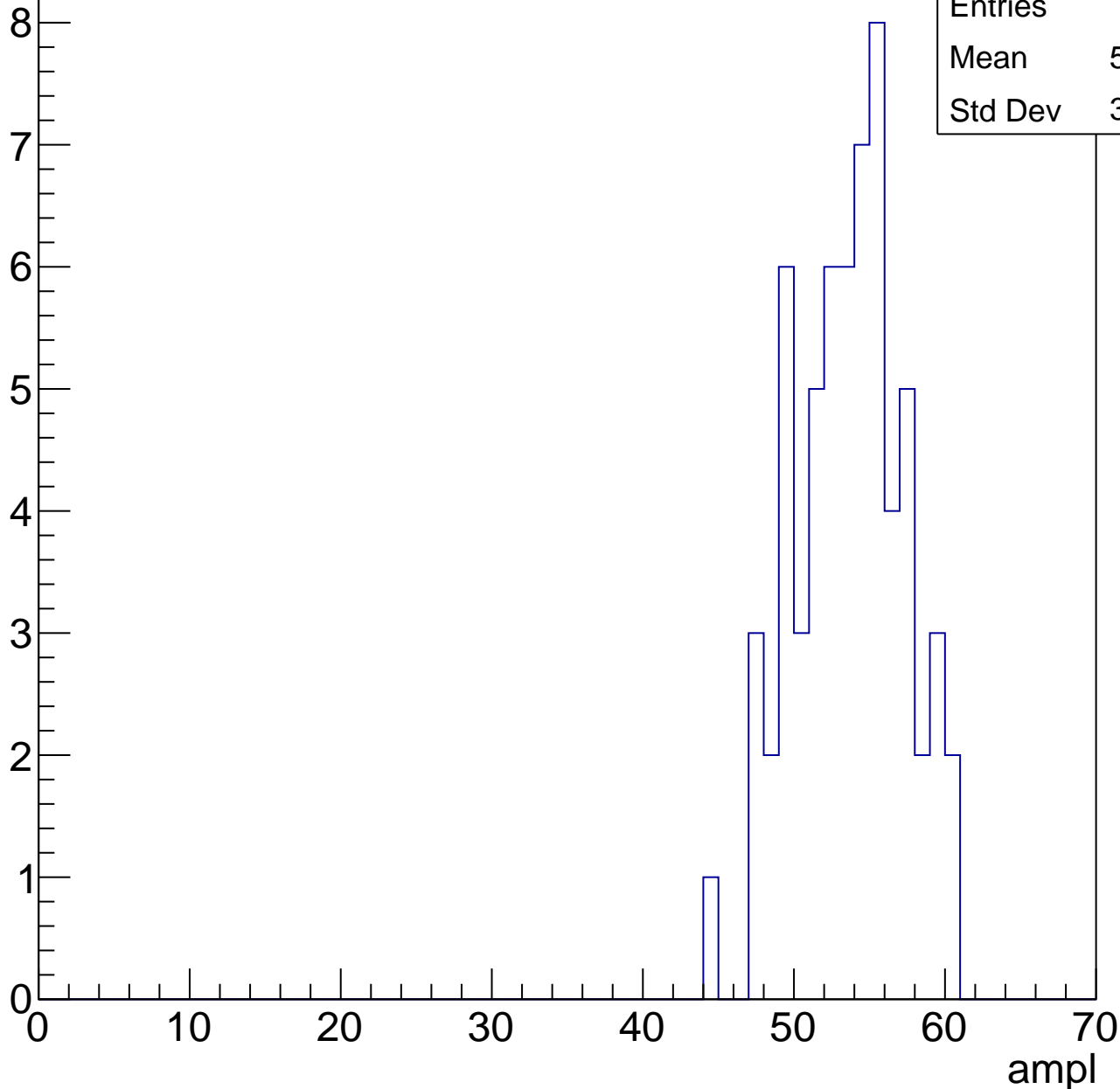


B1L103S, U6-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.17
Std Dev	3.557

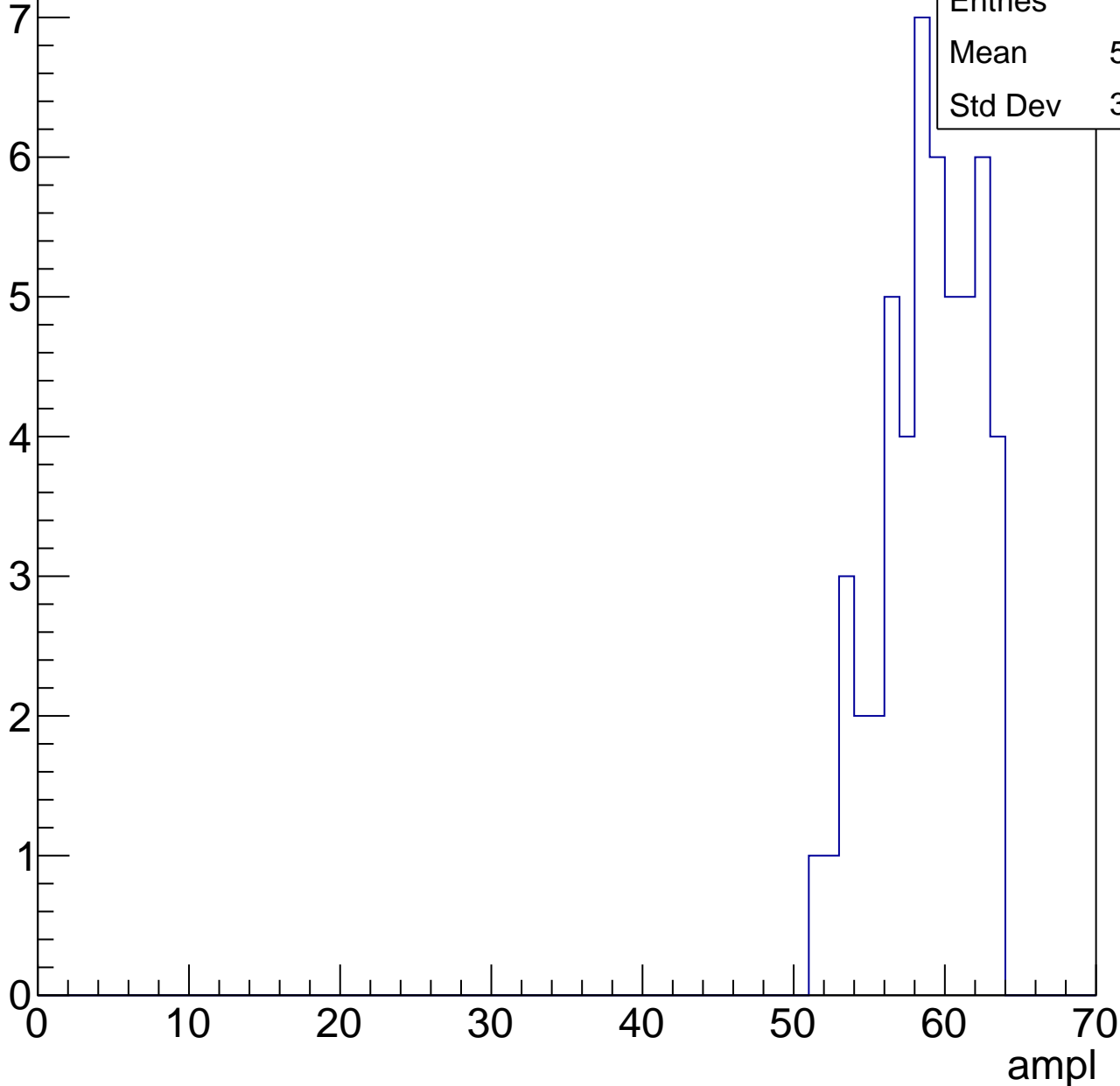


B1L103S, U6-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

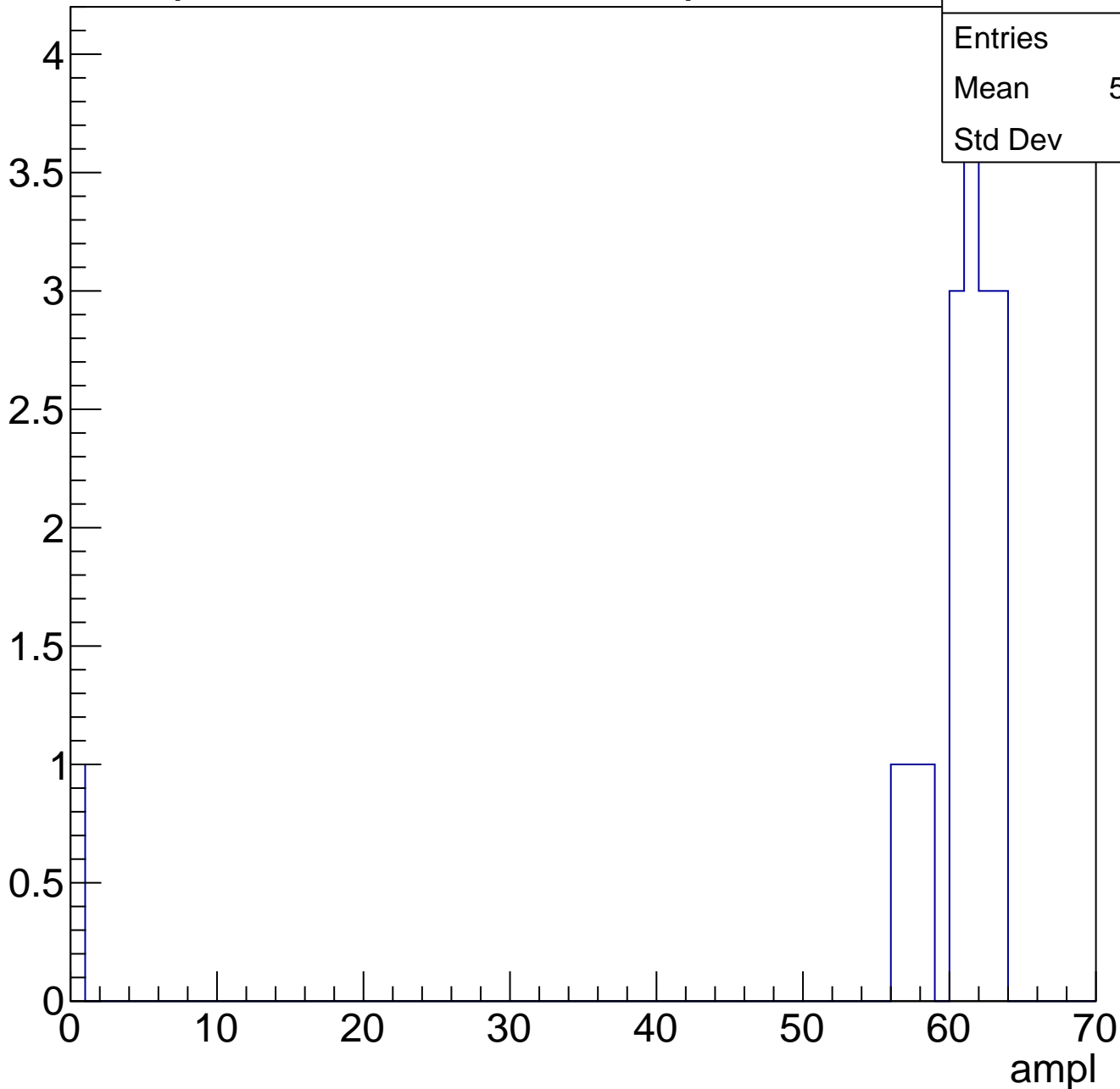
Entries	51
Mean	58.37
Std Dev	3.112



B1L103S, U6-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

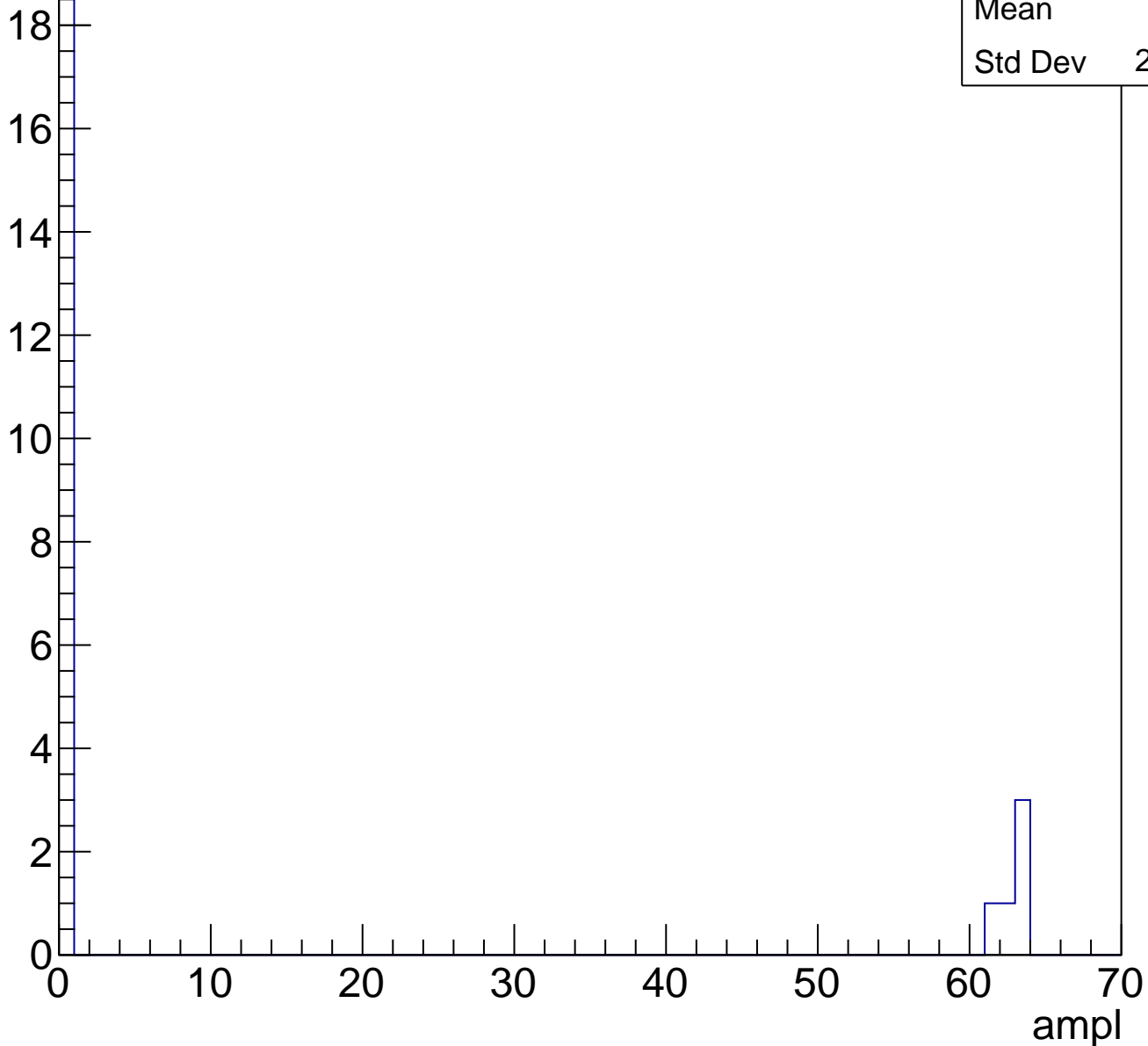


B1L103S, U6-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	13
Std Dev	25.34

Entry



B1L103S, U6-ch101, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	23
Std Dev	11.54

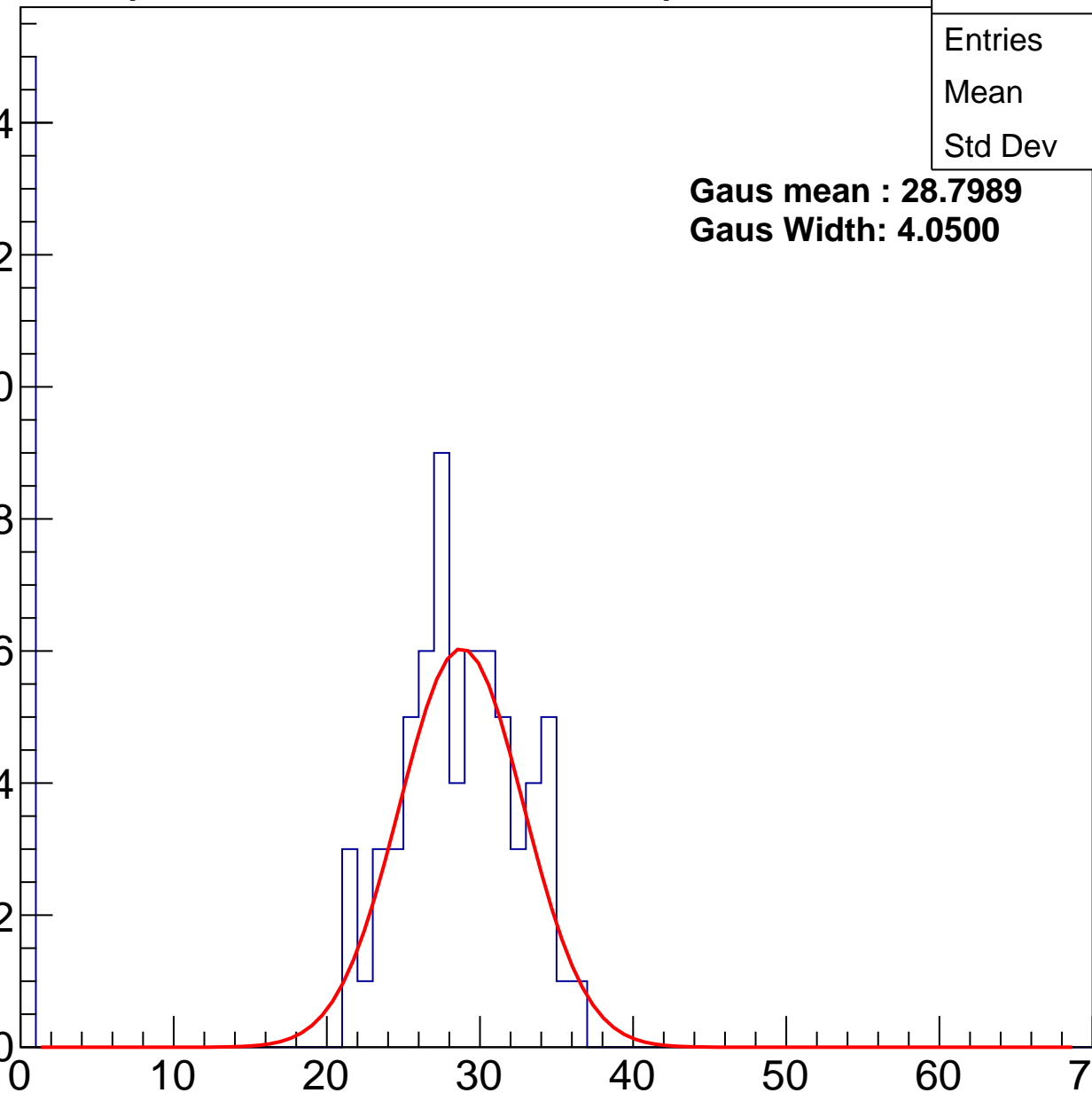
Gaus mean : 28.7989

Gaus Width: 4.0500

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch101, adc1

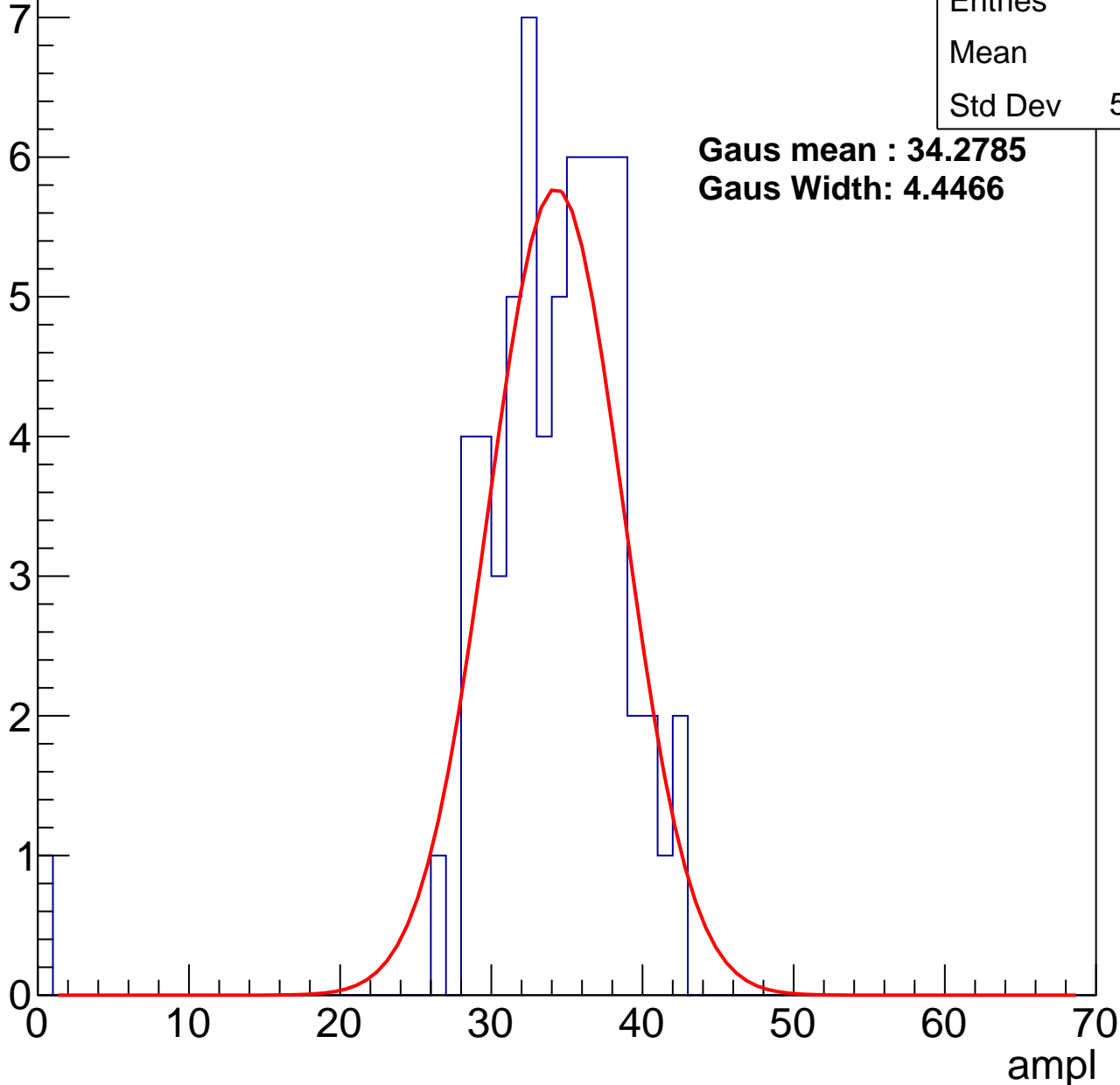
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	33.6
Std Dev	5.626

Gaus mean : 34.2785

Gaus Width: 4.4466



B1L103S, U6-ch101, adc2

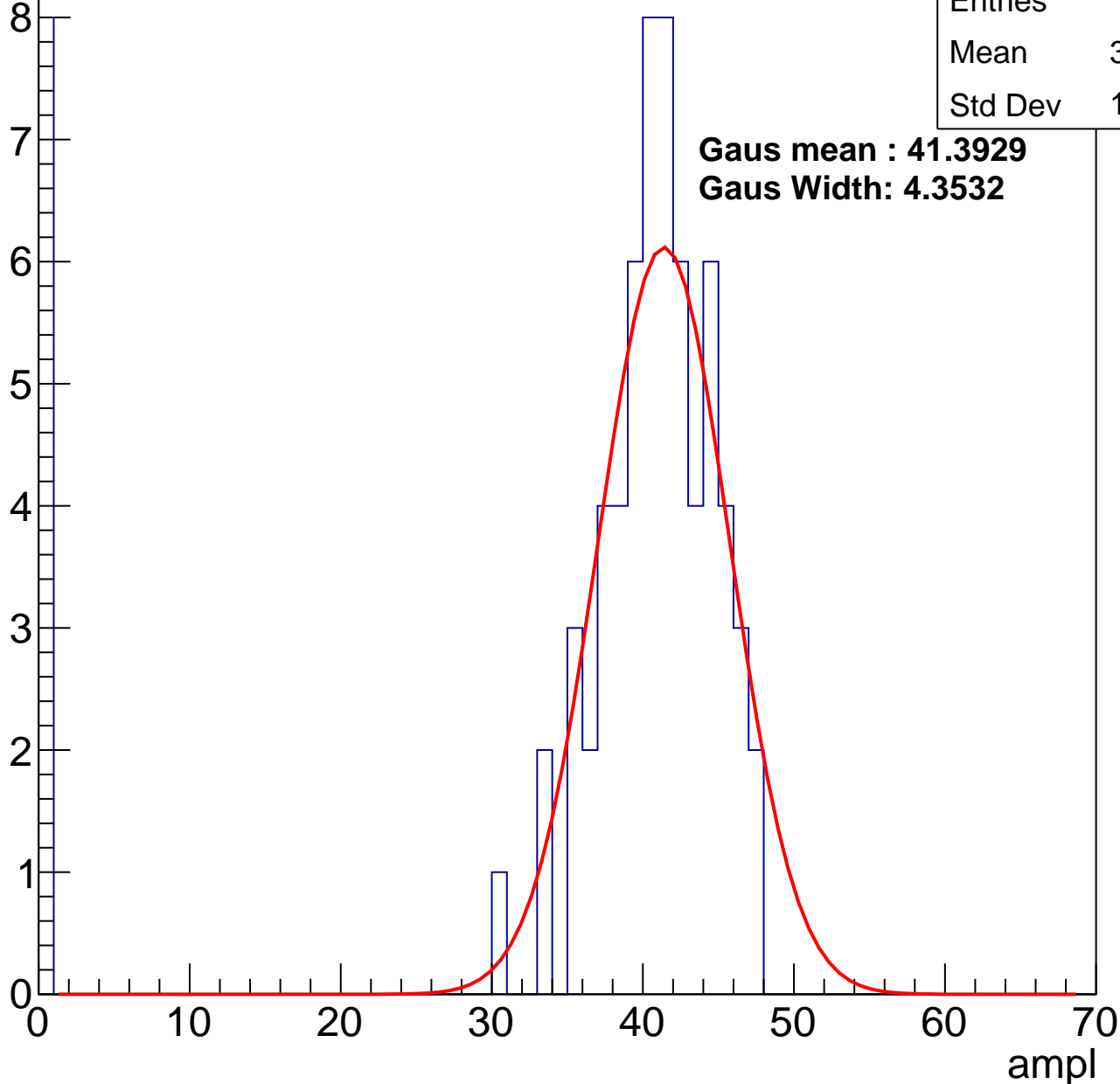
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	35.99
Std Dev	13.26

Gaus mean : 41.3929

Gaus Width: 4.3532

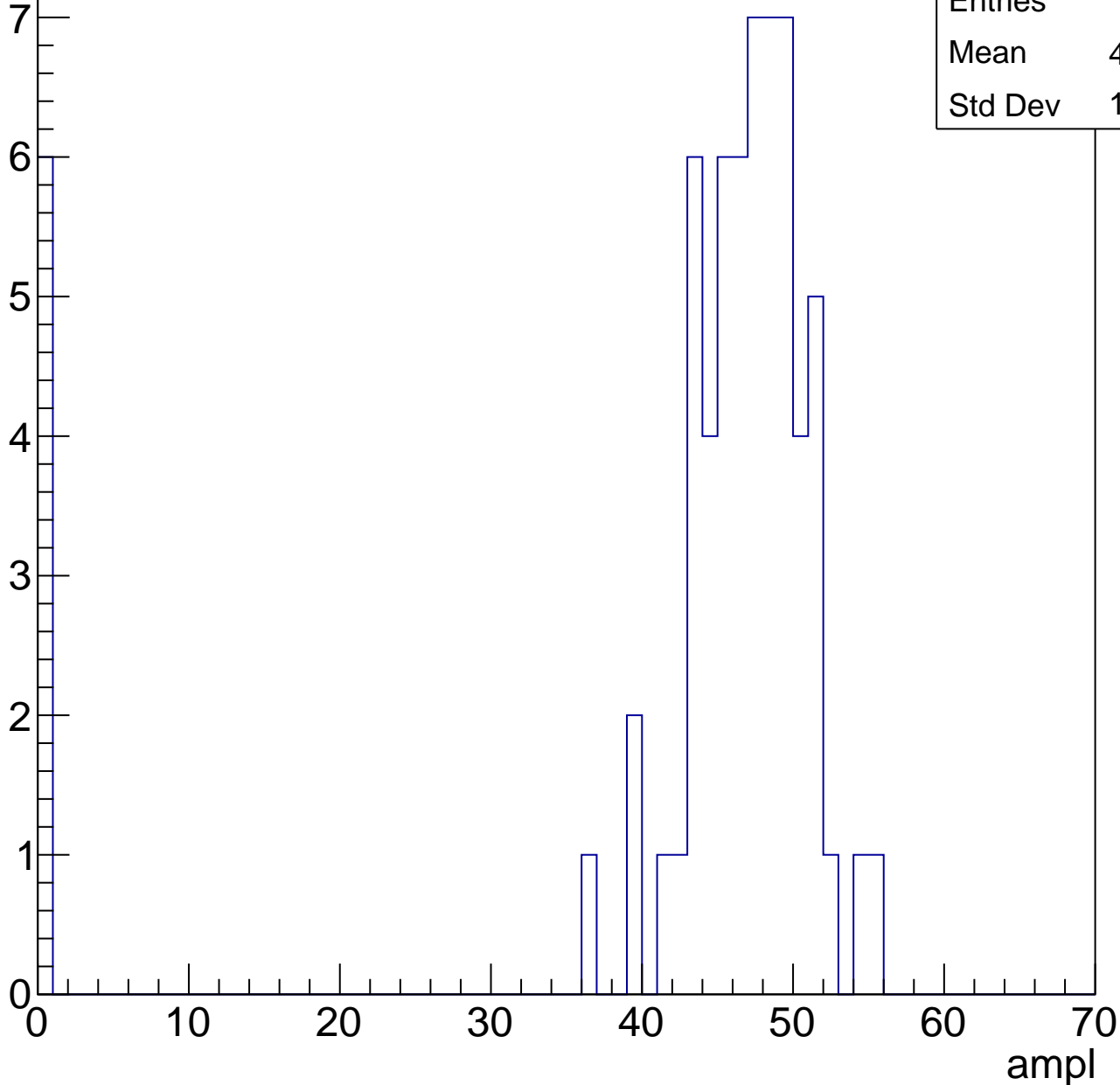


B1L103S, U6-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.44
Std Dev	13.84

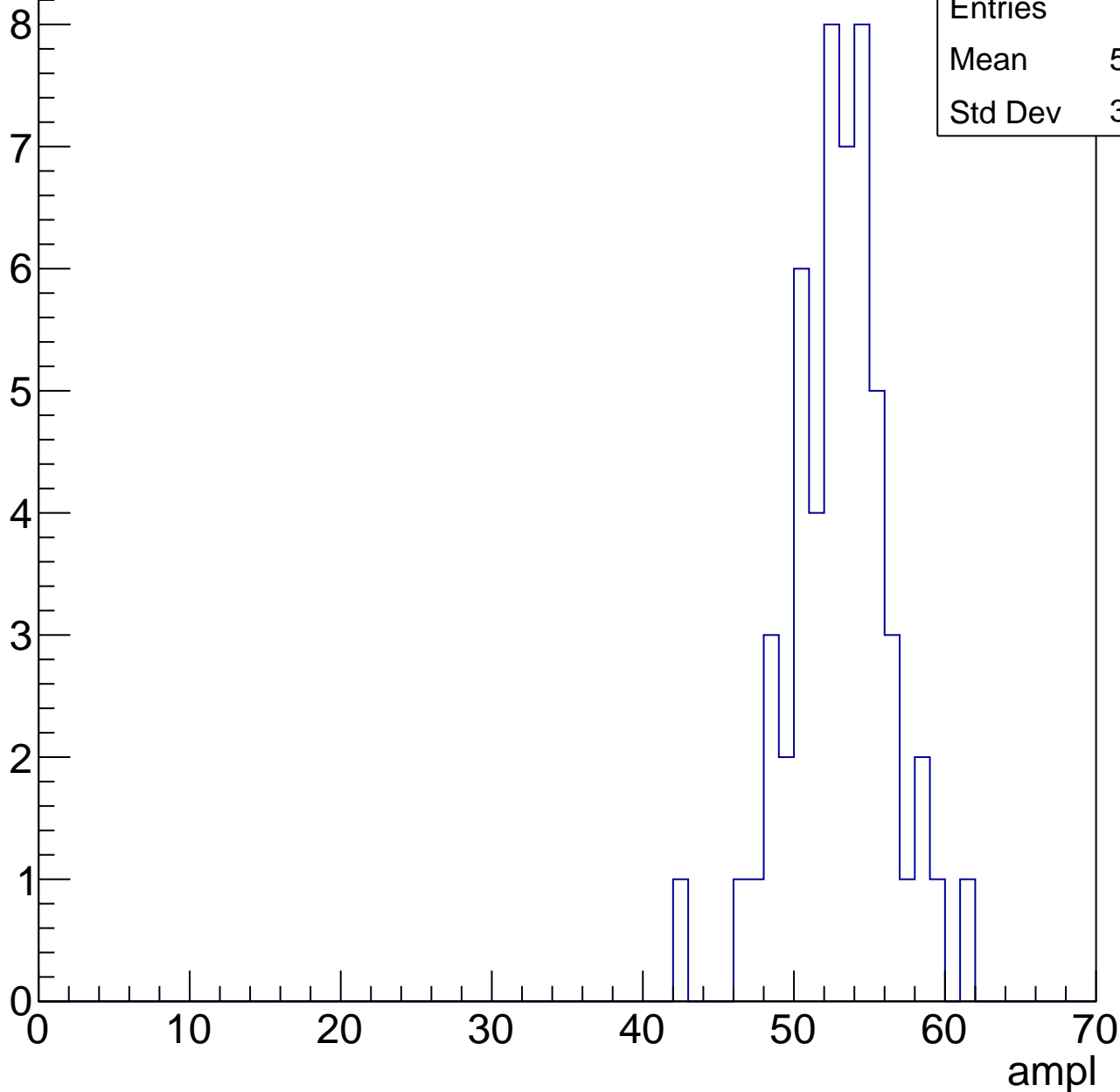


B1L103S, U6-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	52.52
Std Dev	3.332

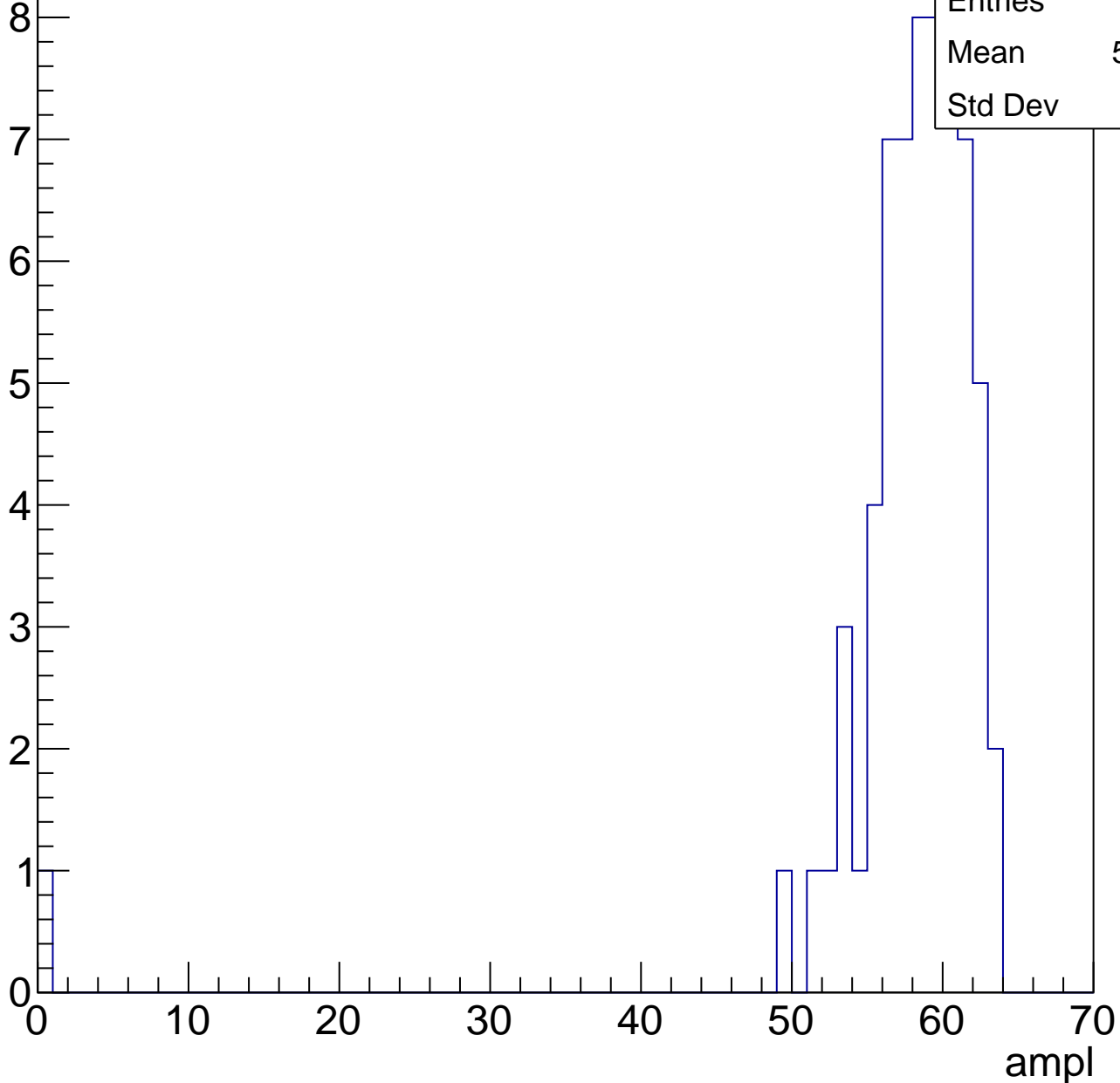


B1L103S, U6-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	57.11
Std Dev	7.78

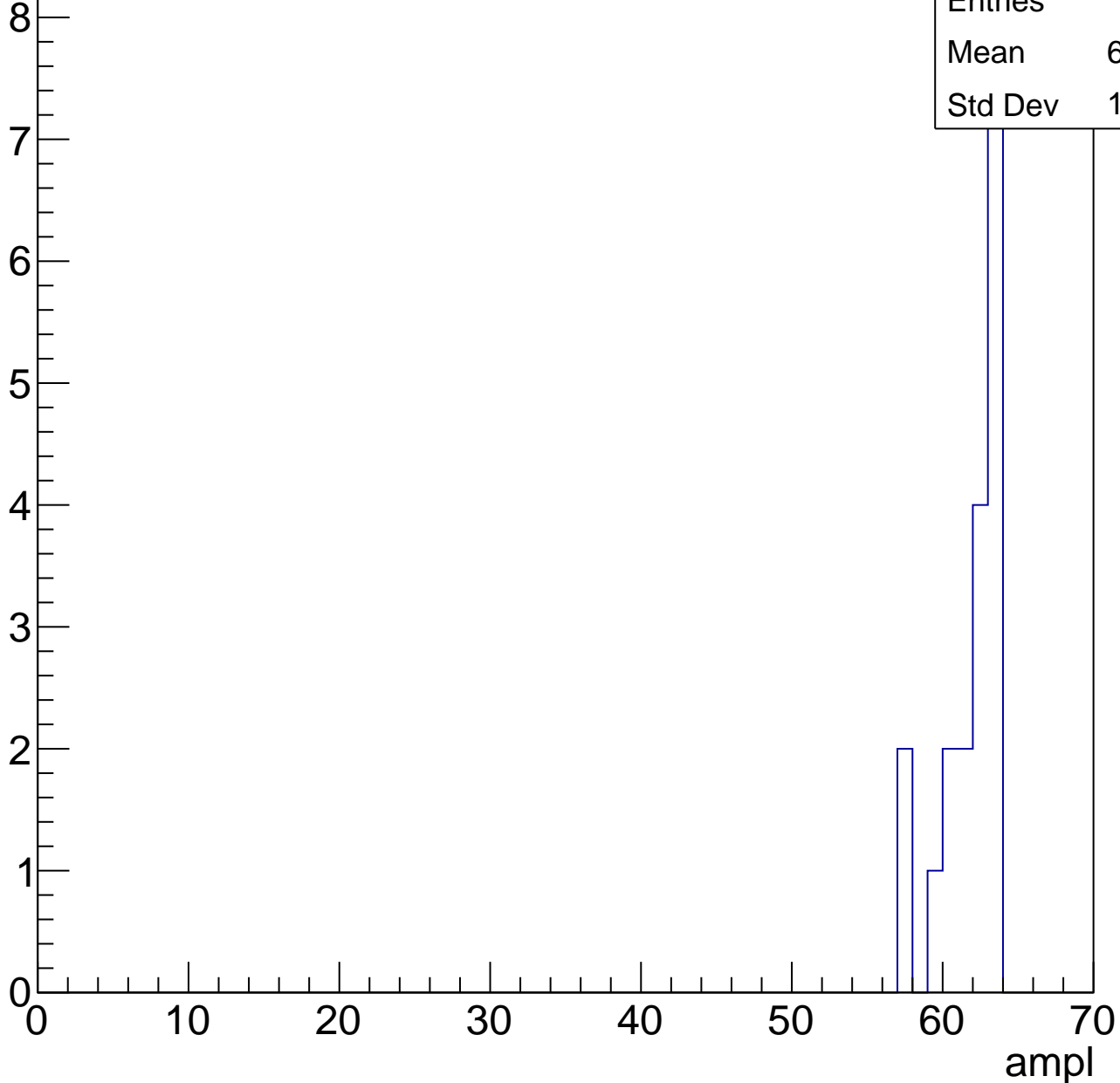


B1L103S, U6-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.42
Std Dev	1.928



B1L103S, U6-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U6-ch102, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	21.71
Std Dev	11.71

Gaus mean : 28.3228

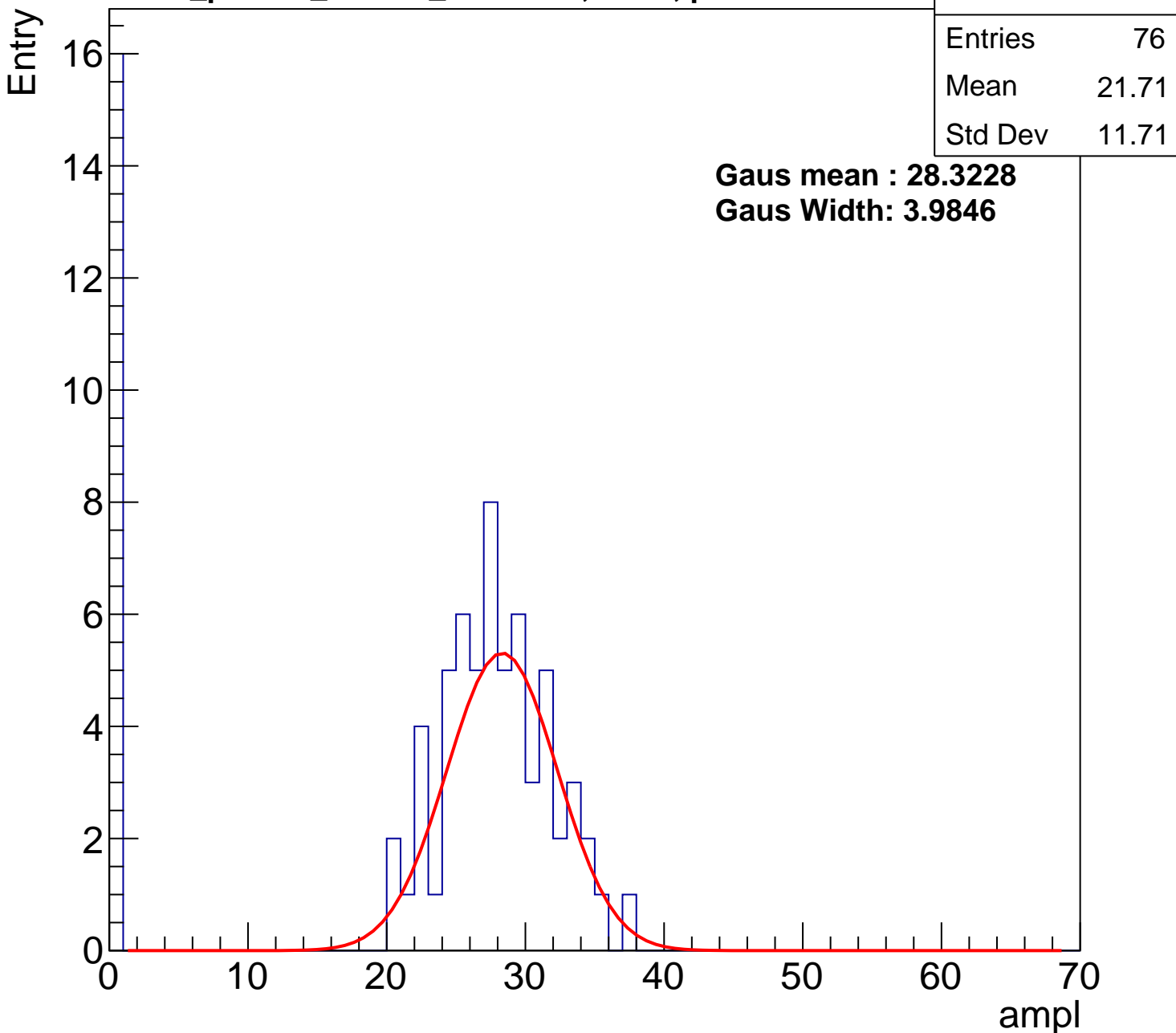
Gaus Width: 3.9846

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch102, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	29.91
Std Dev	11.88

Gaus mean : 35.1916

Gaus Width: 4.3323

Entry

10

8

6

4

2

0

0

10

20

30

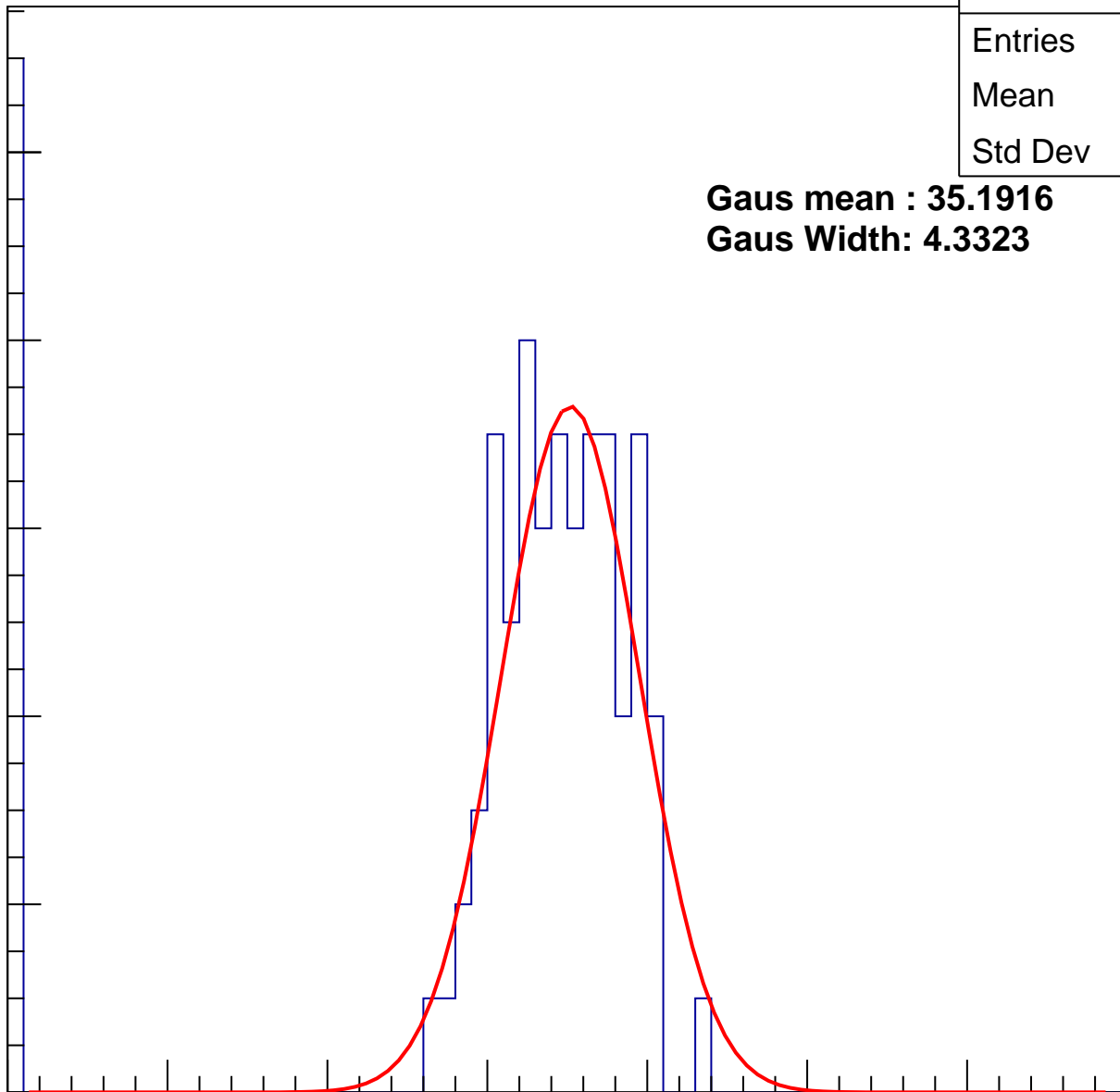
40

50

60

70

ampl



B1L103S, U6-ch102, adc2

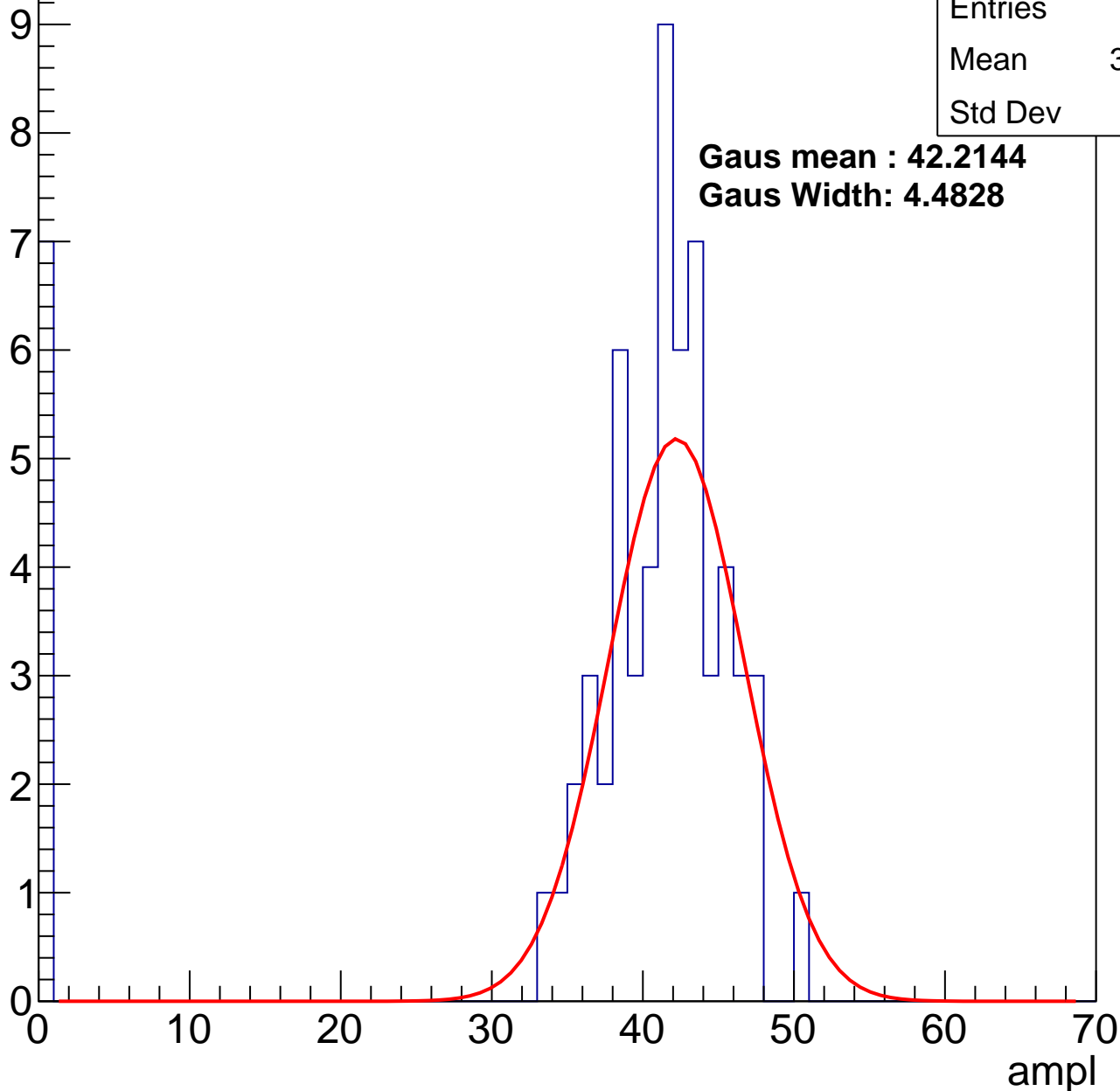
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.72
Std Dev	13.2

Gaus mean : 42.2144

Gaus Width: 4.4828

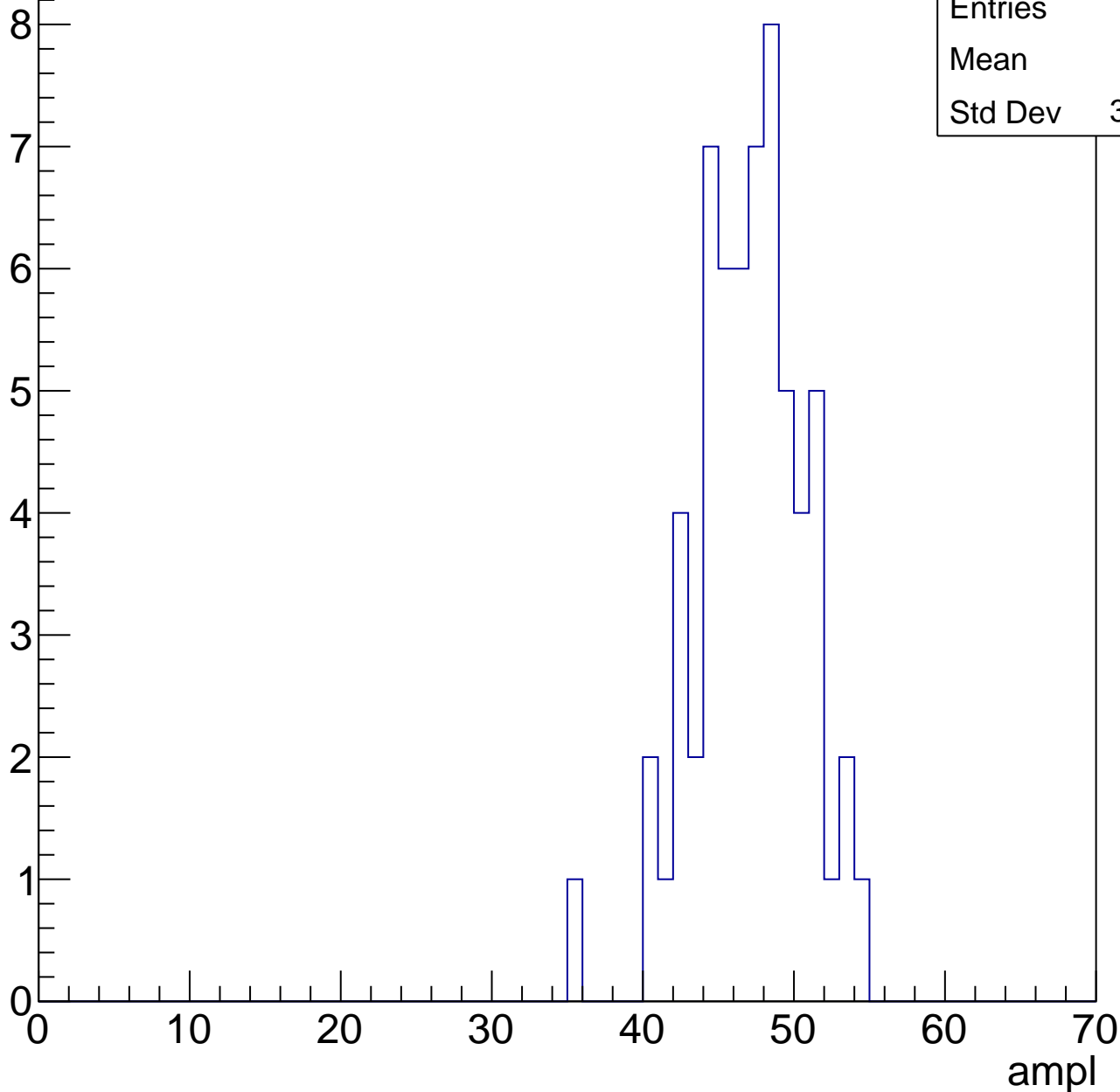


B1L103S, U6-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	46.6
Std Dev	3.554

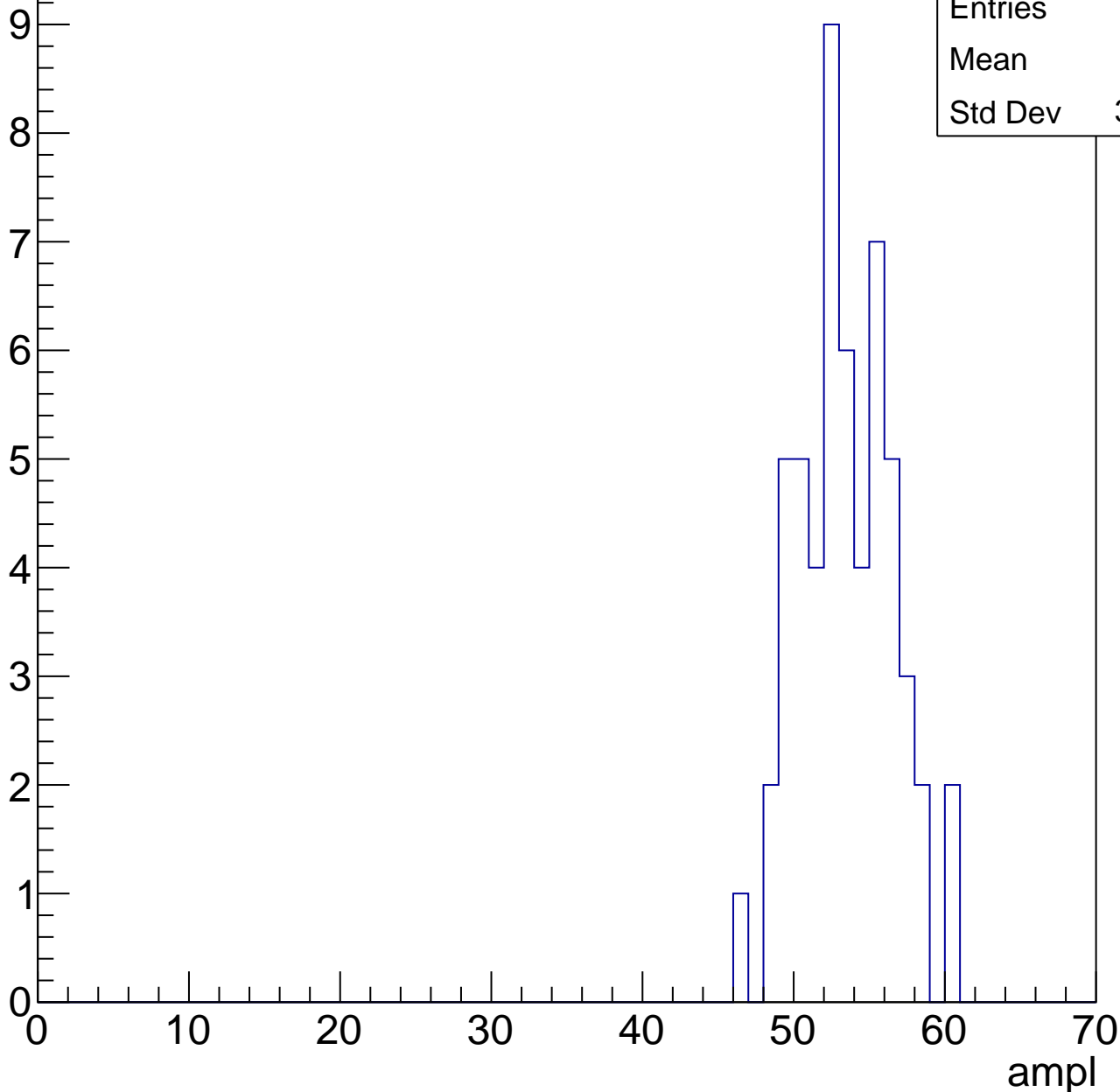


B1L103S, U6-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

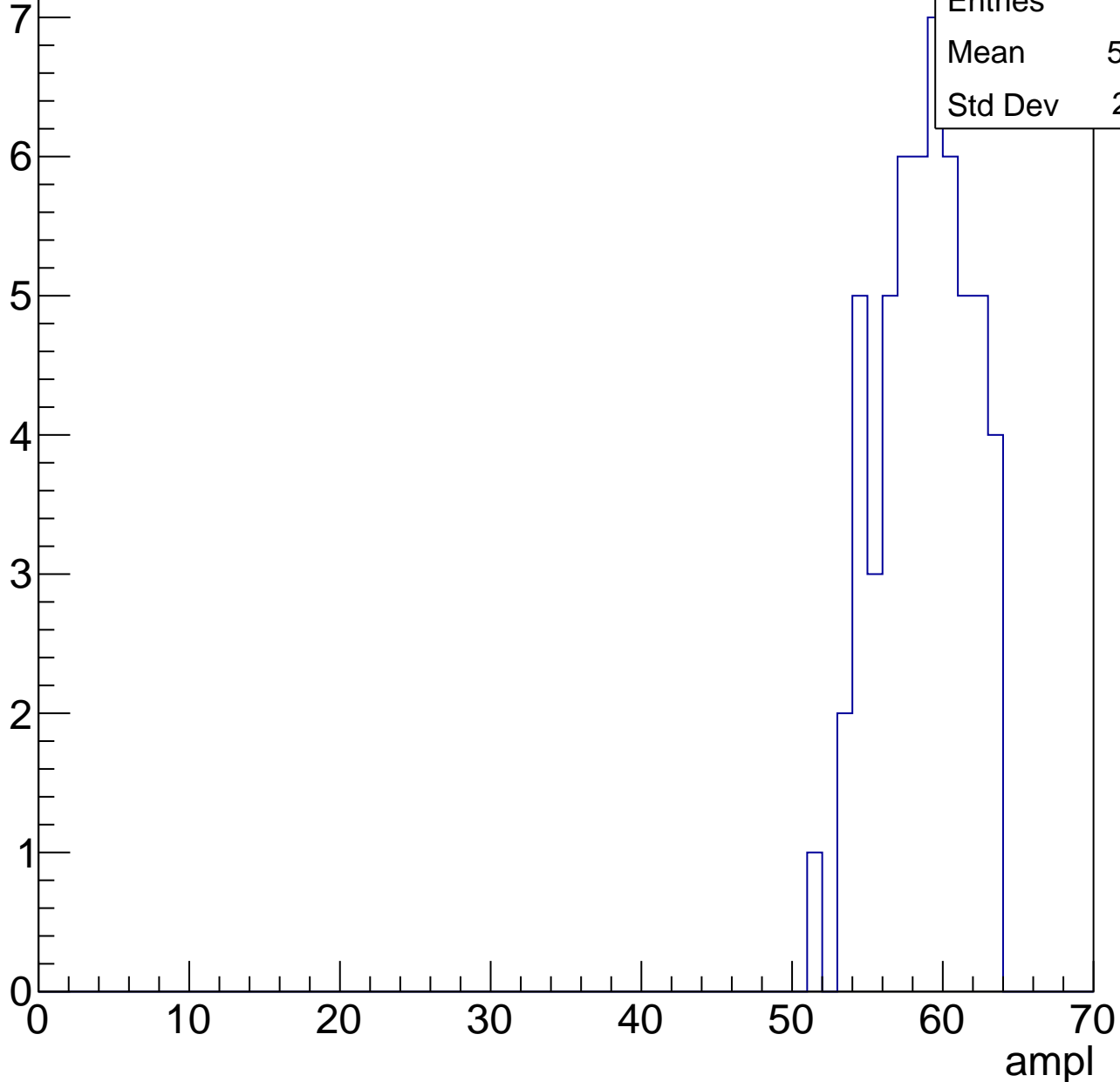
Entries	55
Mean	53
Std Dev	3.081



B1L103S, U6-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

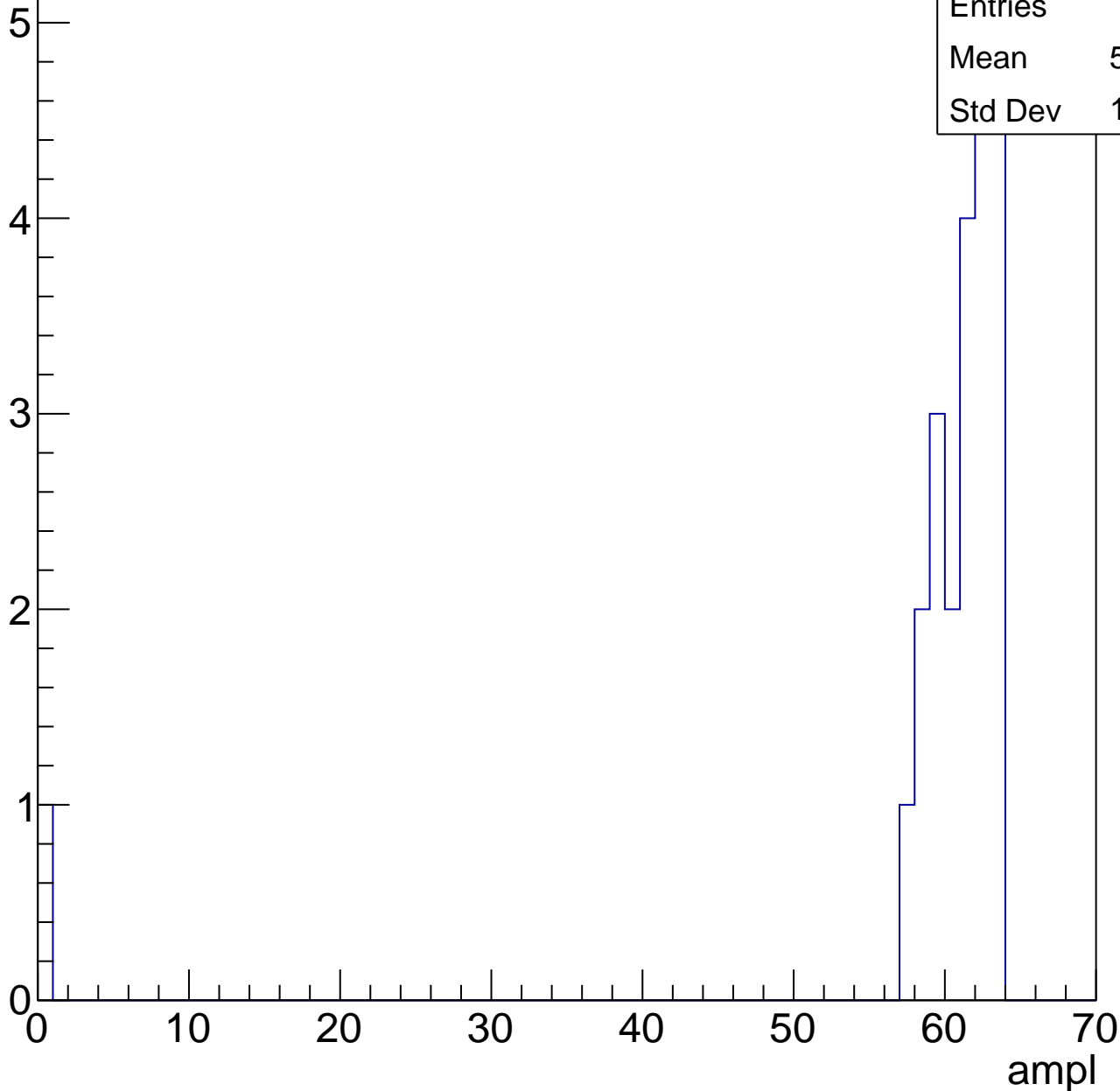


B1L103S, U6-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.22
Std Dev	12.54



B1L103S, U6-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U6-ch103, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	28.45
Std Dev	8.305

Gaus mean : 30.5625

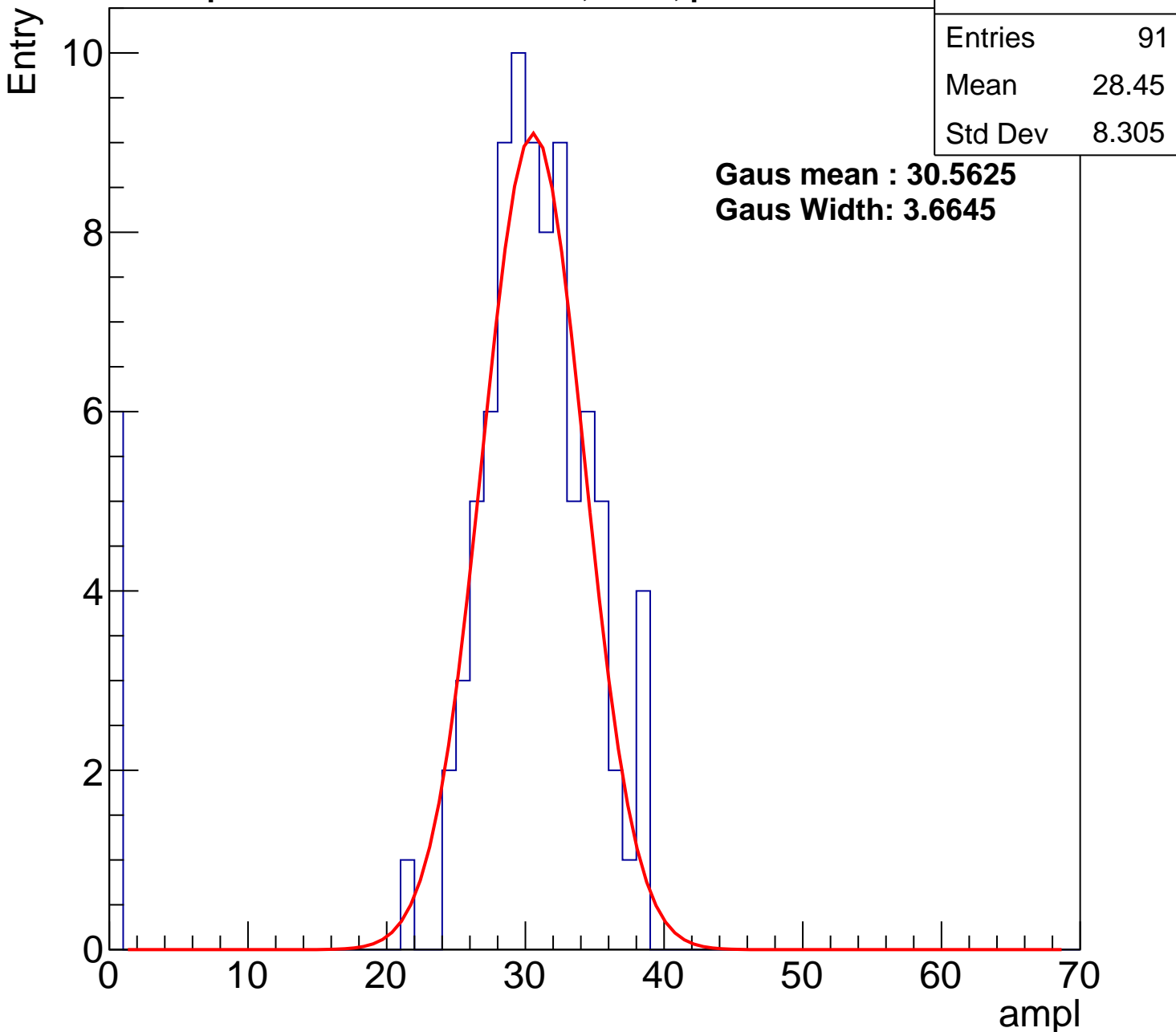
Gaus Width: 3.6645

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



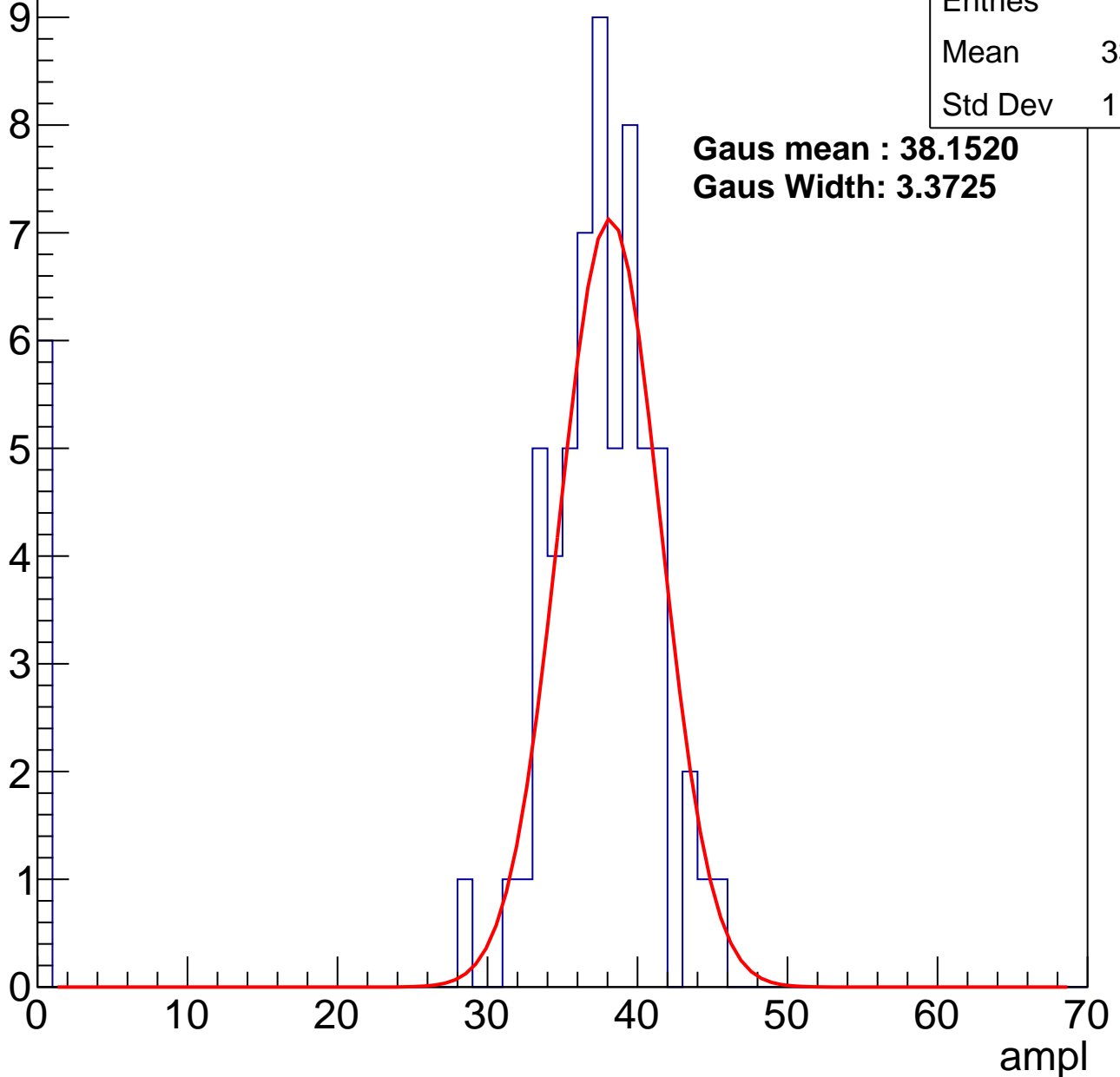
B1L103S, U6-ch103, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.85
Std Dev	11.14

Gaus mean : 38.1520
Gaus Width: 3.3725



B1L103S, U6-ch103, adc2

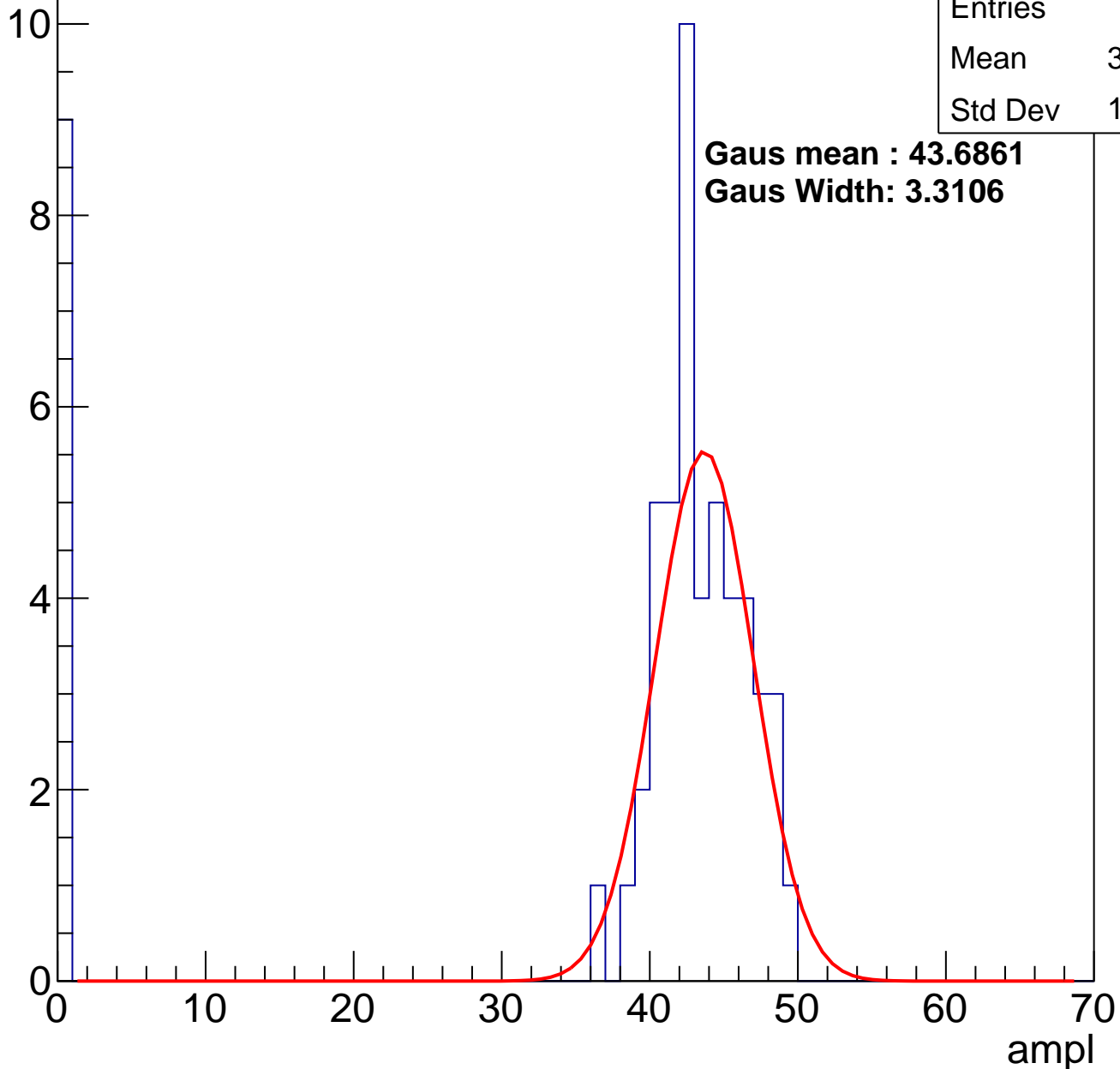
calib_packv5_041523_1651.root, FC#0, port C2

Entries	57
Mean	36.26
Std Dev	15.92

Gaus mean : 43.6861

Gaus Width: 3.3106

Entry

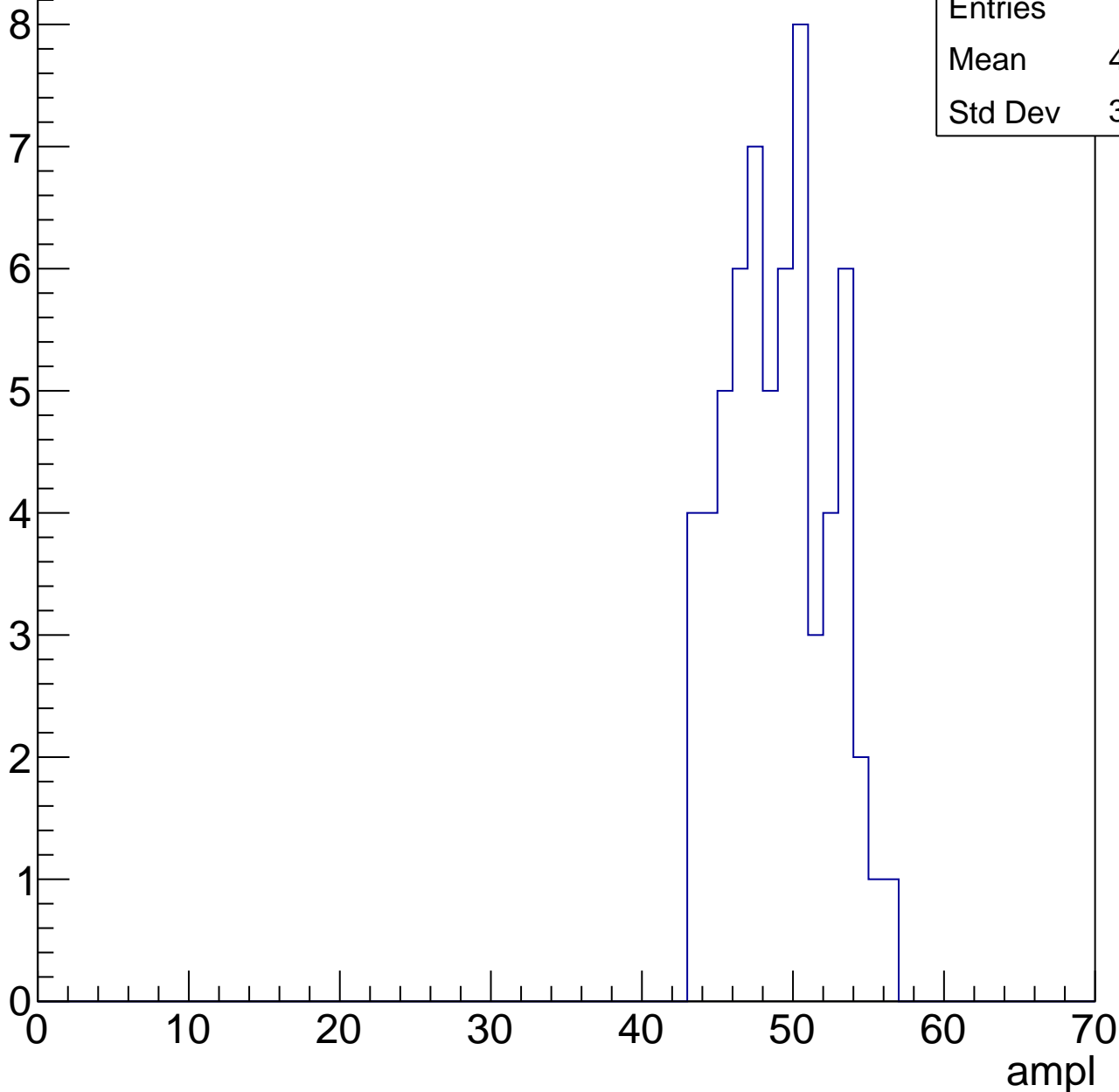


B1L103S, U6-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.55
Std Dev	3.325

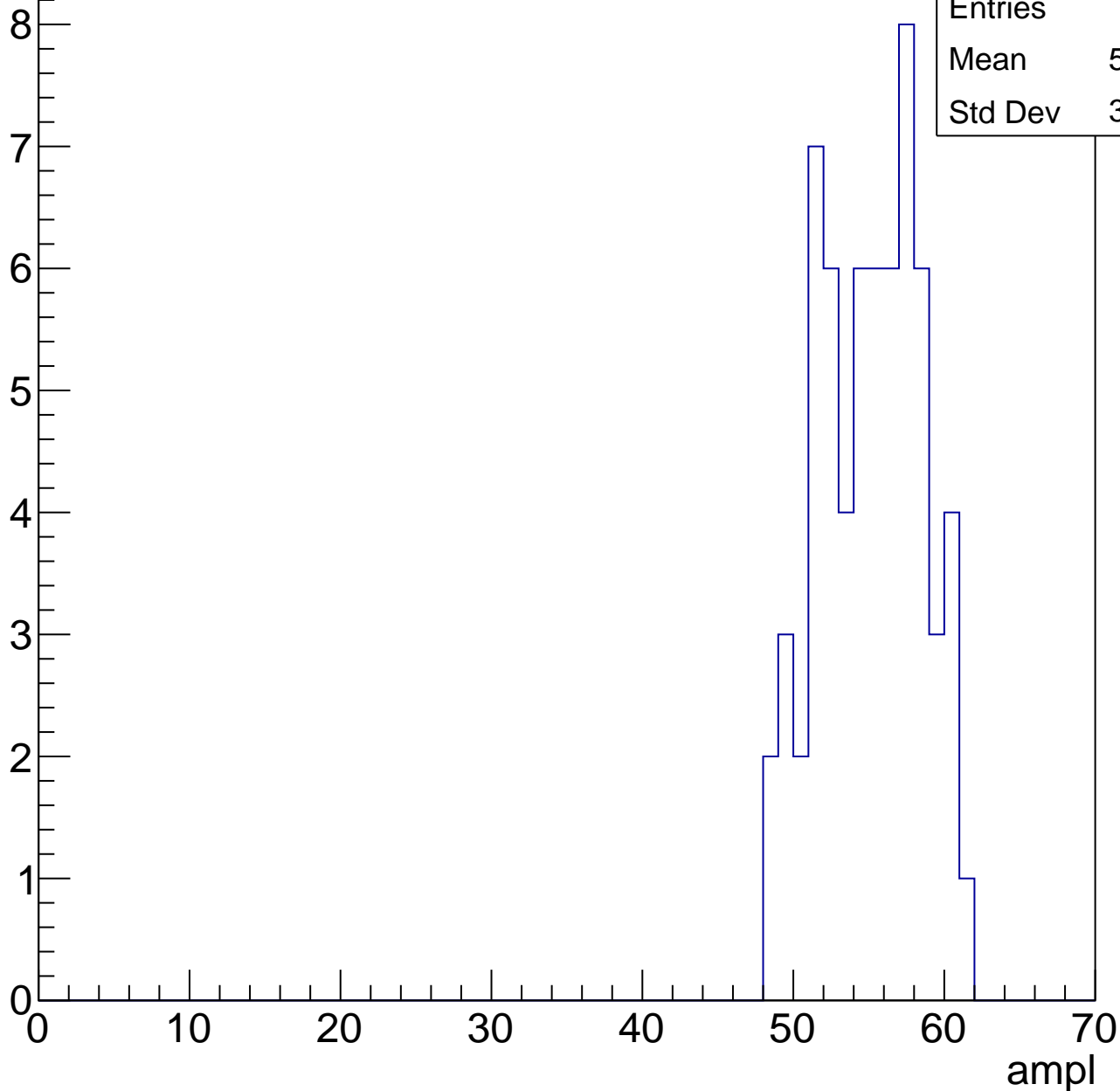


B1L103S, U6-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

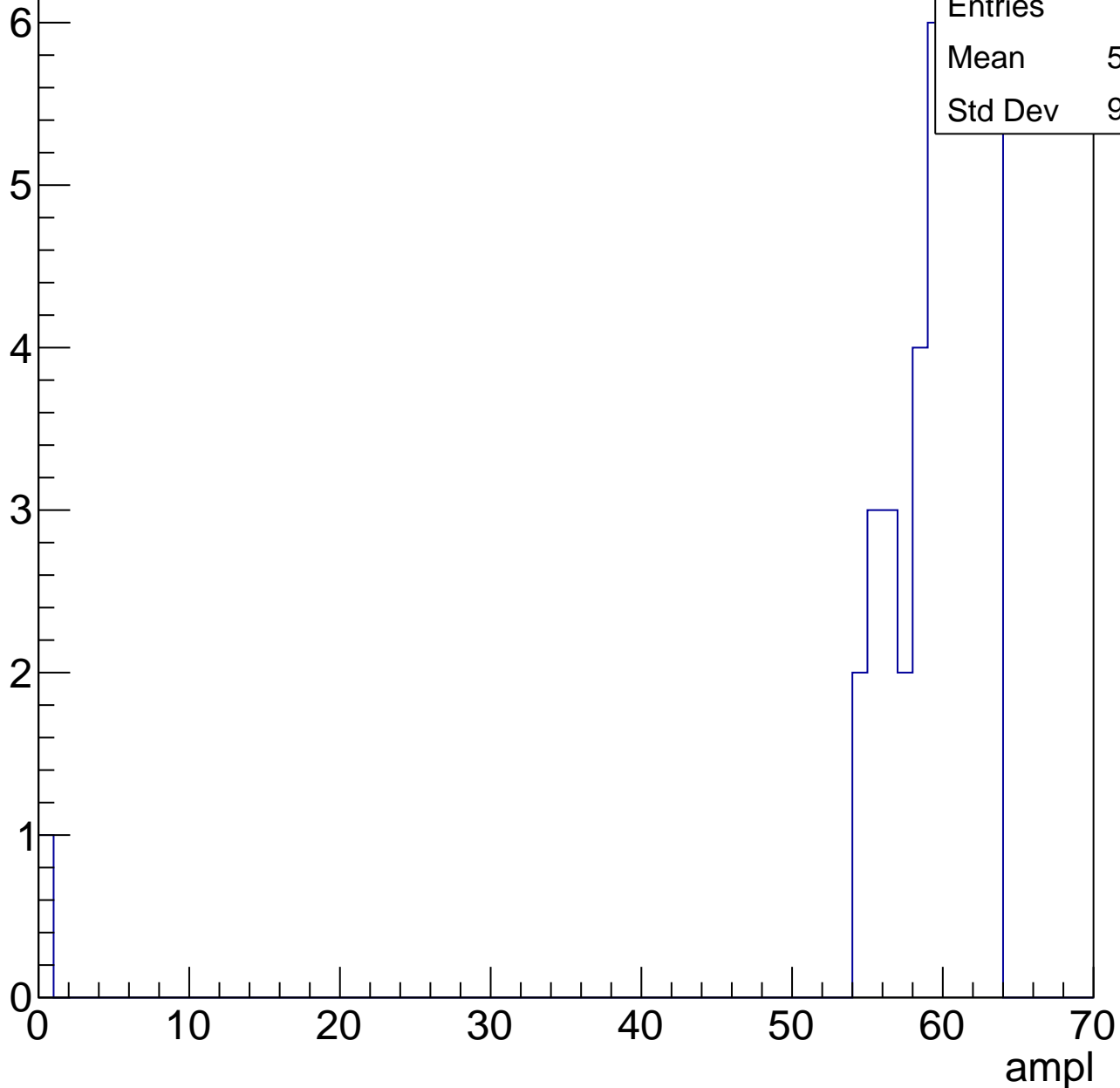
Entries	64
Mean	54.62
Std Dev	3.333



B1L103S, U6-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

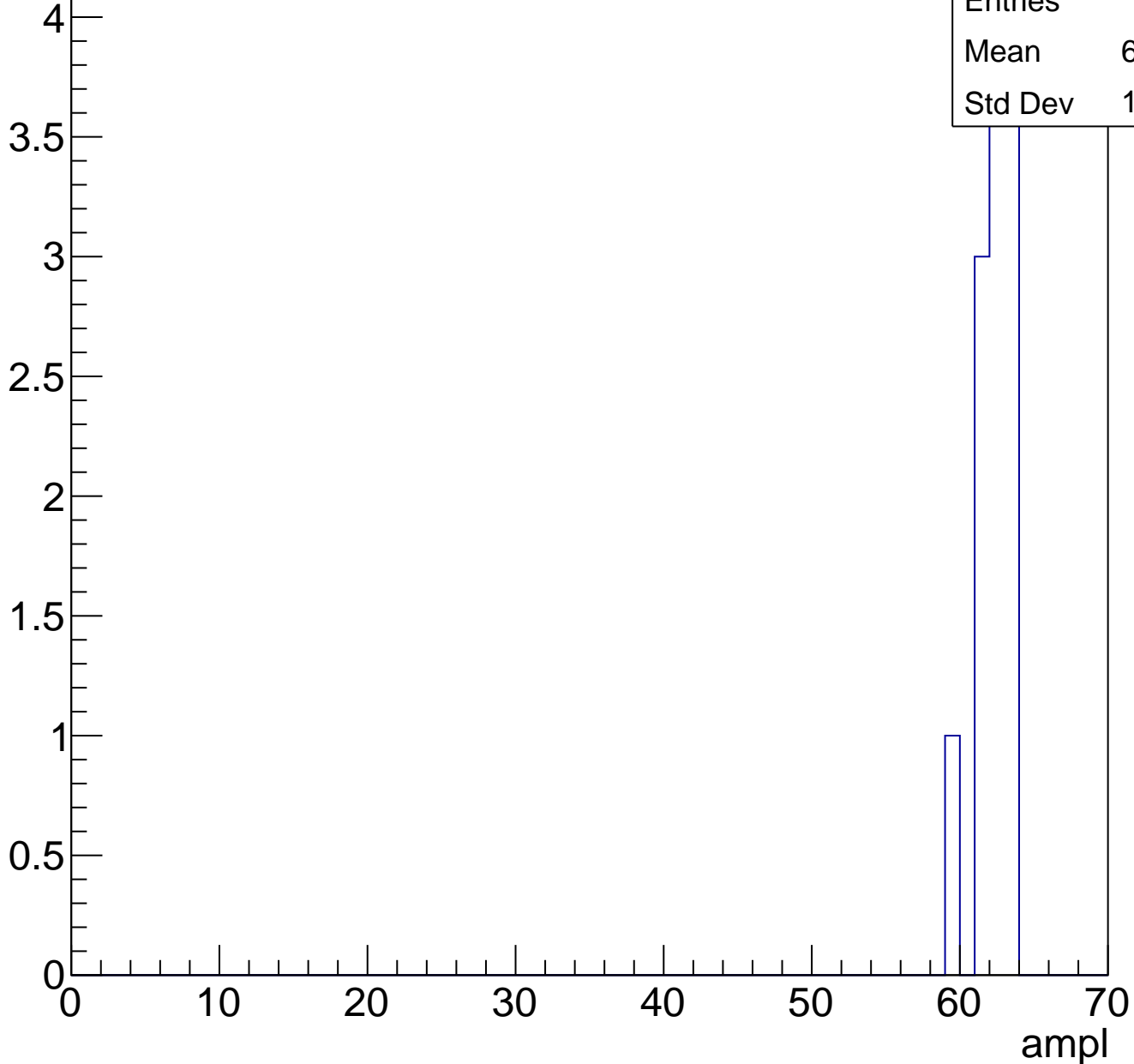
Entry



B1L103S, U6-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch104, adc0

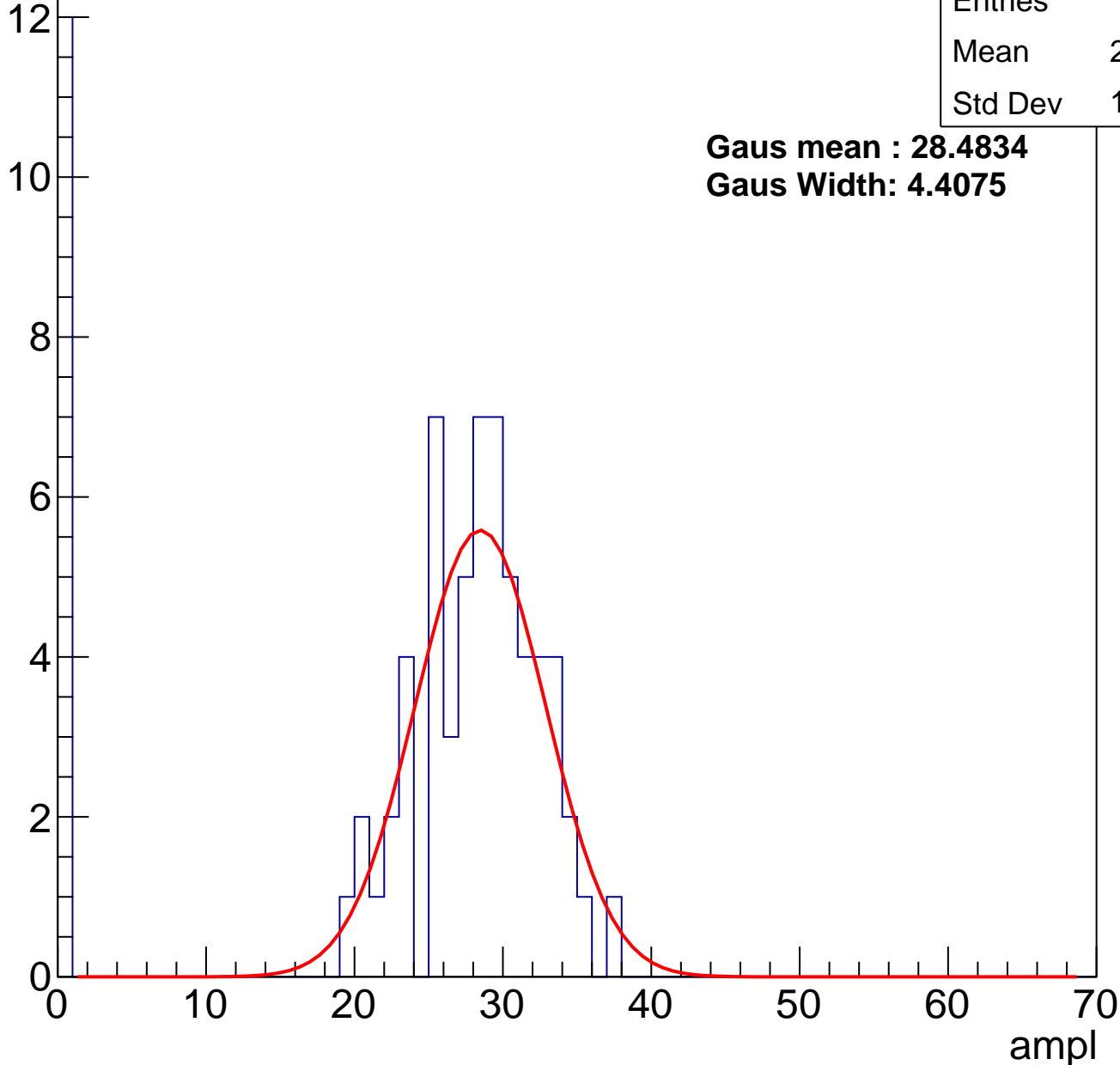
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	23.29
Std Dev	11.02

Gaus mean : 28.4834

Gaus Width: 4.4075

Entry



B1L103S, U6-ch104, adc1

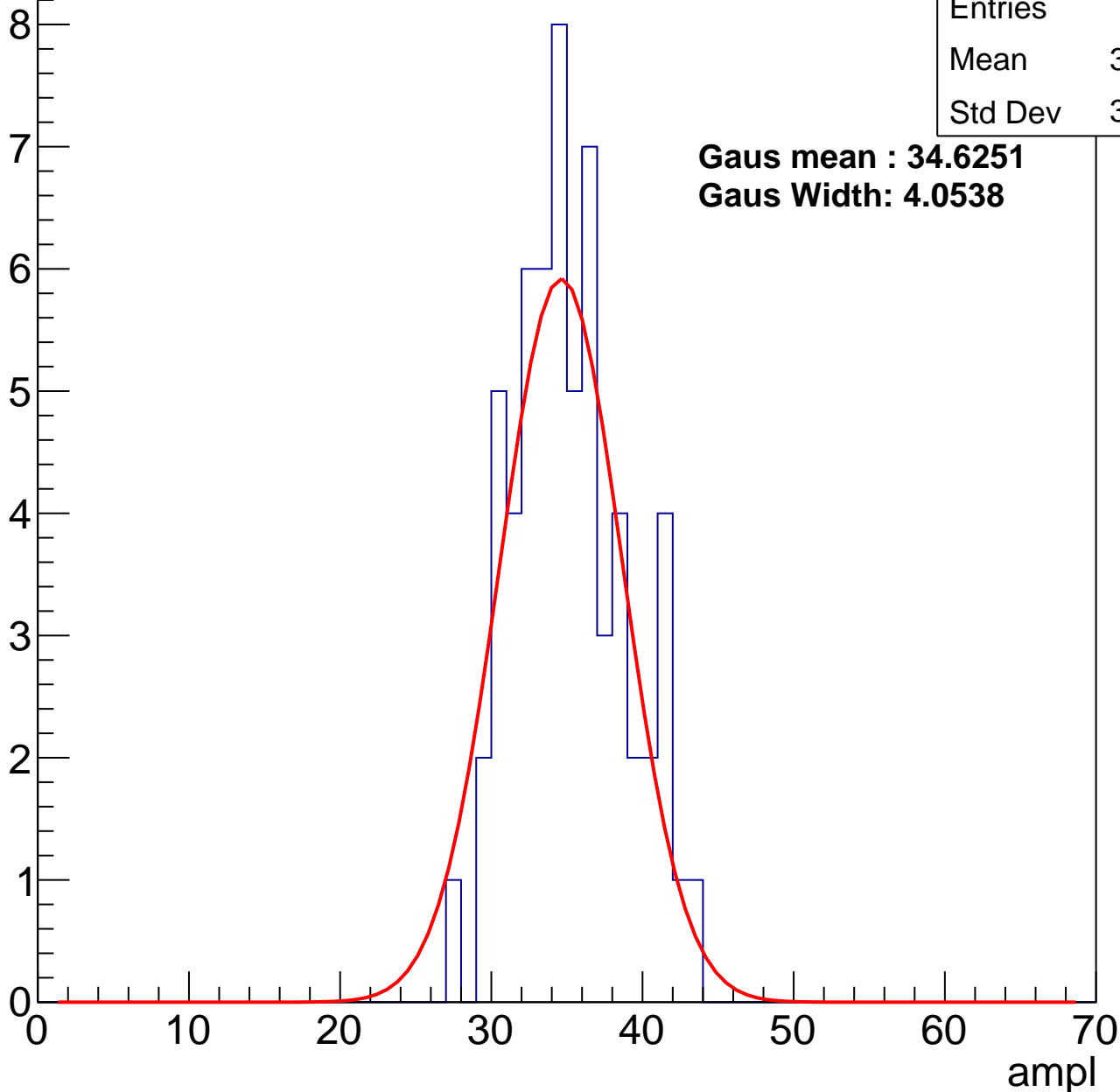
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.72
Std Dev	3.622

Gaus mean : 34.6251

Gaus Width: 4.0538



B1L103S, U6-ch104, adc2

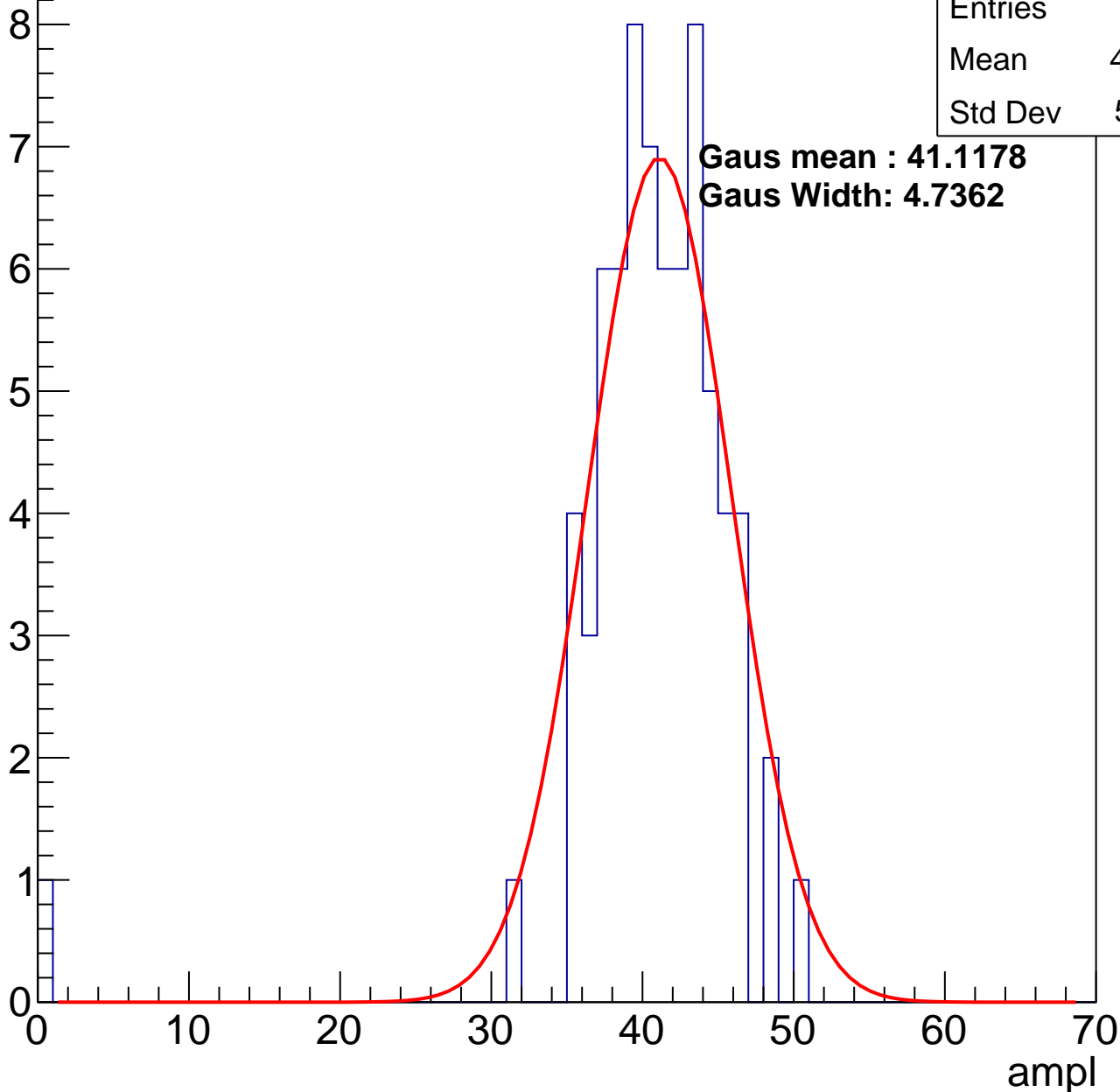
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	40.18
Std Dev	5.971

Gaus mean : 41.1178

Gaus Width: 4.7362



B1L103S, U6-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	41.22
Std Dev	17.14

Entry

10

8

6

4

2

0

0

10

20

30

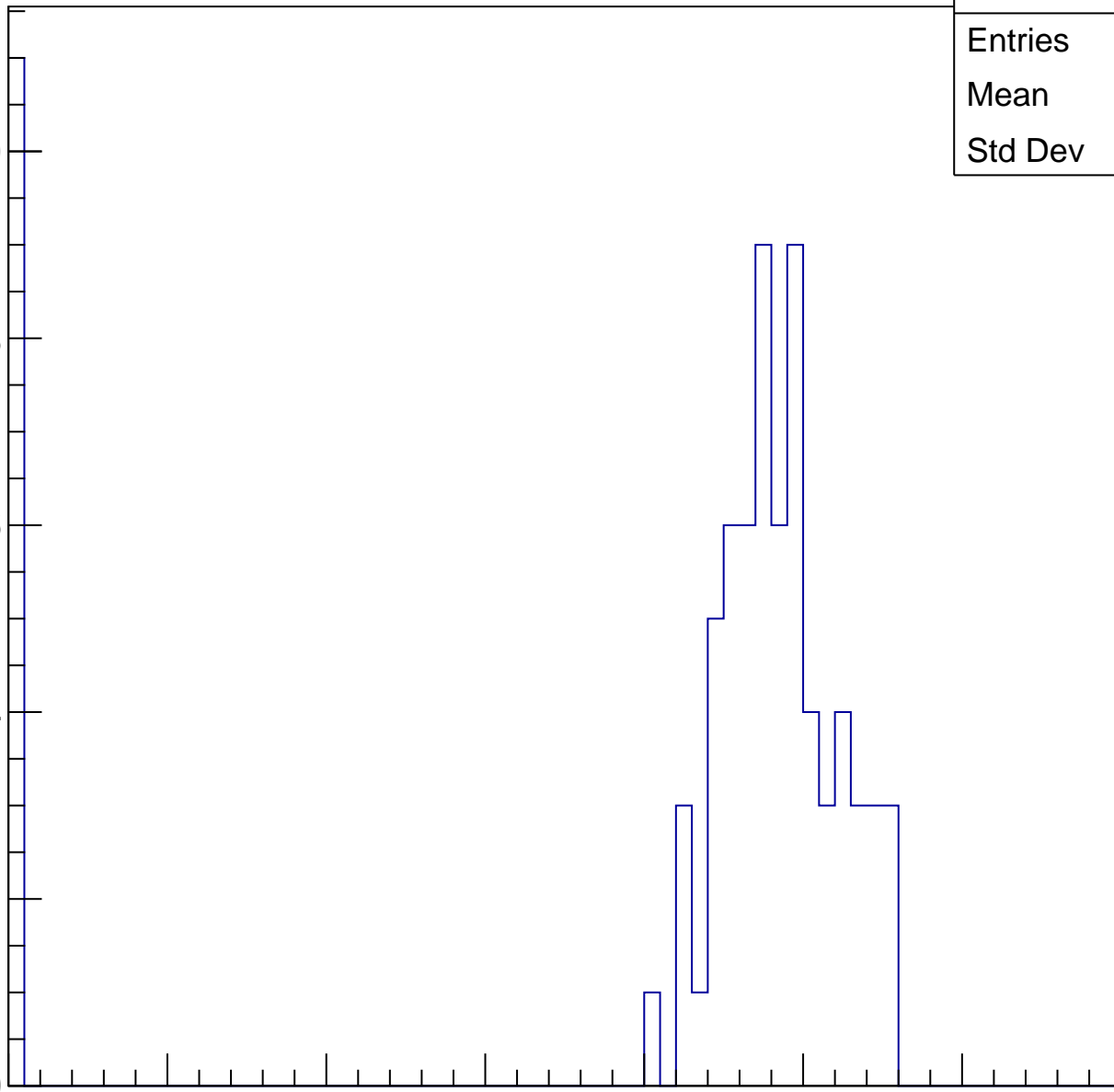
40

50

60

70

ampl

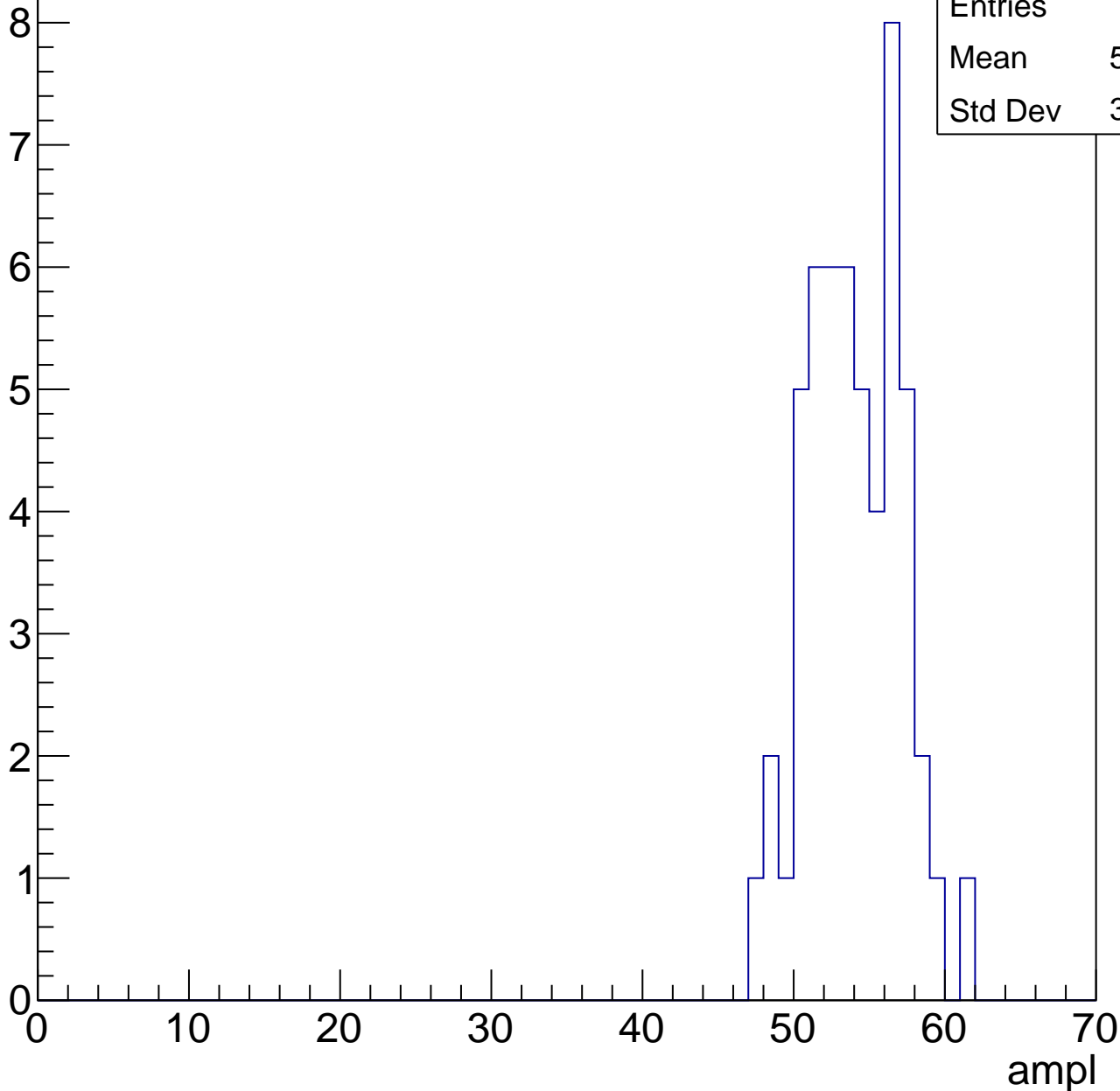


B1L103S, U6-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.53
Std Dev	3.026

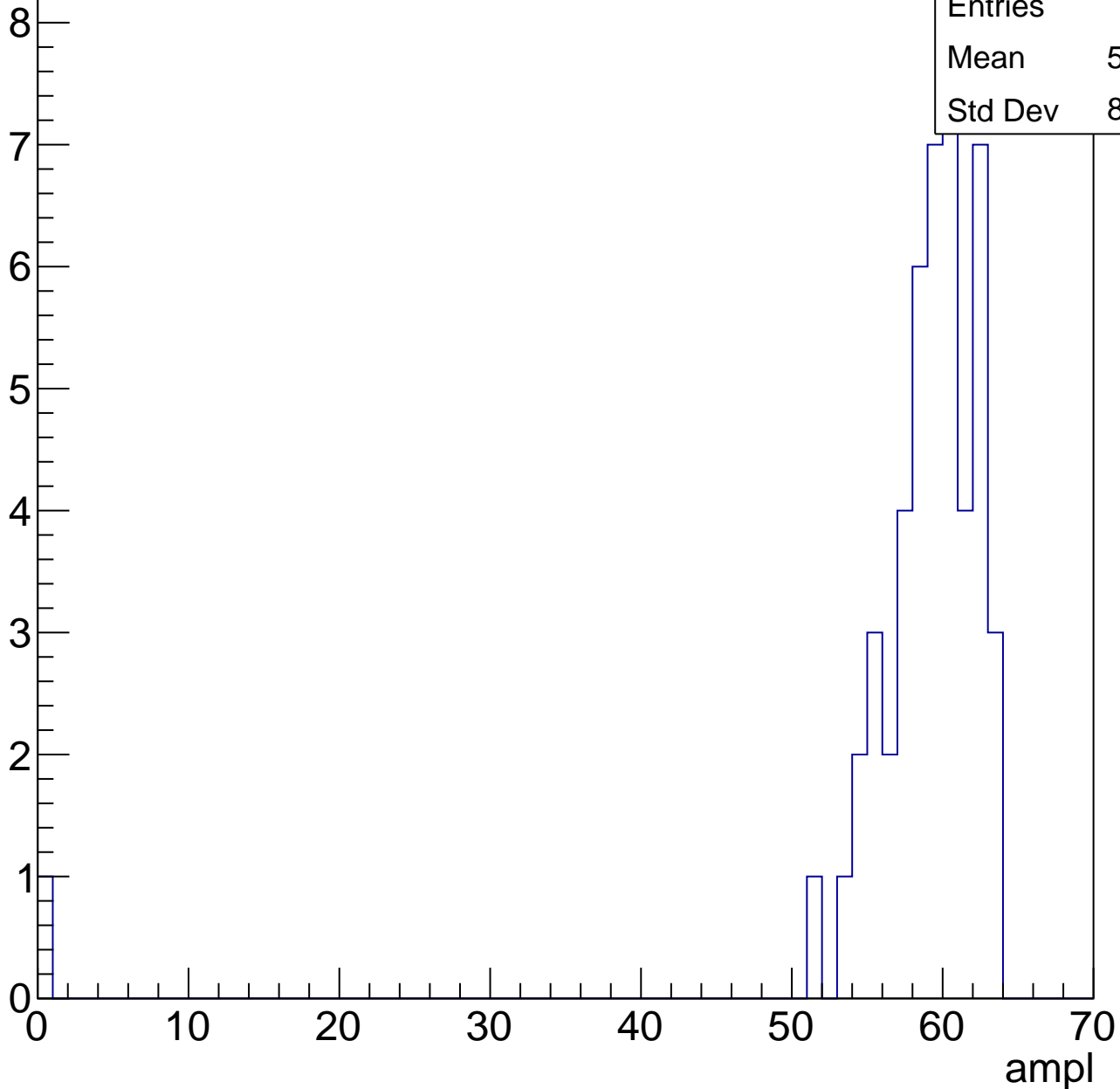


B1L103S, U6-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.65
Std Dev	8.766

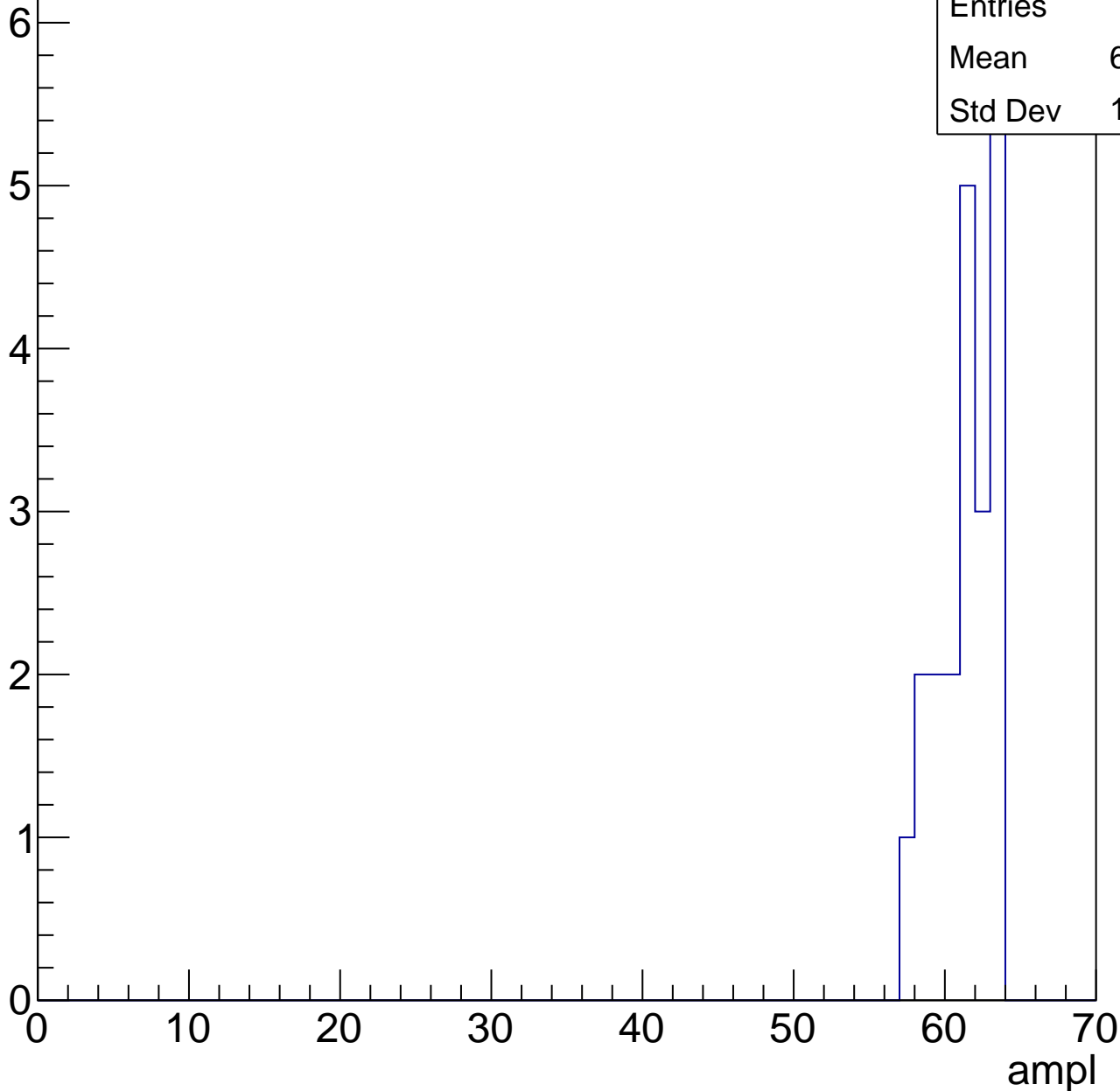


B1L103S, U6-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	60.95
Std Dev	1.838



B1L103S, U6-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U6-ch105, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.24
Std Dev	11.03

Gaus mean : 28.7259

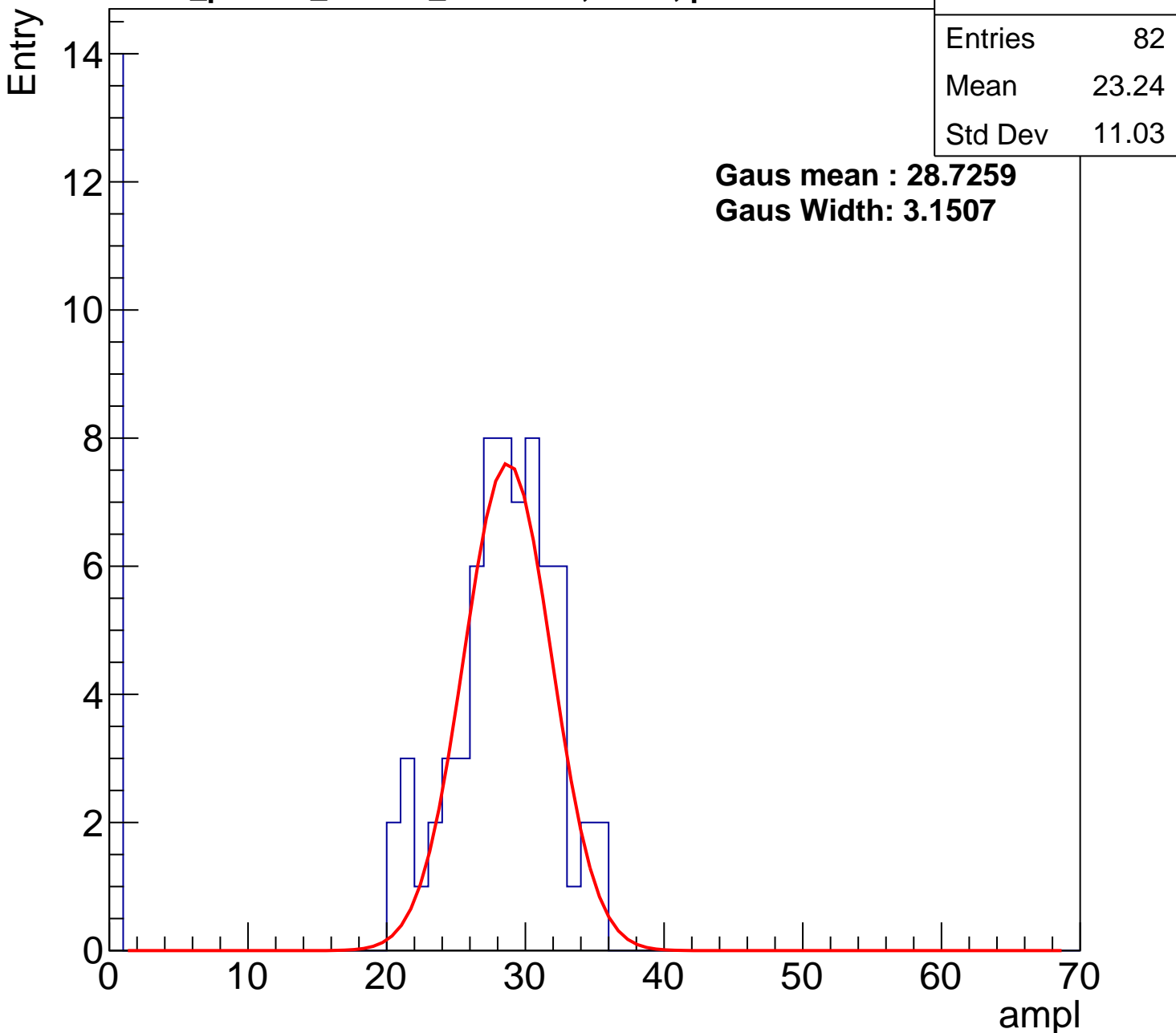
Gaus Width: 3.1507

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch105, adc1

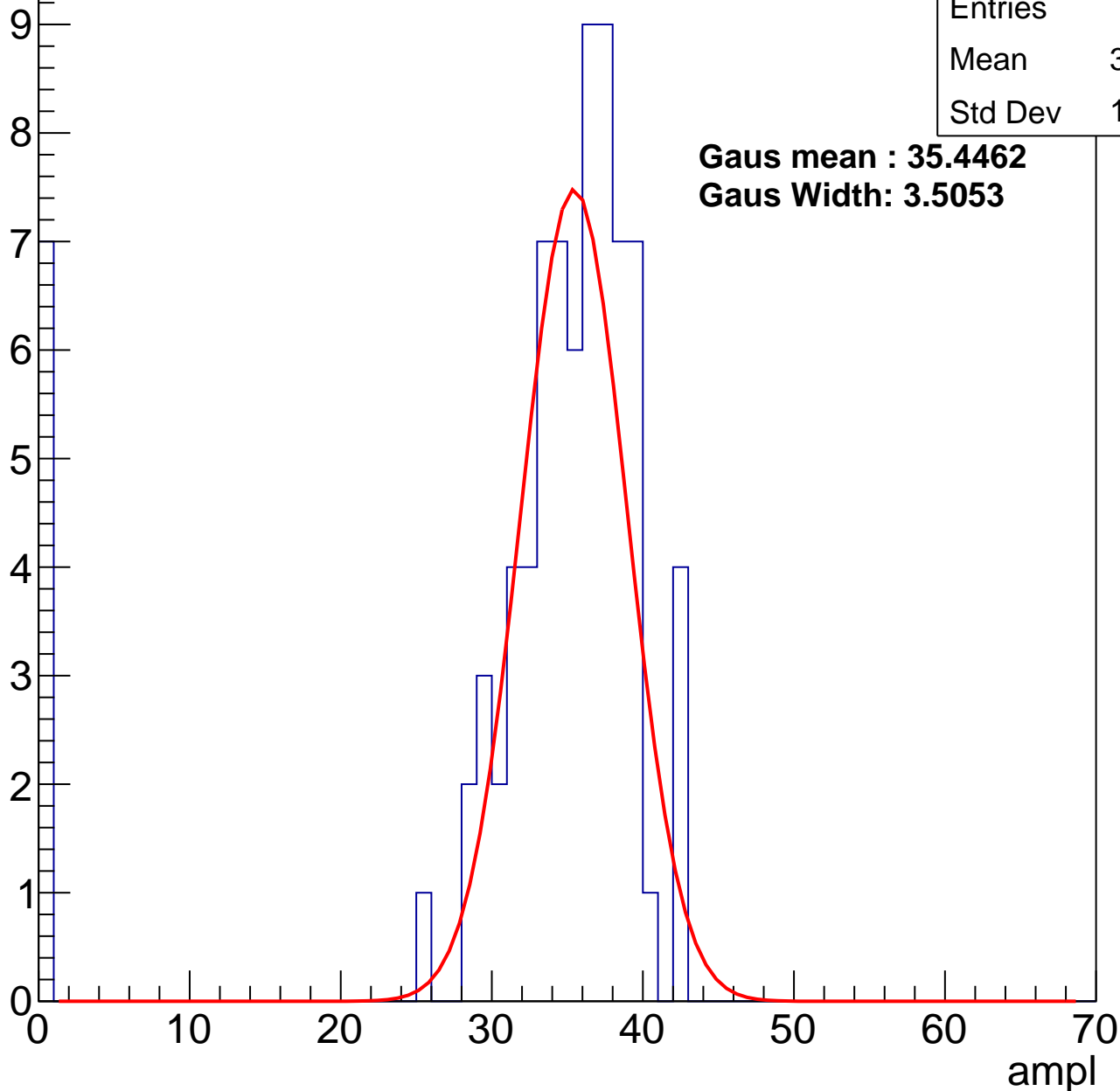
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	32.04
Std Dev	10.49

Gaus mean : 35.4462

Gaus Width: 3.5053



B1L103S, U6-ch105, adc2

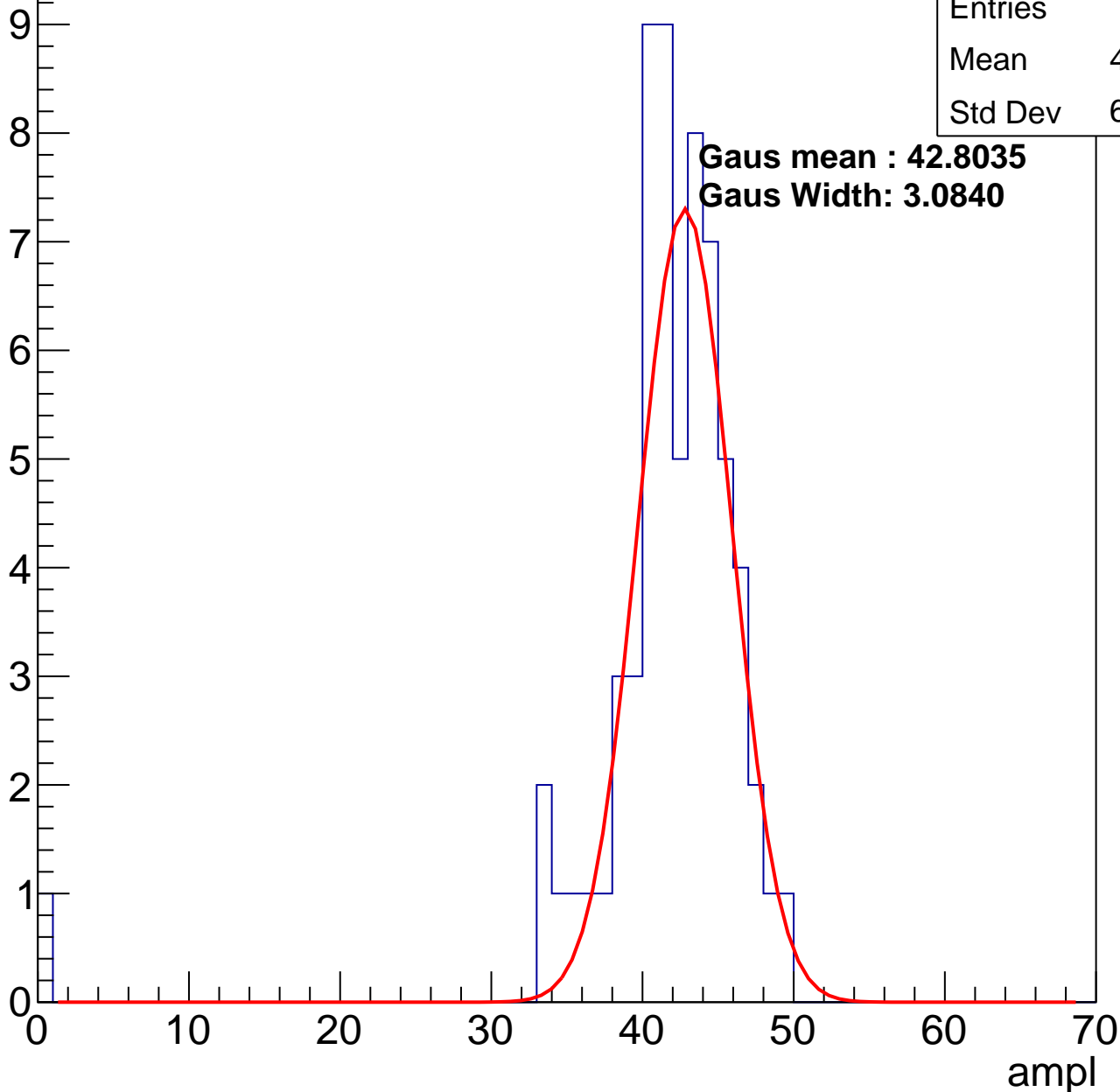
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	41.09
Std Dev	6.184

Gaus mean : 42.8035

Gaus Width: 3.0840

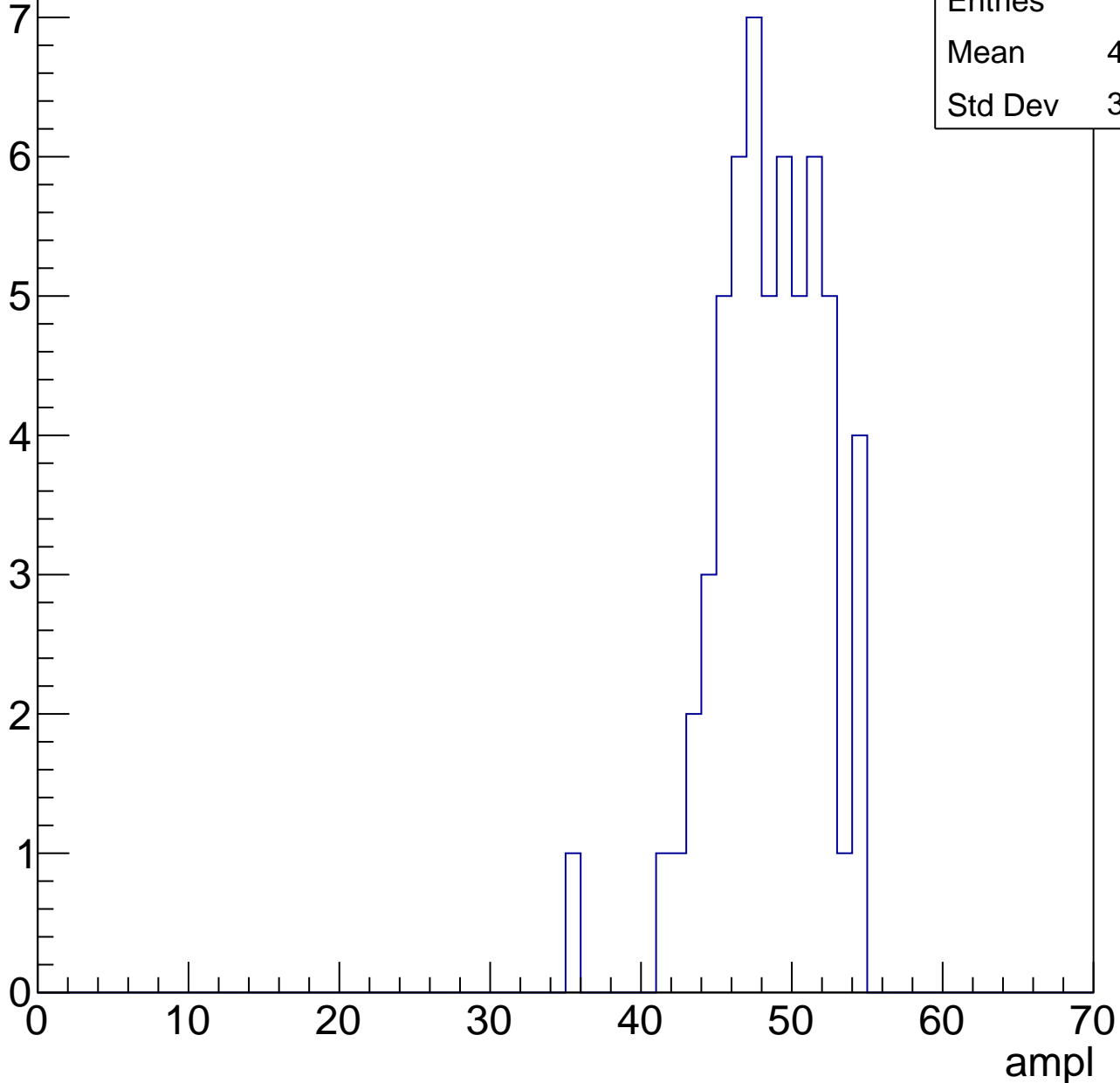


B1L103S, U6-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

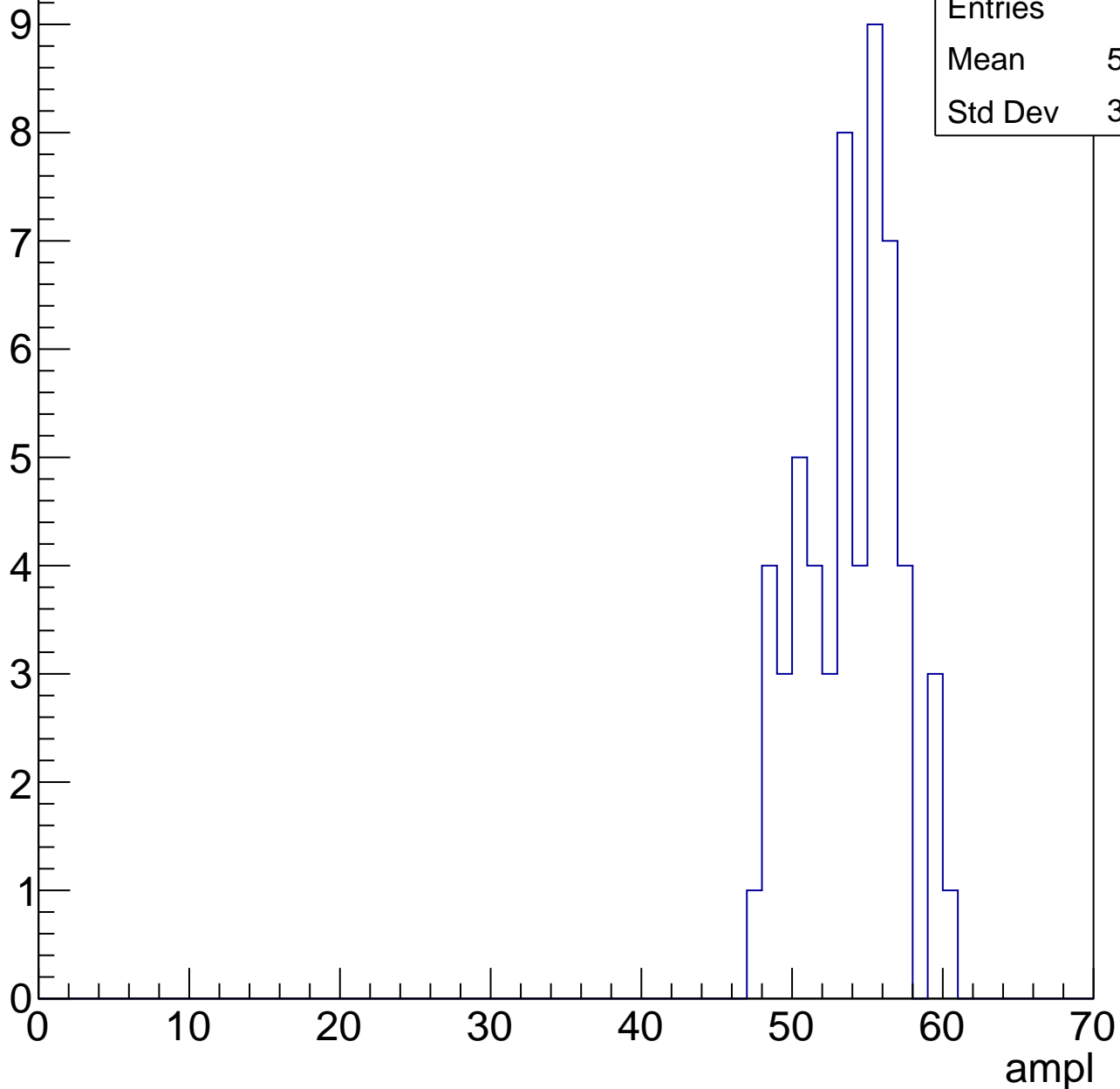
Entries	58
Mean	48.02
Std Dev	3.613



B1L103S, U6-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

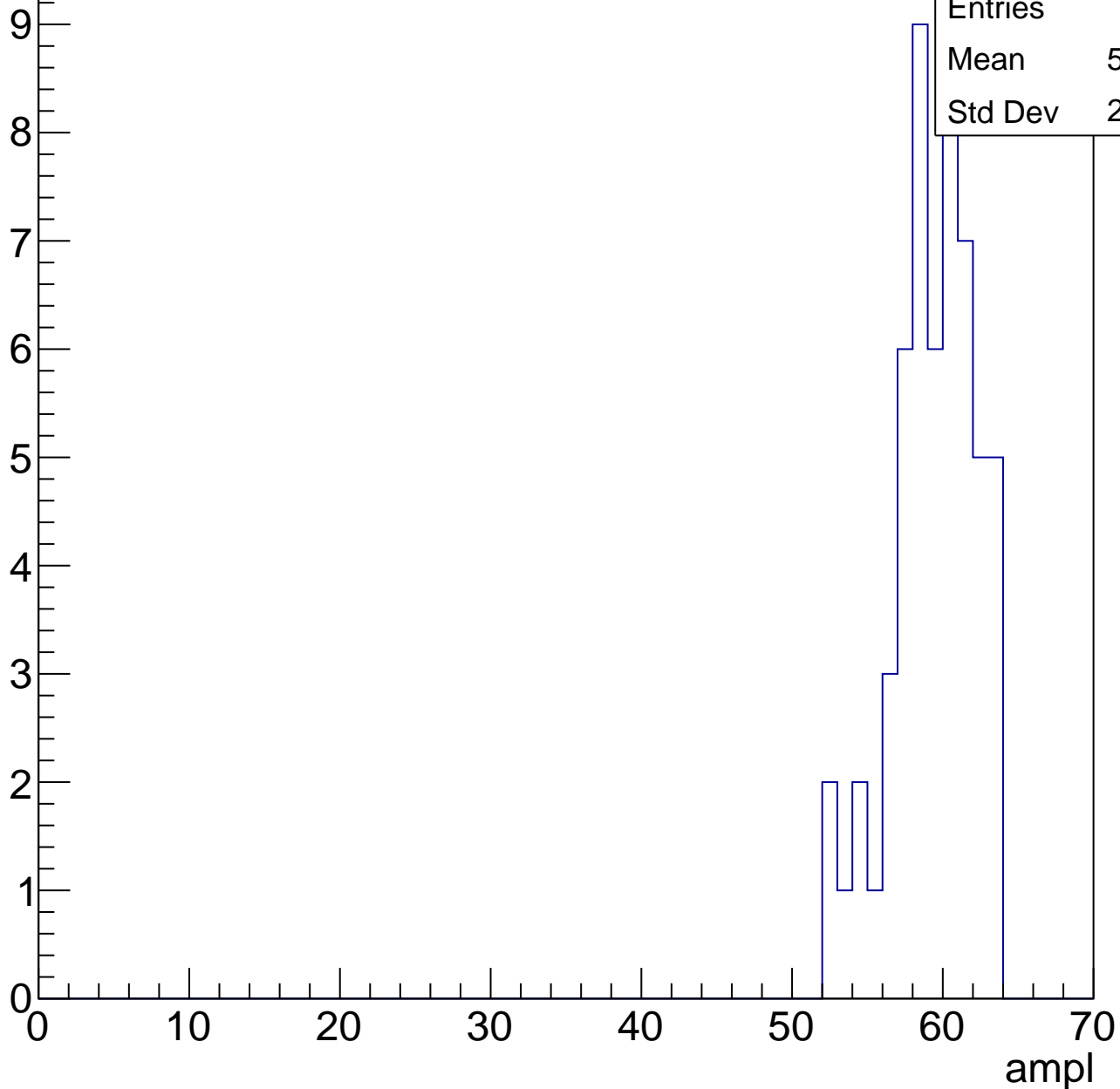
Entry



B1L103S, U6-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

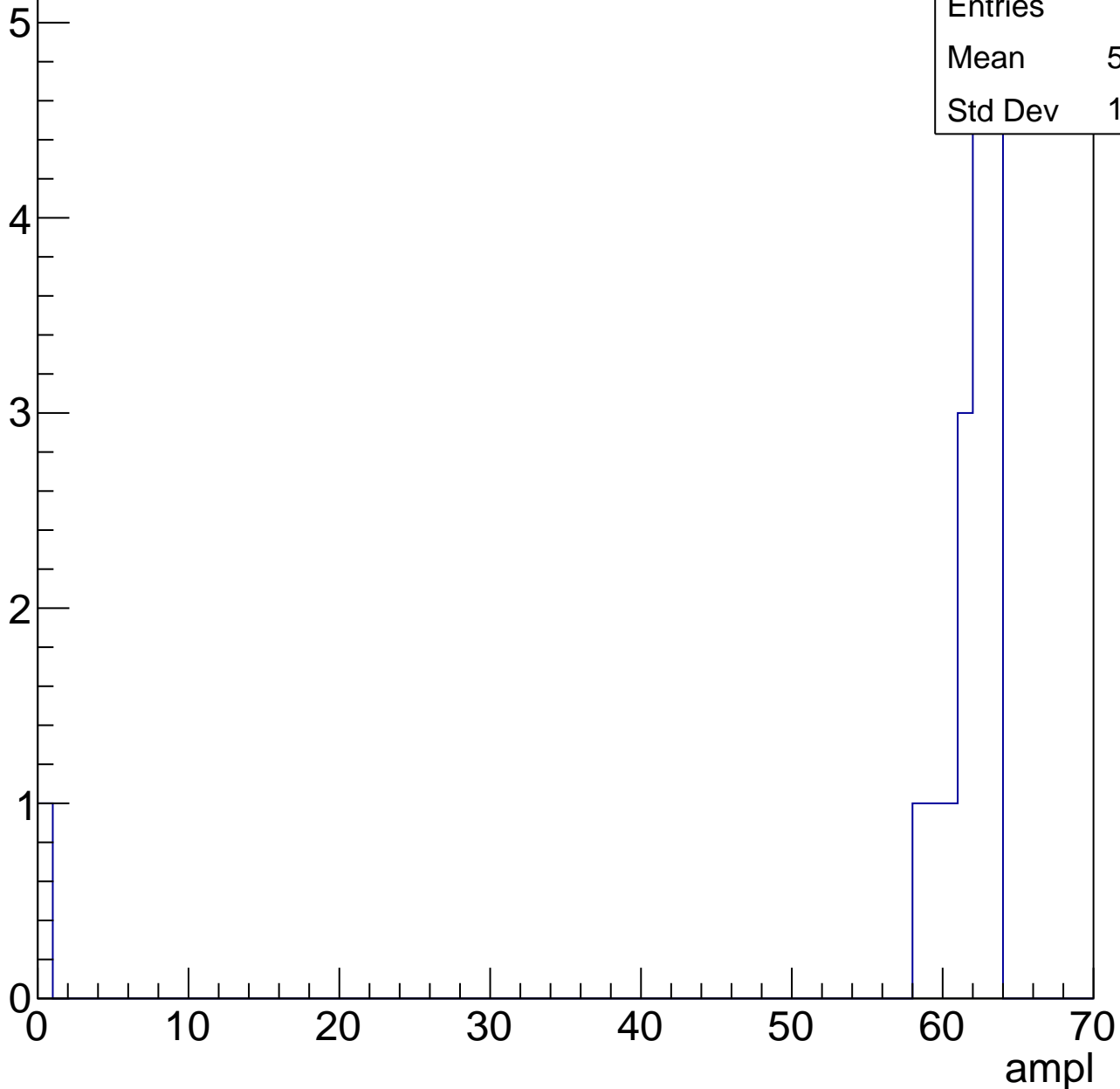


B1L103S, U6-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.94
Std Dev	14.55



B1L103S, U6-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch106, adc0

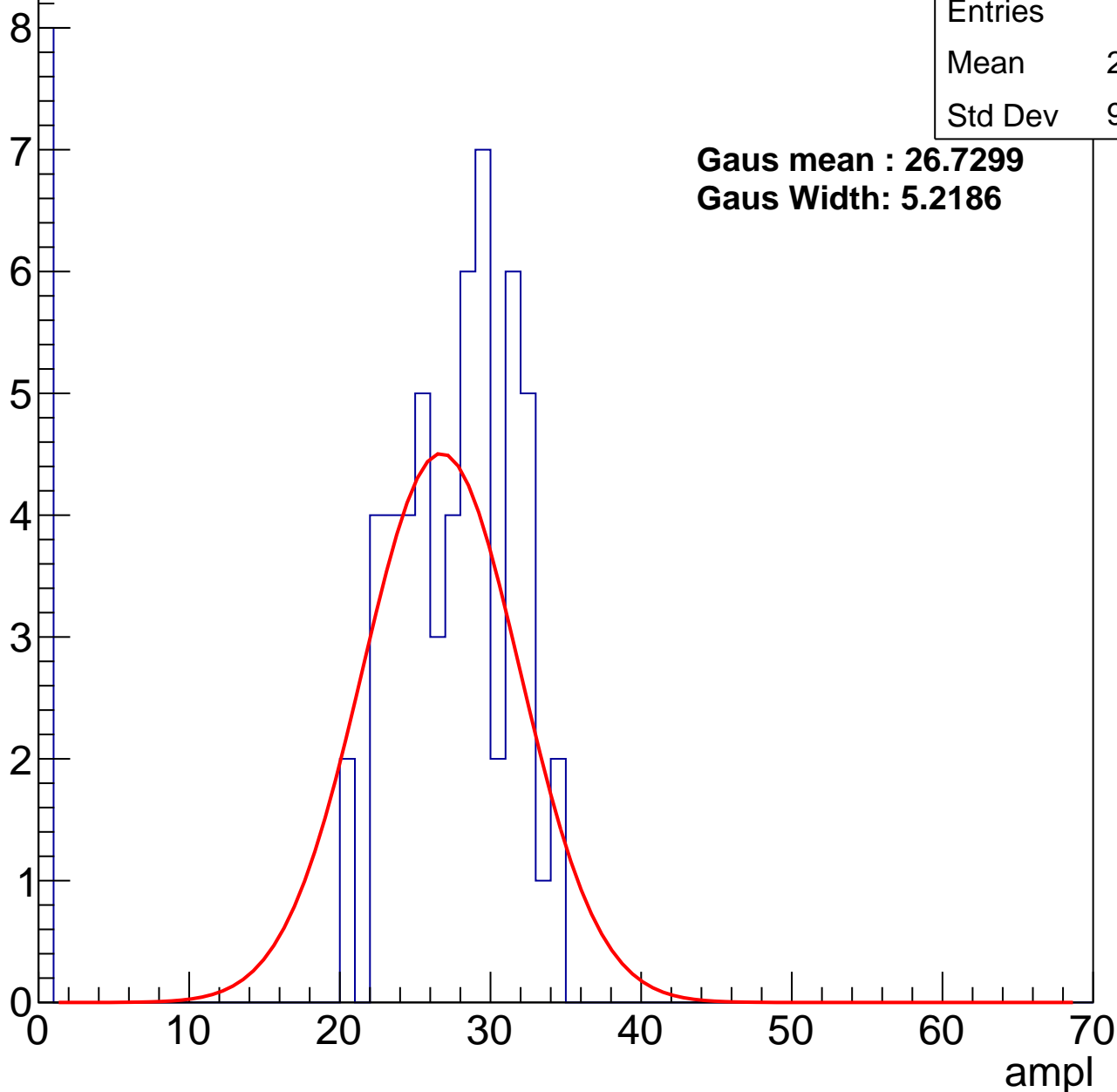
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	23.89
Std Dev	9.718

Gaus mean : 26.7299

Gaus Width: 5.2186



B1L103S, U6-ch106, adc1

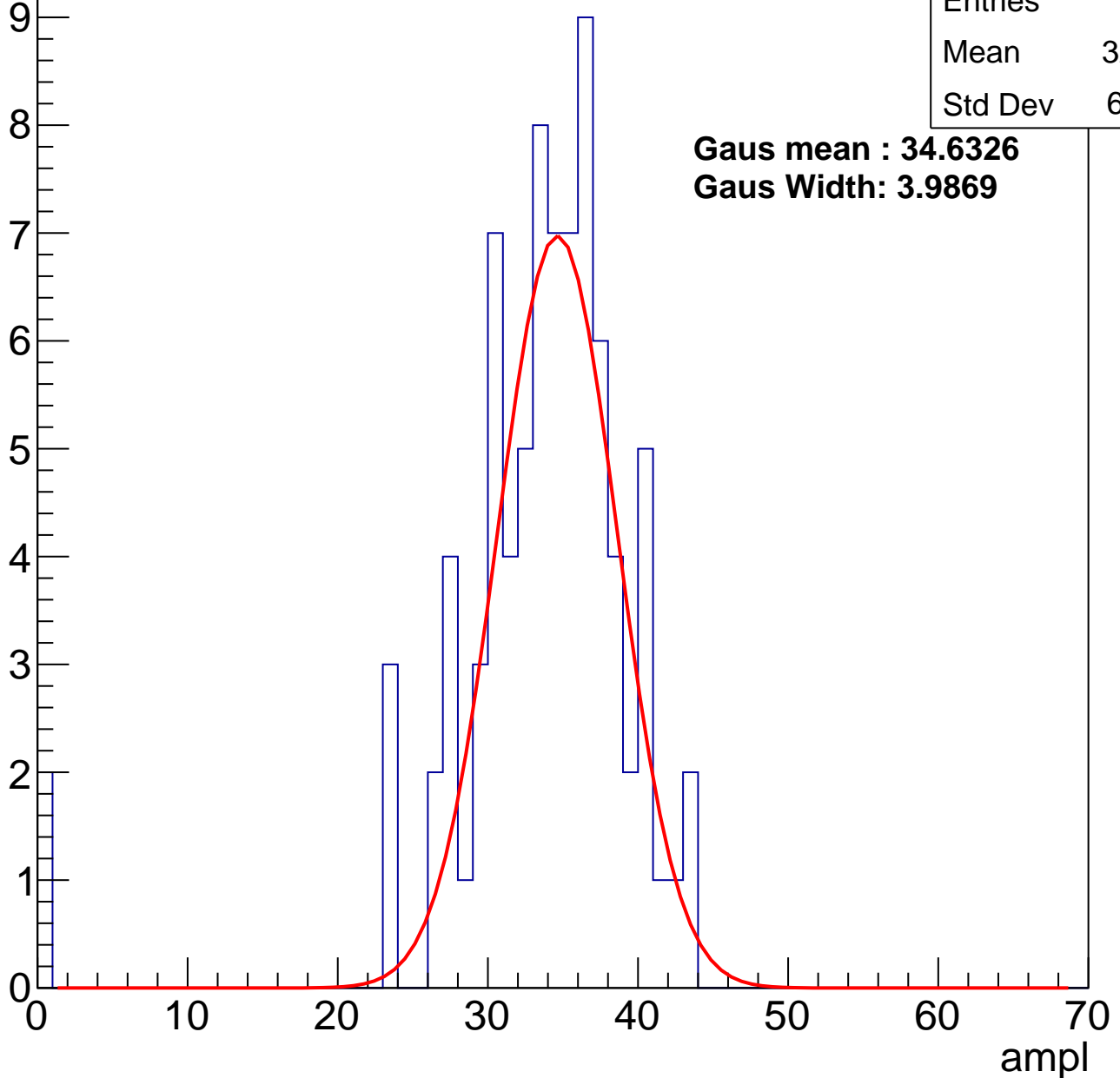
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	32.89
Std Dev	6.811

Gaus mean : 34.6326

Gaus Width: 3.9869



B1L103S, U6-ch106, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.41
Std Dev	15.52

Gaus mean : 41.2090

Gaus Width: 3.6158

10

8

6

4

2

0

0

10

20

30

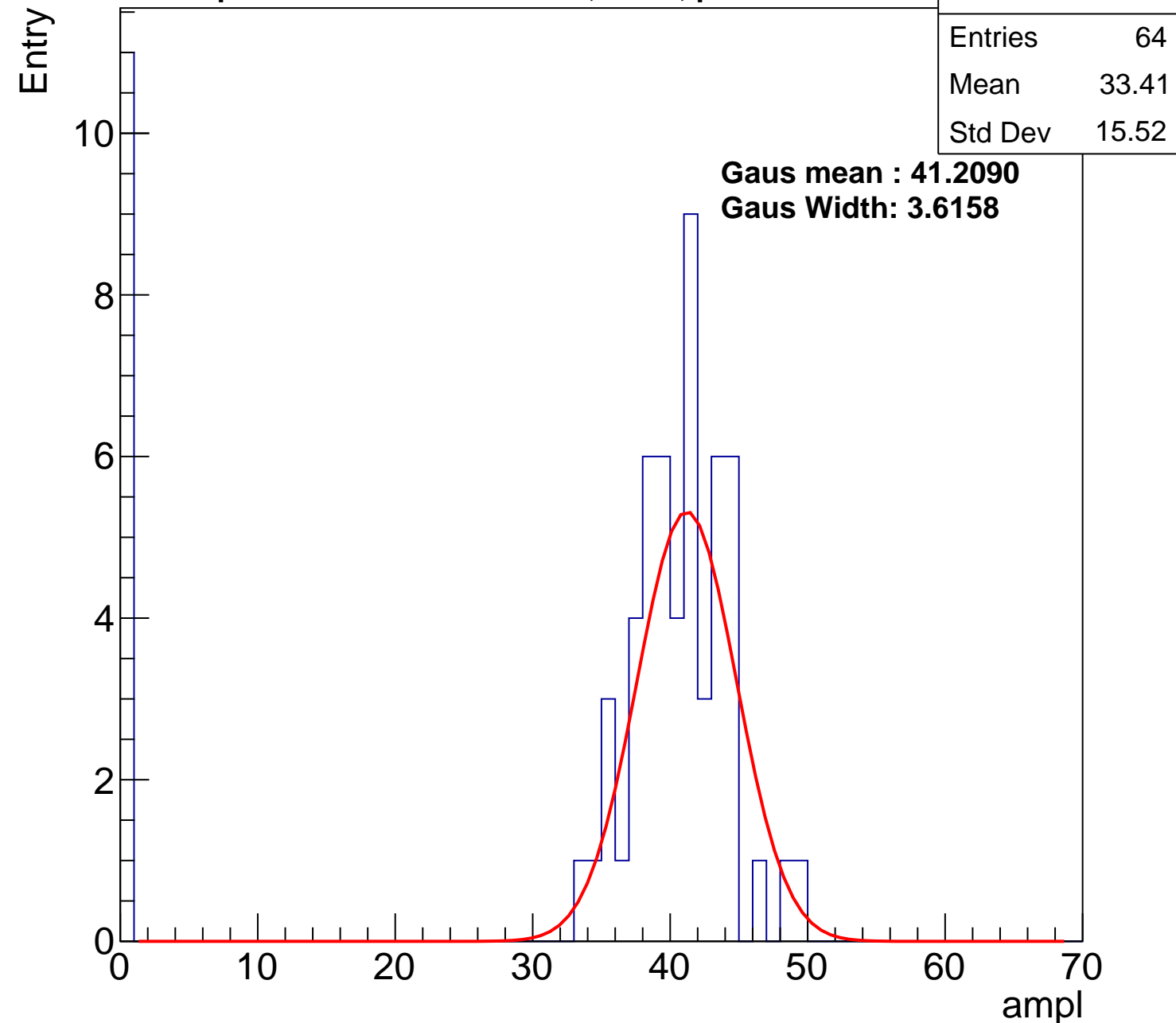
40

50

60

70

ampl



B1L103S, U6-ch106, adc3

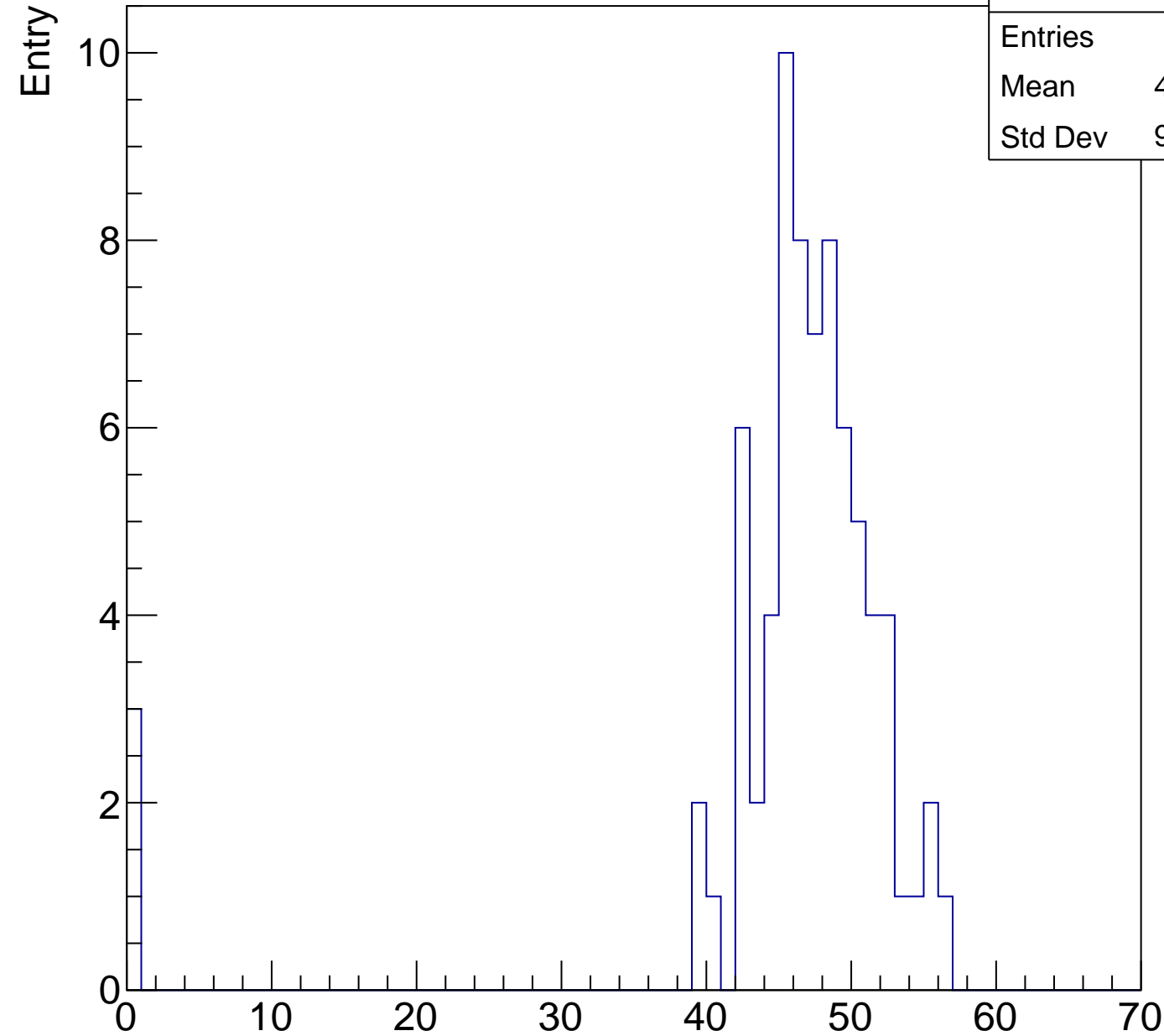
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	45.23
Std Dev	9.912

Entry

10
8
6
4
2
0

ampl

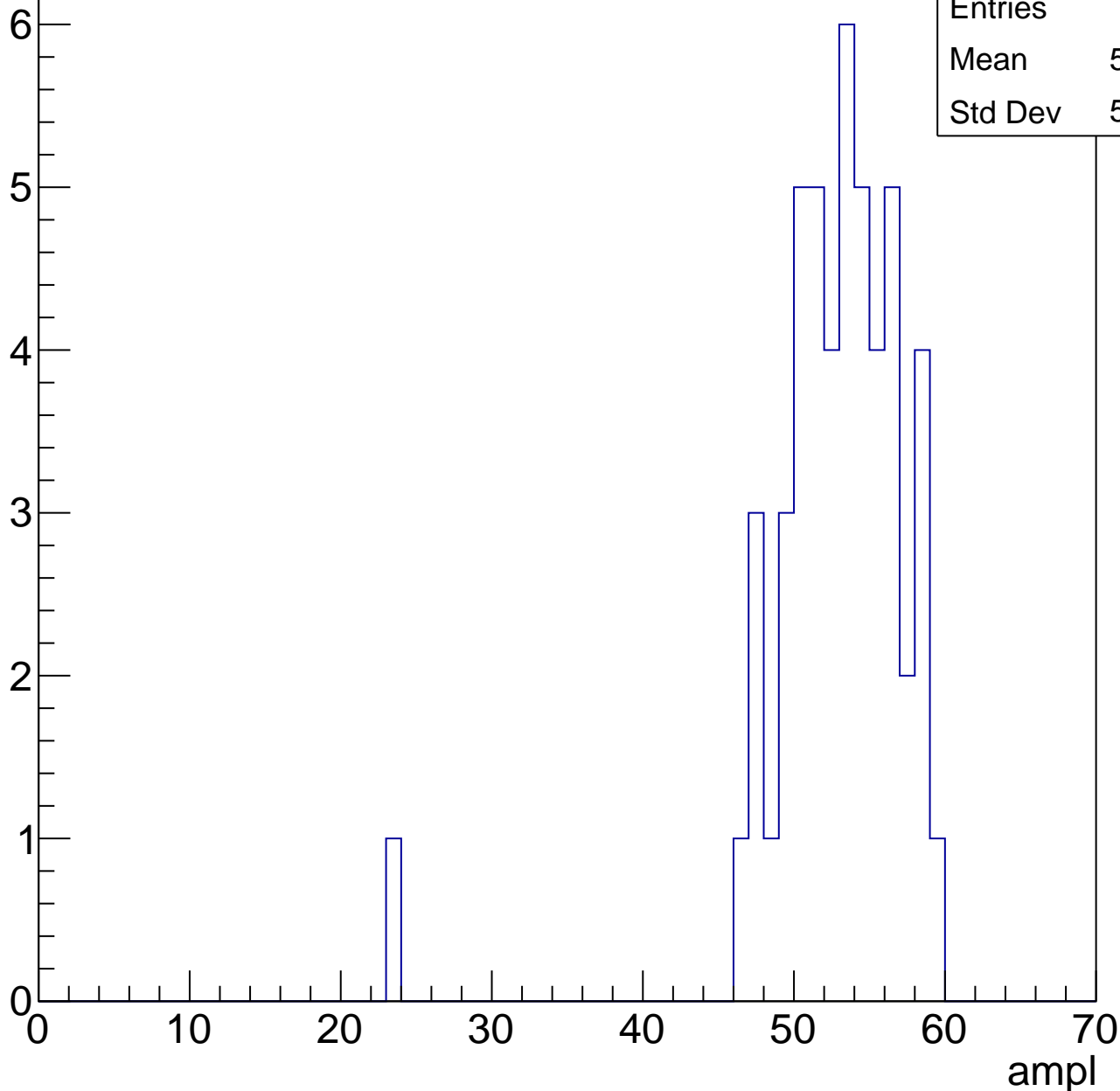


B1L103S, U6-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	52.22
Std Dev	5.296

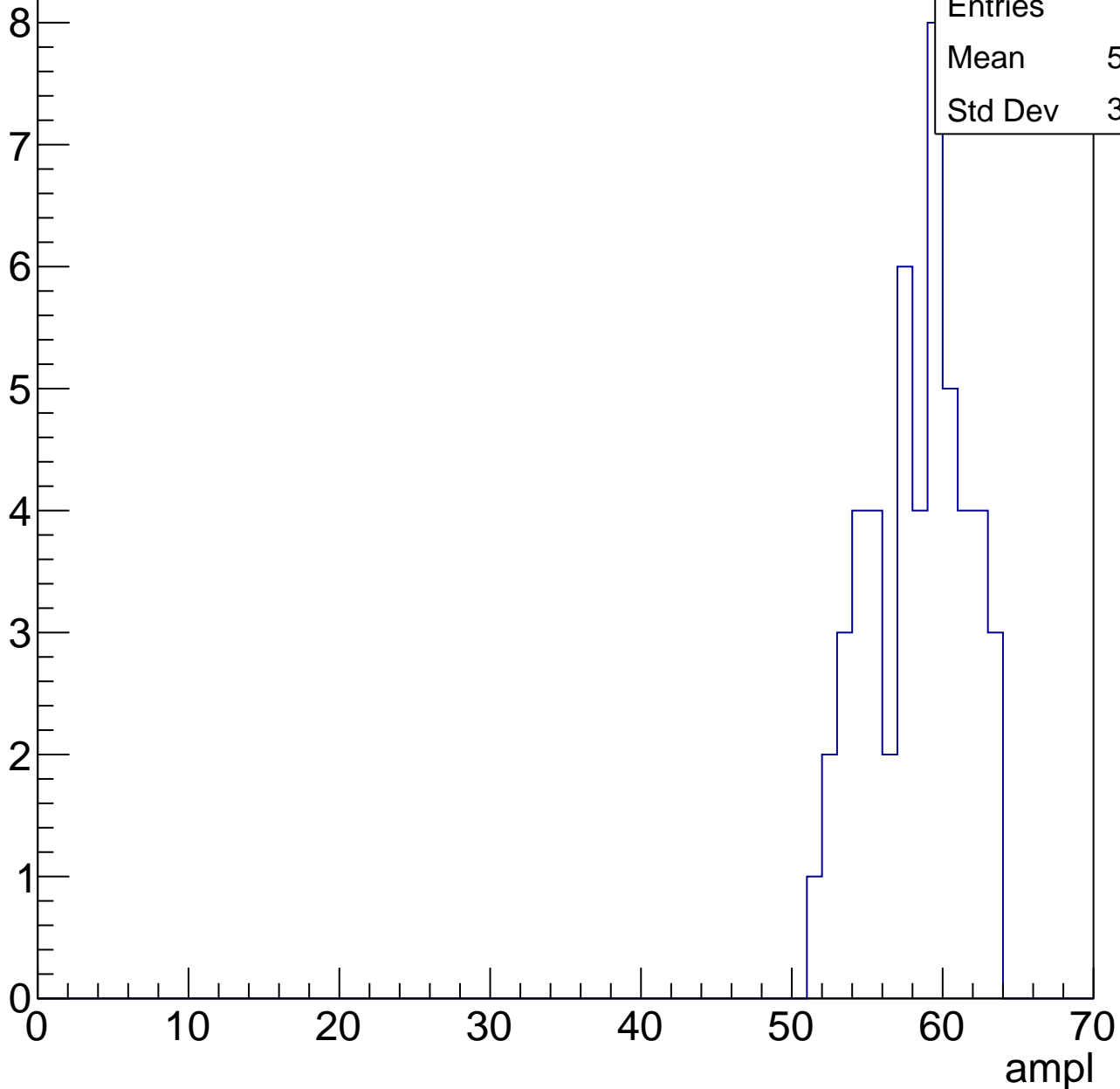


B1L103S, U6-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.78
Std Dev	3.196

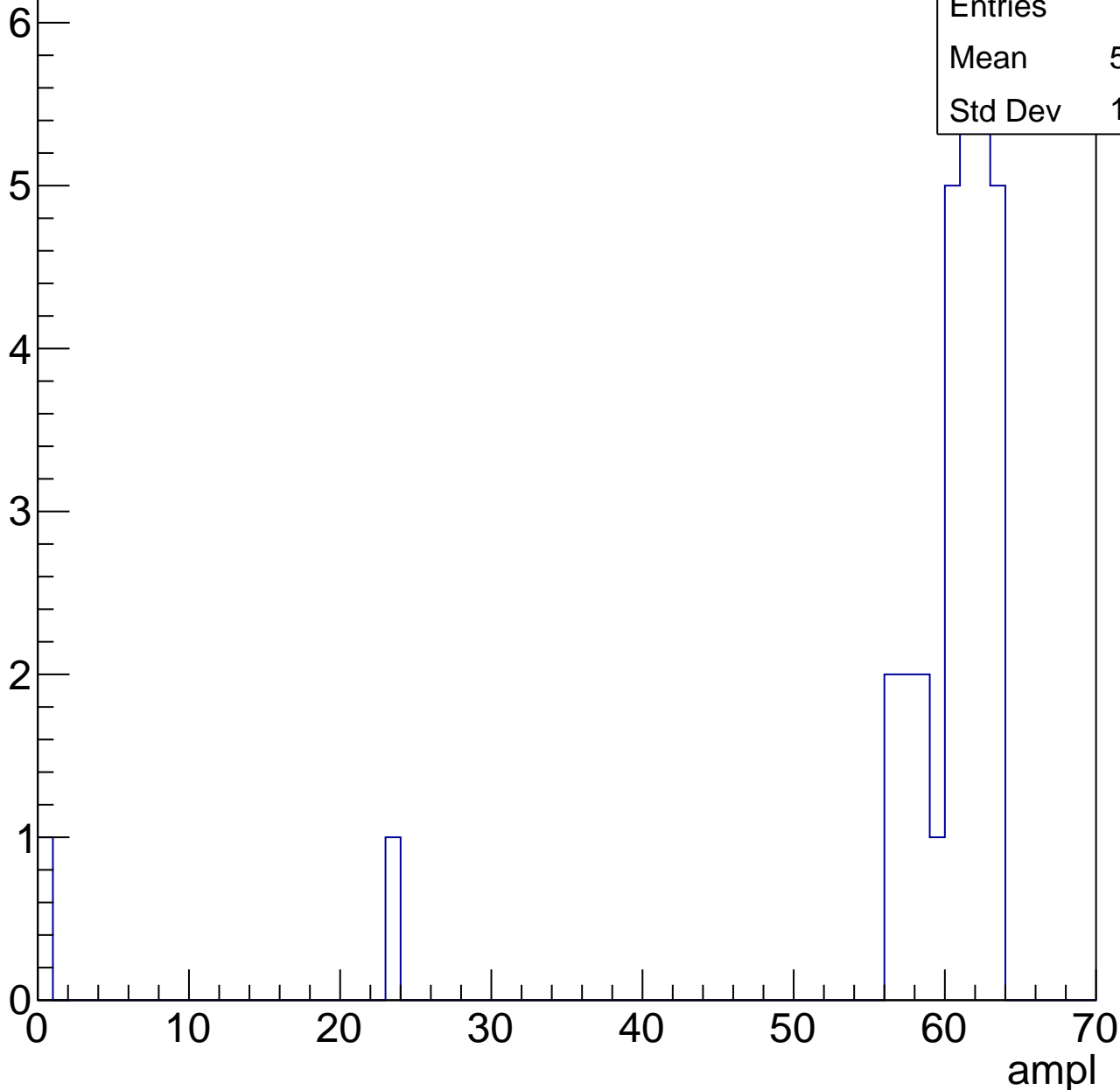


B1L103S, U6-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

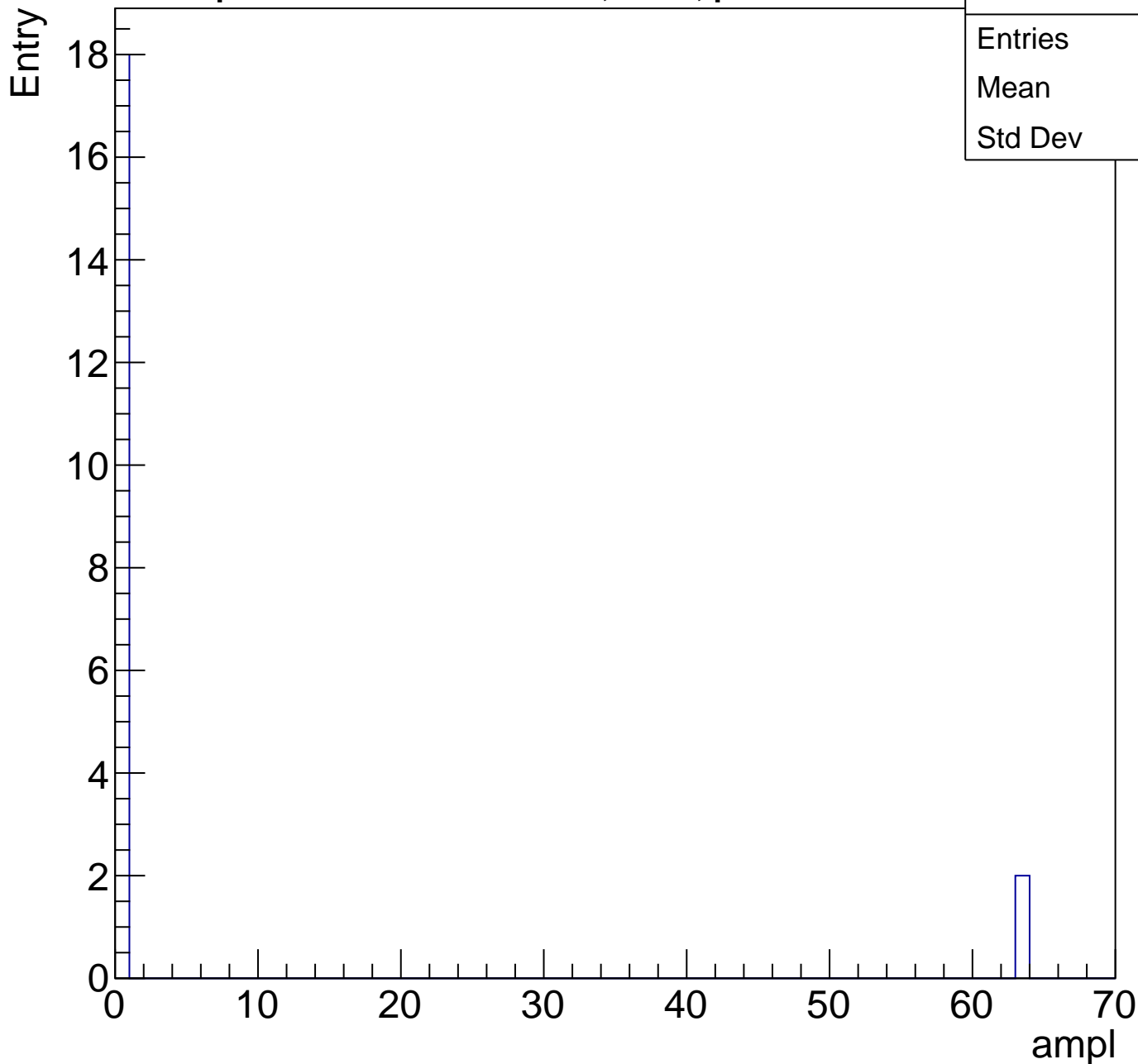
Entries	31
Mean	57.32
Std Dev	12.55



B1L103S, U6-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.3
Std Dev	18.9



B1L103S, U6-ch107, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	25.75
Std Dev	9.67

Gaus mean : 29.6997

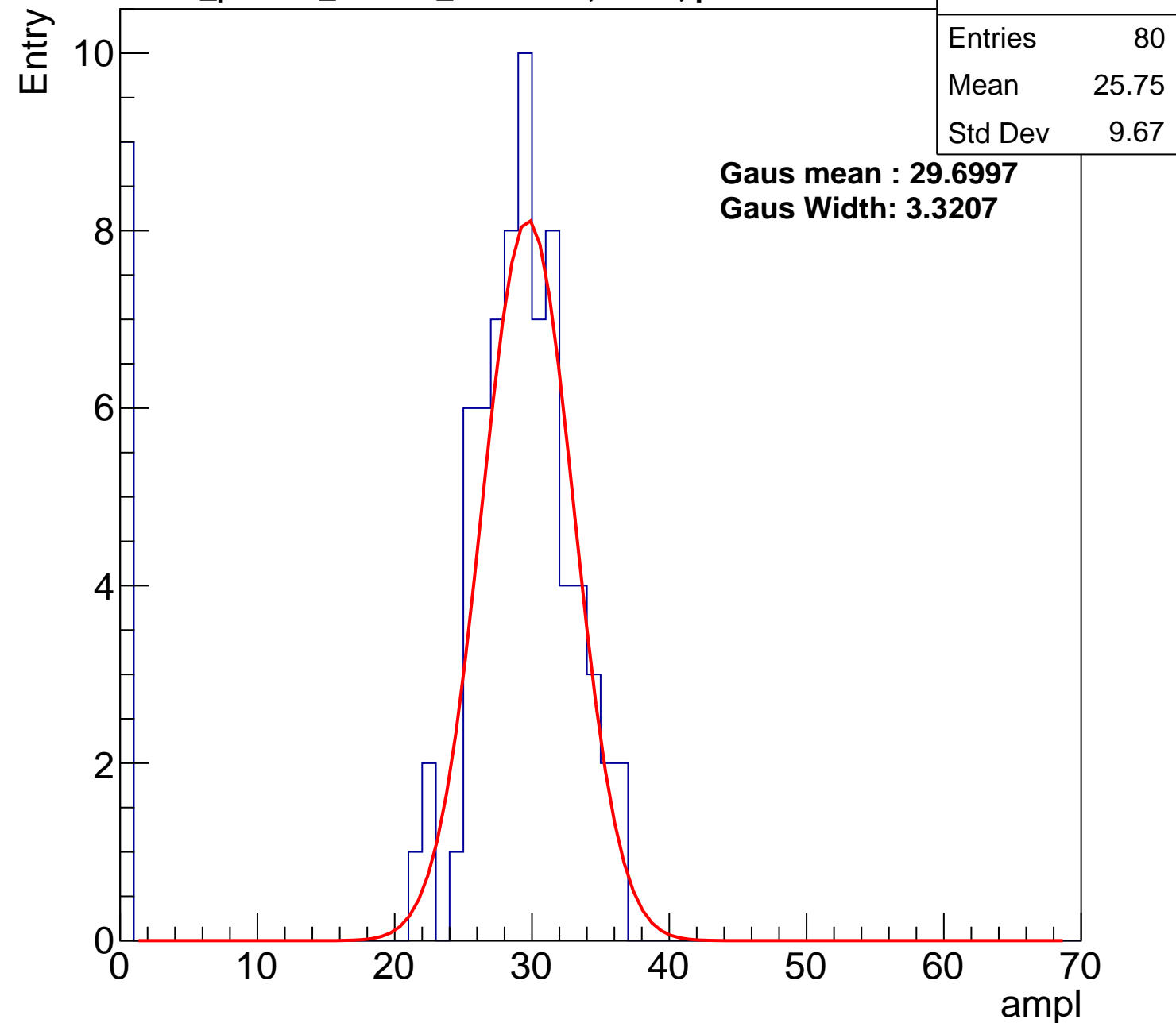
Gaus Width: 3.3207

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch107, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	30.12
Std Dev	12.98

Gaus mean : 35.8962

Gaus Width: 3.3309

Entry

10

8

6

4

2

0

0

10

20

30

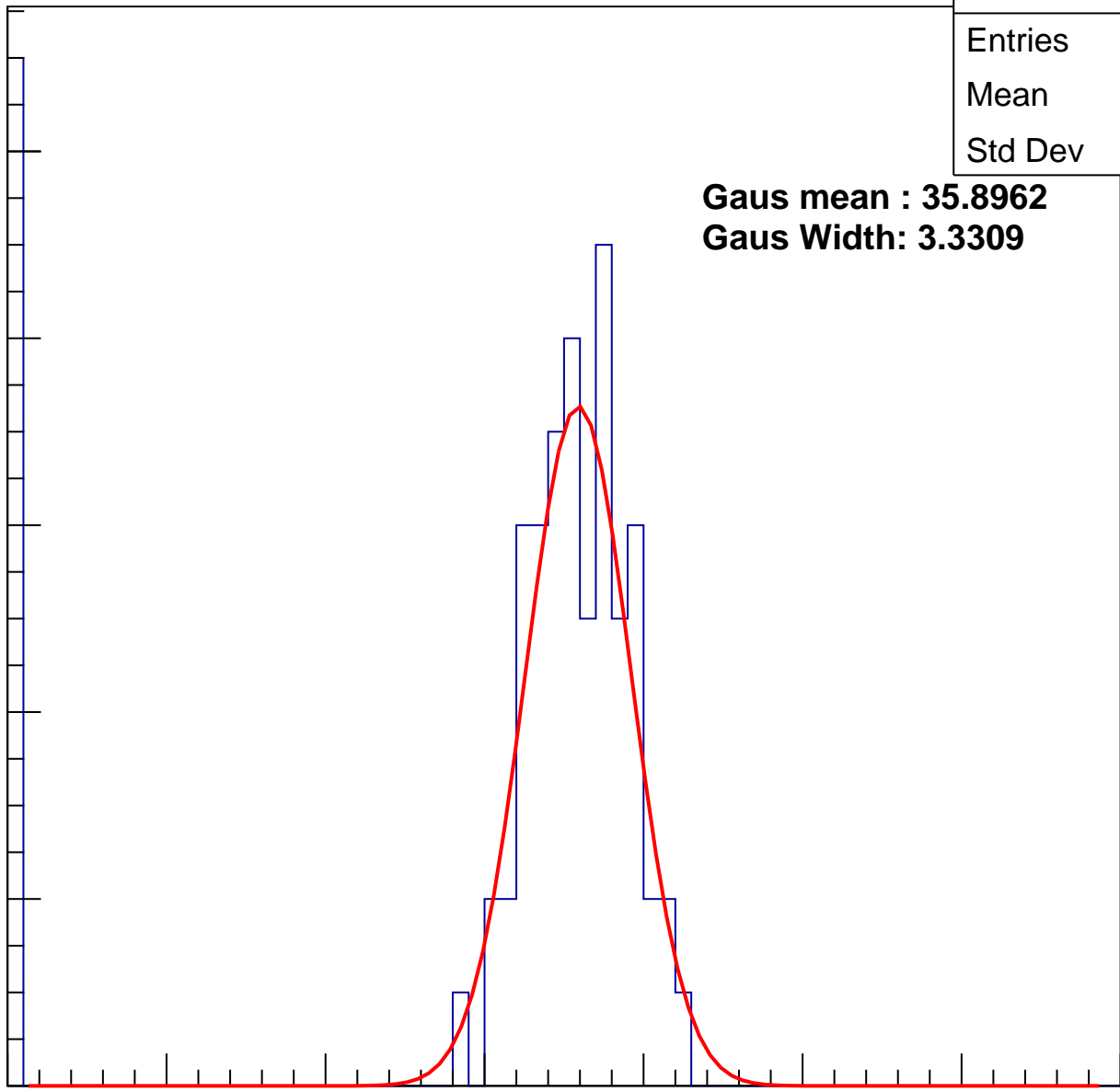
40

50

60

70

ampl



B1L103S, U6-ch107, adc2

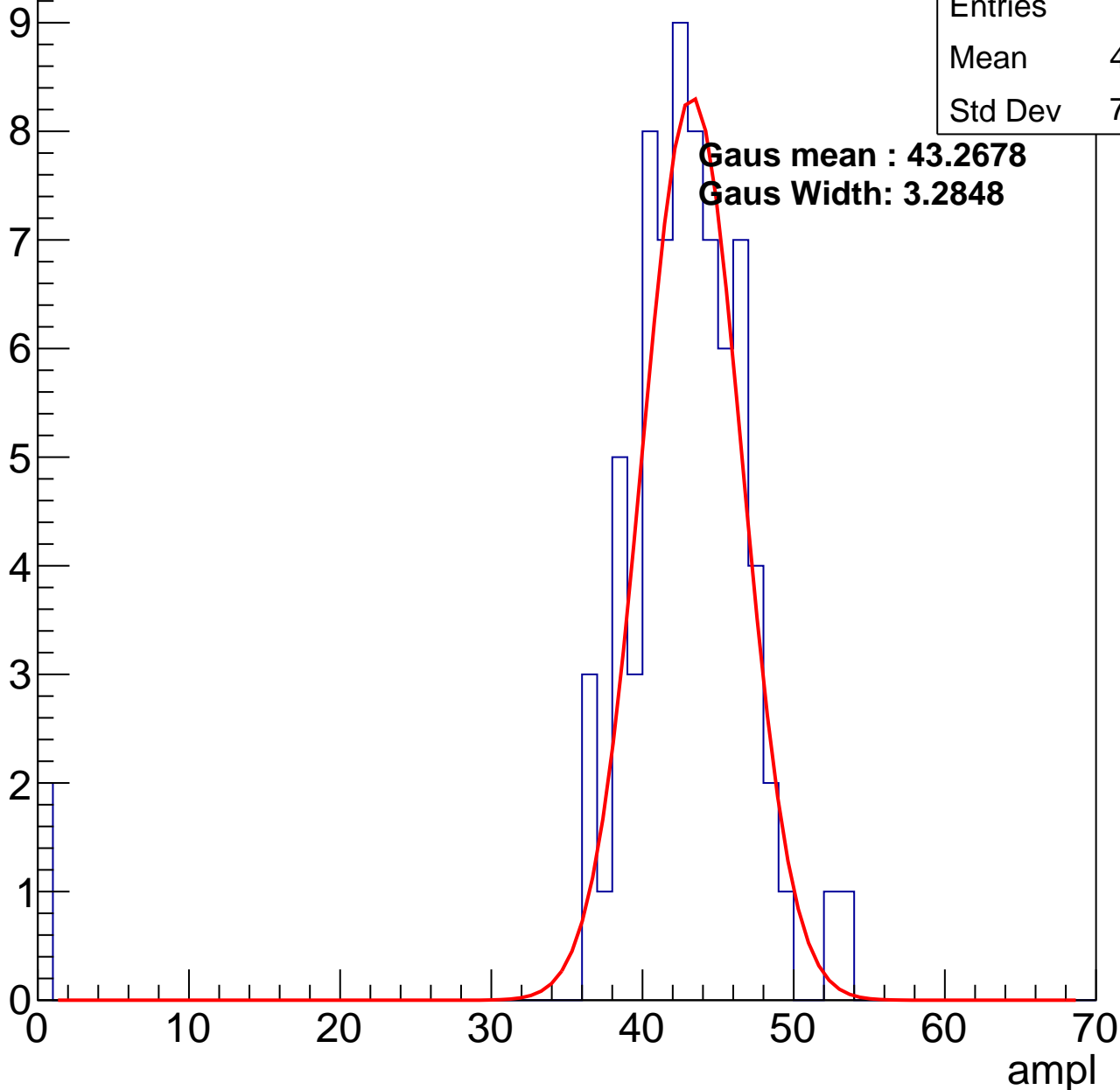
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	41.59
Std Dev	7.693

Gaus mean : 43.2678

Gaus Width: 3.2848

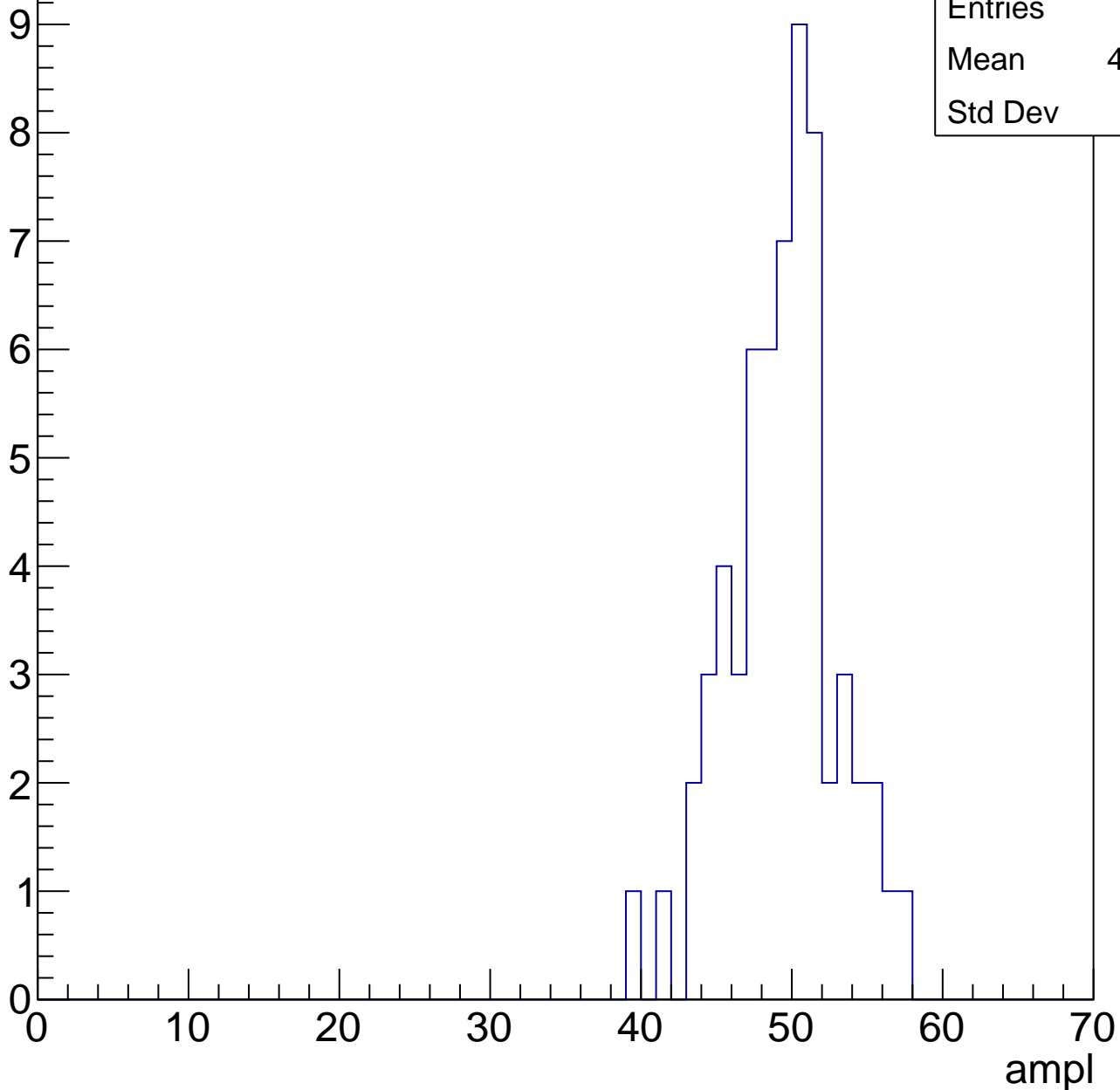


B1L103S, U6-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	48.87
Std Dev	3.56

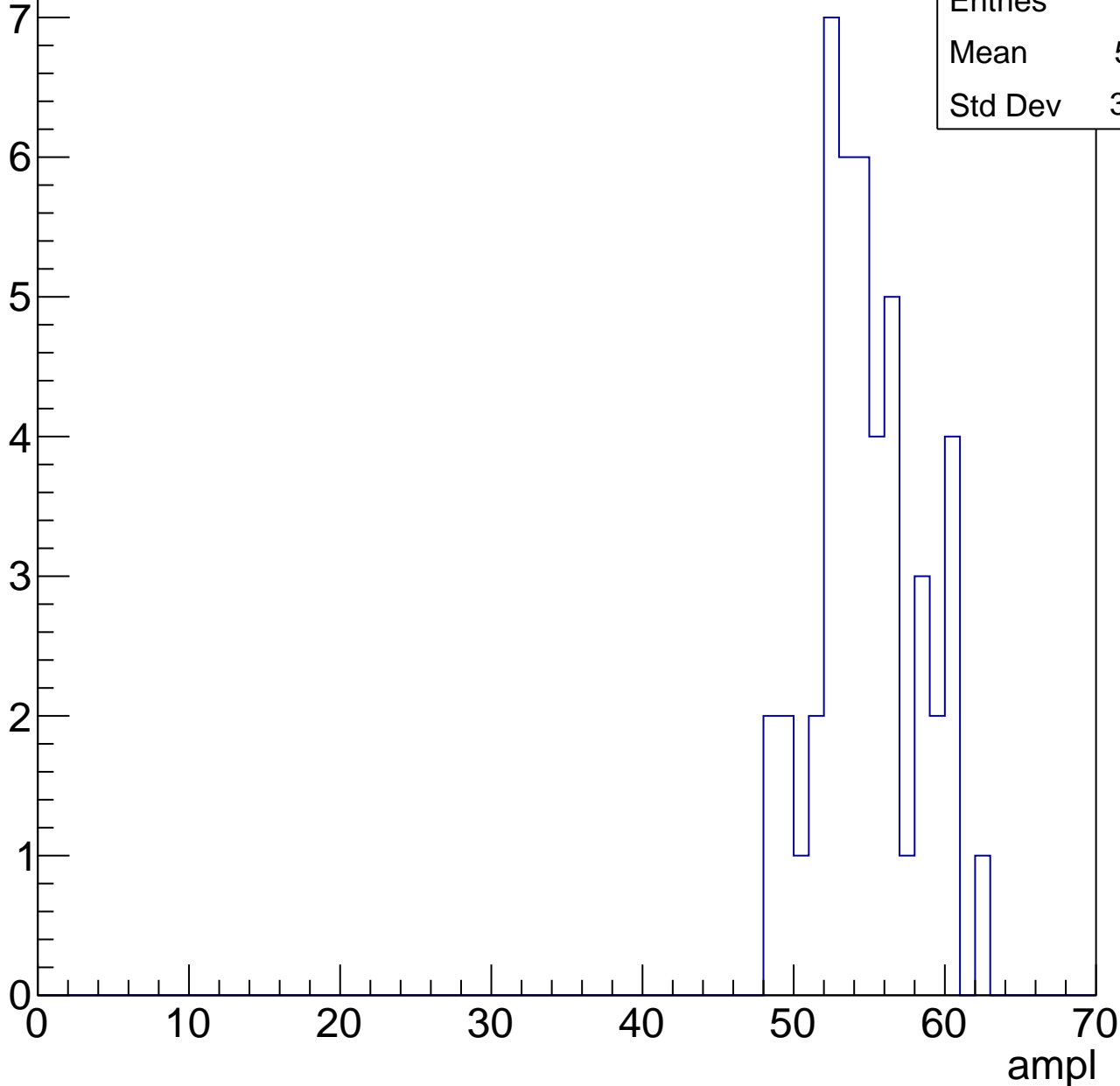


B1L103S, U6-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

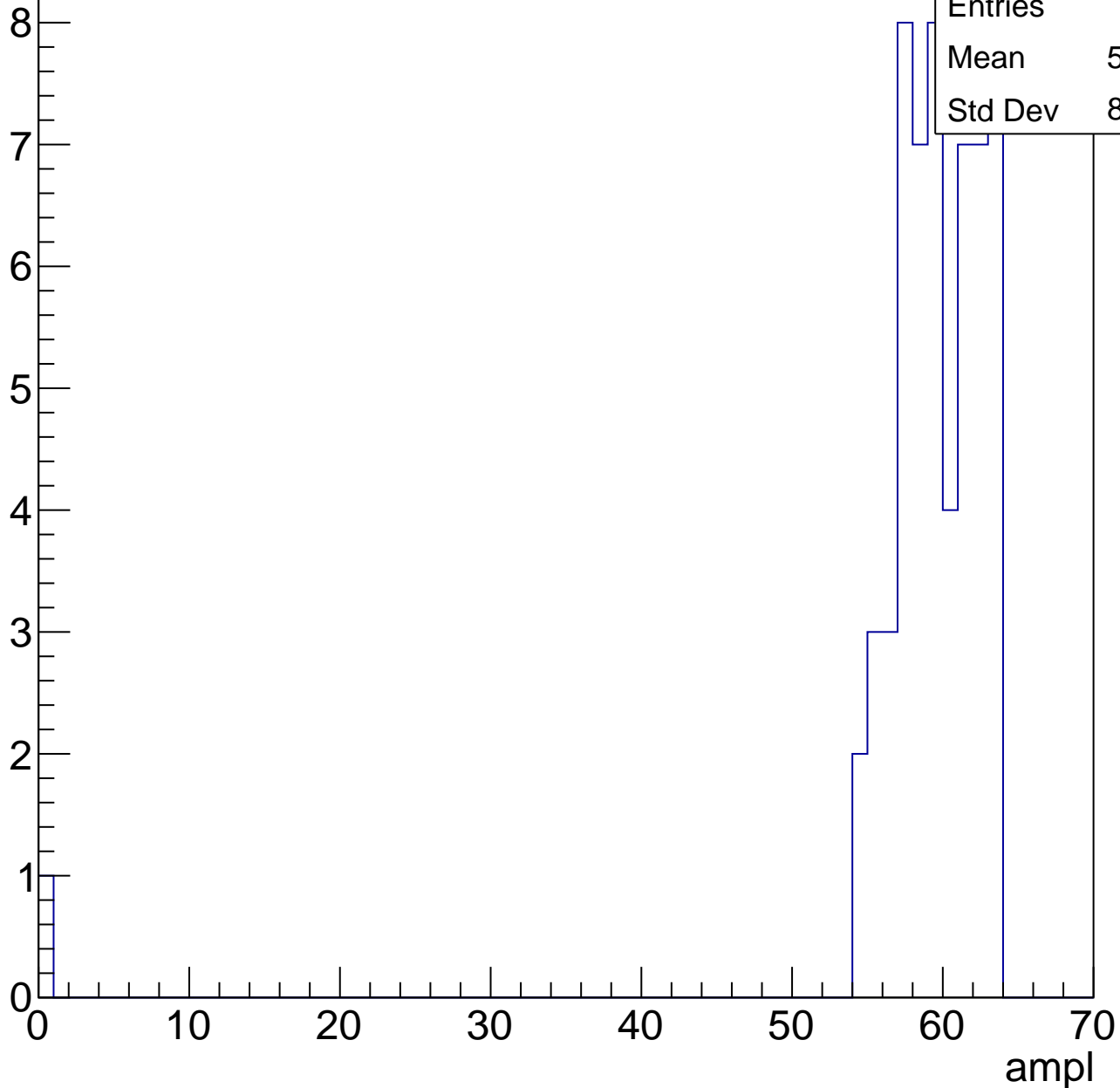
Entries	46
Mean	54.41
Std Dev	3.366



B1L103S, U6-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

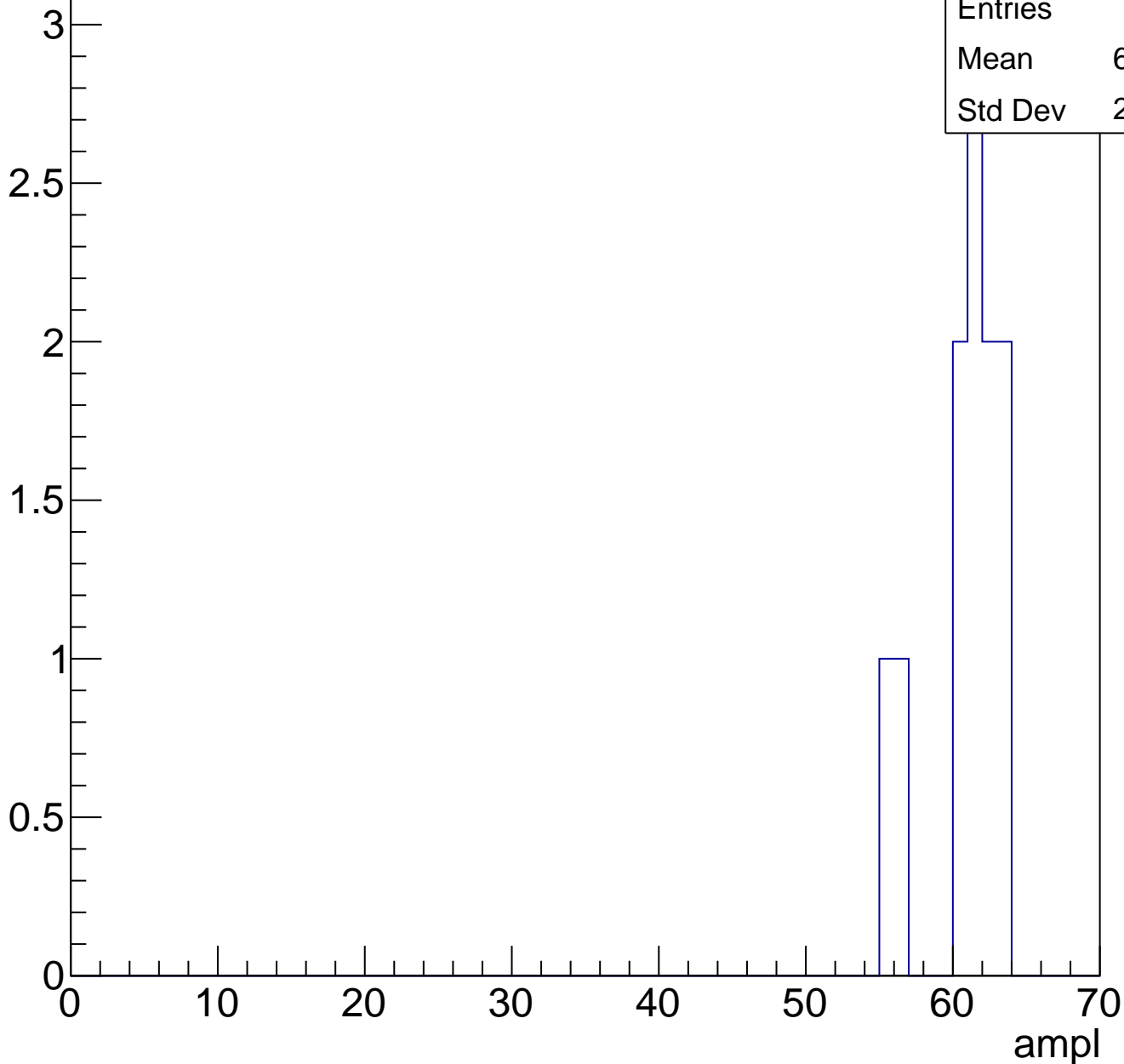


Entries	58
Mean	58.28
Std Dev	8.134

B1L103S, U6-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U6-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	23.61
Std Dev	9.314

Gaus mean : 27.1525

Gaus Width: 3.8347

Entry

10

8

6

4

2

0

0

10

20

30

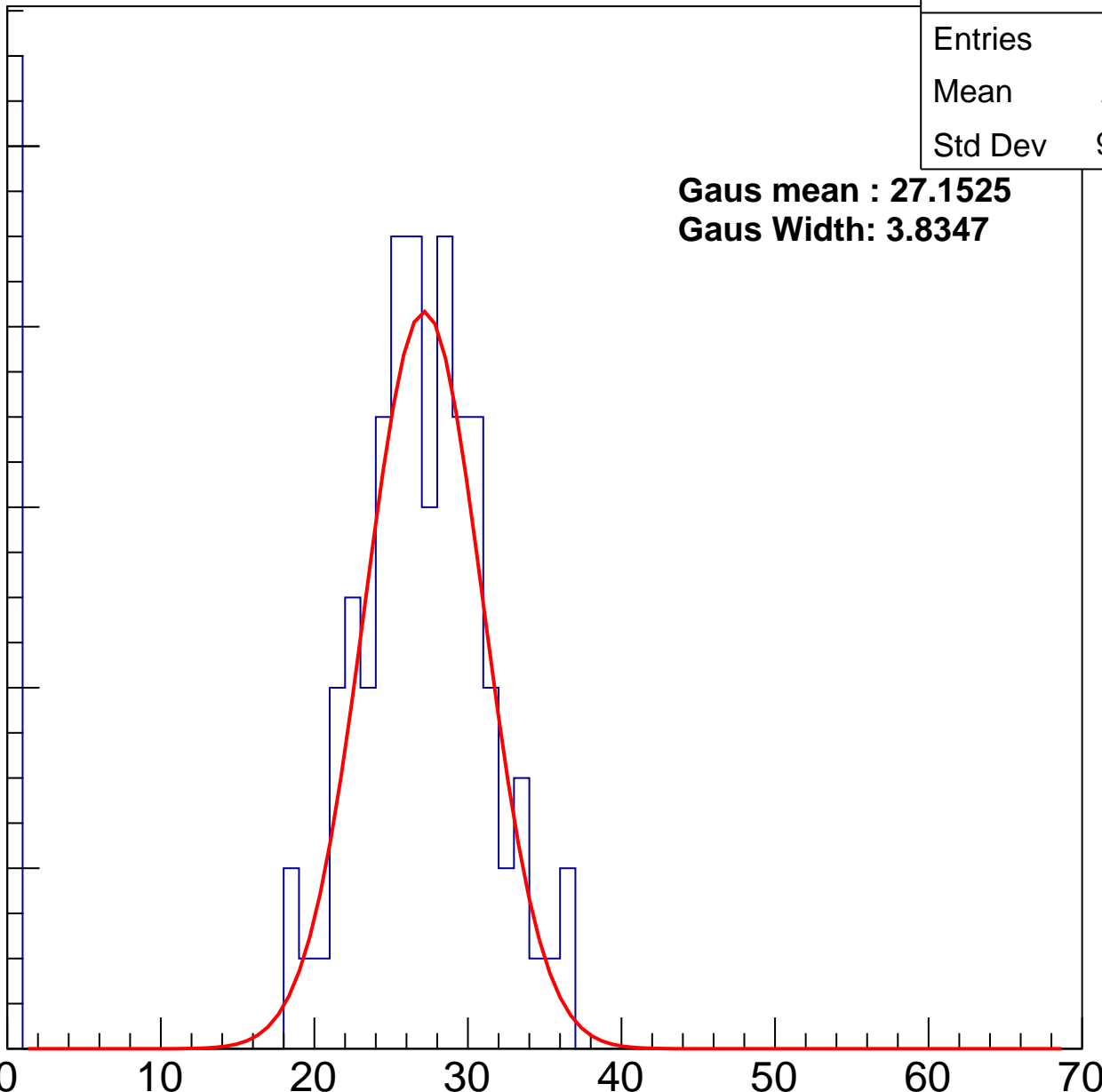
40

50

60

70

ampl



B1L103S, U6-ch108, adc1

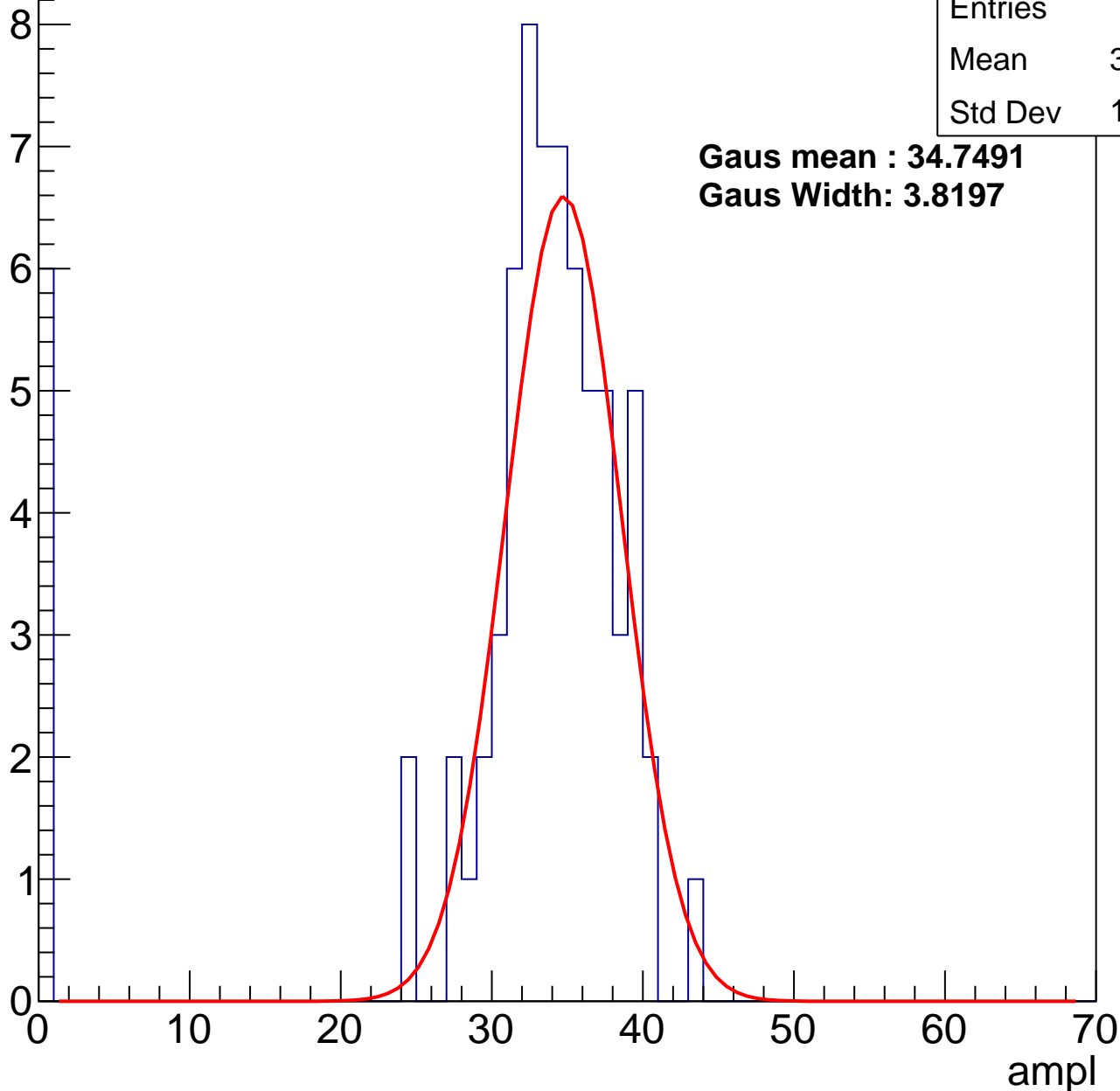
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.93
Std Dev	10.06

Gaus mean : 34.7491

Gaus Width: 3.8197



B1L103S, U6-ch108, adc2

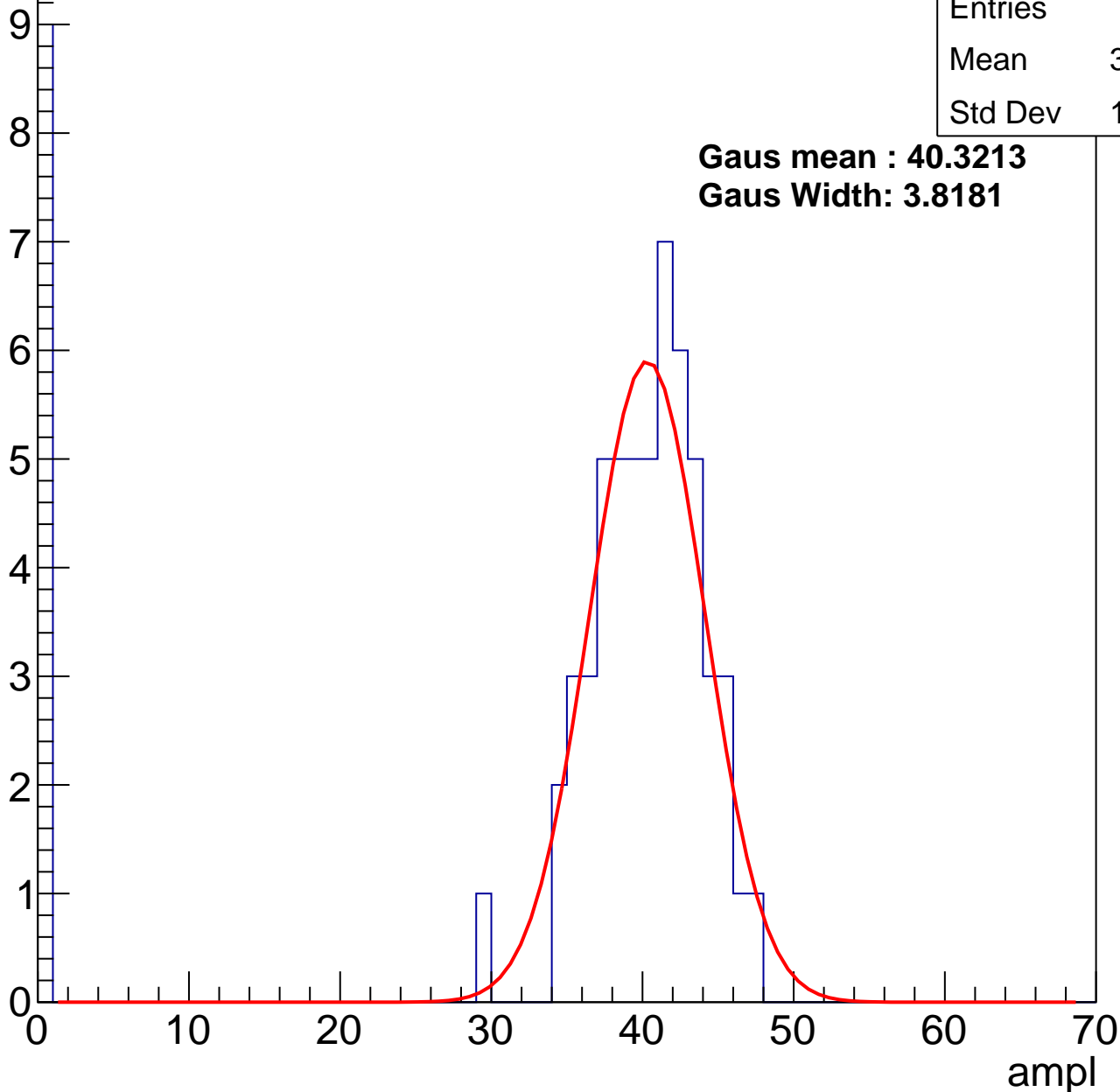
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.28
Std Dev	14.24

Gaus mean : 40.3213

Gaus Width: 3.8181

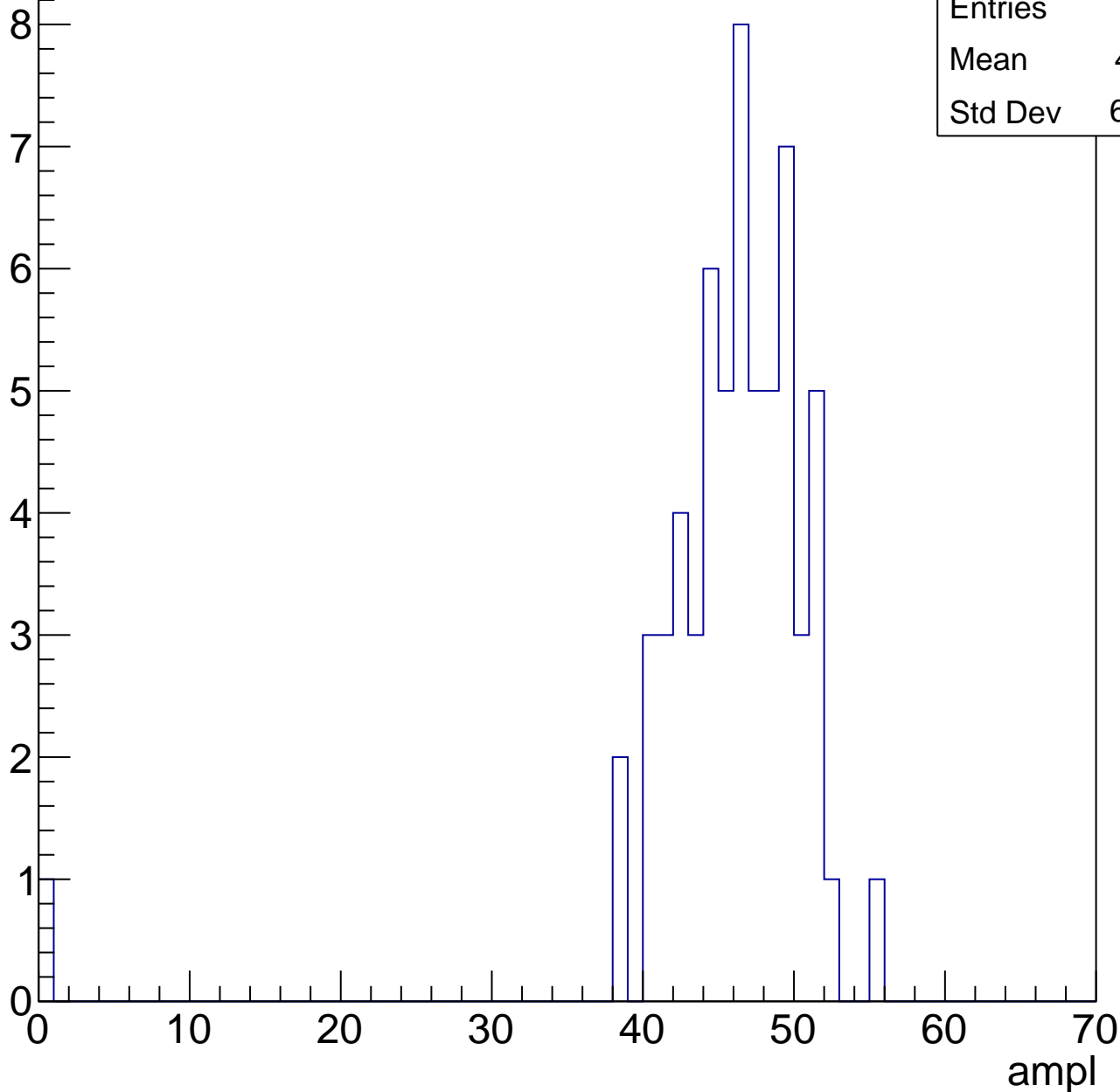


B1L103S, U6-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	45.21
Std Dev	6.823

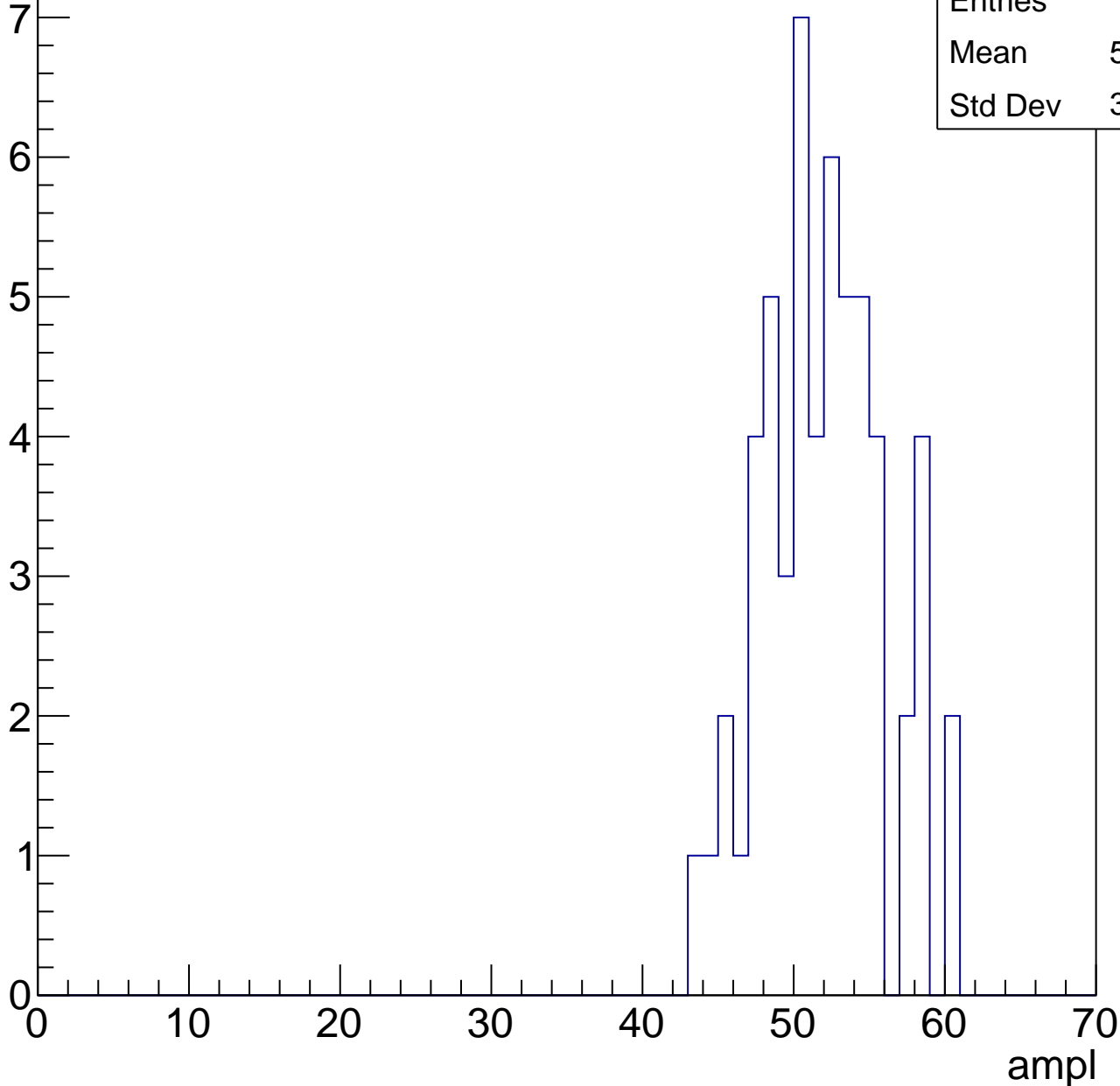


B1L103S, U6-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	51.52
Std Dev	3.982



B1L103S, U6-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

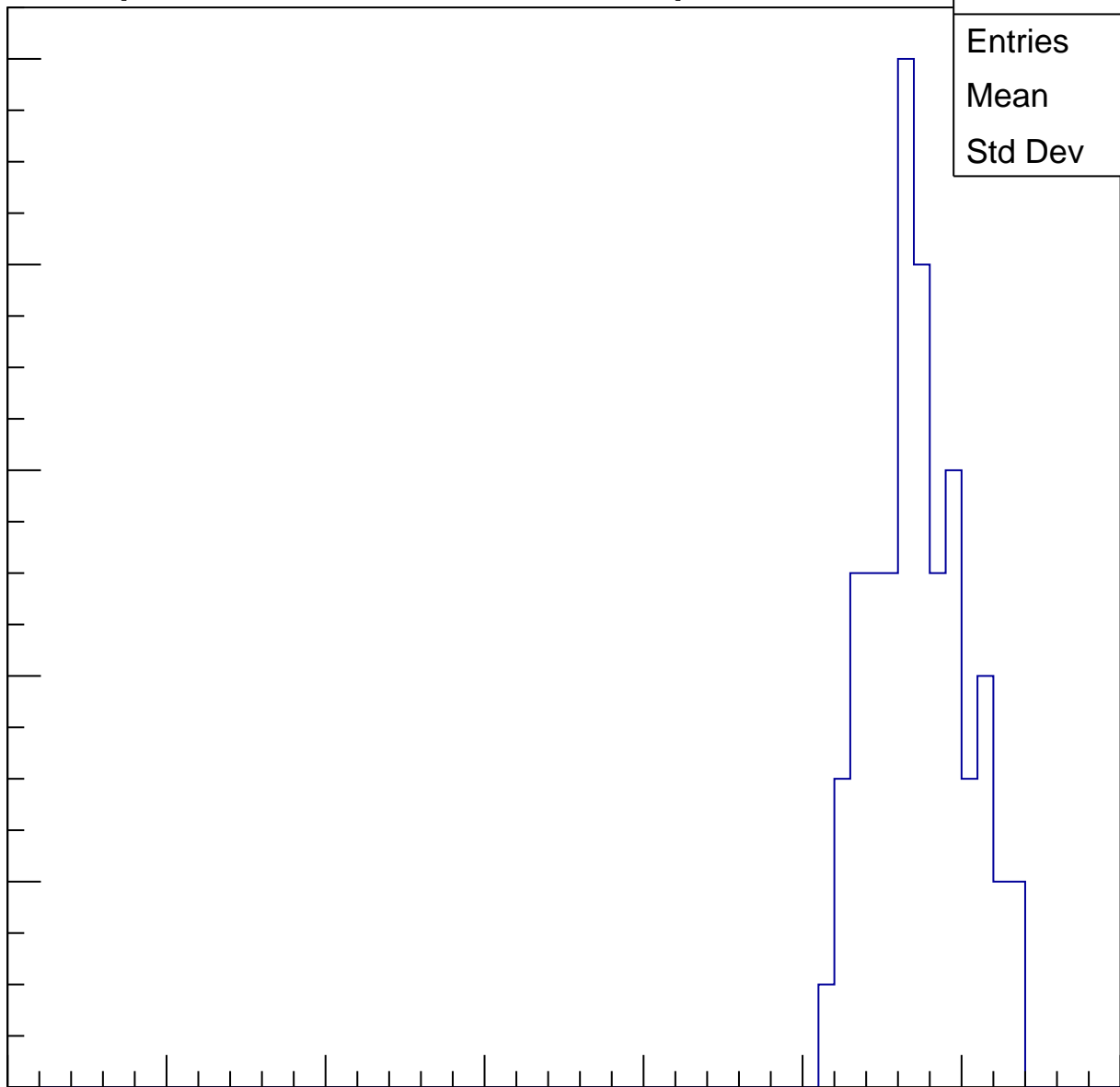
Entries	59
Mean	56.8
Std Dev	2.927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

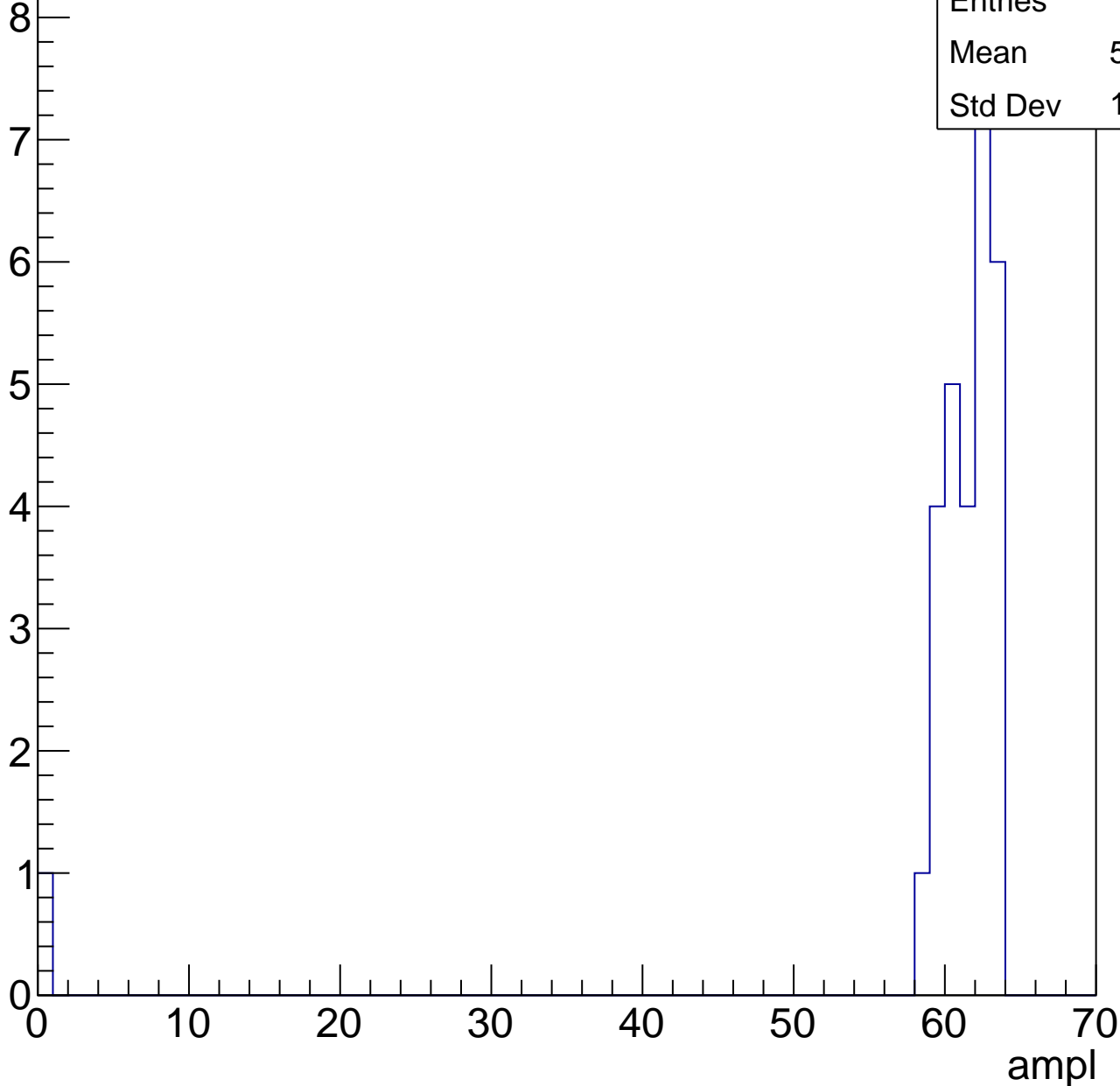


B1L103S, U6-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	59.03
Std Dev	11.25

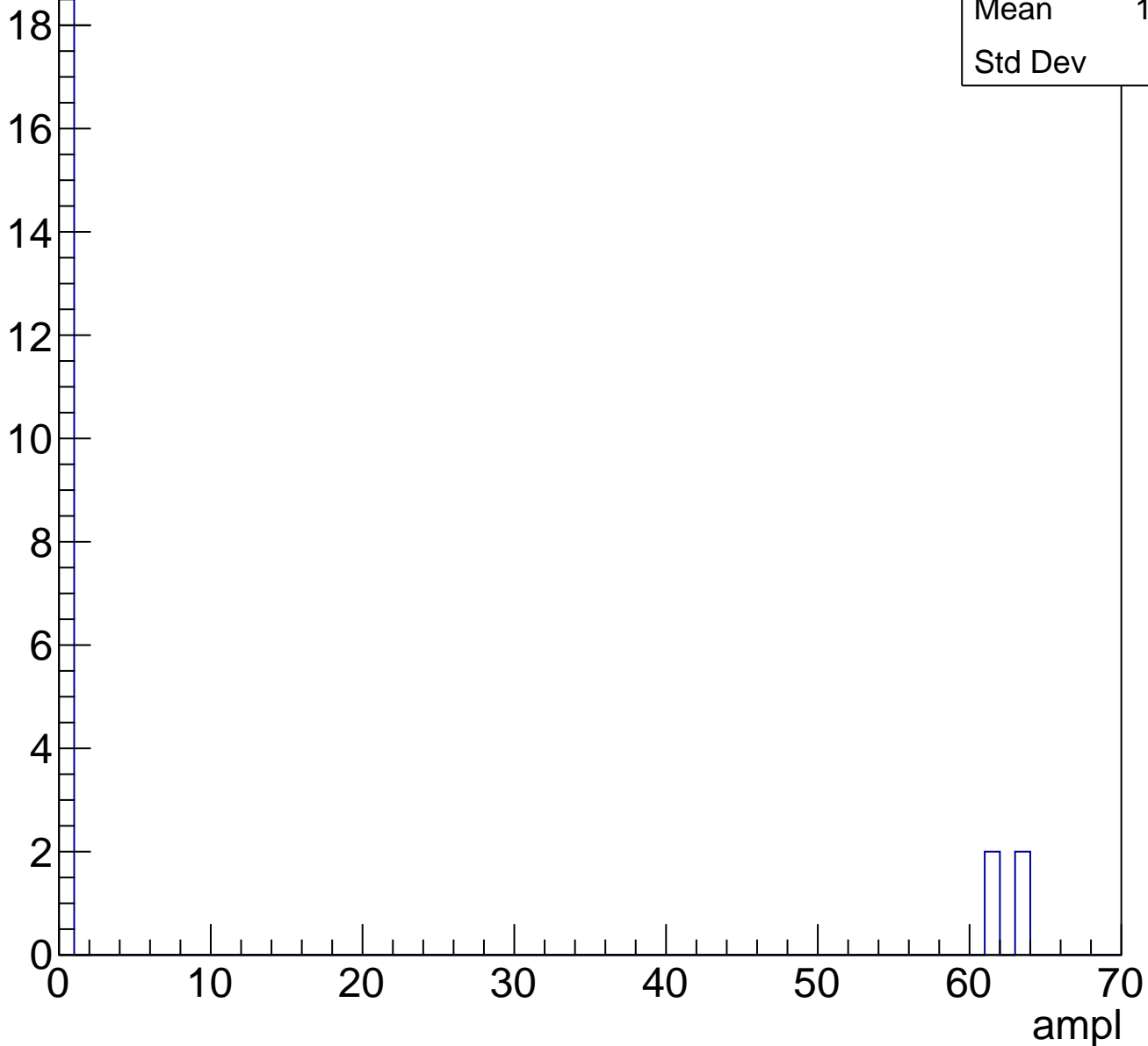


B1L103S, U6-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.78
Std Dev	23.5

Entry



B1L103S, U6-ch109, adc0

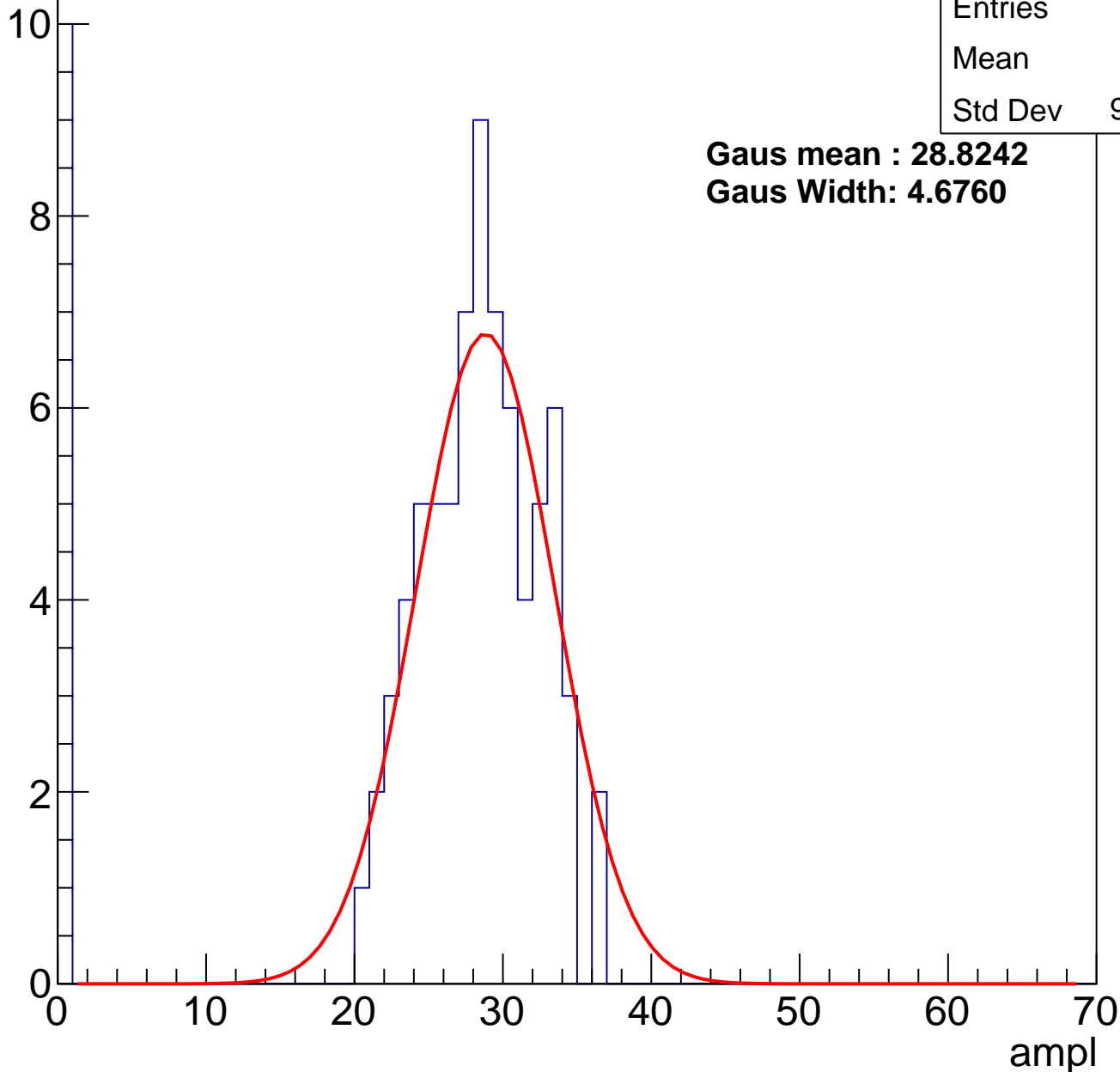
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.7
Std Dev	9.748

Gaus mean : 28.8242

Gaus Width: 4.6760

Entry



B1L103S, U6-ch109, adc1

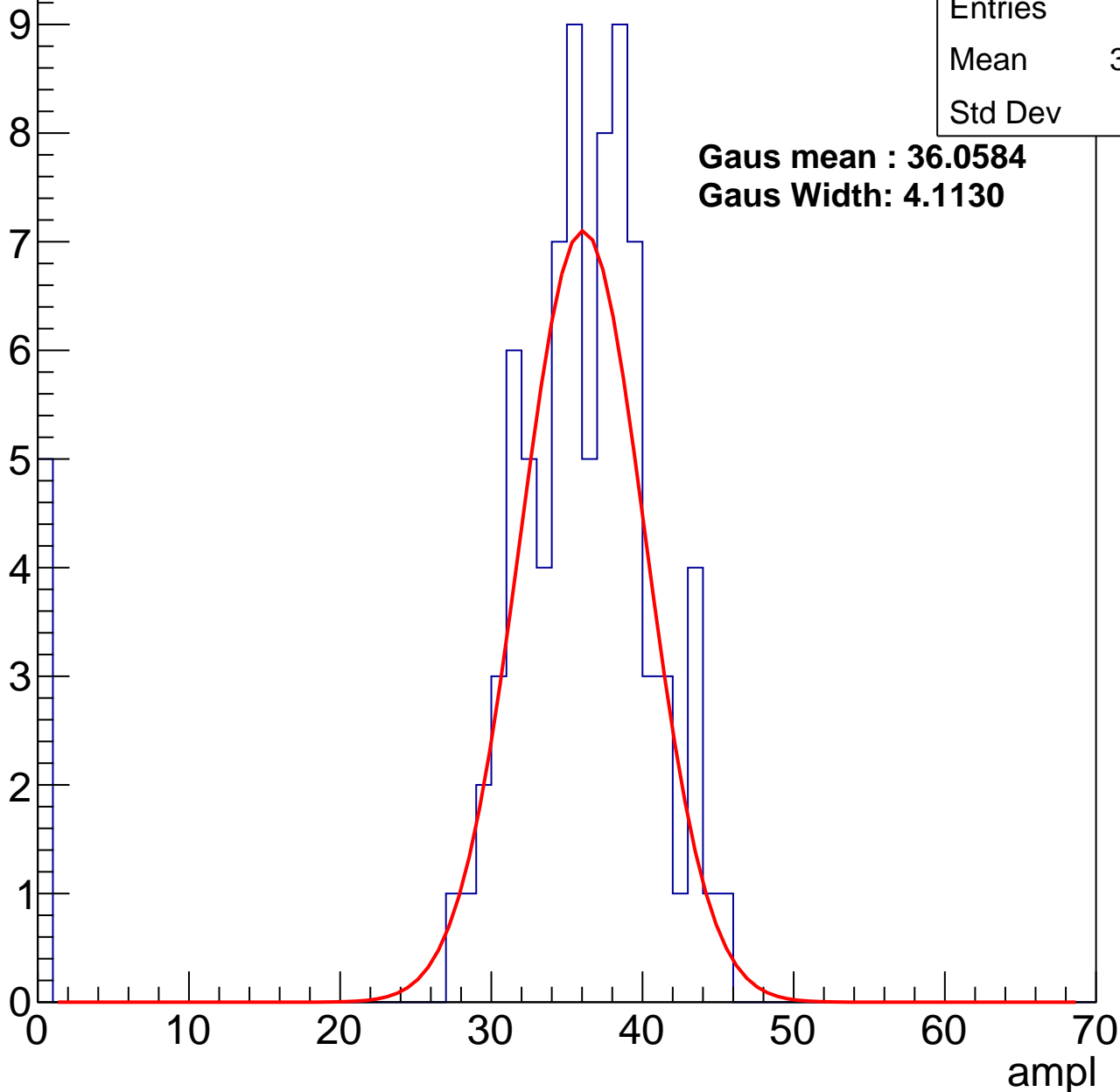
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	33.78
Std Dev	9.28

Gaus mean : 36.0584

Gaus Width: 4.1130



B1L103S, U6-ch109, adc2

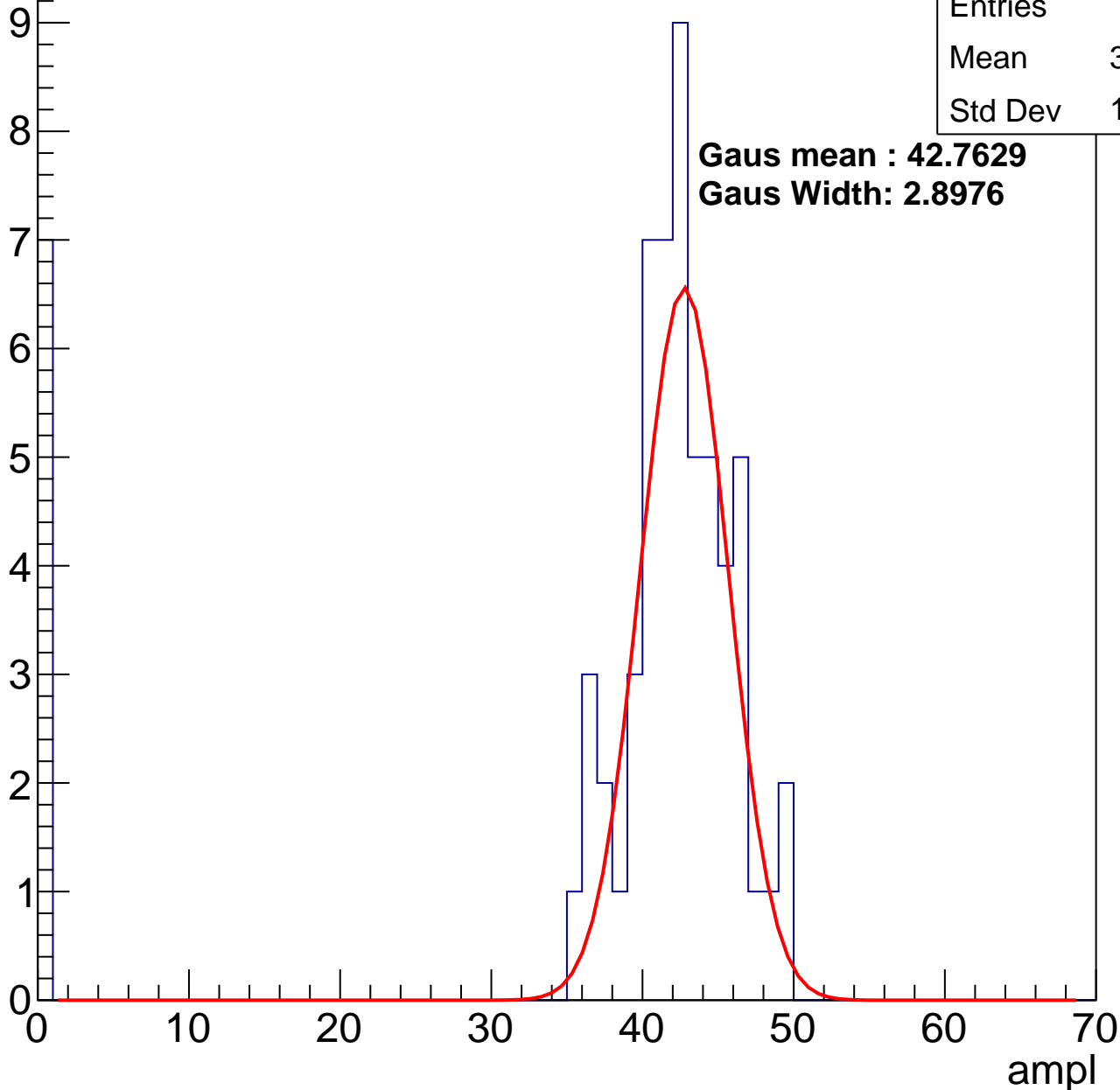
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	37.38
Std Dev	13.56

Gaus mean : 42.7629

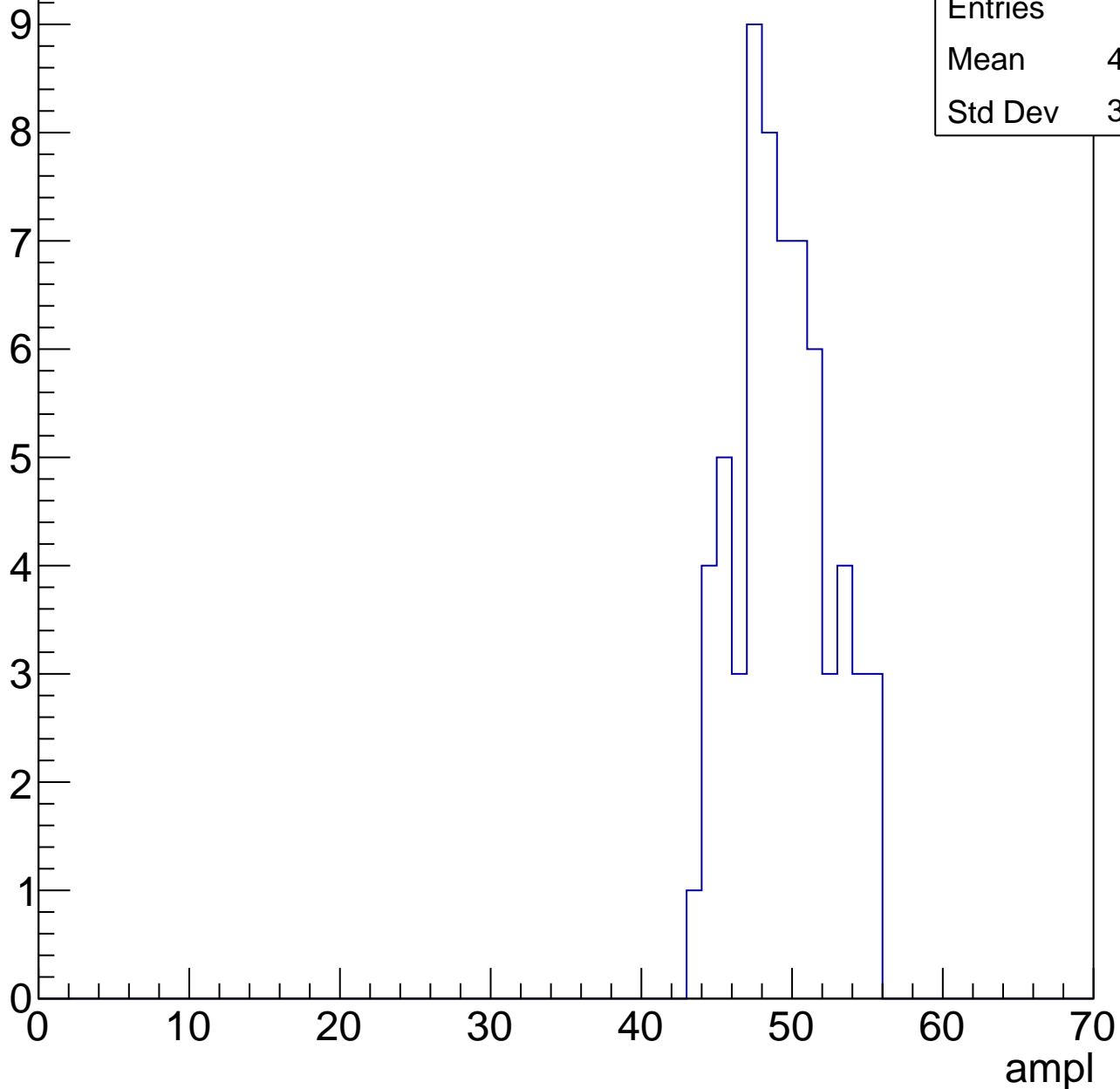
Gaus Width: 2.8976



B1L103S, U6-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



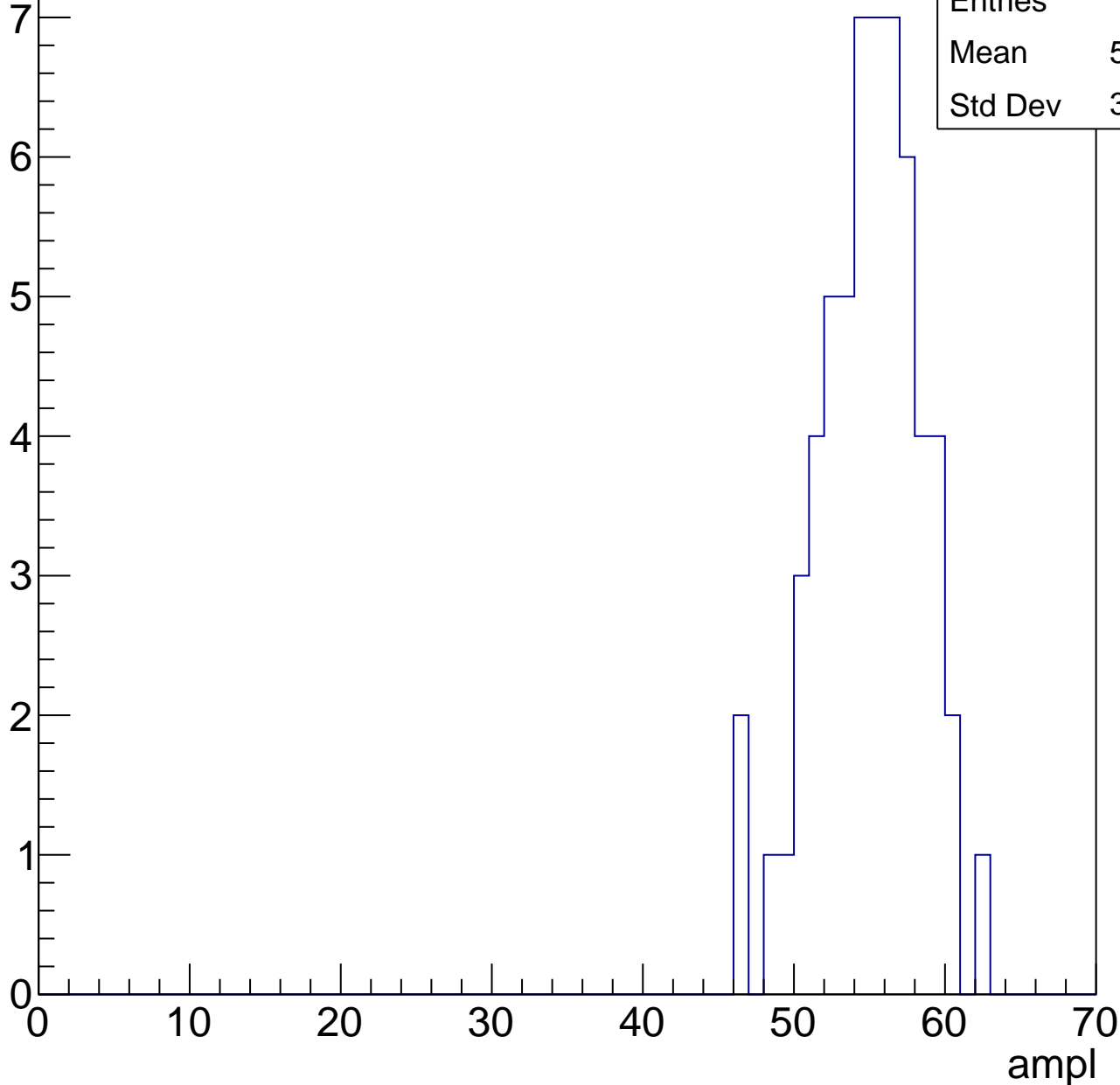
Entries	63
Mean	48.94
Std Dev	3.065

B1L103S, U6-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.49
Std Dev	3.377

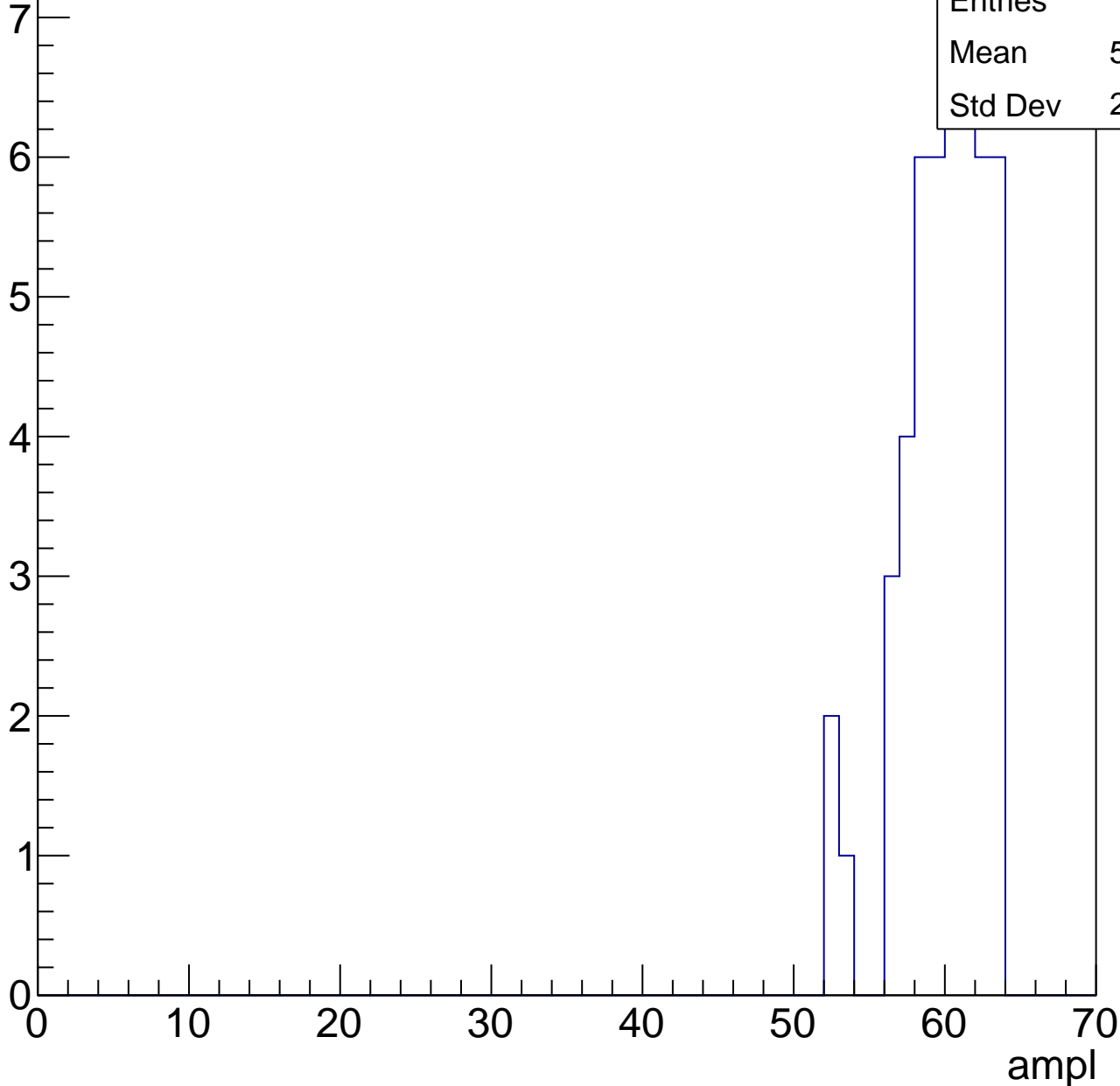


B1L103S, U6-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

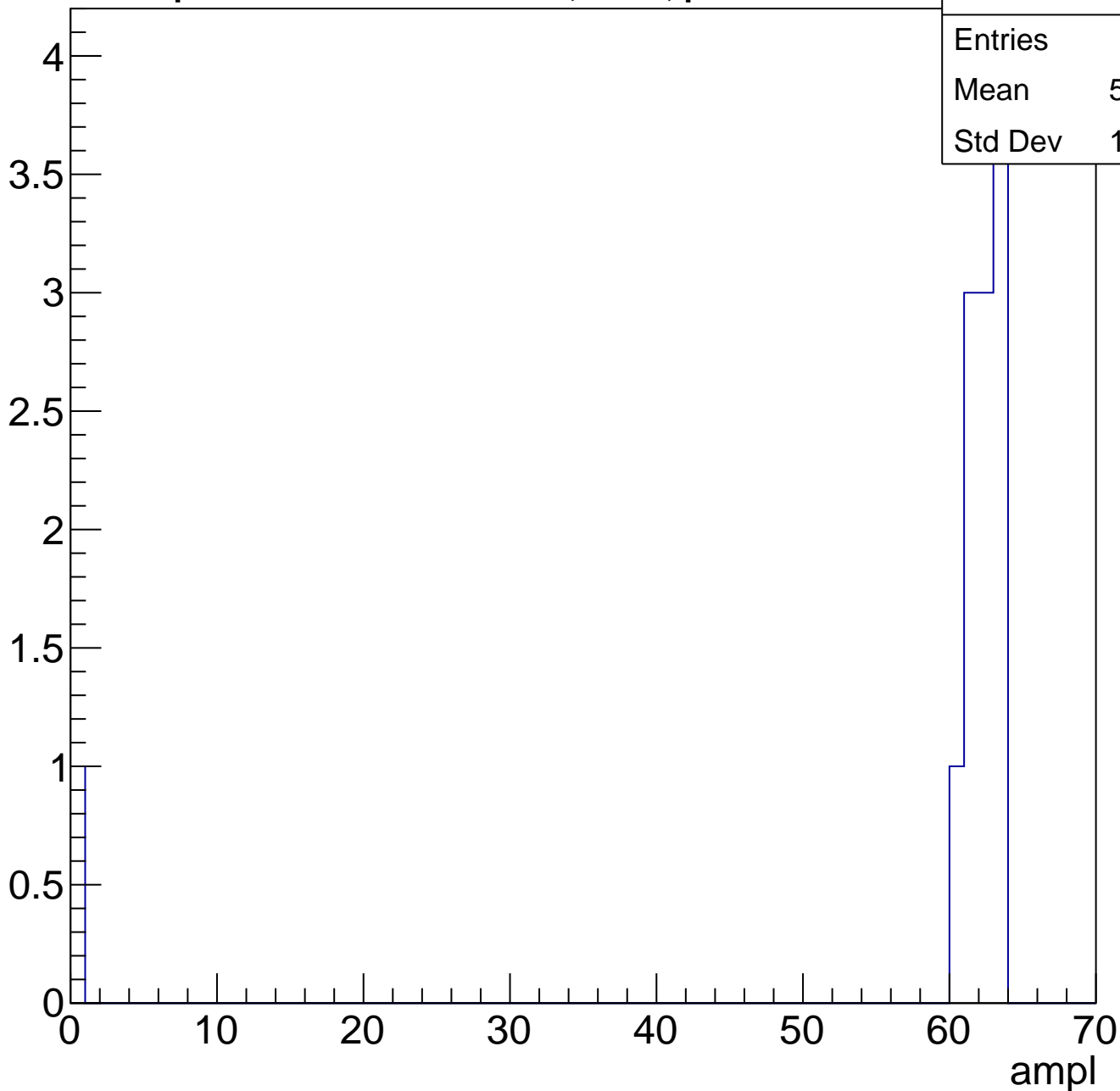
Entries	48
Mean	59.42
Std Dev	2.737



B1L103S, U6-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch110, adc0

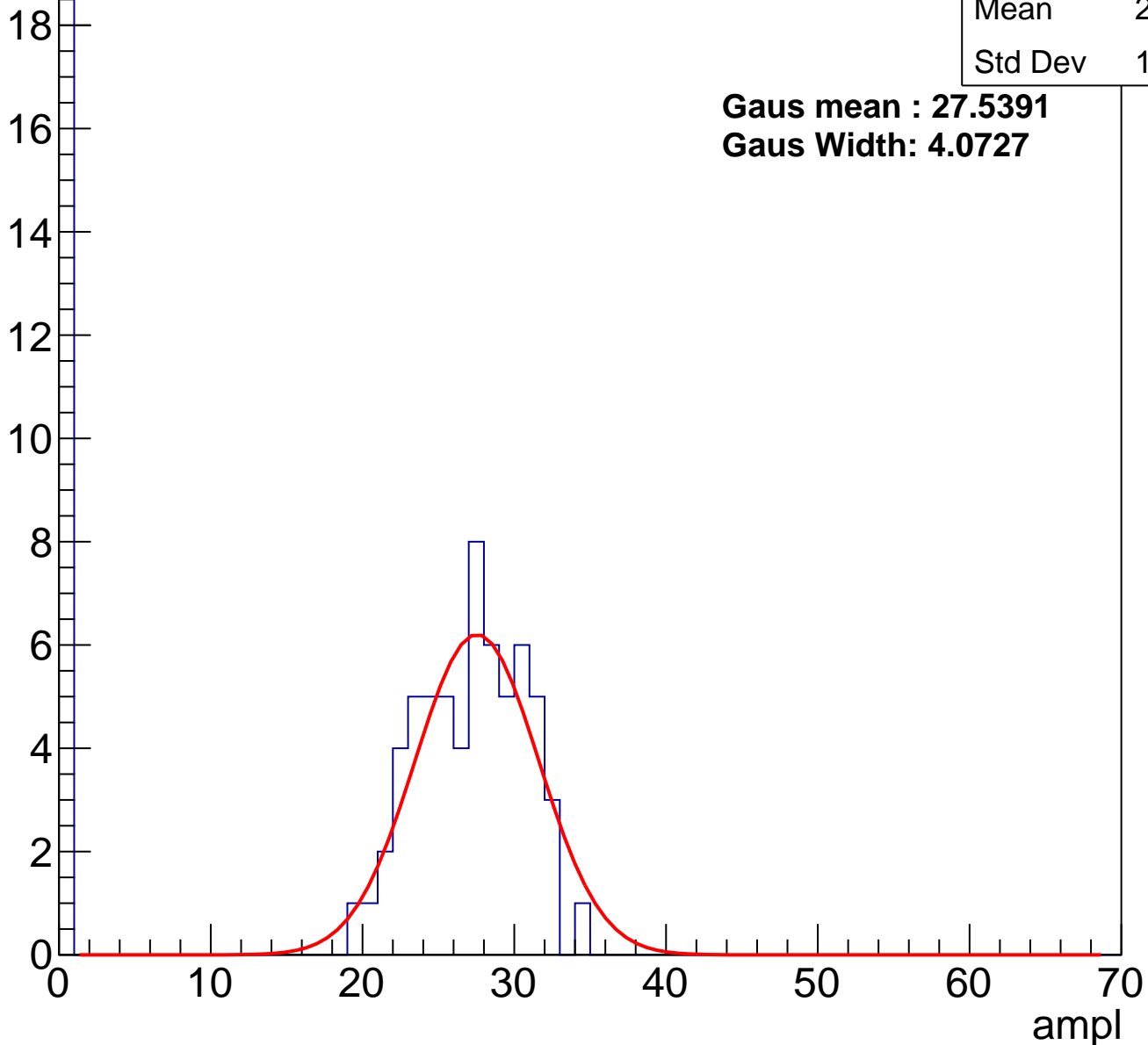
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	20.34
Std Dev	11.74

Gaus mean : 27.5391

Gaus Width: 4.0727

Entry



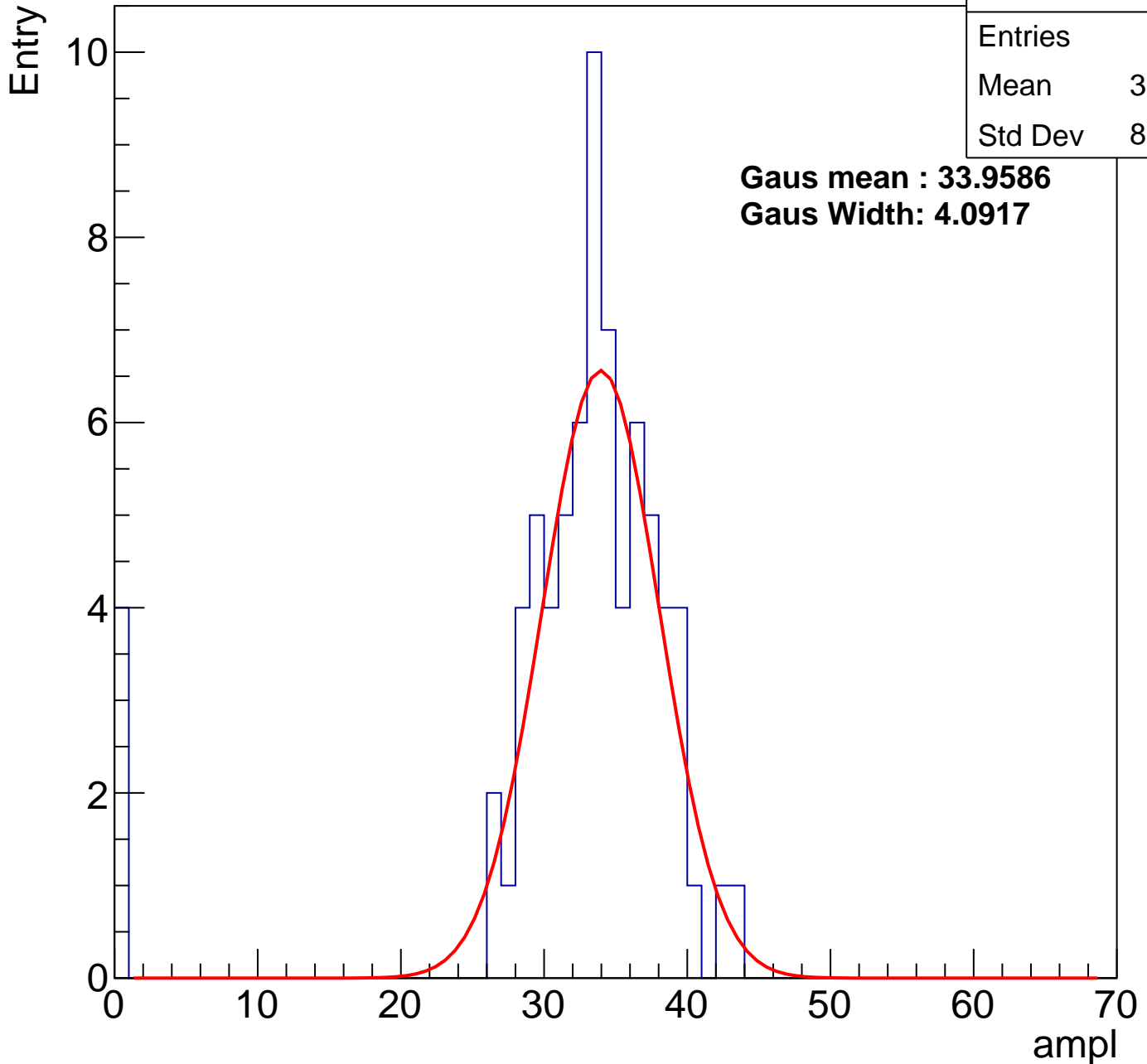
B1L103S, U6-ch110, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.69
Std Dev	8.412

Gaus mean : 33.9586

Gaus Width: 4.0917



B1L103S, U6-ch110, adc2

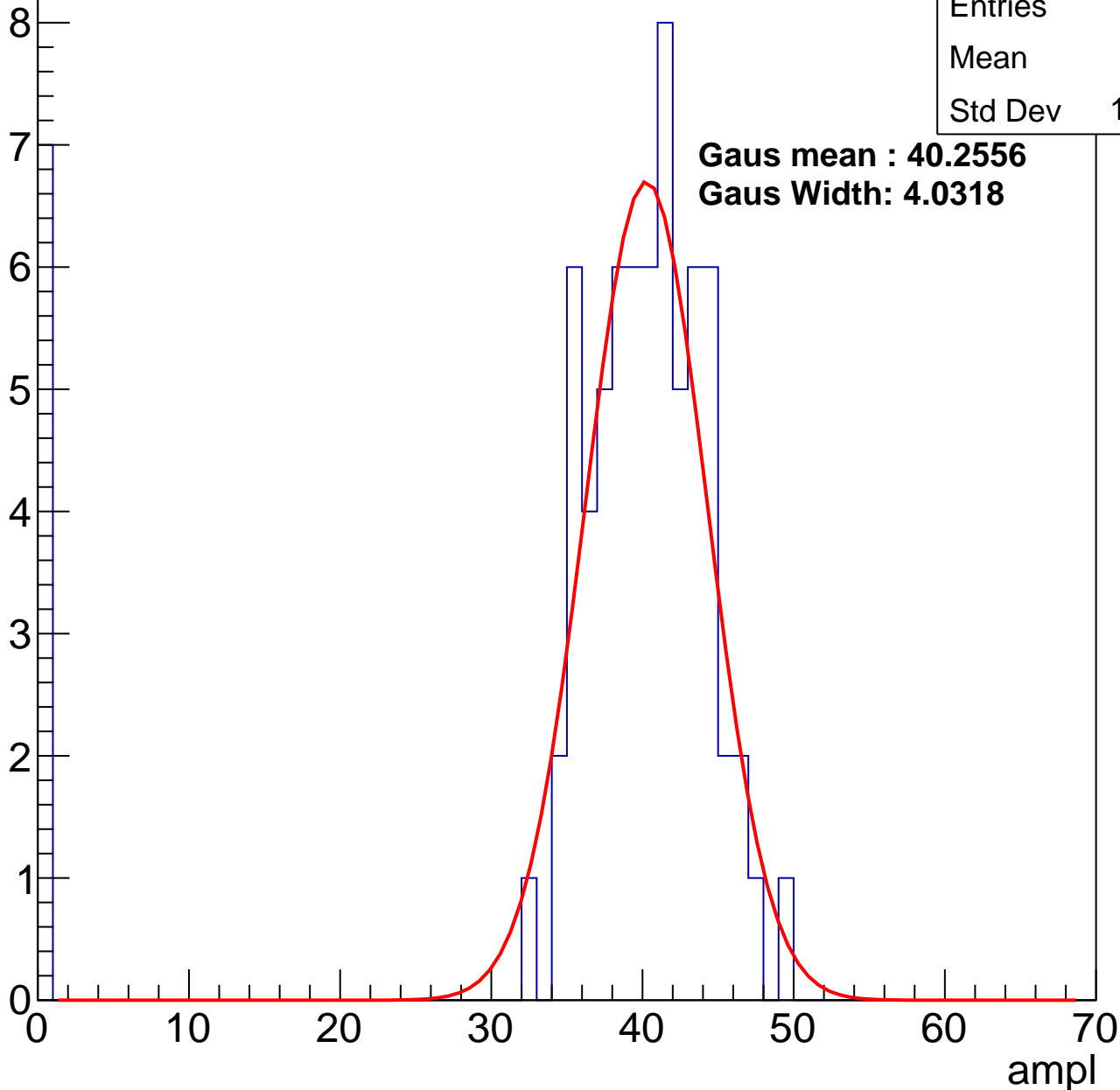
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.2
Std Dev	12.19

Gaus mean : 40.2556

Gaus Width: 4.0318

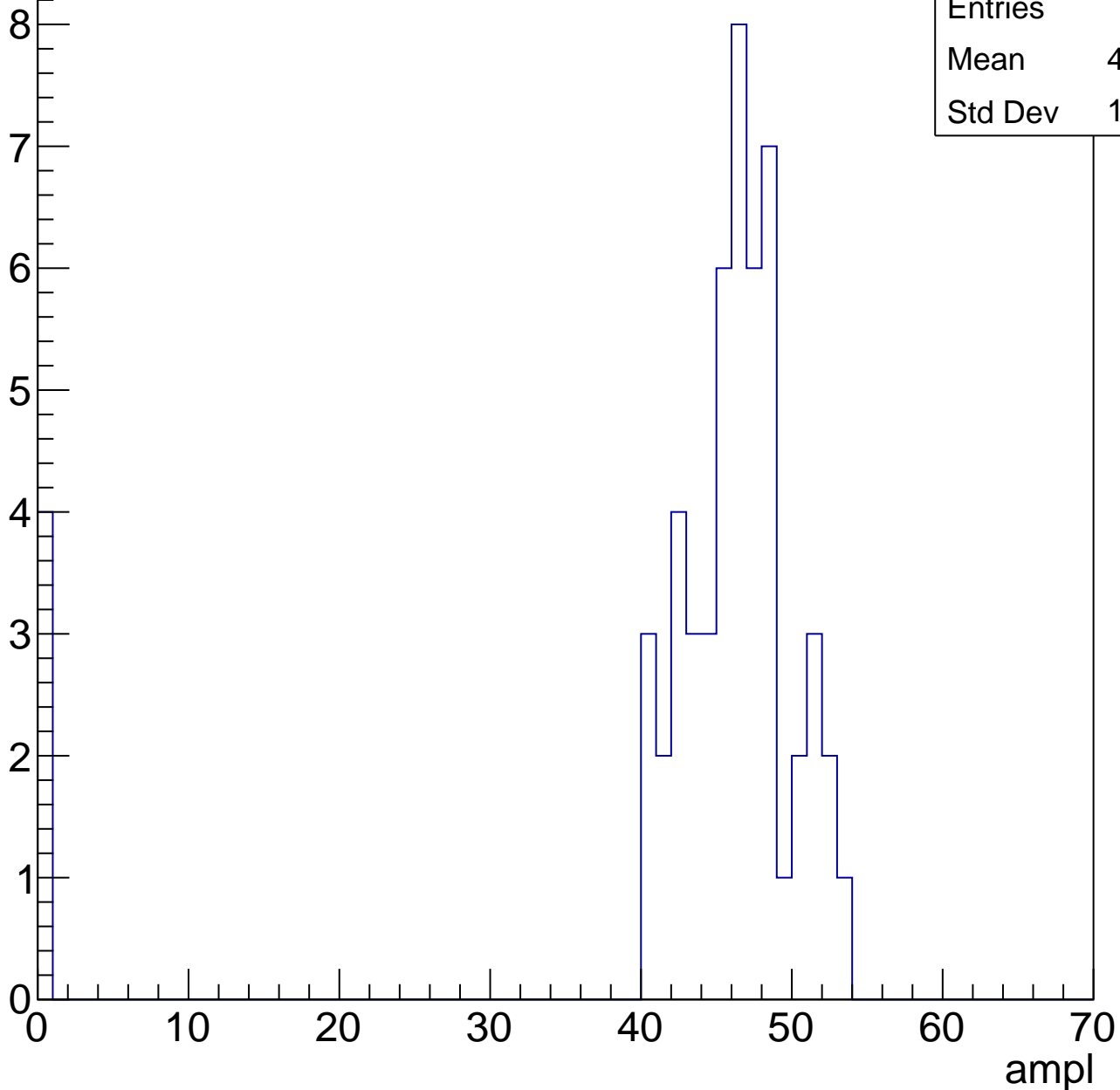


B1L103S, U6-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	42.65
Std Dev	12.35

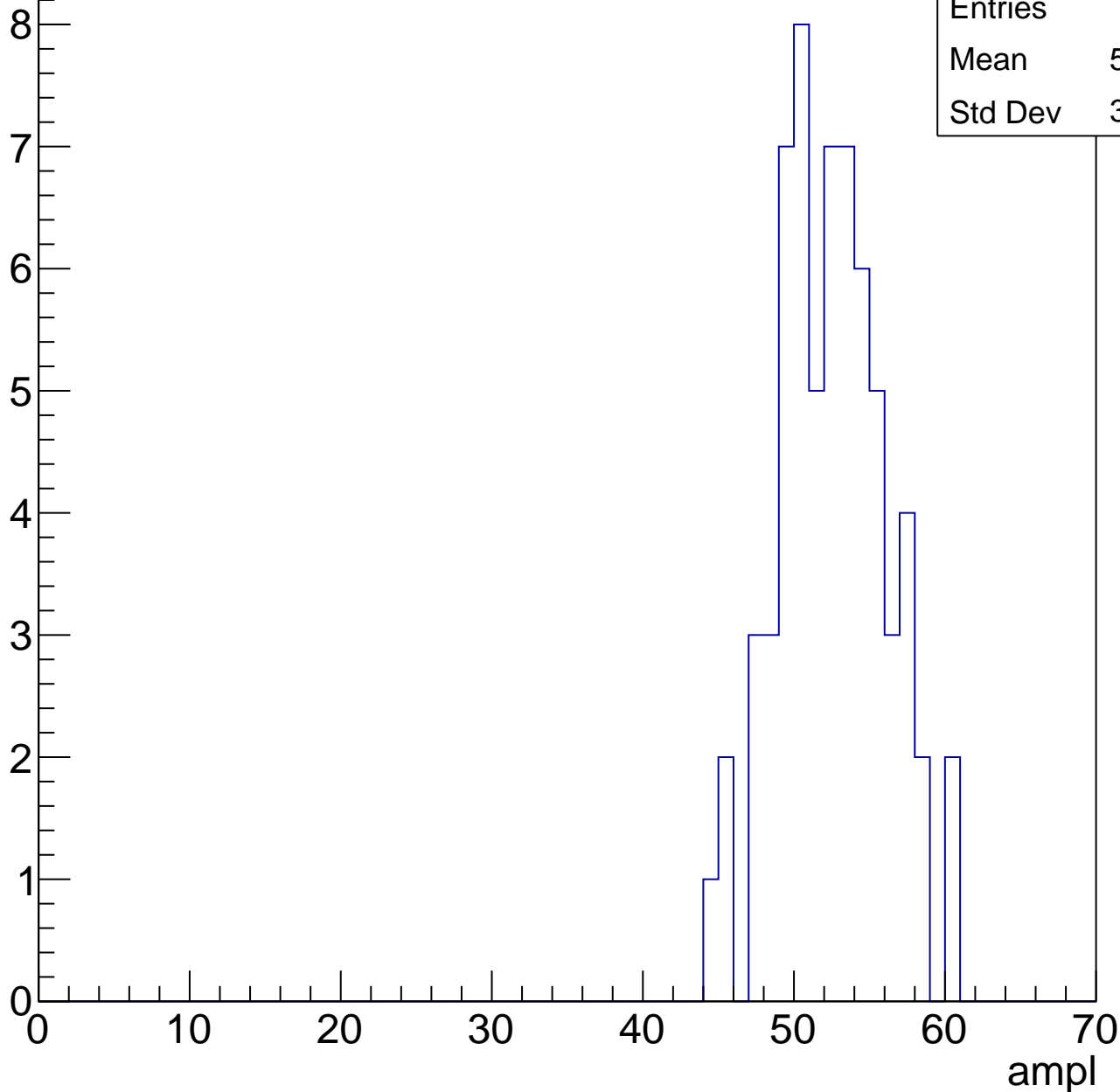


B1L103S, U6-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	52.05
Std Dev	3.528

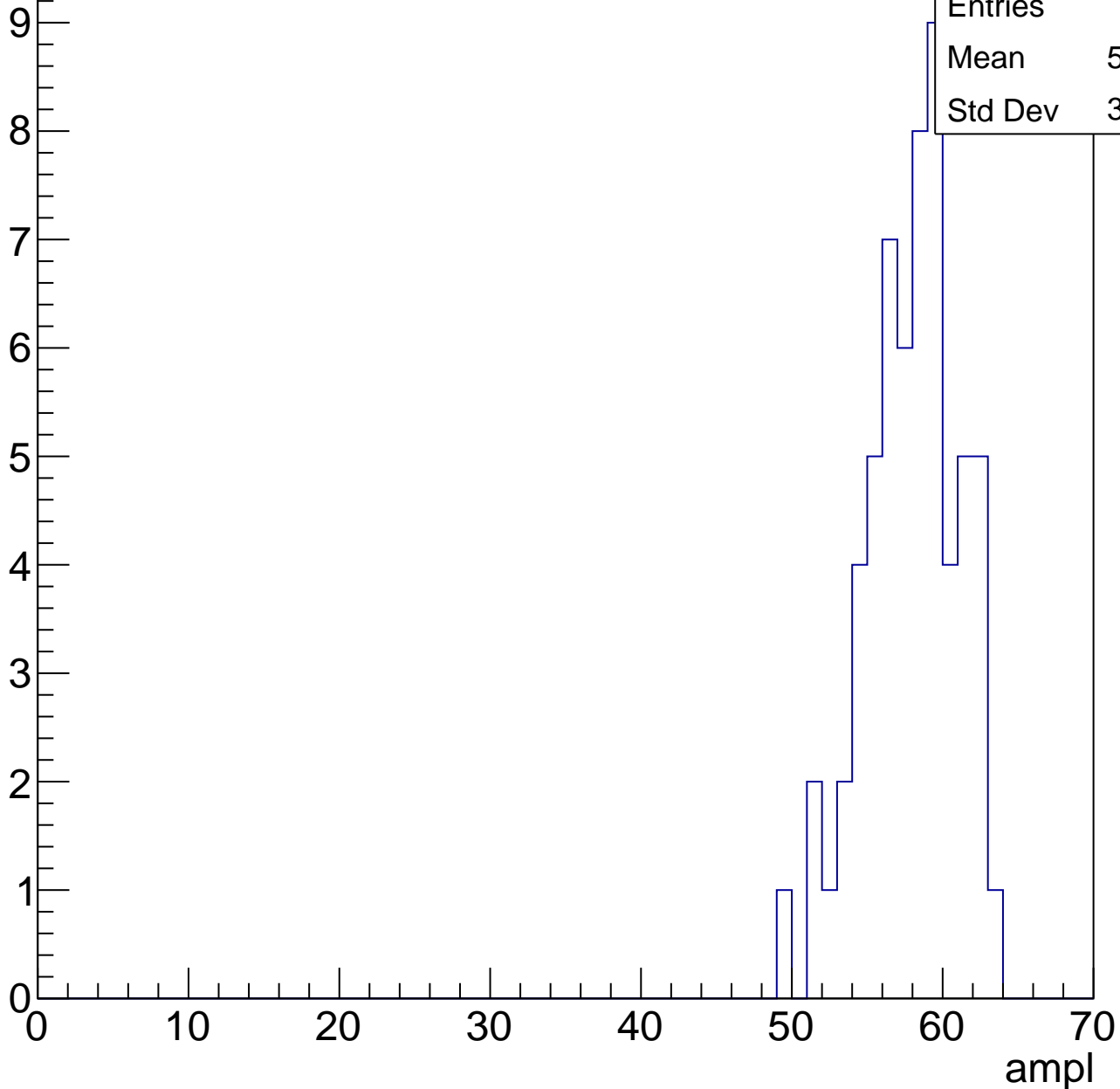


B1L103S, U6-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.45
Std Dev	3.068

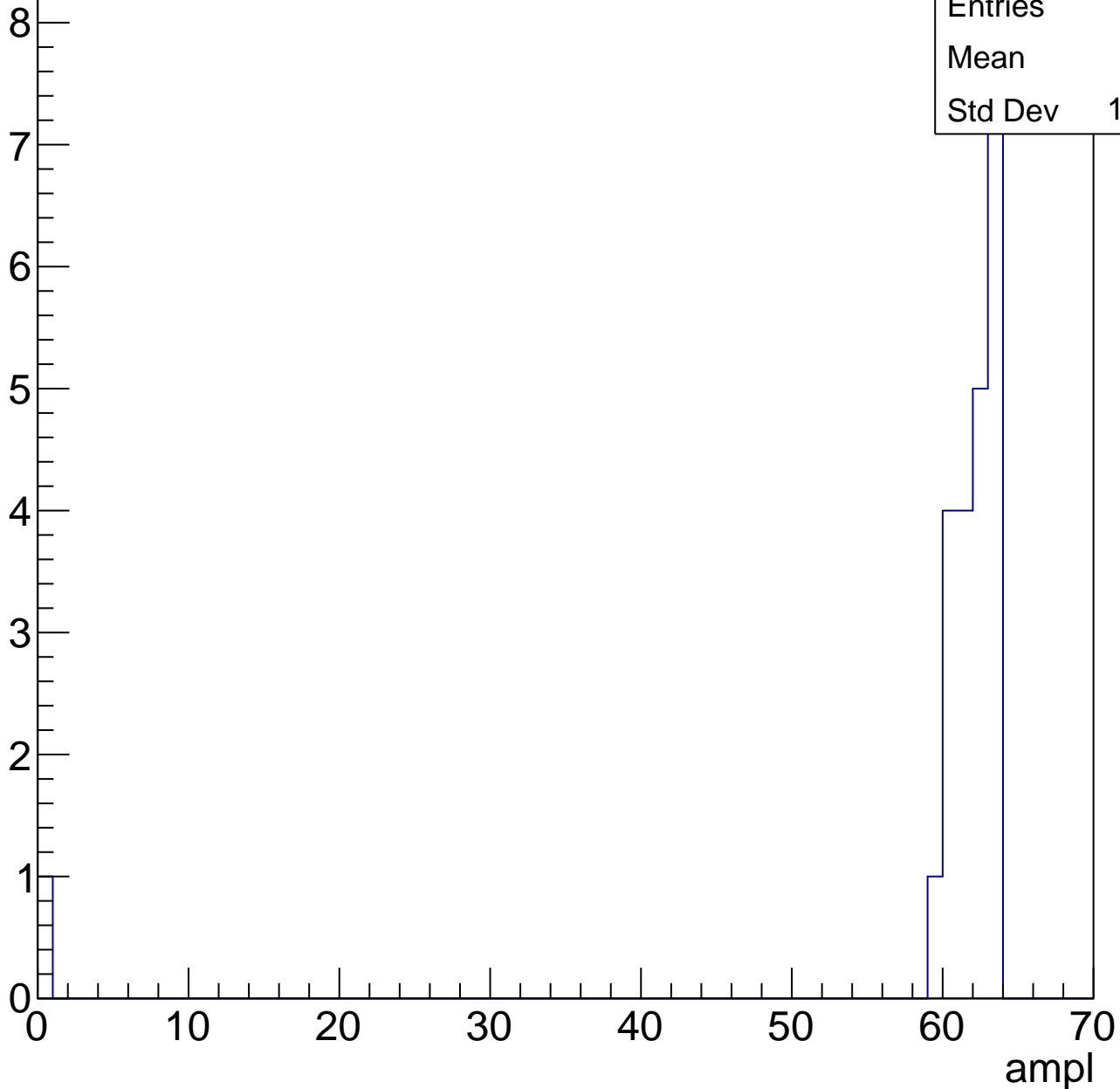


B1L103S, U6-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	59
Std Dev	12.64



B1L103S, U6-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U6-ch111, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.93
Std Dev	7.865

Gaus mean : 28.1859

Gaus Width: 3.9198

Entry

10
8
6
4
2
0

ampl

0

10

20

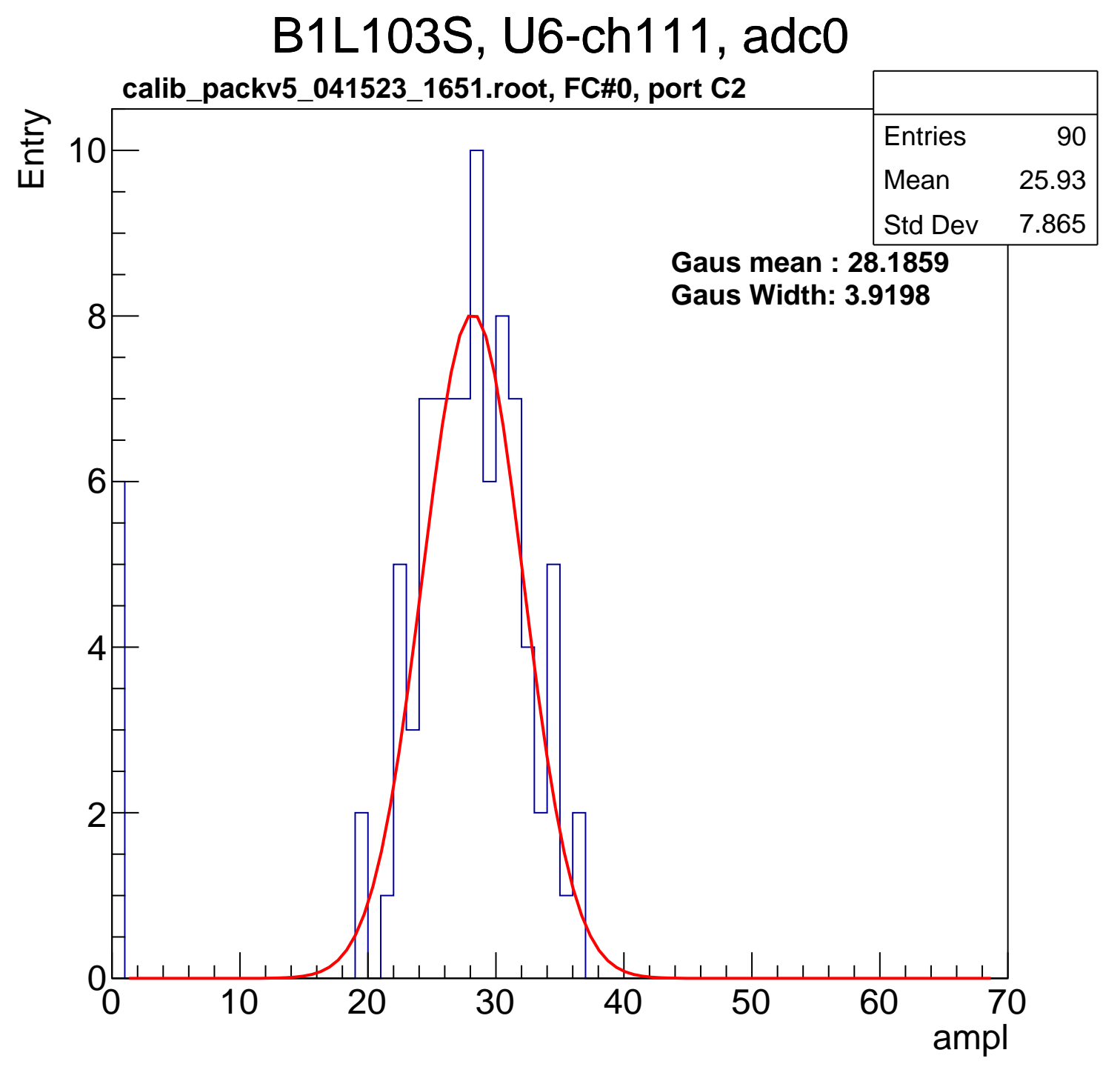
30

40

50

60

70



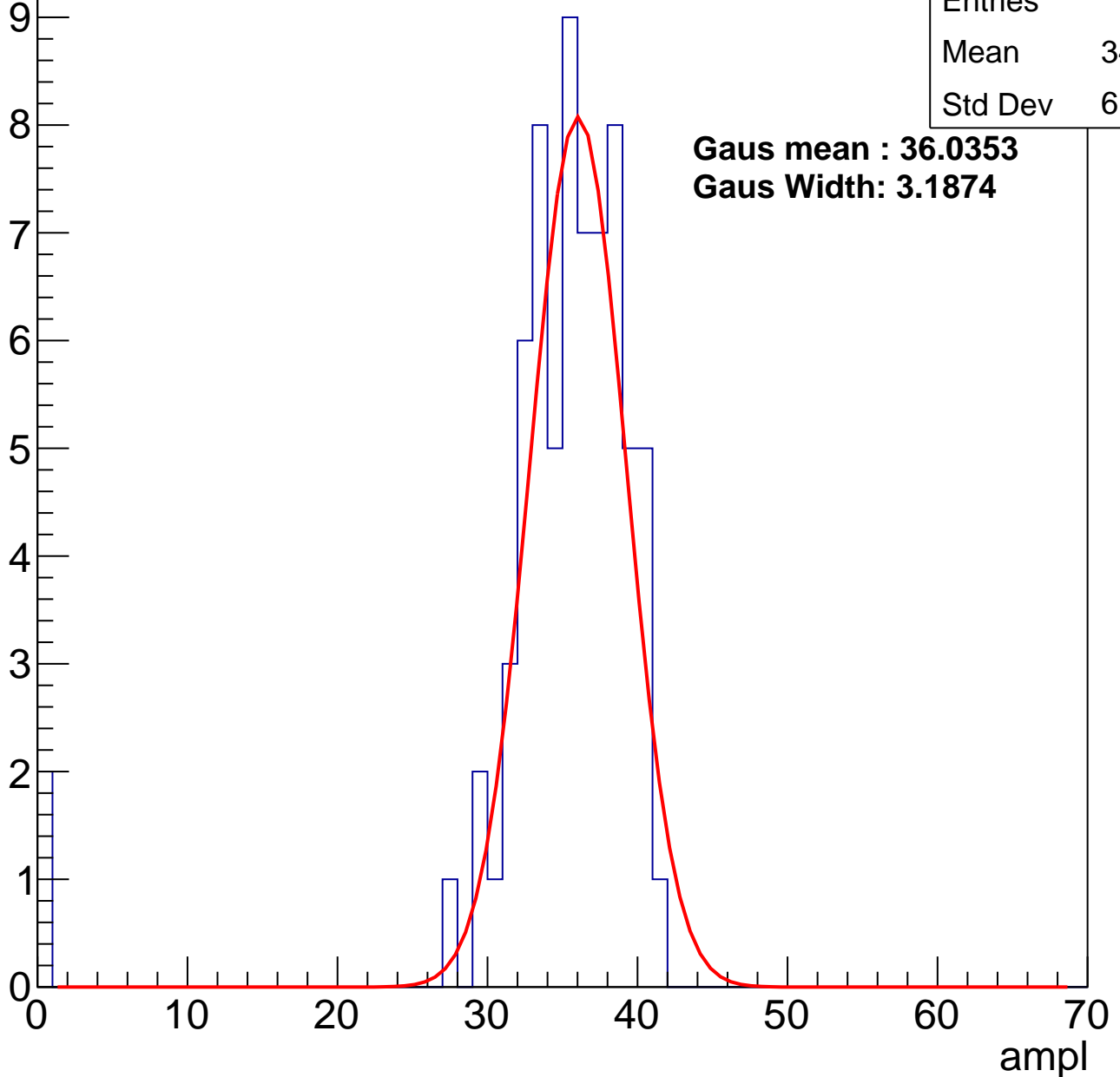
B1L103S, U6-ch111, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.29
Std Dev	6.614

Gaus mean : 36.0353
Gaus Width: 3.1874



B1L103S, U6-ch111, adc2

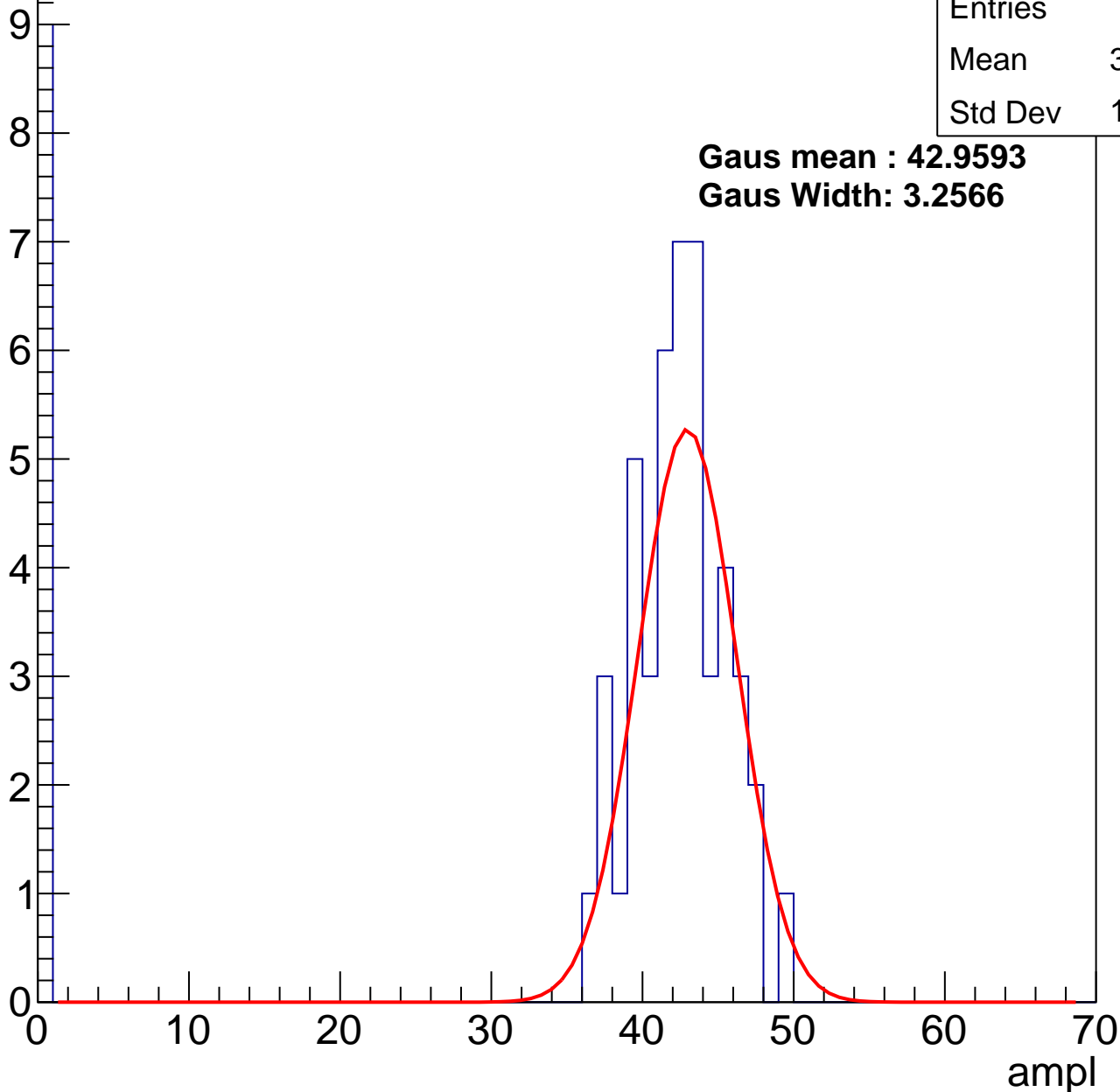
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.16
Std Dev	15.78

Gaus mean : 42.9593

Gaus Width: 3.2566

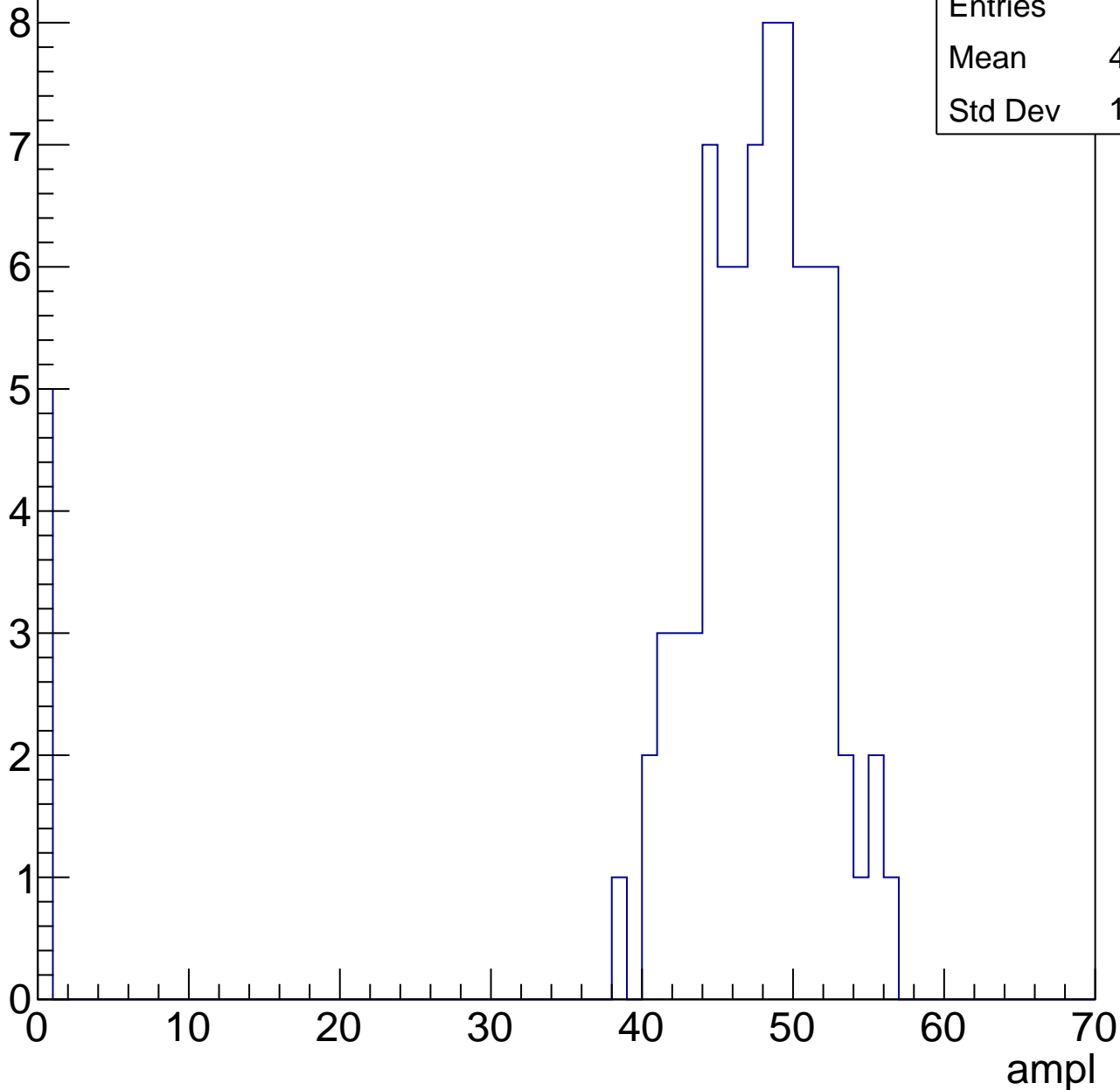


B1L103S, U6-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	44.57
Std Dev	11.89

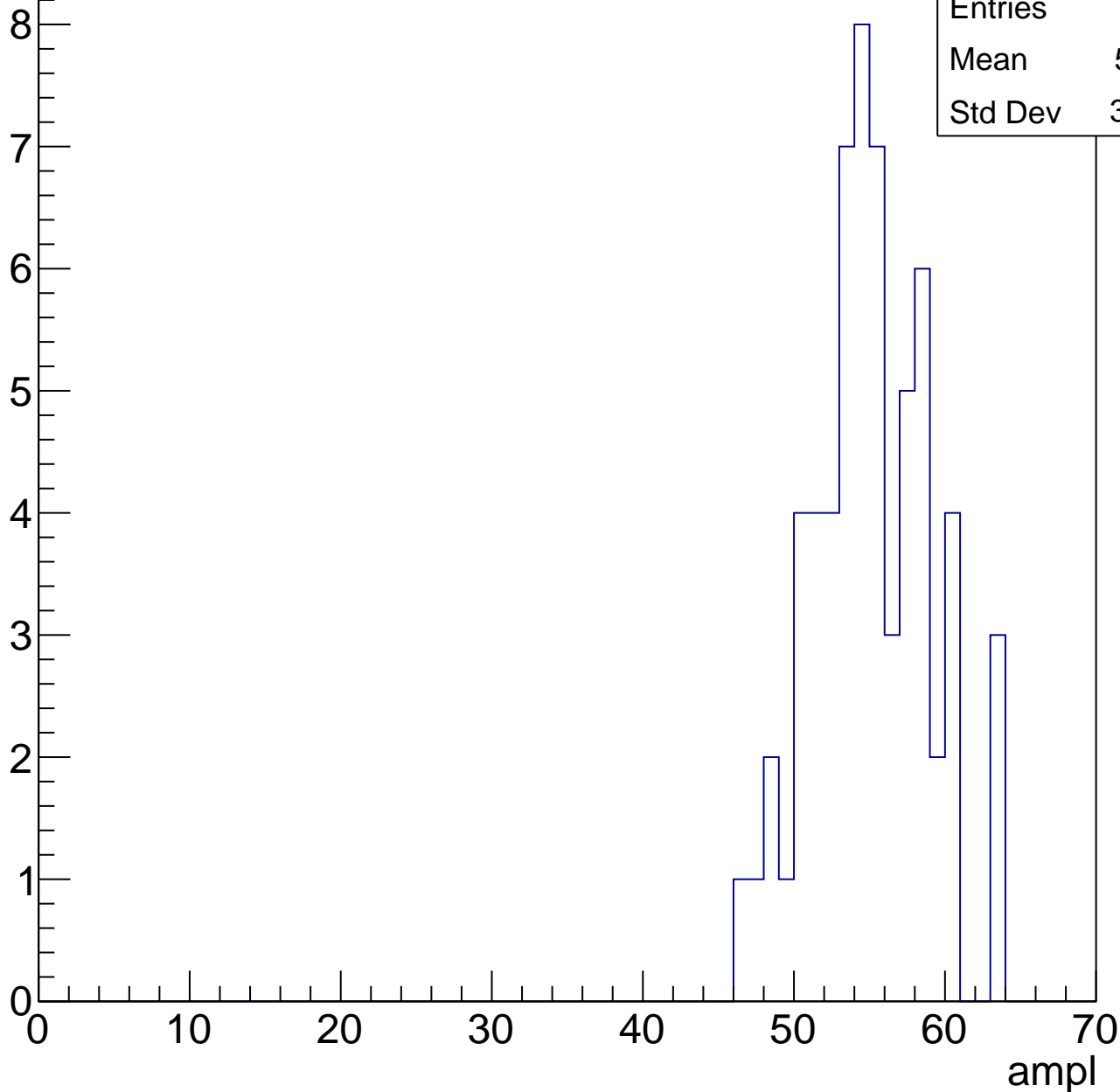


B1L103S, U6-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

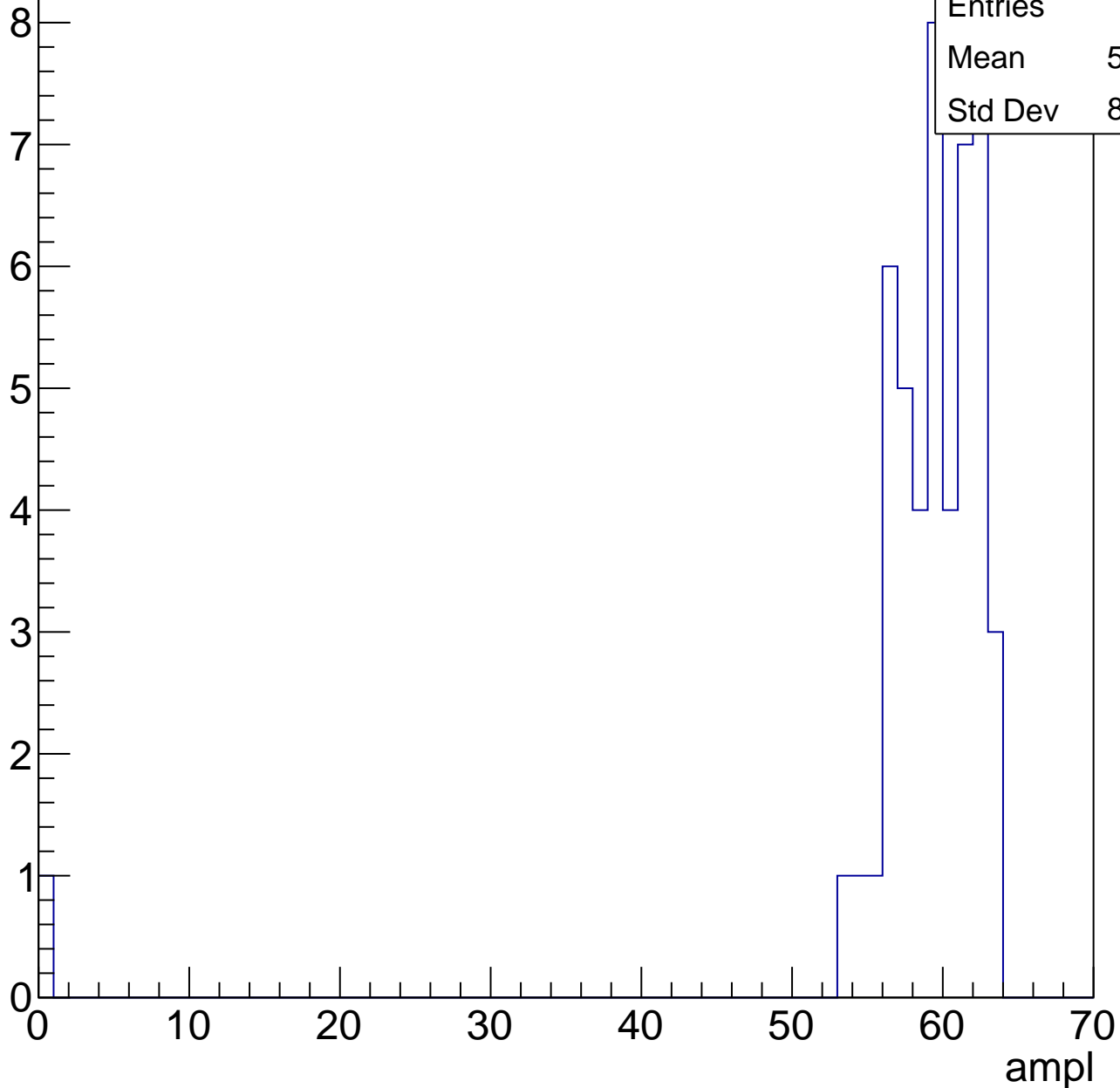
Entries	62
Mean	54.61
Std Dev	3.824



B1L103S, U6-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

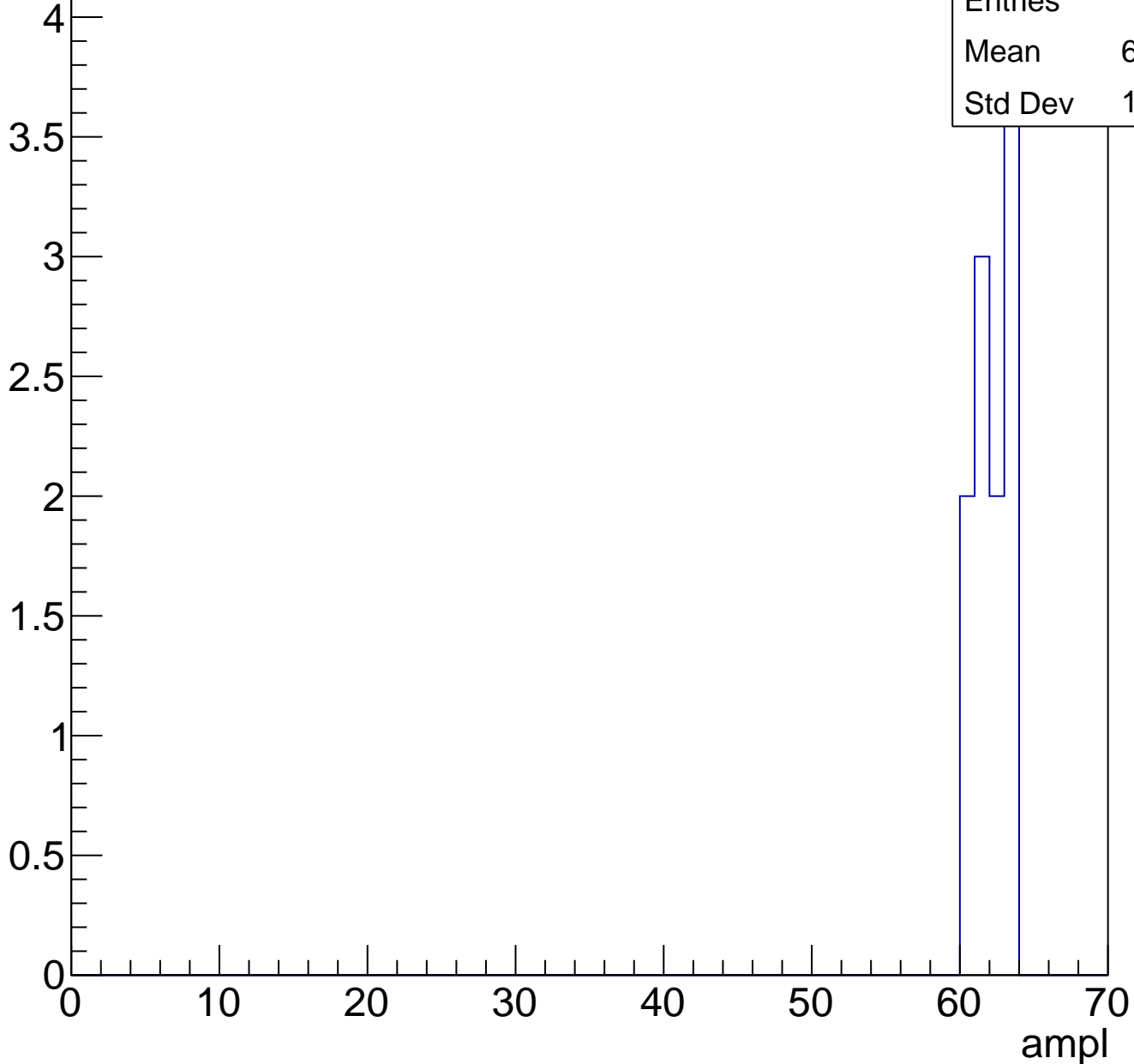
Entry



B1L103S, U6-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

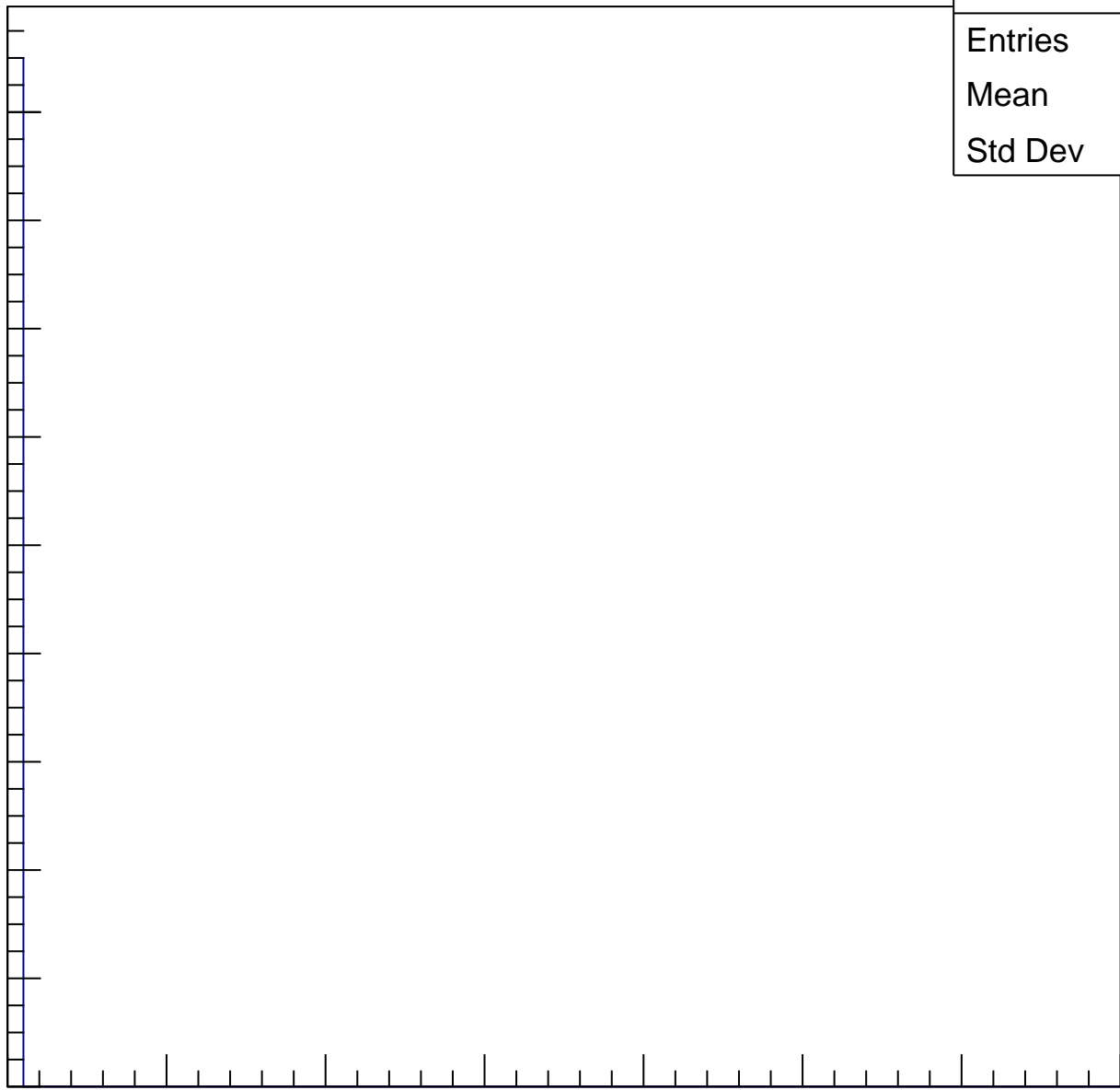
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

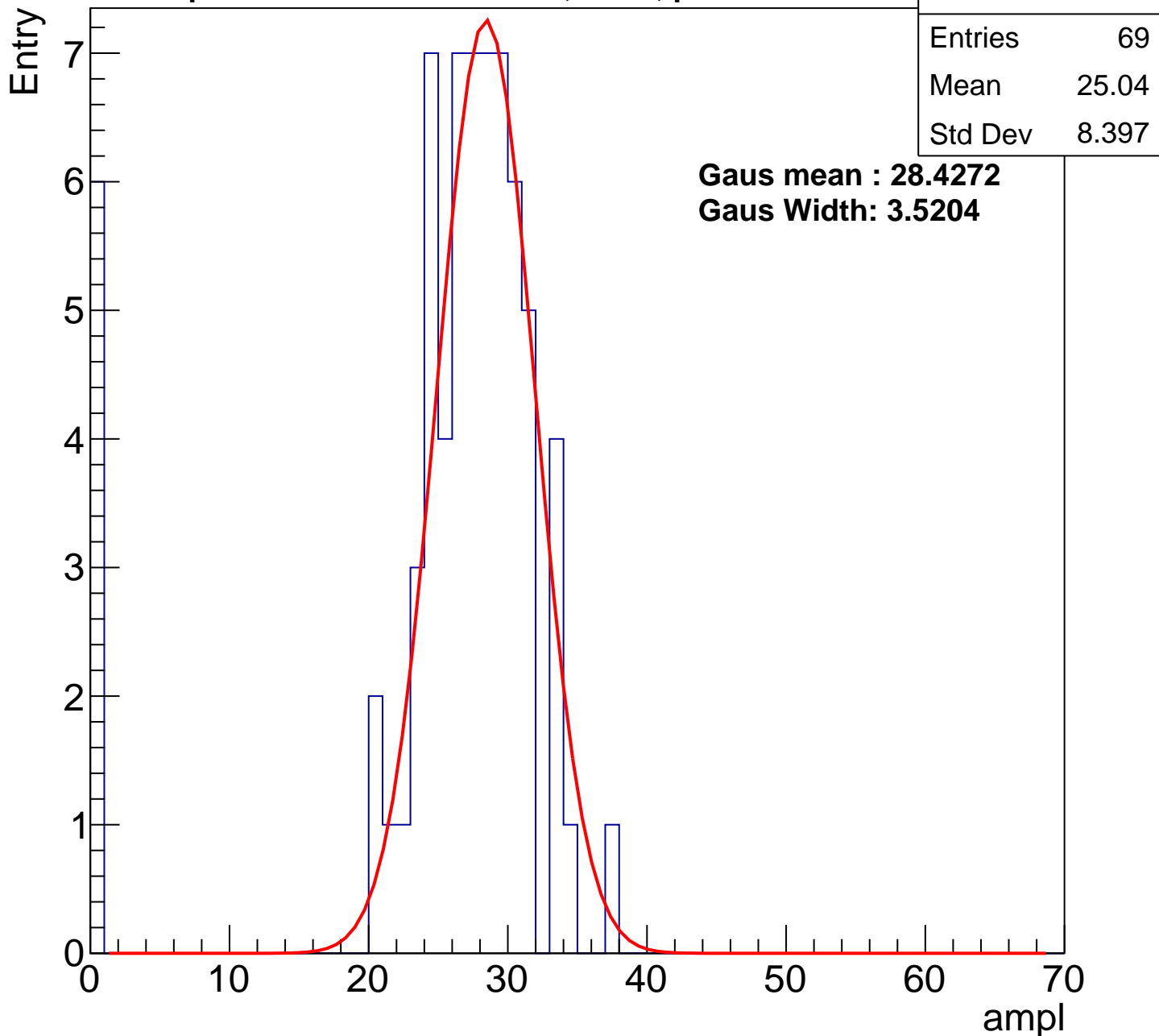
0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch112, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U6-ch112, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	31.96
Std Dev	8.449

Gaus mean : 34.2624

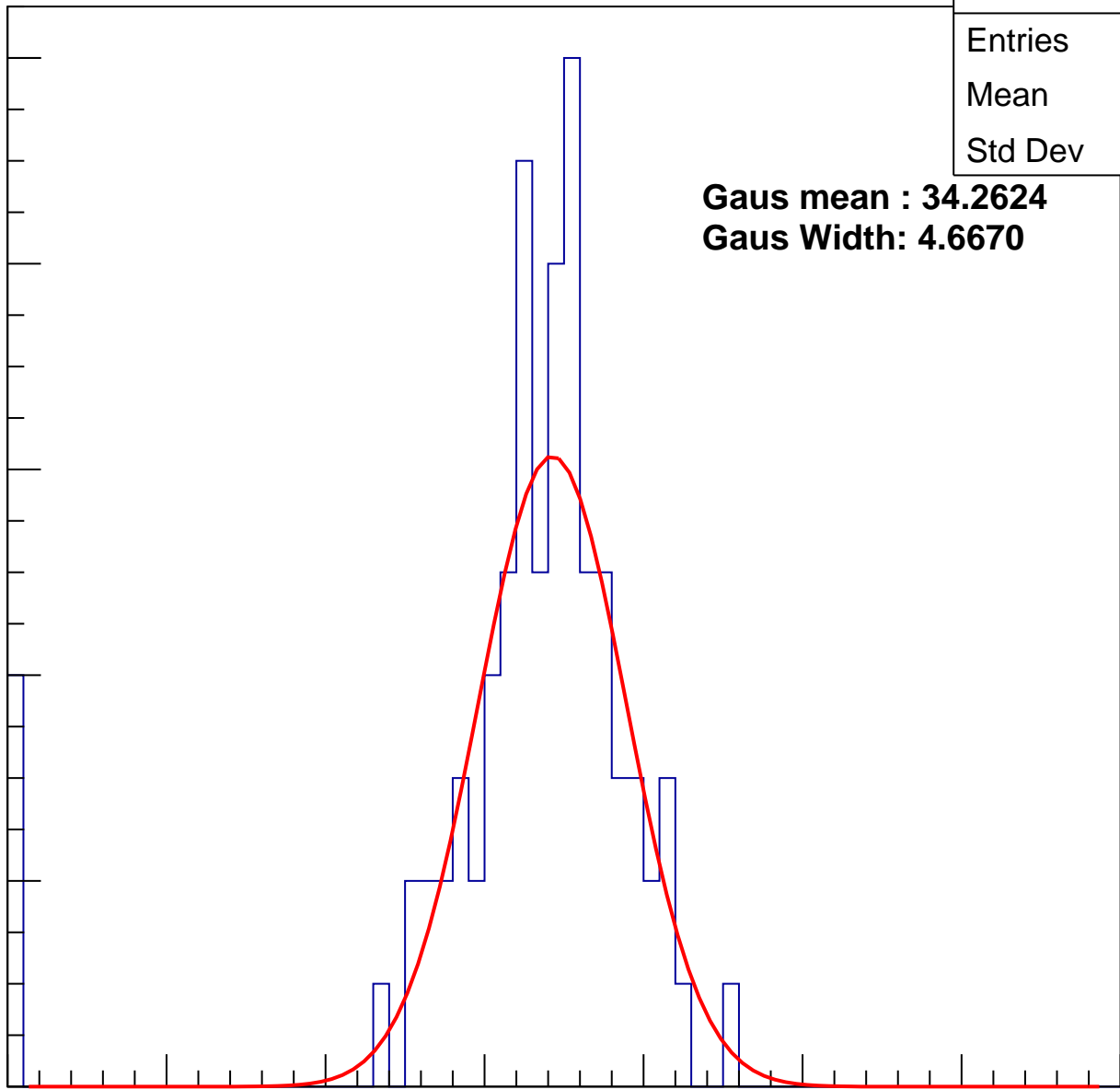
Gaus Width: 4.6670

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch112, adc2

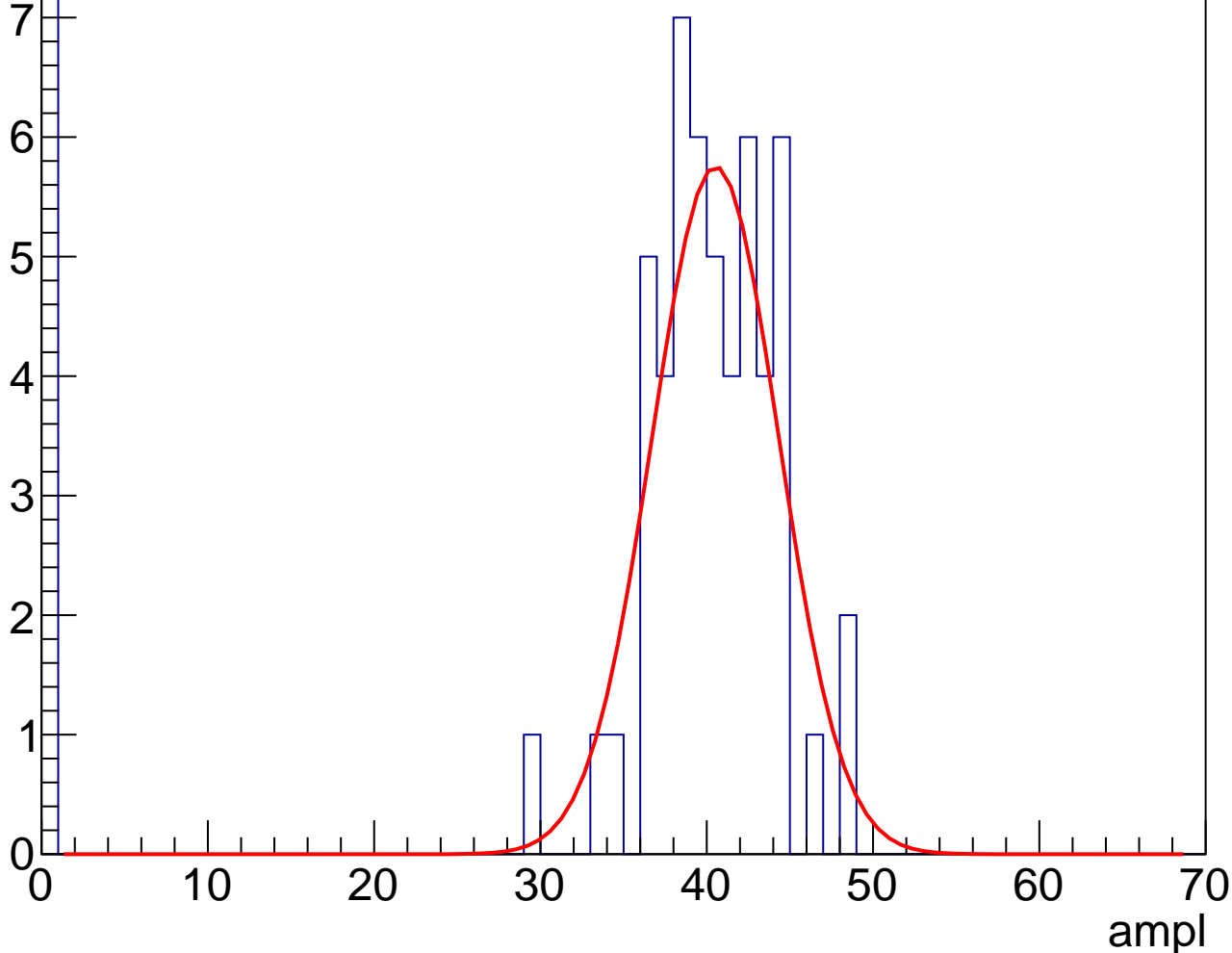
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.16
Std Dev	14.46

Gaus mean : 40.5234

Gaus Width: 3.8092

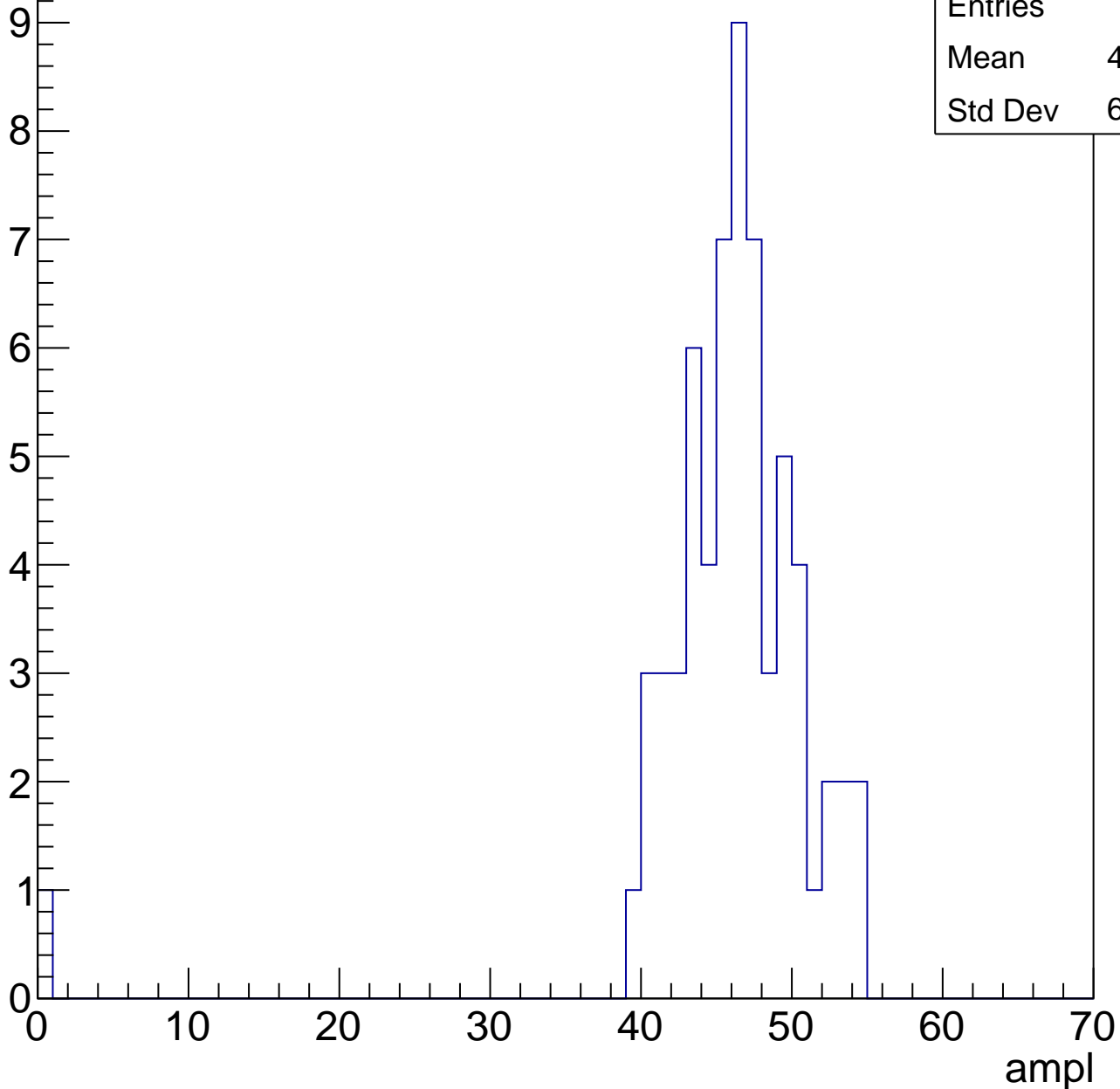


B1L103S, U6-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	45.37
Std Dev	6.783

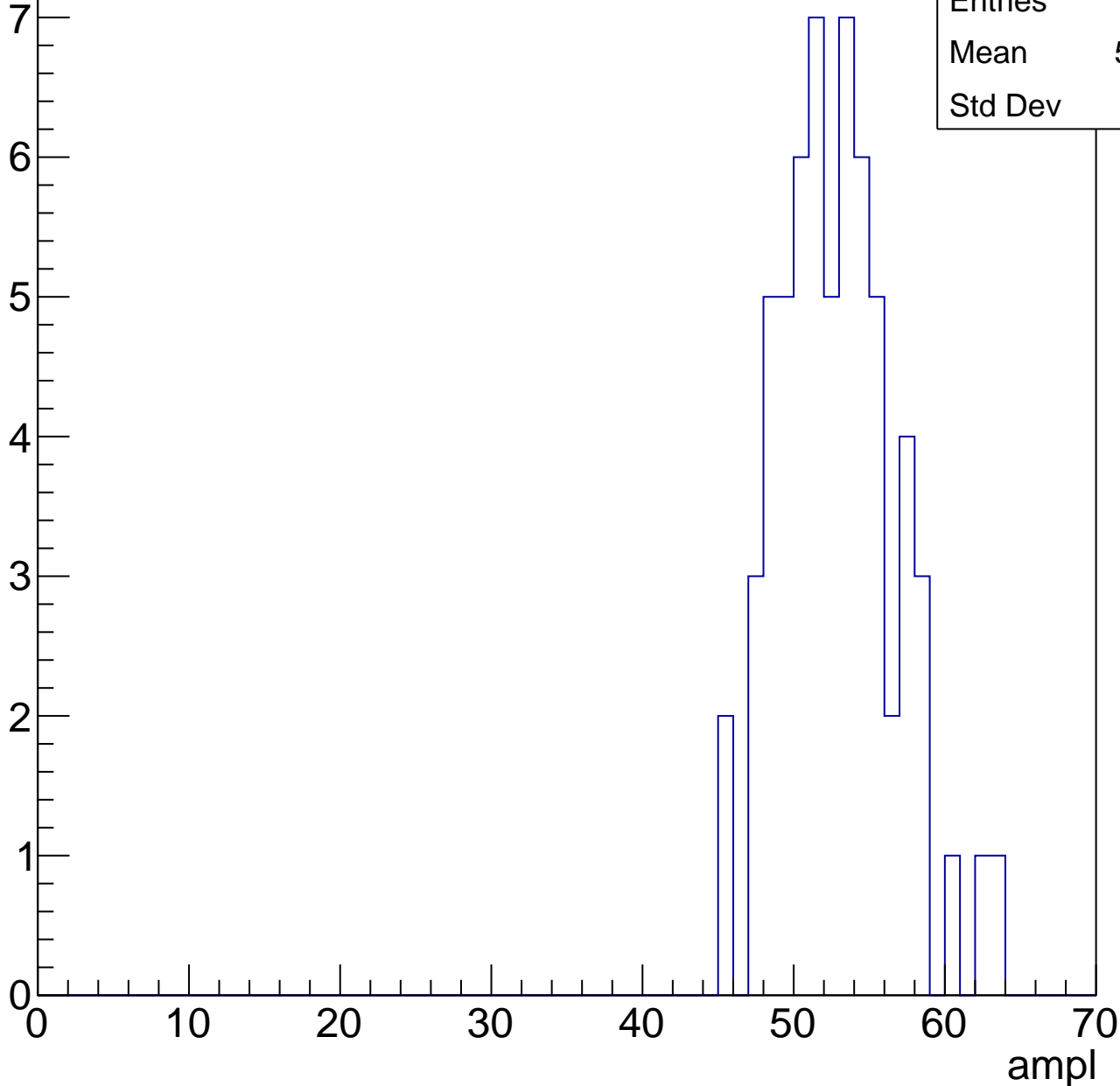


B1L103S, U6-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.41
Std Dev	3.82

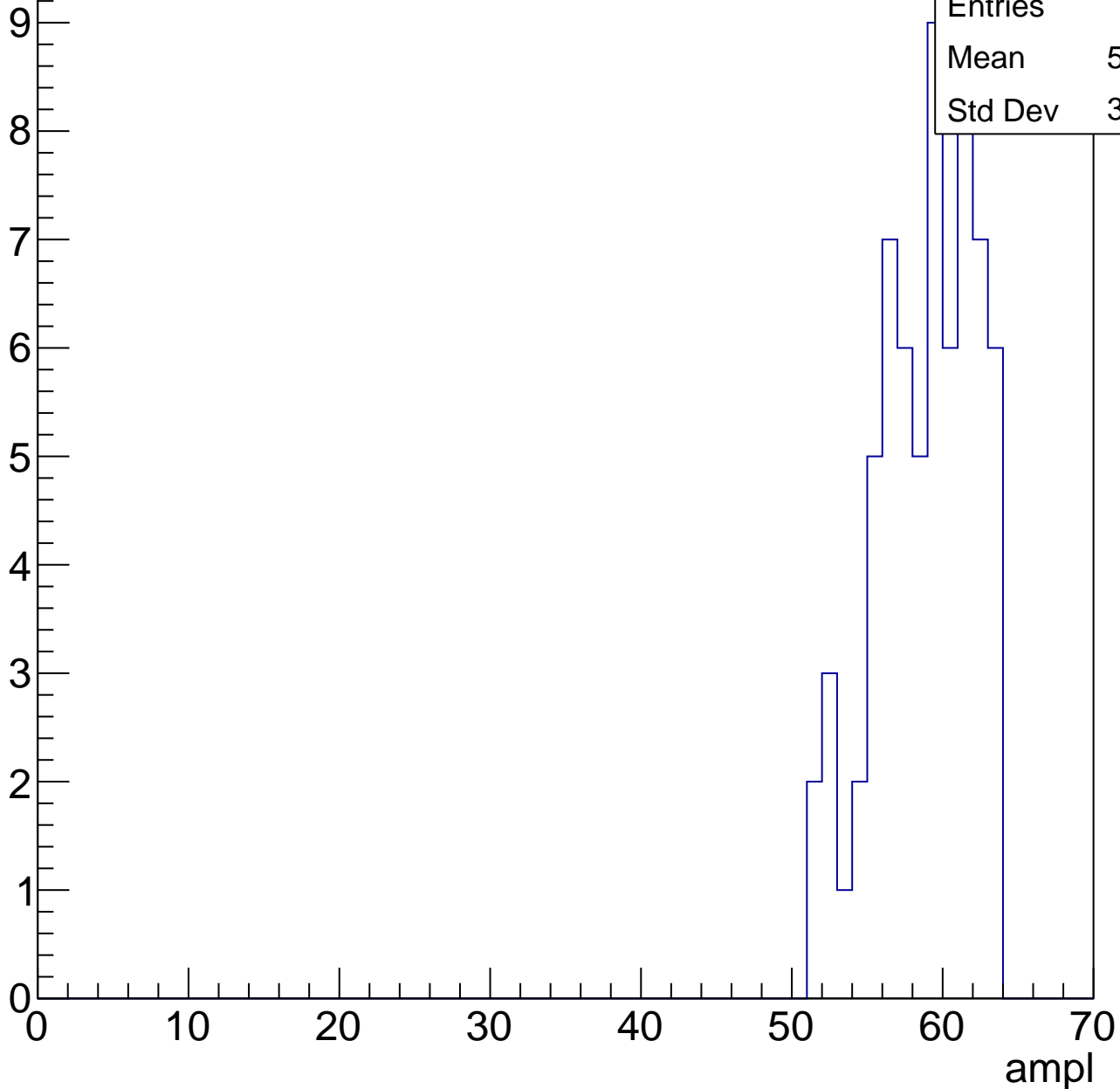


B1L103S, U6-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

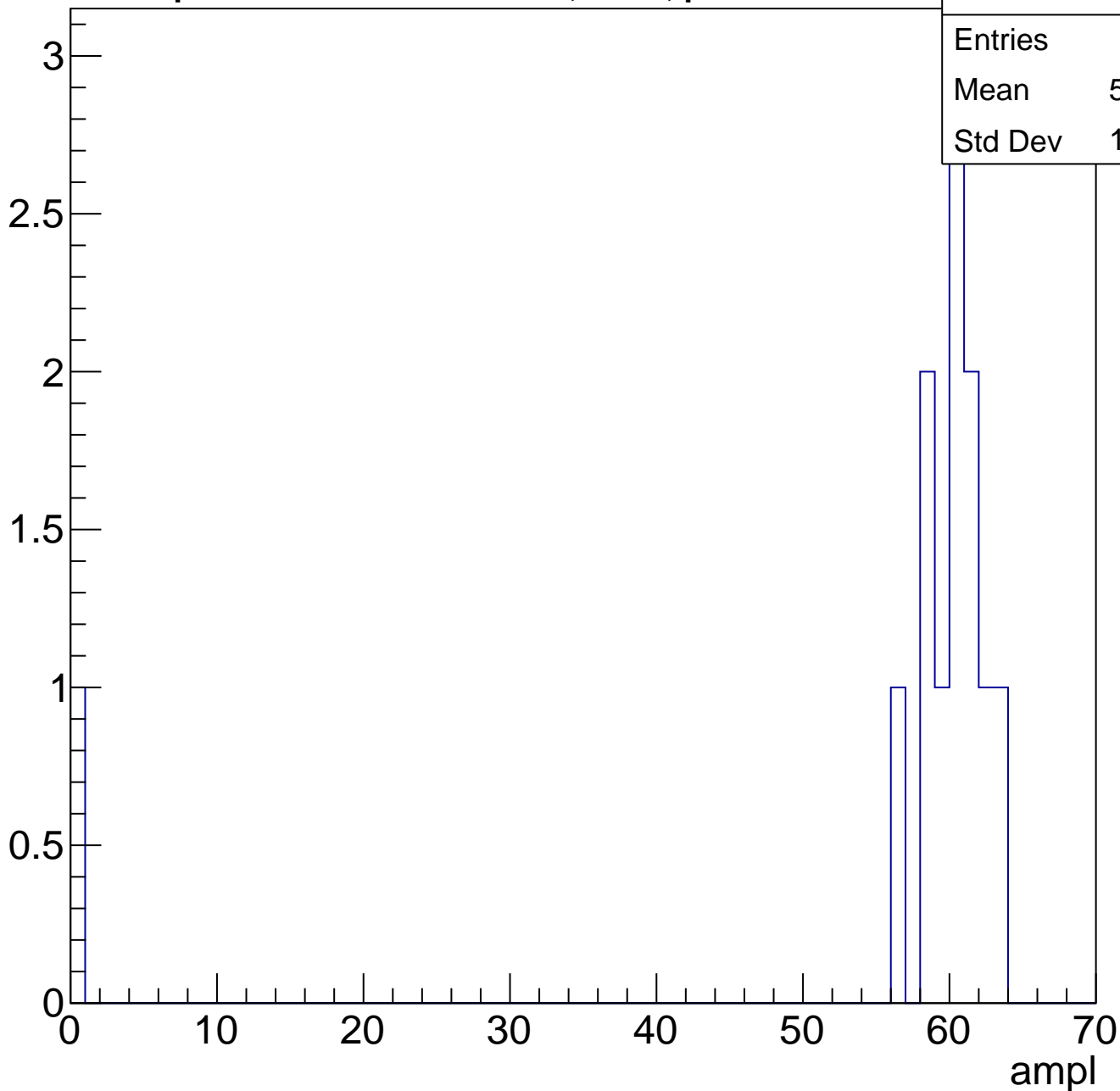
Entries	67
Mean	58.34
Std Dev	3.235



B1L103S, U6-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

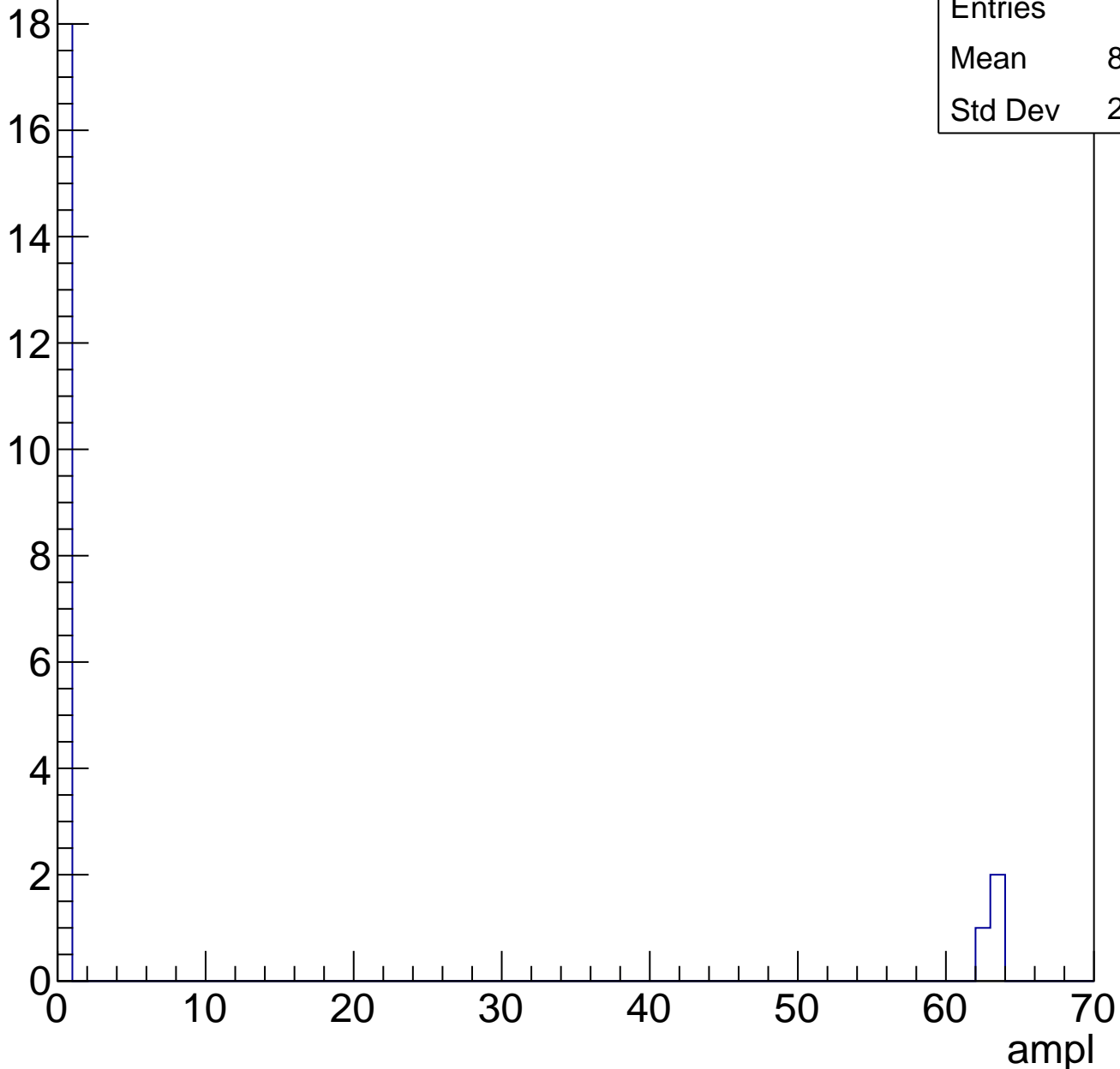


B1L103S, U6-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.952
Std Dev	21.93

Entry



B1L103S, U6-ch113, adc0

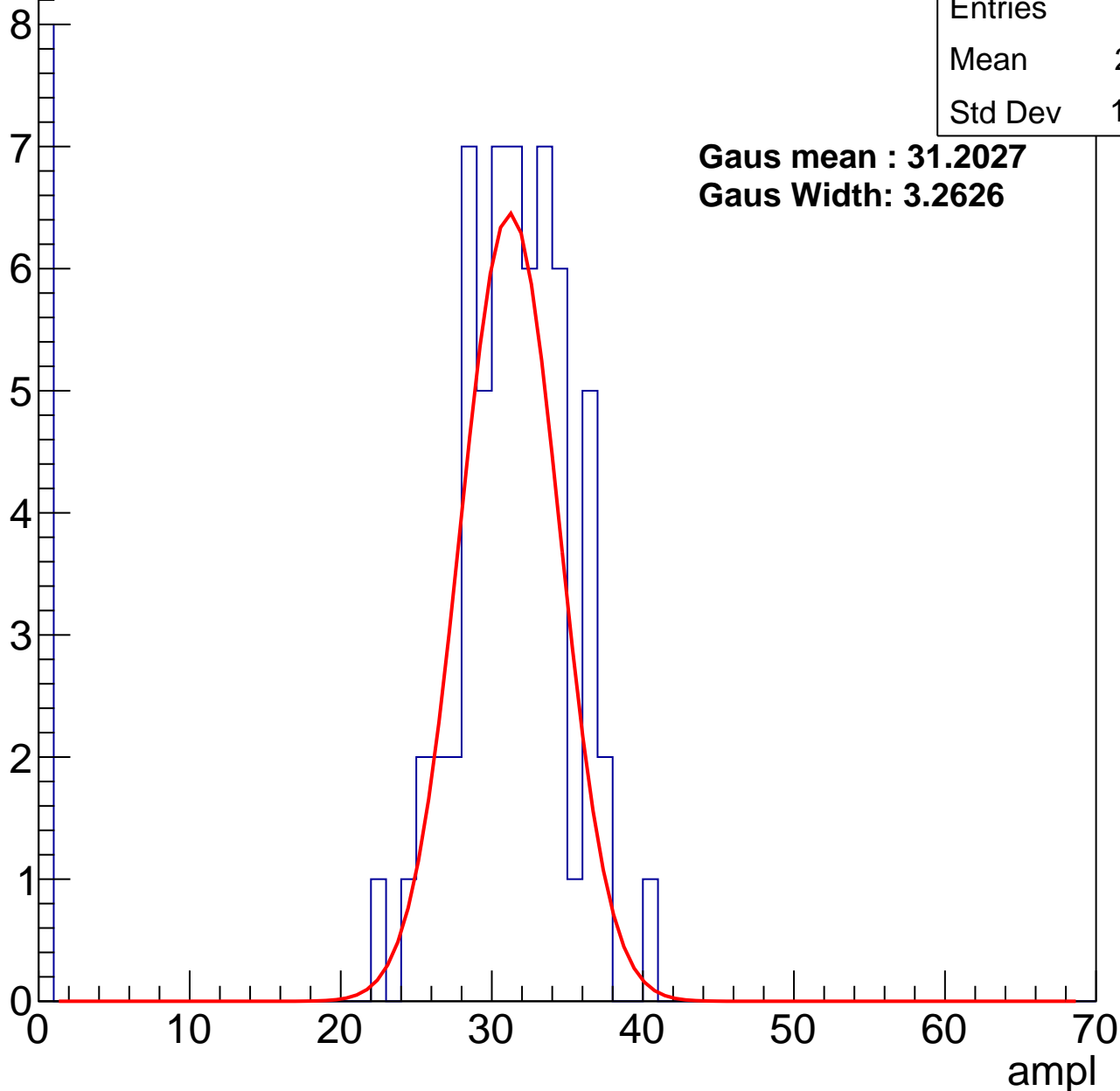
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	27.51
Std Dev	10.42

Gaus mean : 31.2027

Gaus Width: 3.2626



B1L103S, U6-ch113, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	32.02
Std Dev	13.27

Gaus mean : 38.2015

Gaus Width: 3.9735

Entry

10

8

6

4

2

0

0

10

20

30

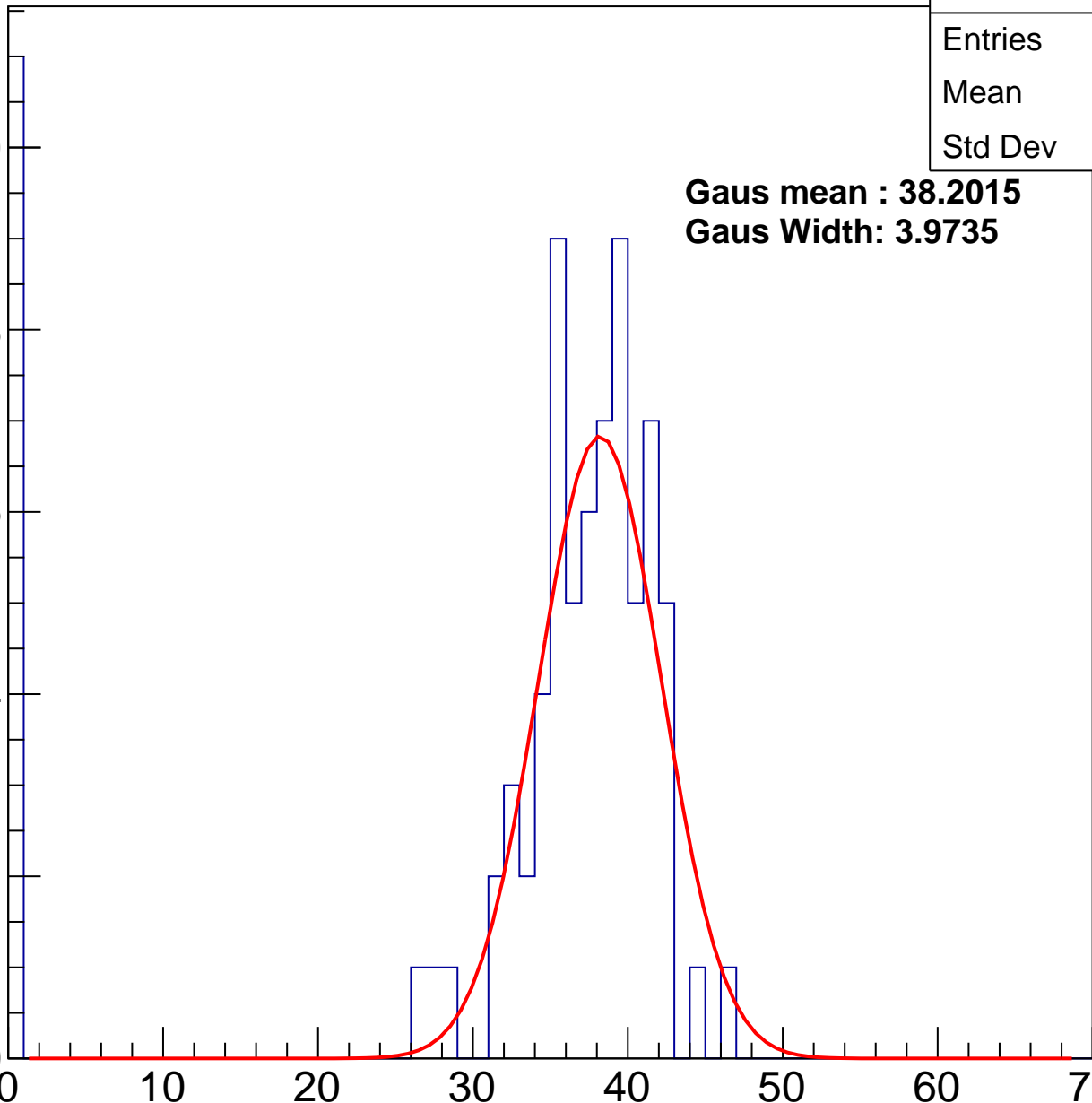
40

50

60

70

ampl



B1L103S, U6-ch113, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	42.56
Std Dev	8.466

Gaus mean : 44.6506

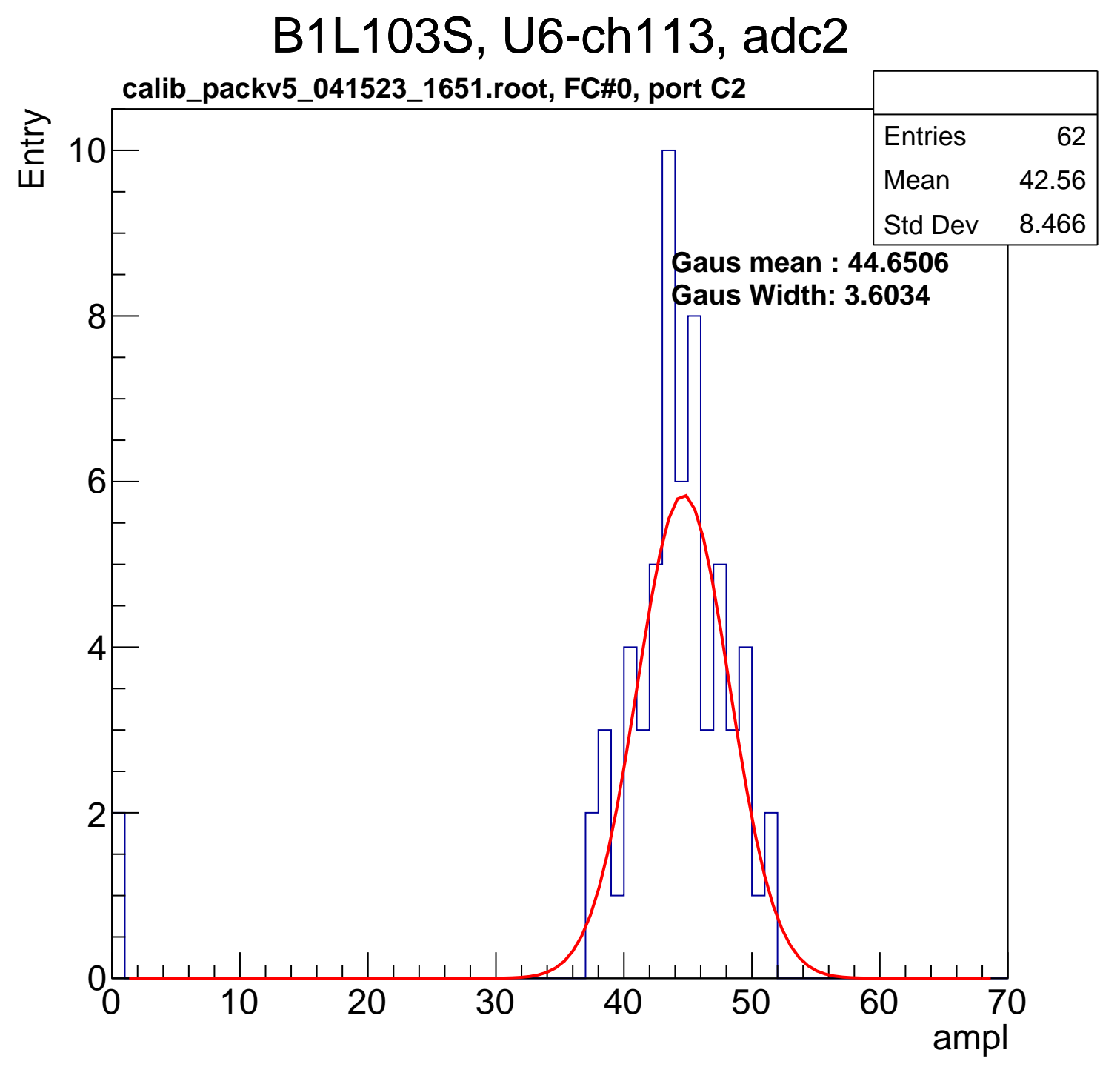
Gaus Width: 3.6034

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

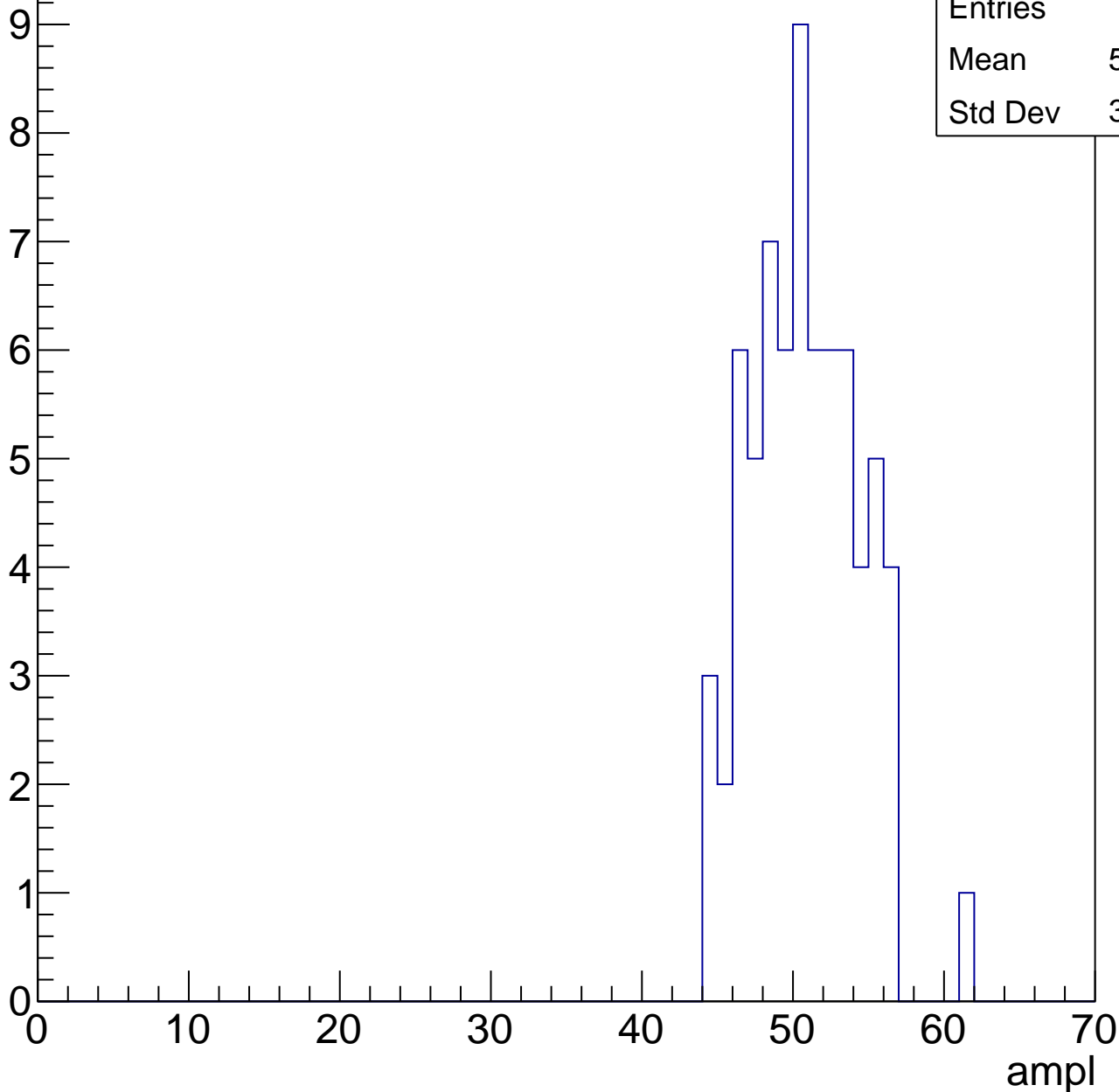


B1L103S, U6-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	50.36
Std Dev	3.509

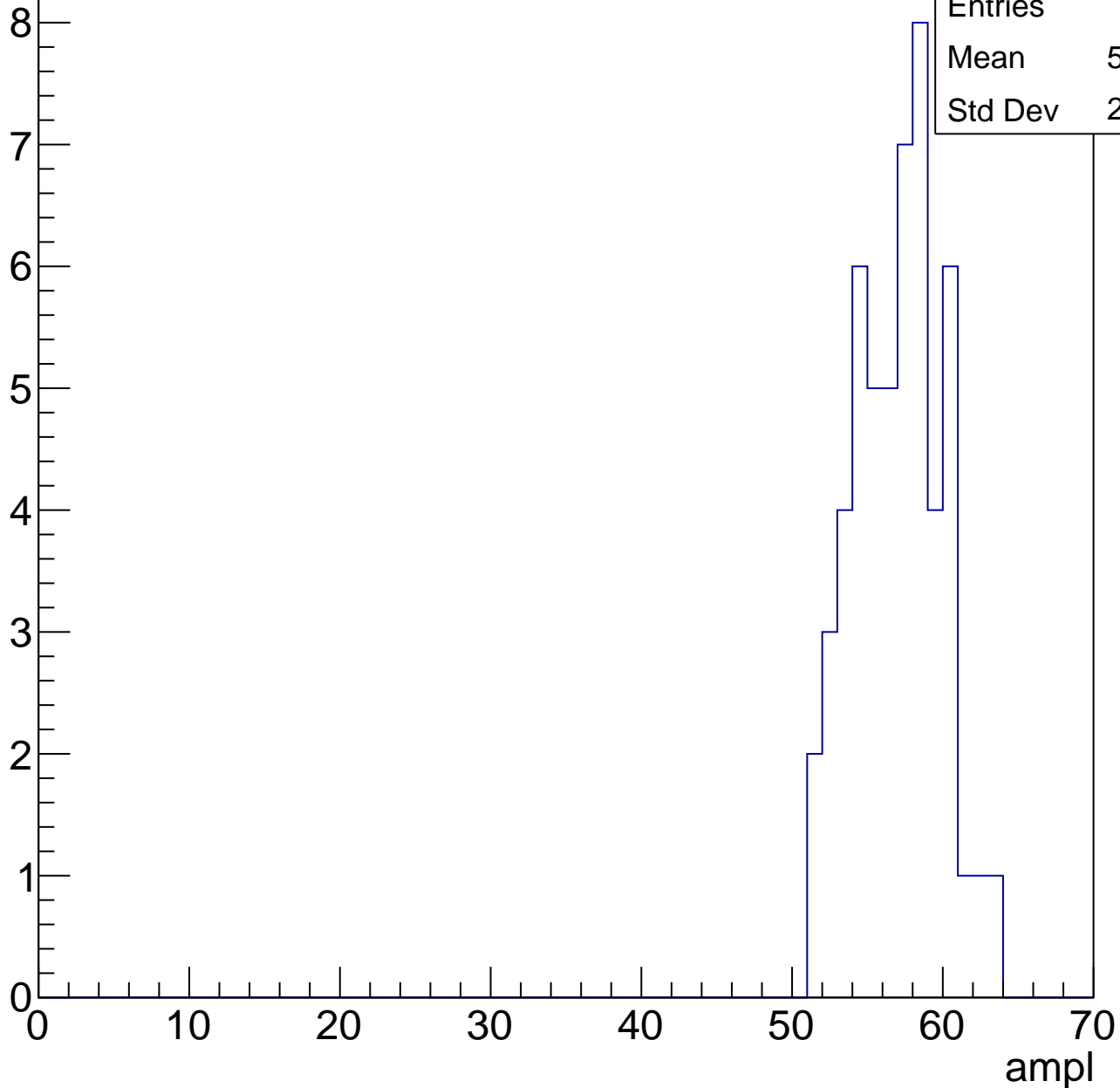


B1L103S, U6-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	56.49
Std Dev	2.852

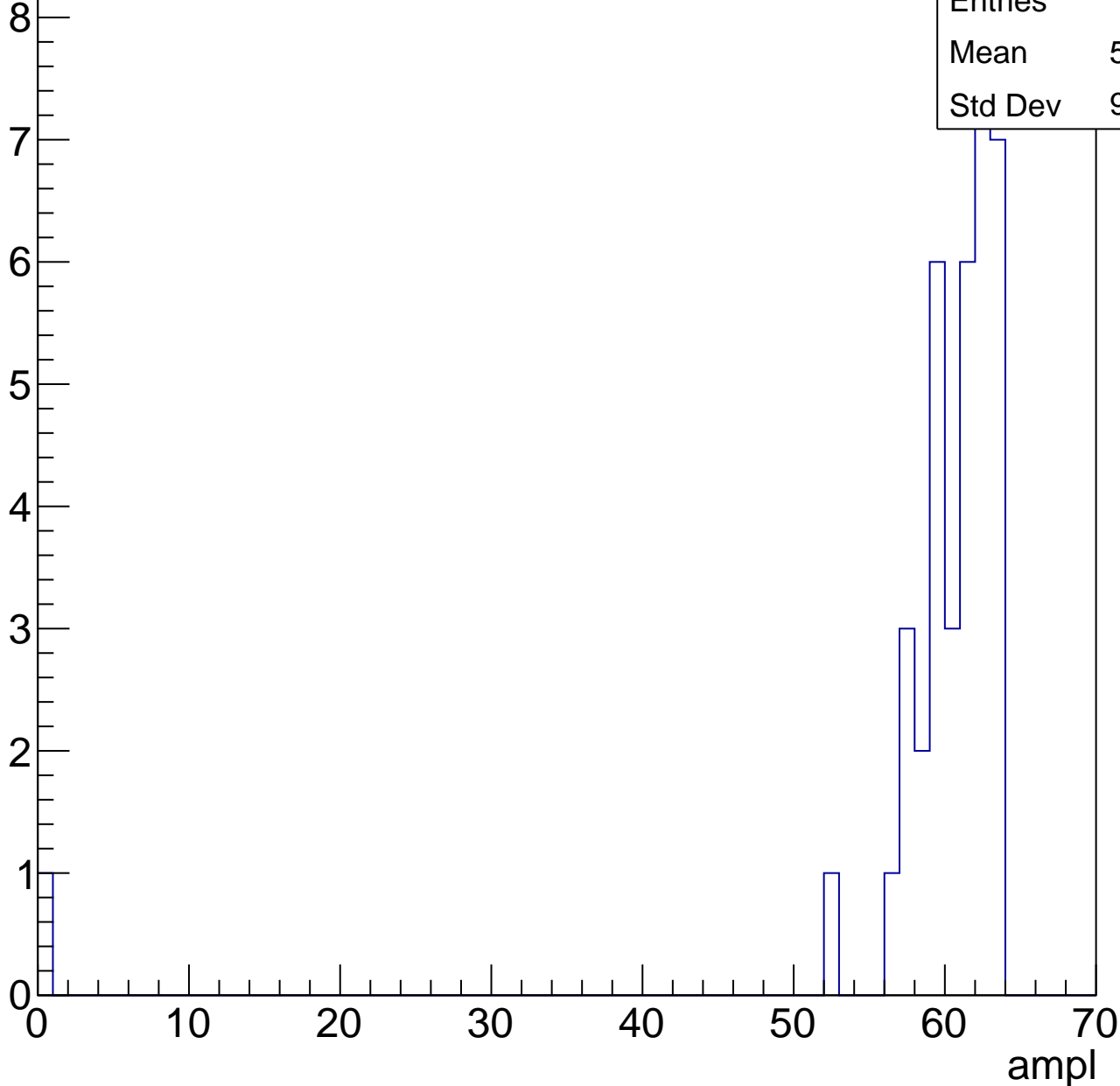


B1L103S, U6-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.74
Std Dev	9.949



B1L103S, U6-ch113, adc6

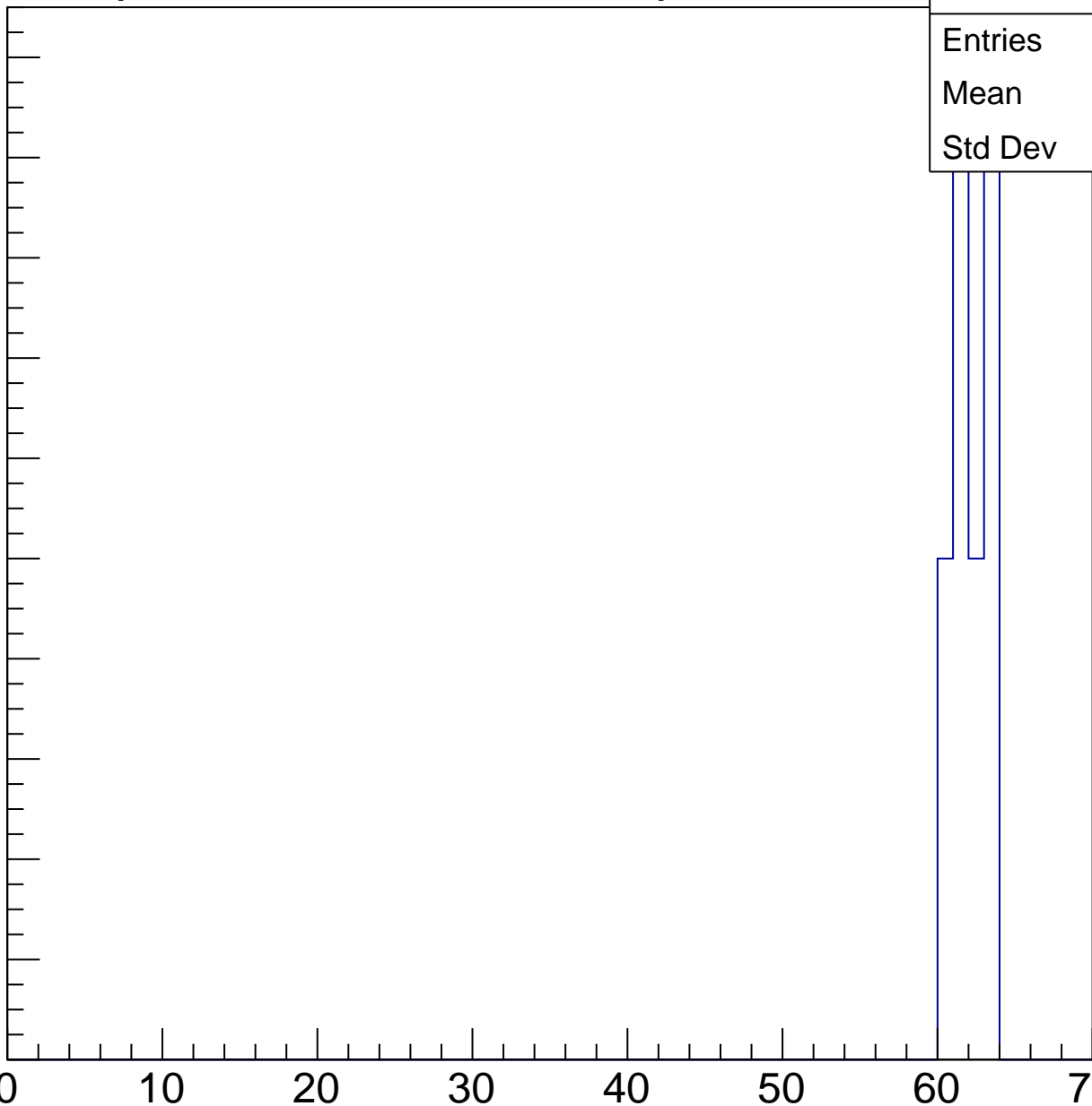
calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl



B1L103S, U6-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch114, adc0

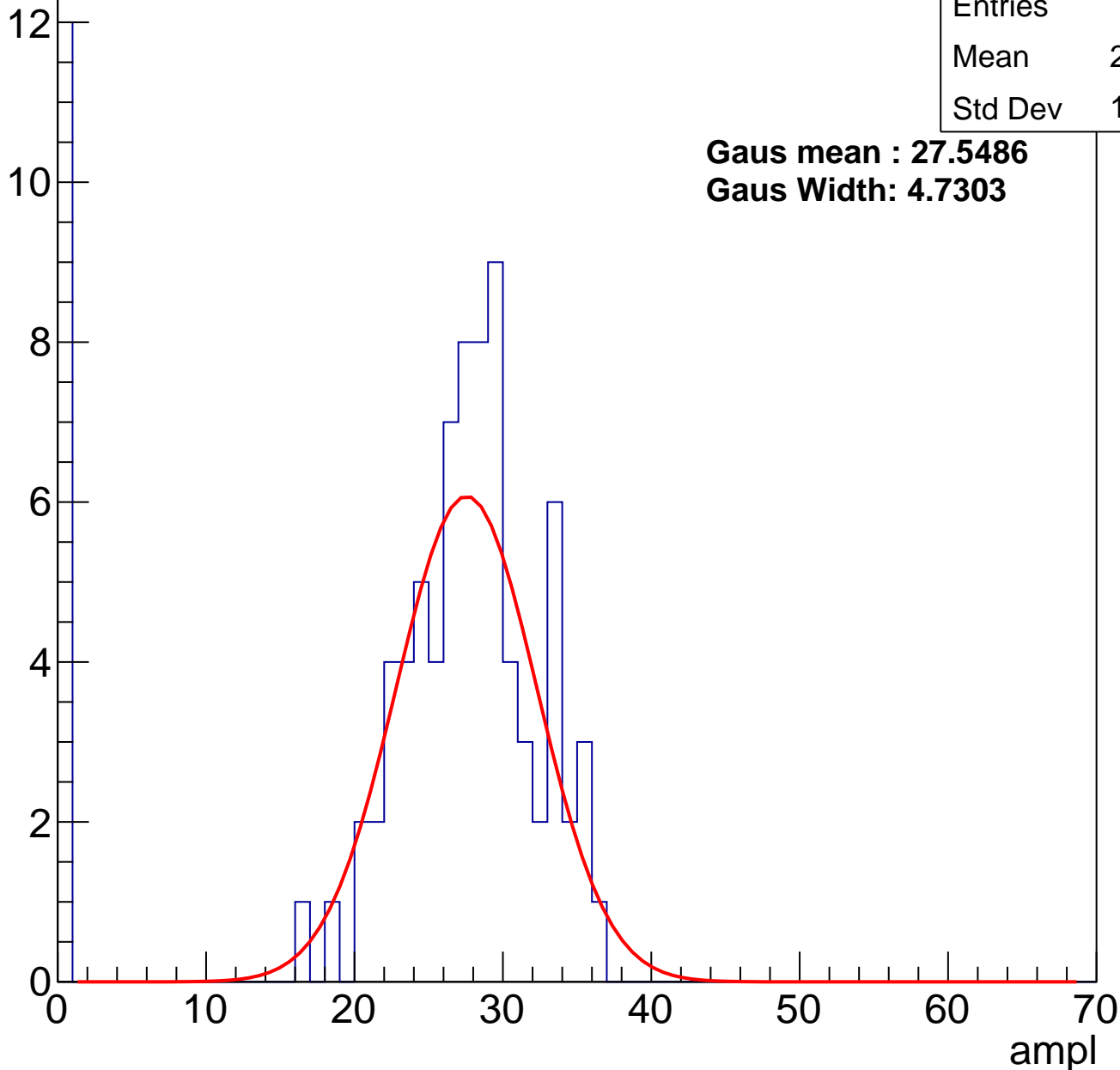
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.67
Std Dev	10.19

Gaus mean : 27.5486

Gaus Width: 4.7303

Entry



B1L103S, U6-ch114, adc1

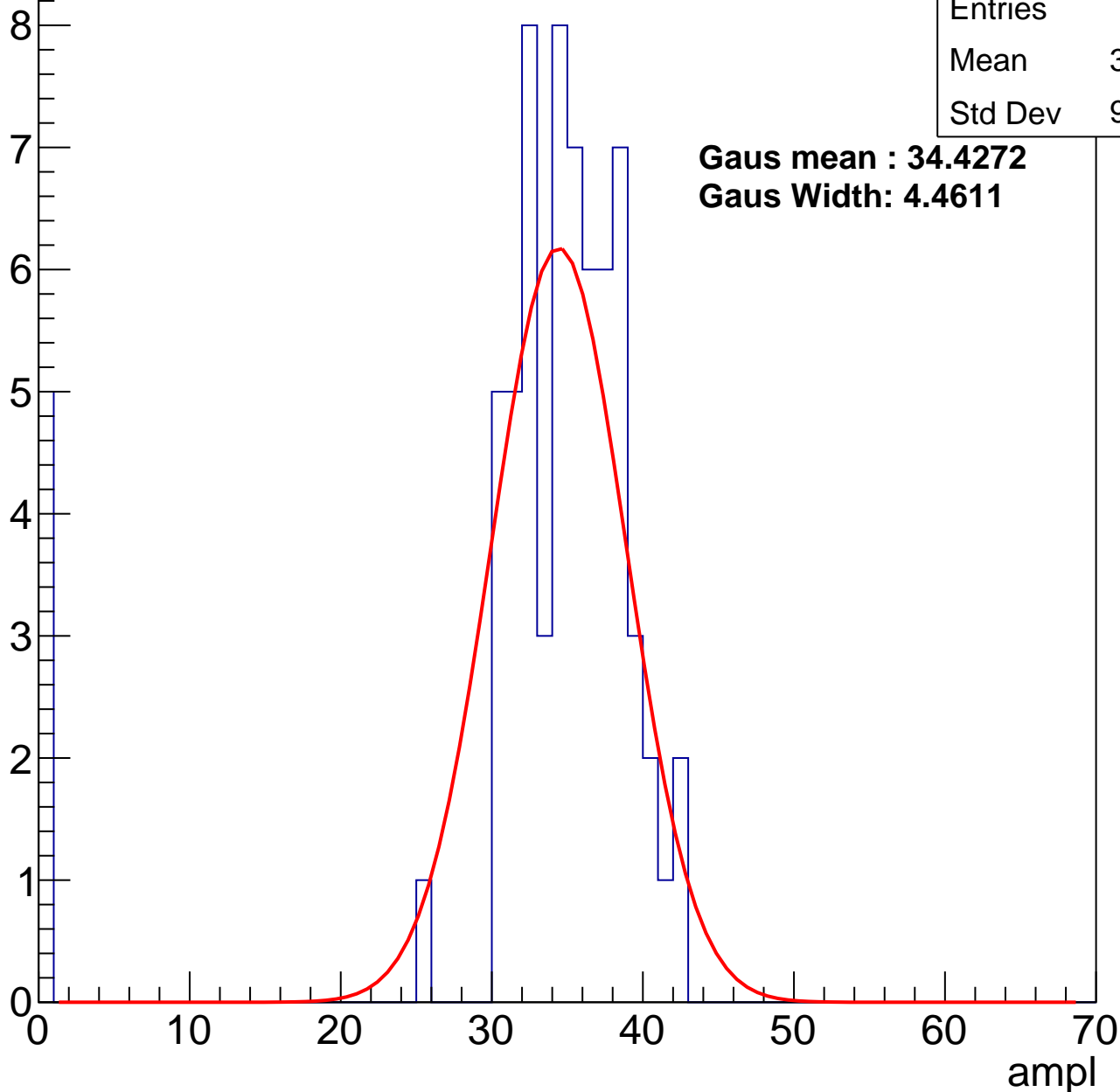
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	32.29
Std Dev	9.586

Gaus mean : 34.4272

Gaus Width: 4.4611

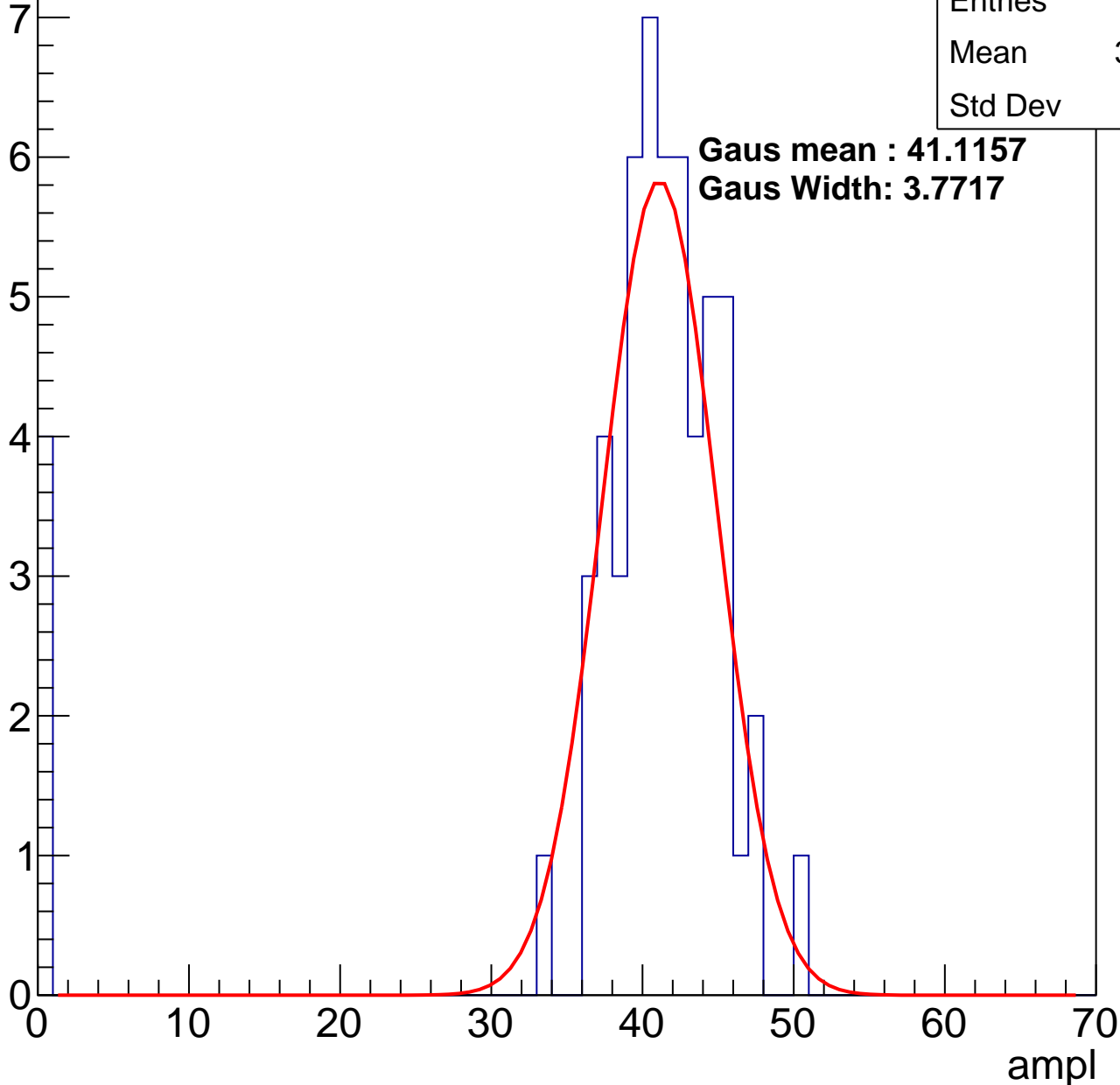


B1L103S, U6-ch114, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.31
Std Dev	10.9

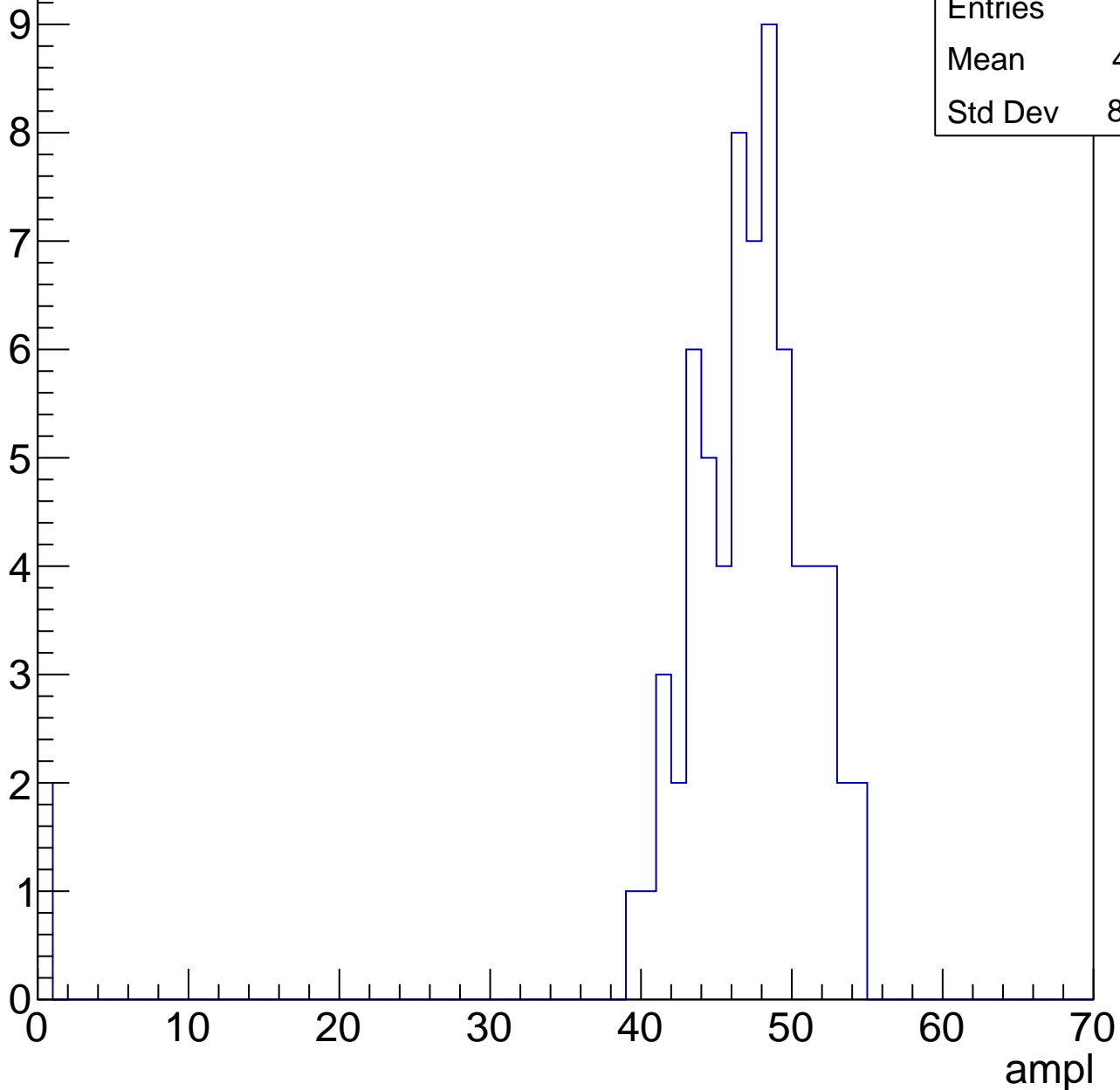


B1L103S, U6-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.61
Std Dev	8.555

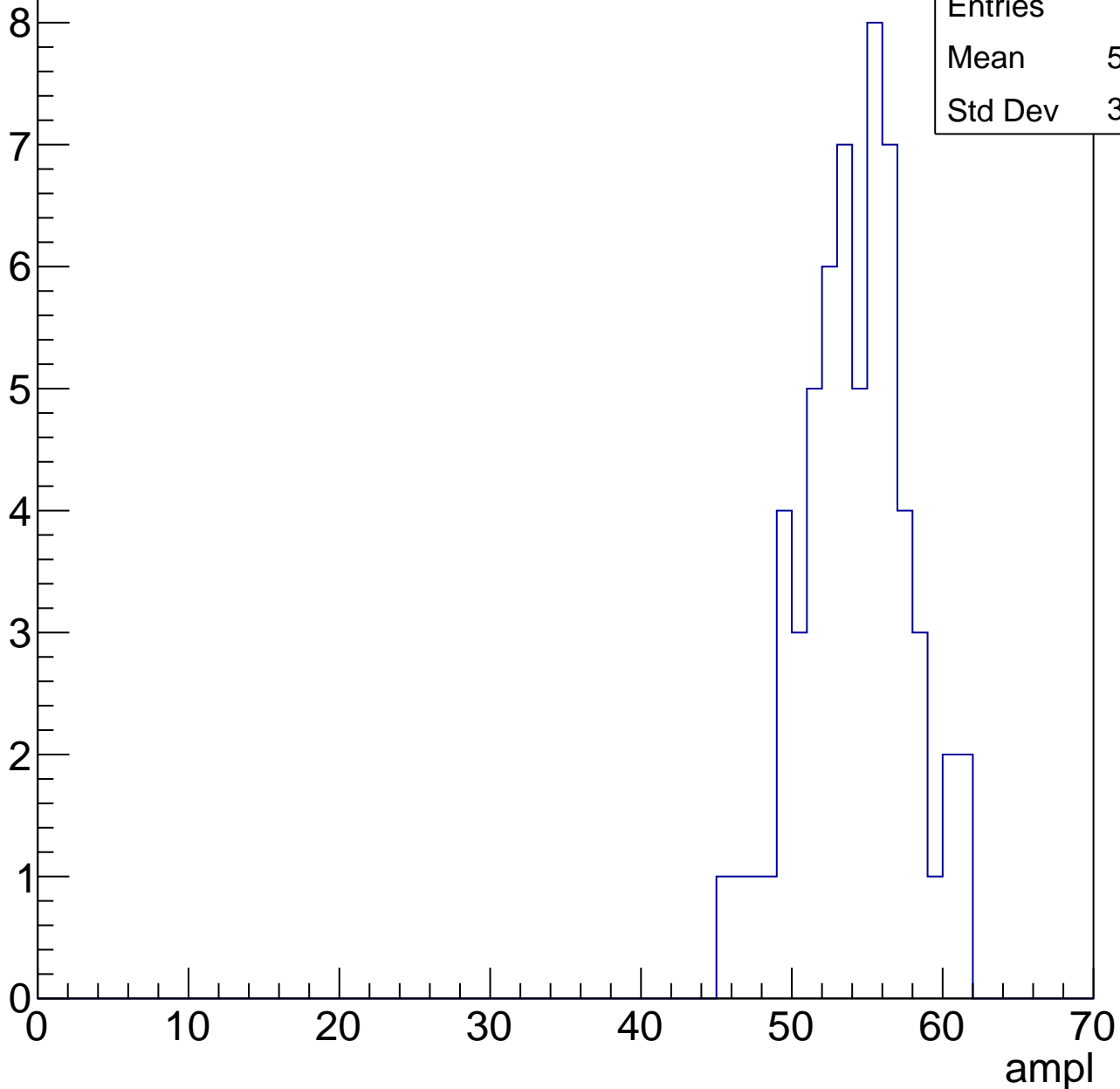


B1L103S, U6-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.69
Std Dev	3.537

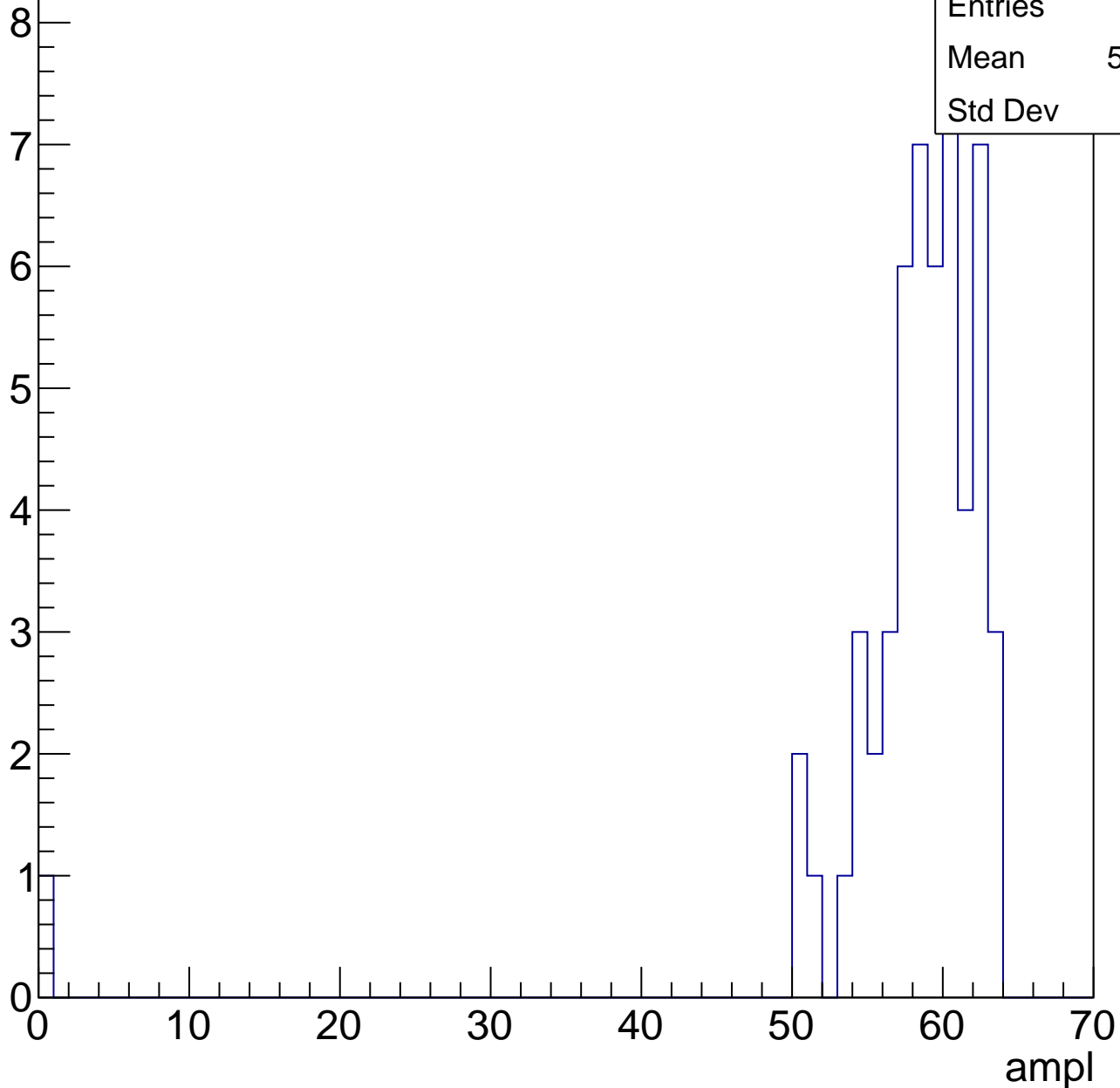


B1L103S, U6-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.28
Std Dev	8.48

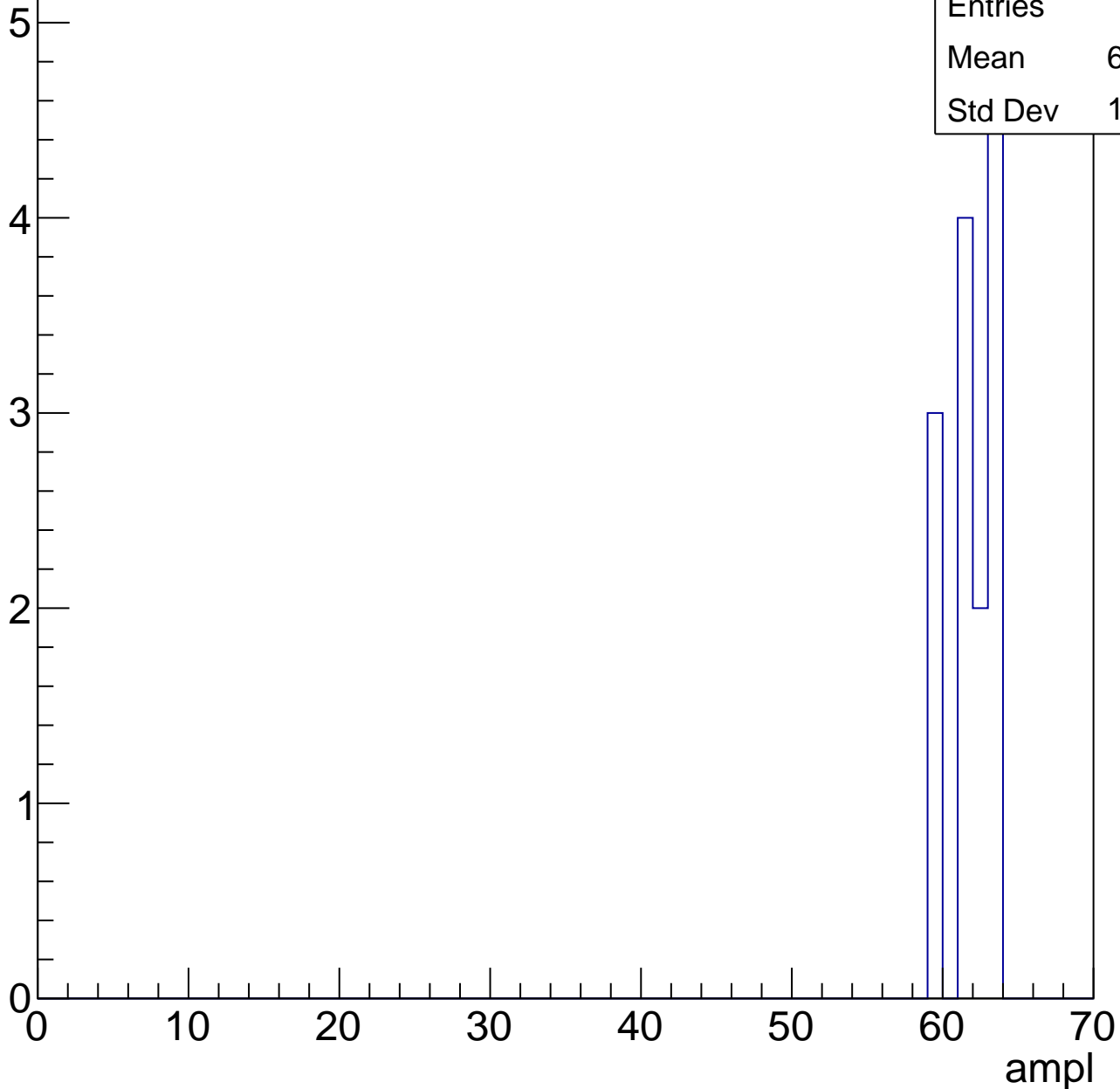


B1L103S, U6-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.43
Std Dev	1.498



B1L103S, U6-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

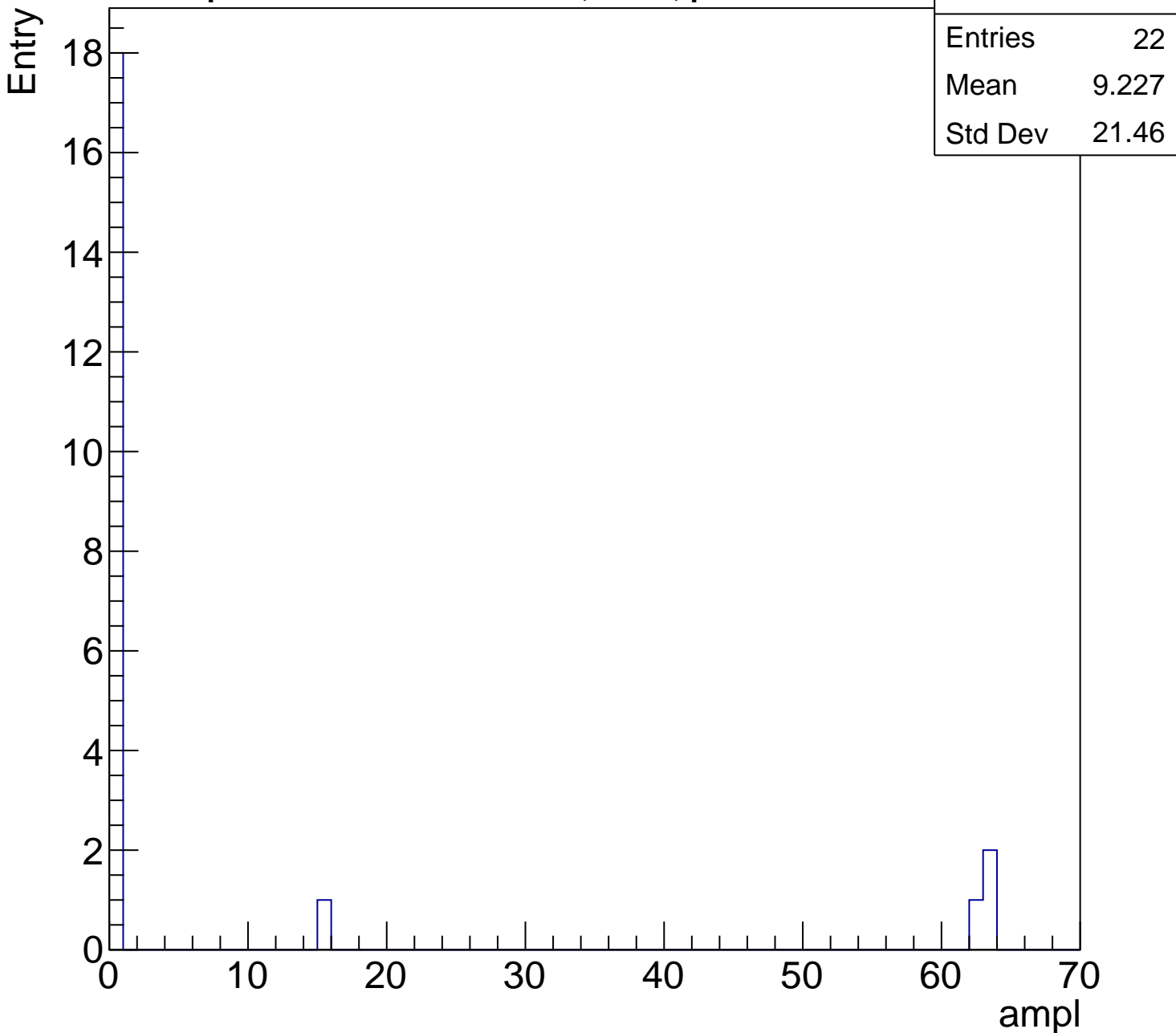
Entries	22
Mean	9.227
Std Dev	21.46

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	22.76
Std Dev	13.38

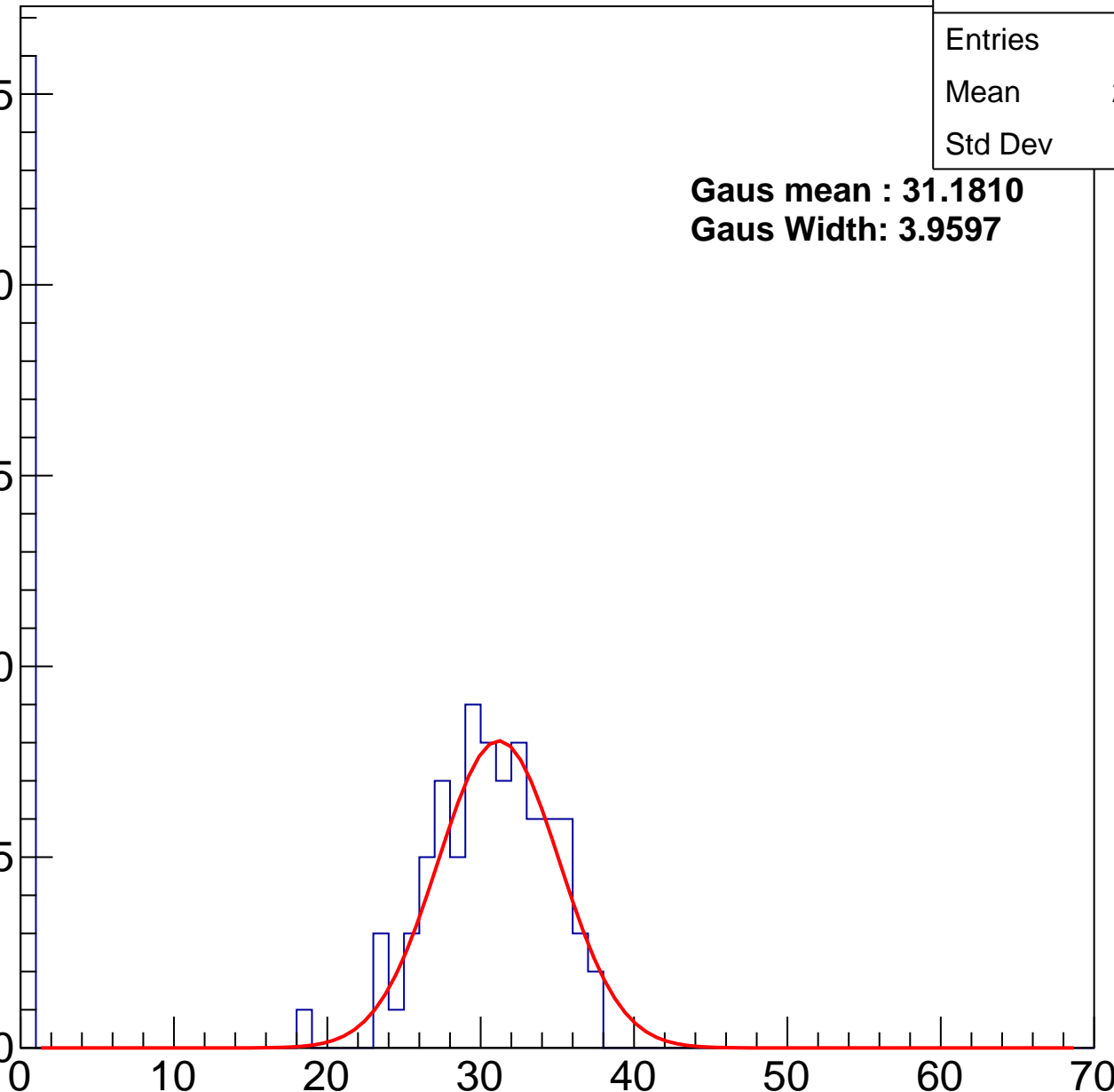
Gaus mean : 31.1810

Gaus Width: 3.9597

Entry

25
20
15
10
5
0

ampl

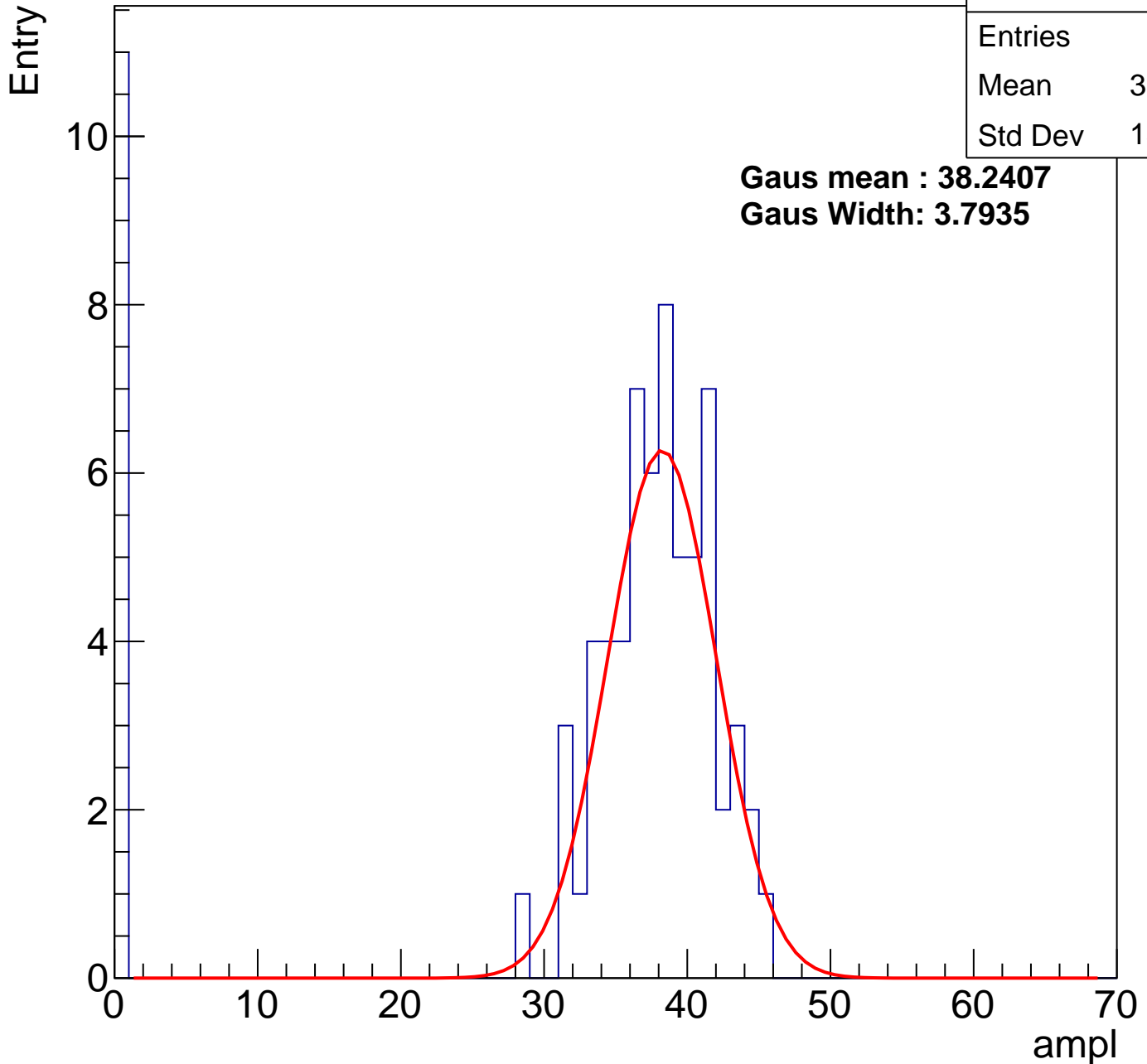


B1L103S, U6-ch115, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.99
Std Dev	13.77

Gaus mean : 38.2407
Gaus Width: 3.7935



B1L103S, U6-ch115, adc2

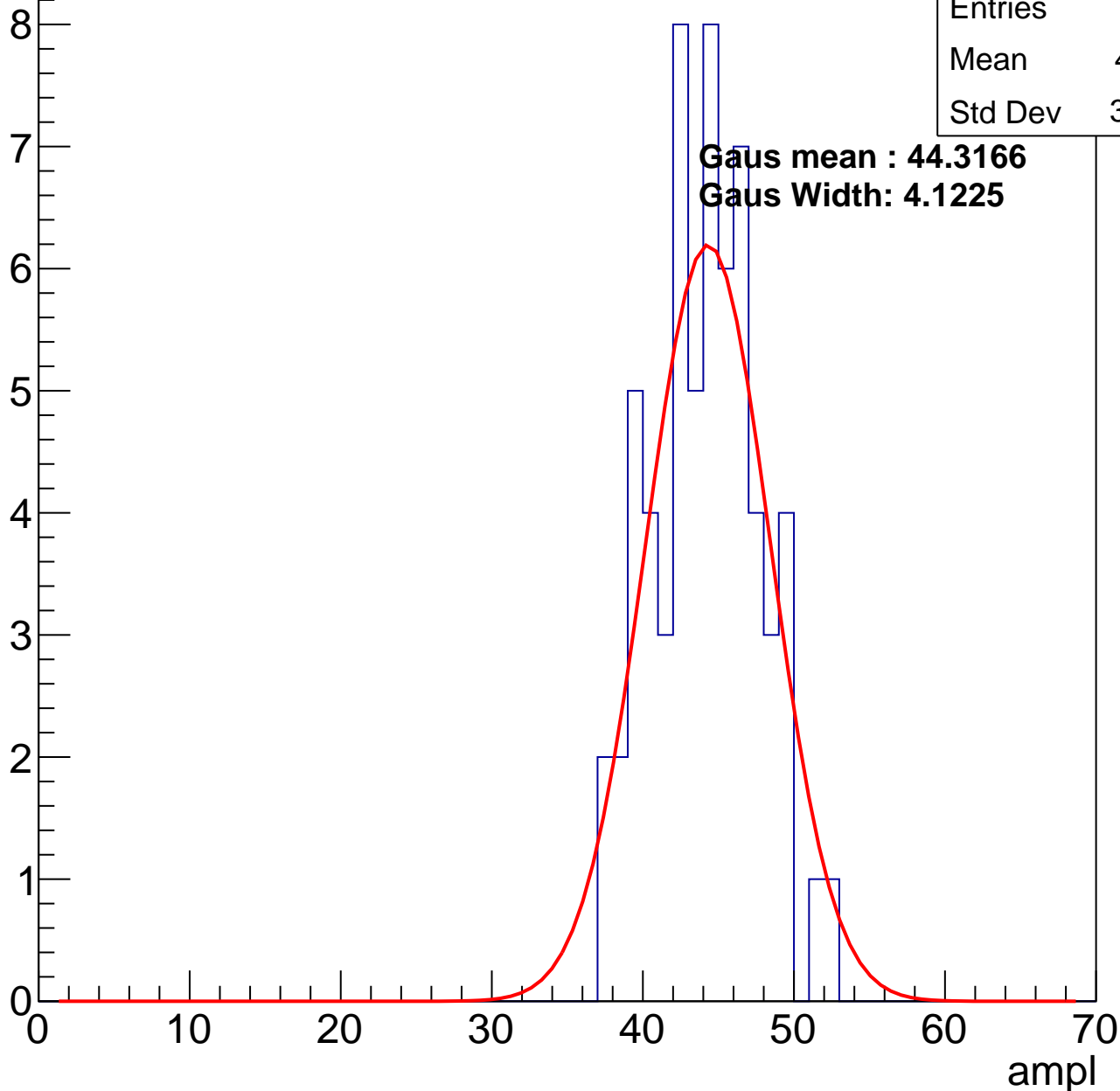
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	43.71
Std Dev	3.448

Gaus mean : 44.3166

Gaus Width: 4.1225

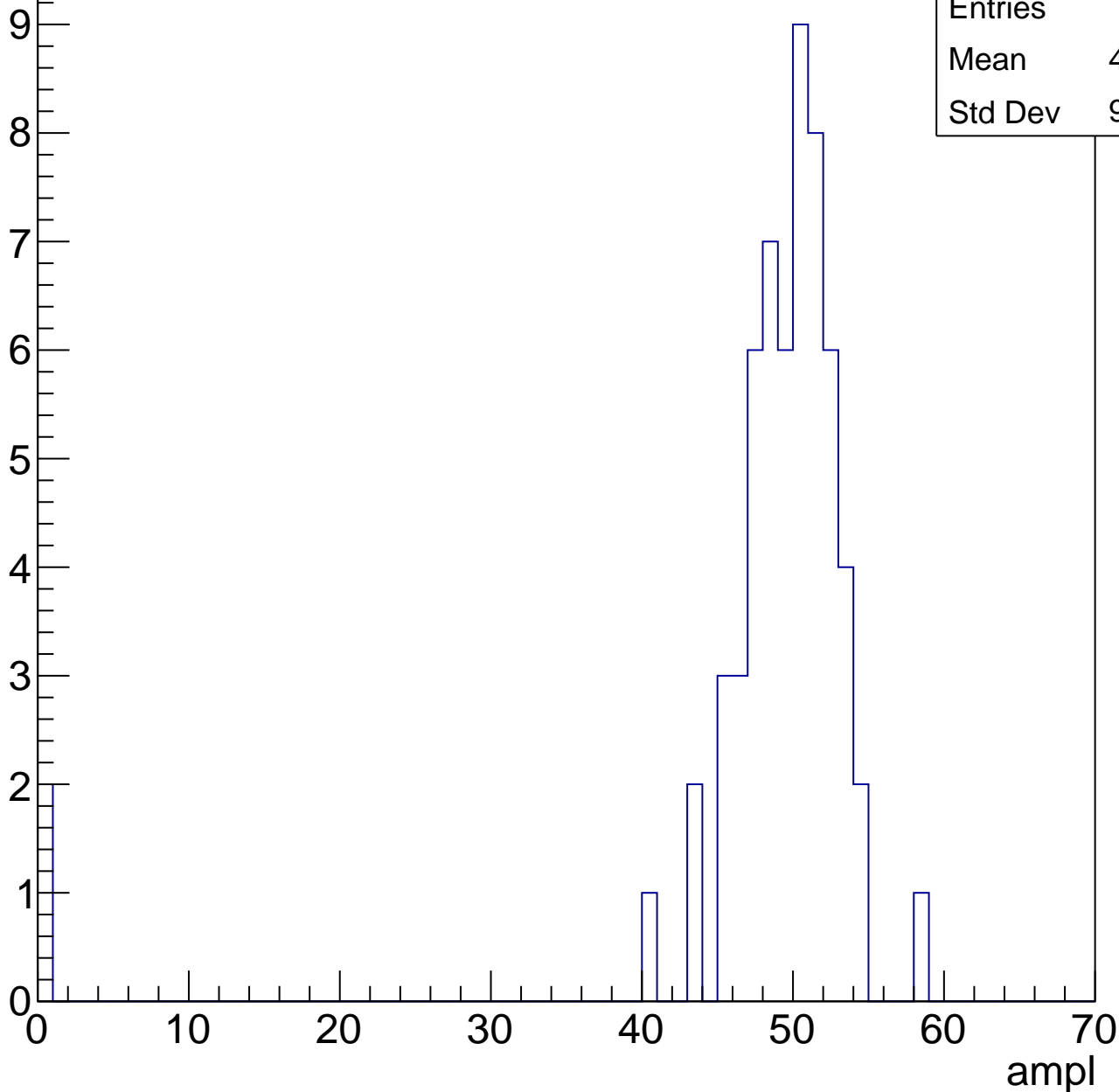


B1L103S, U6-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	47.65
Std Dev	9.347

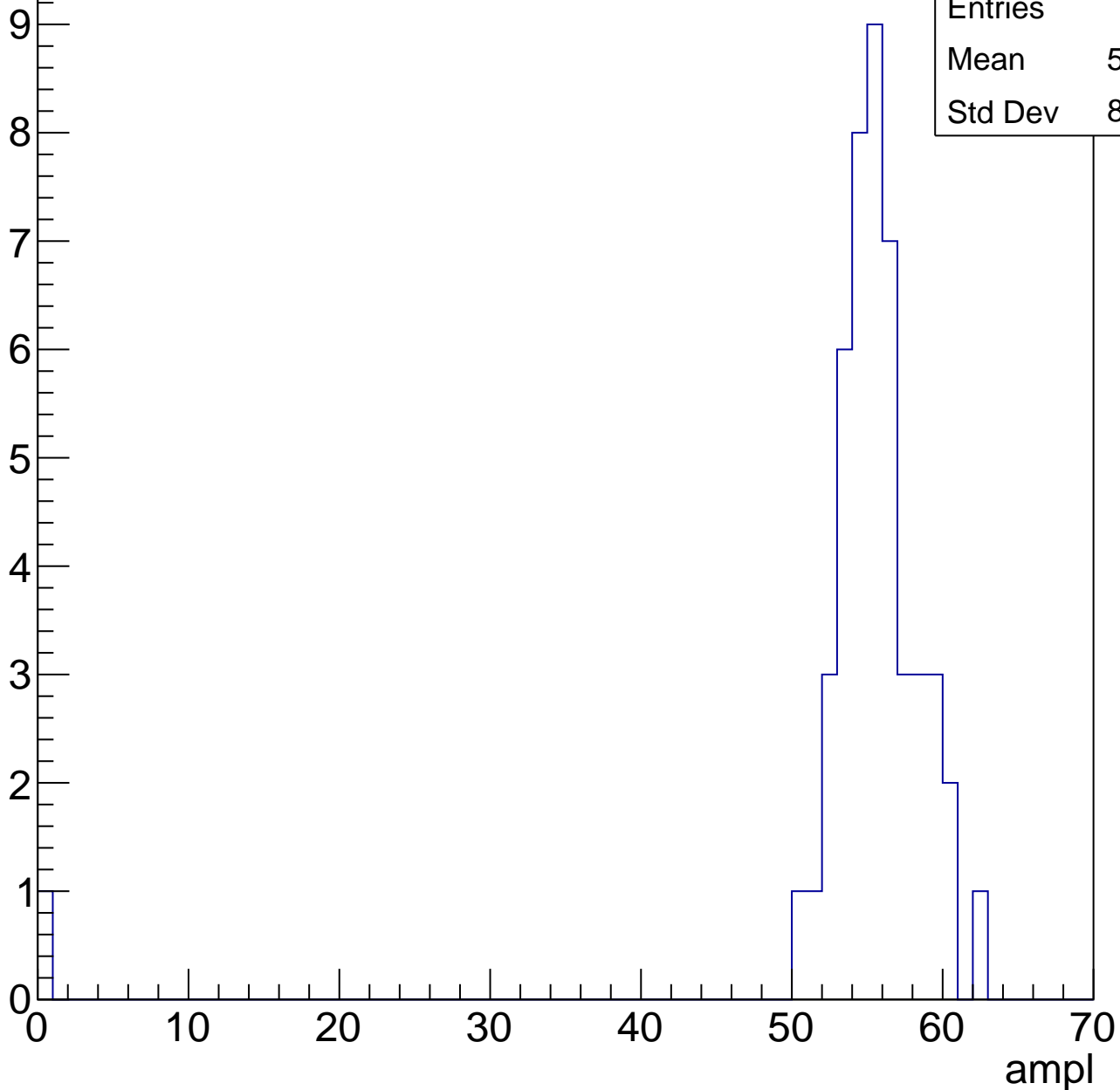


B1L103S, U6-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.12
Std Dev	8.268

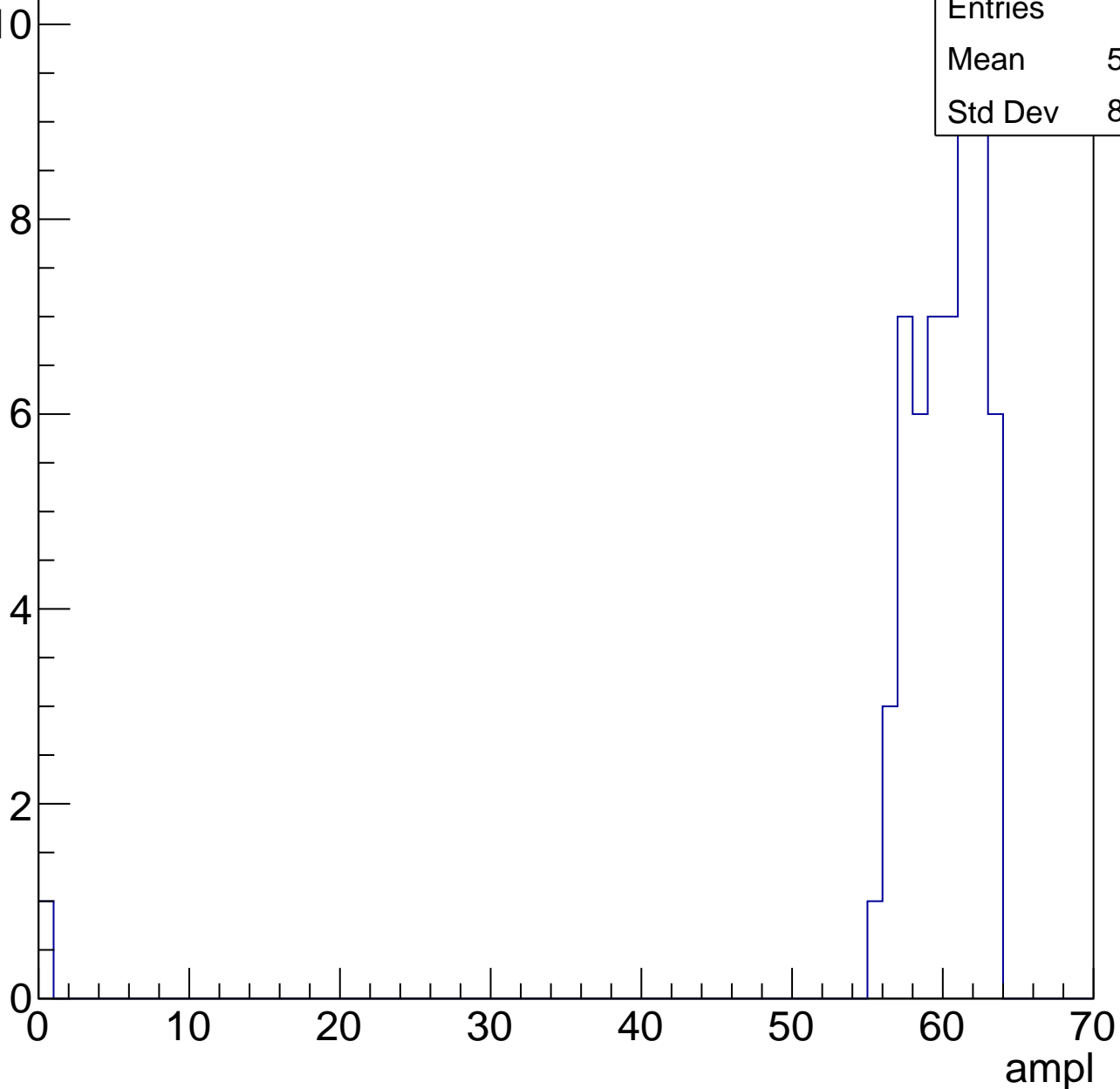


B1L103S, U6-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

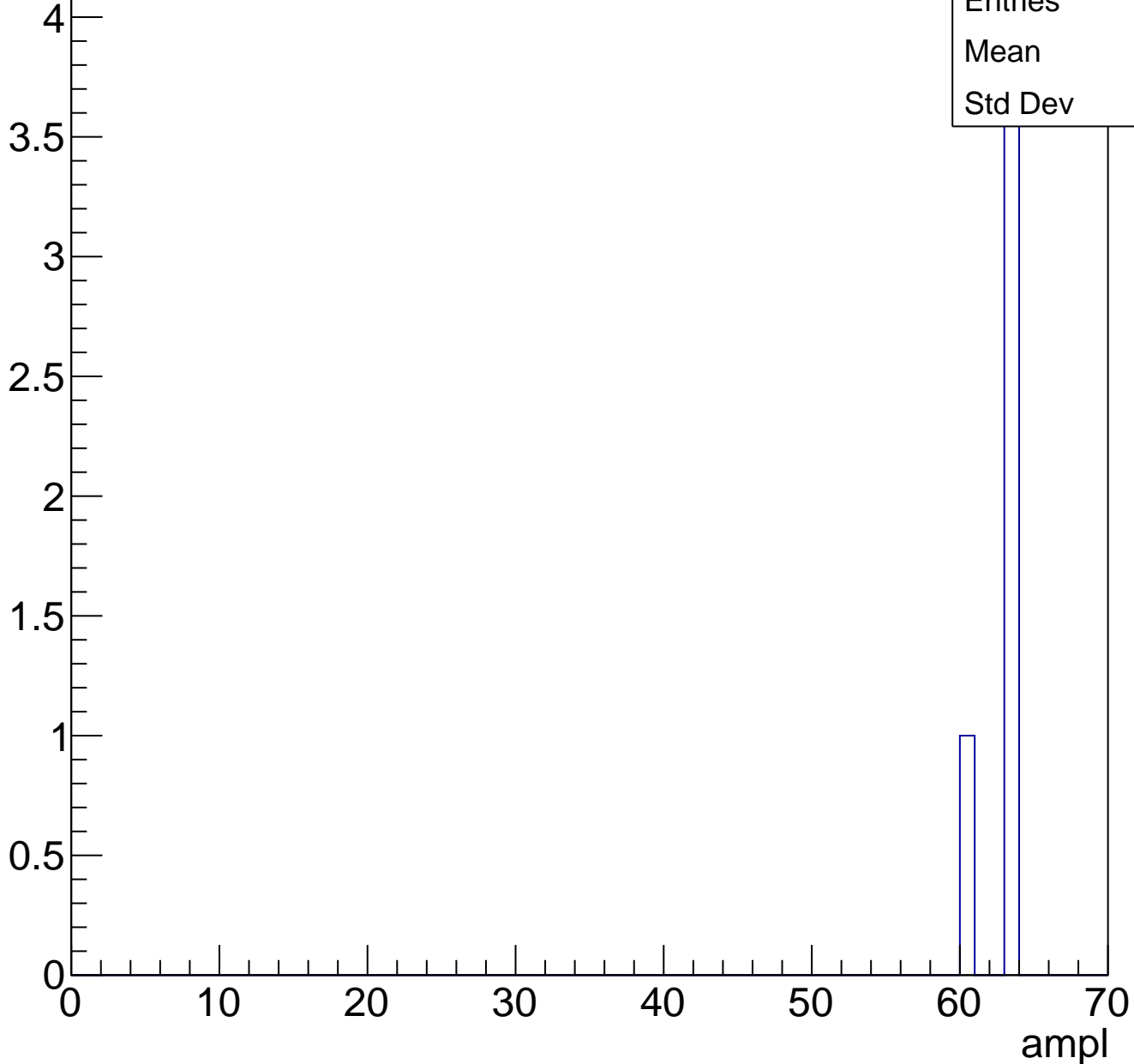
Entries	57
Mean	58.75
Std Dev	8.142



B1L103S, U6-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch116, adc0

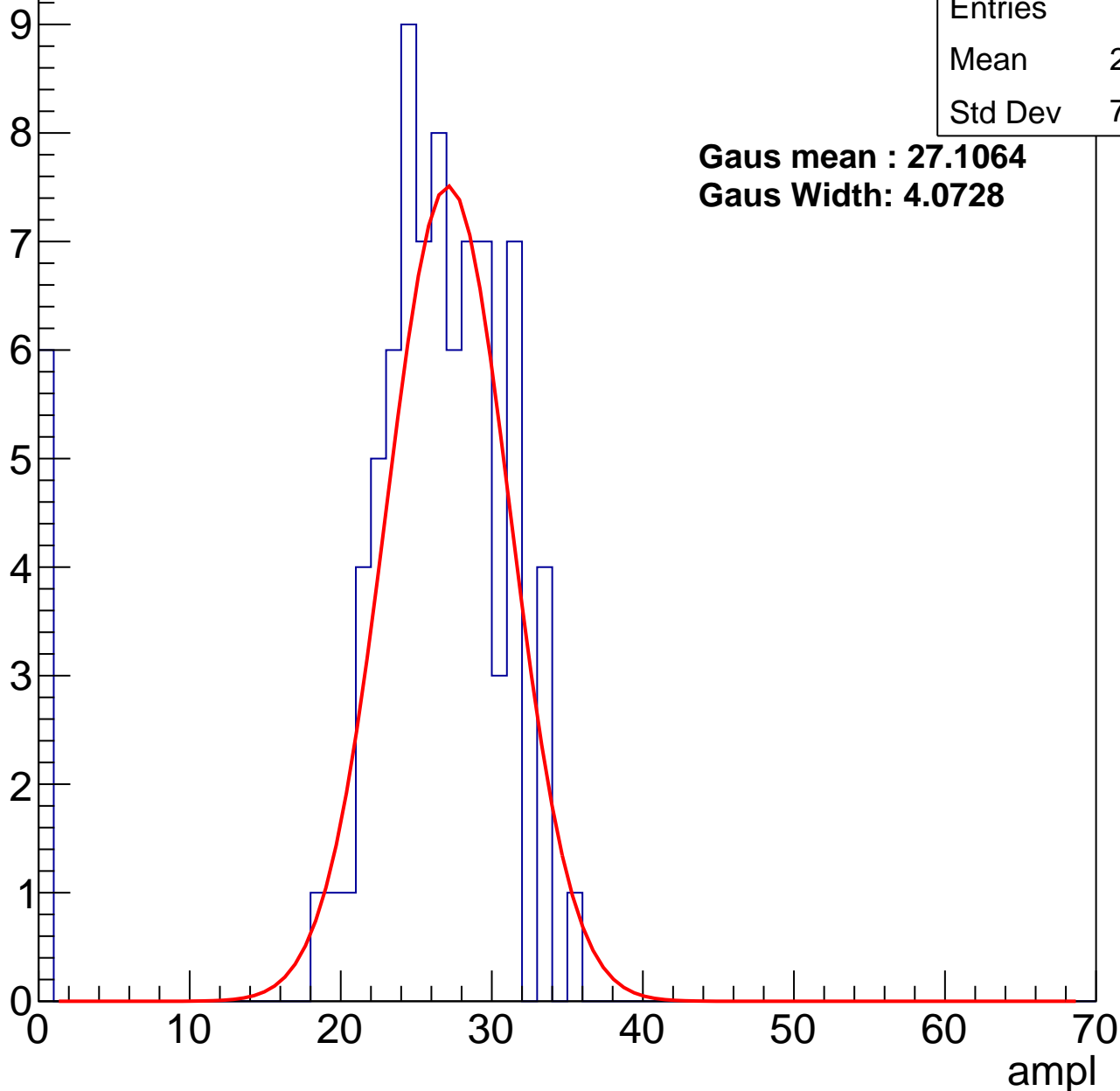
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	24.37
Std Dev	7.652

Gaus mean : 27.1064

Gaus Width: 4.0728



B1L103S, U6-ch116, adc1

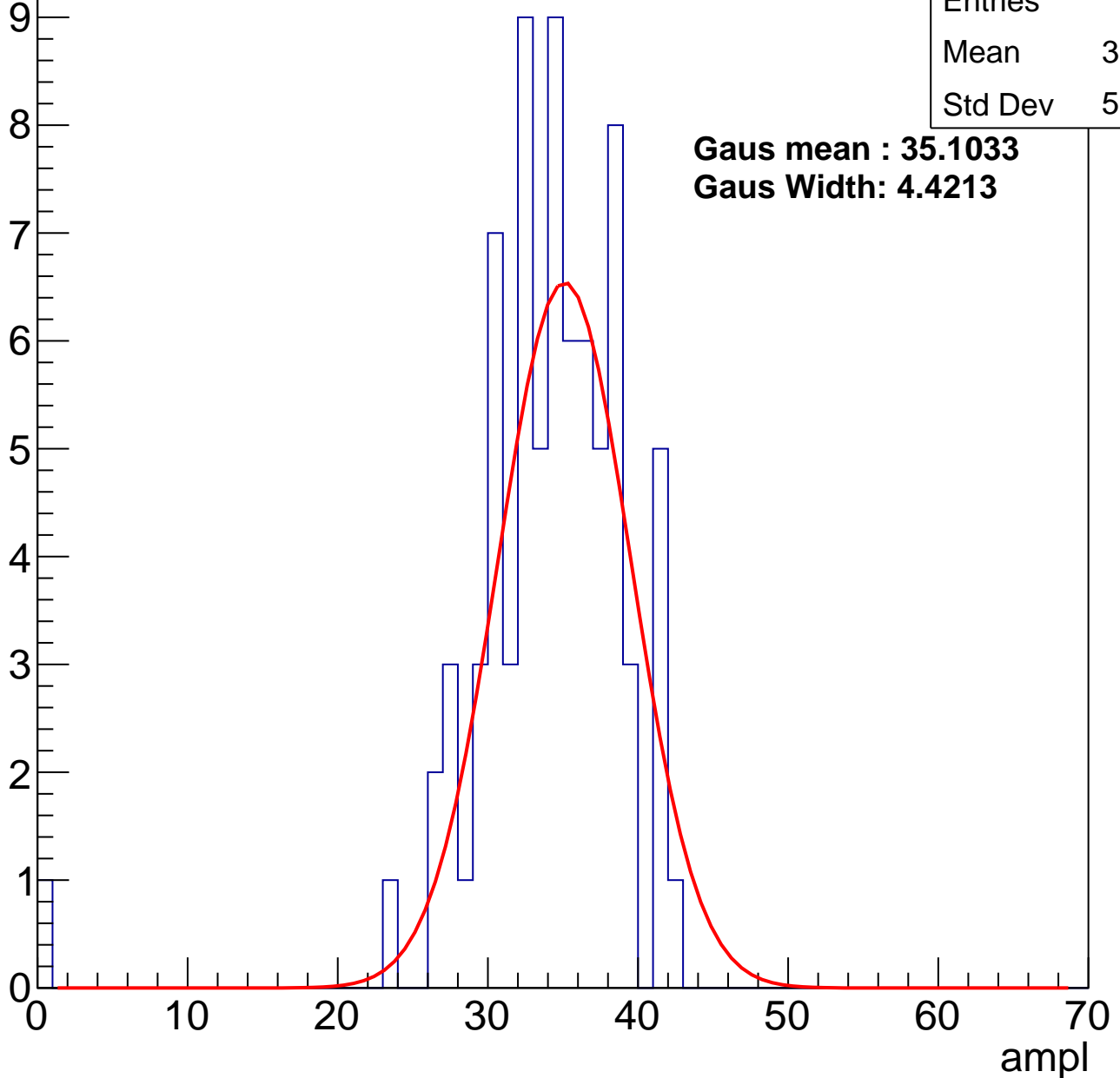
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.49
Std Dev	5.558

Gaus mean : 35.1033

Gaus Width: 4.4213



B1L103S, U6-ch116, adc2

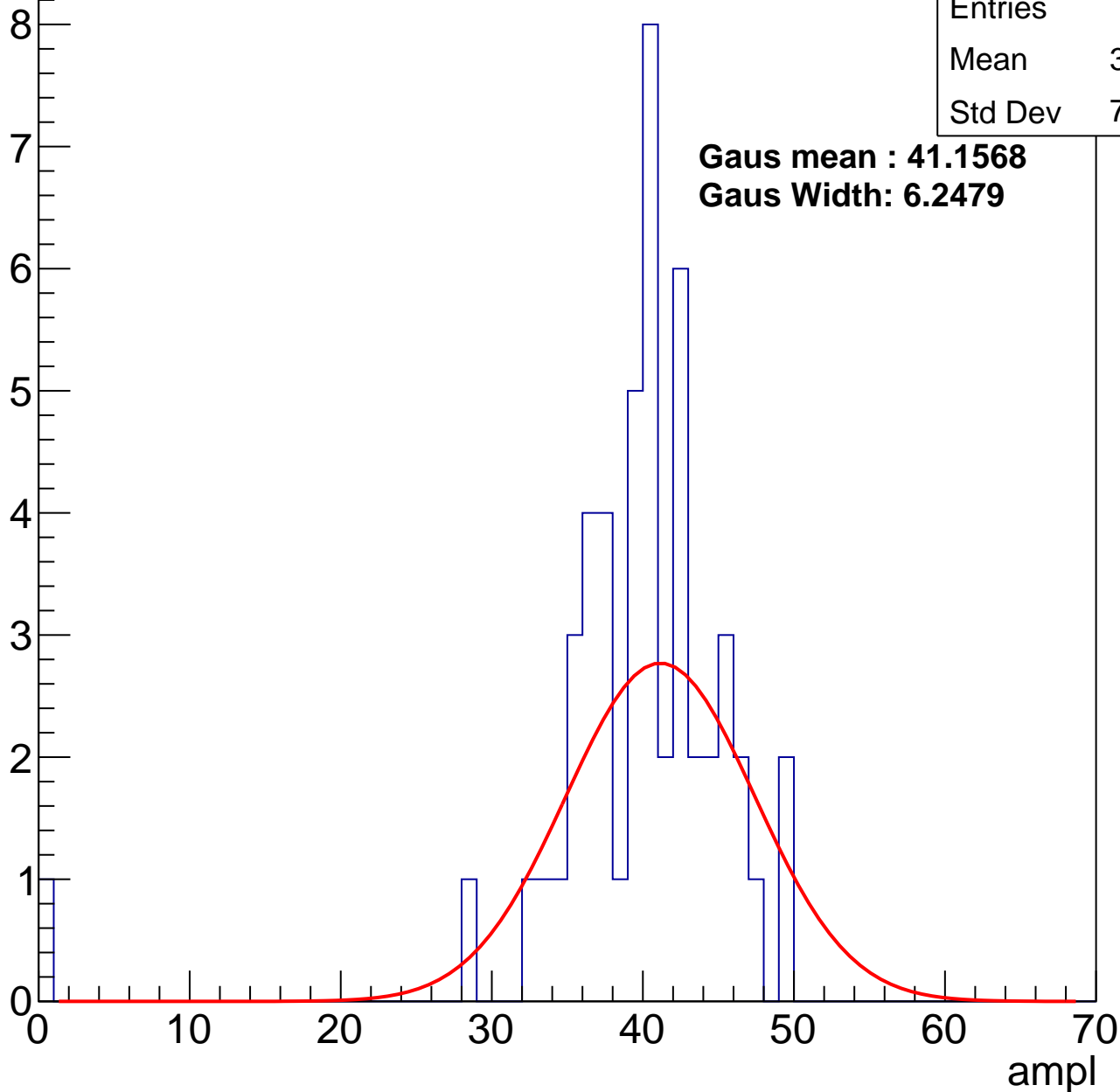
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	39.14
Std Dev	7.034

Gaus mean : 41.1568

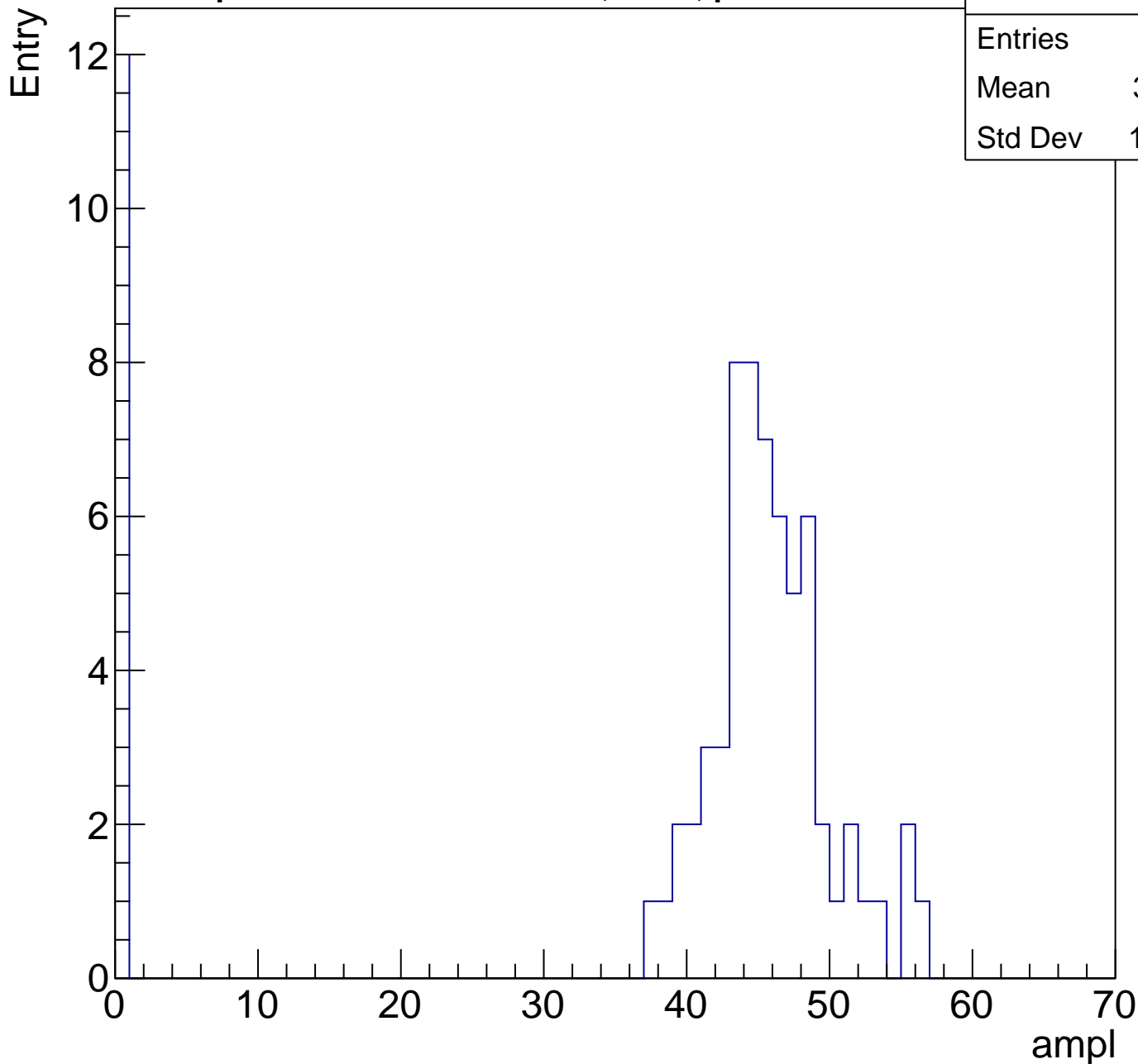
Gaus Width: 6.2479



B1L103S, U6-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	38.01
Std Dev	17.12

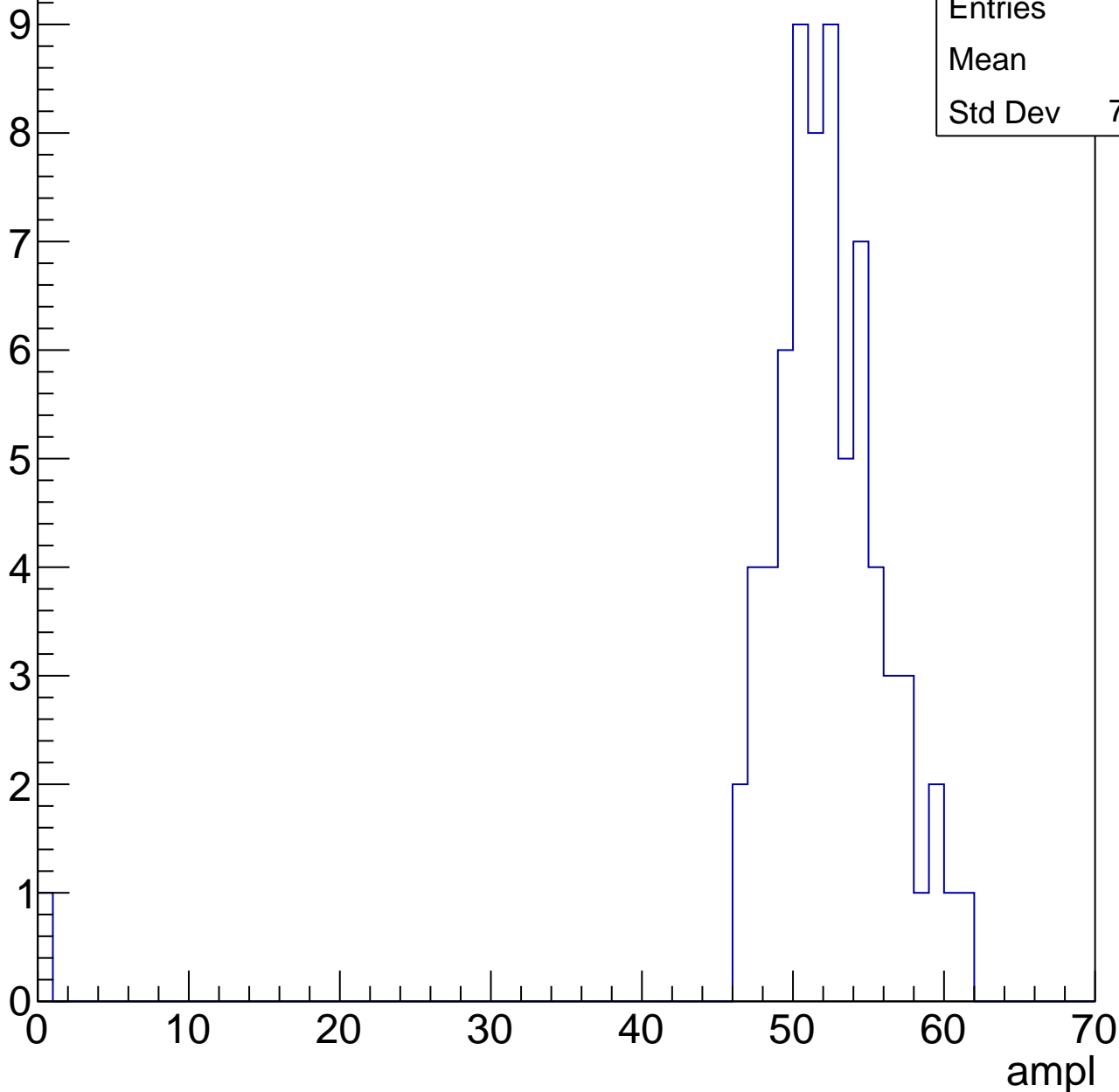


B1L103S, U6-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	51.3
Std Dev	7.045

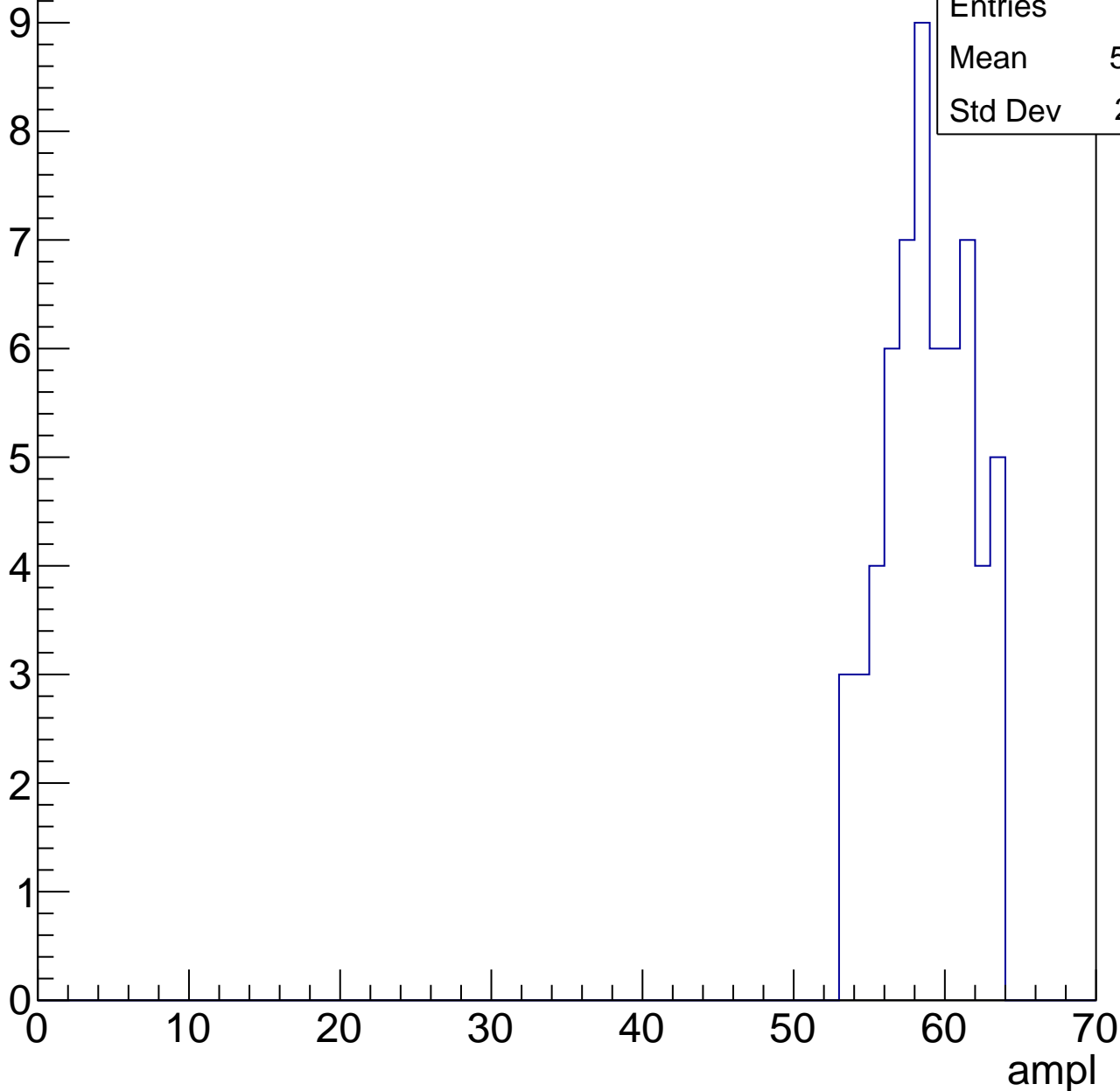


B1L103S, U6-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.37
Std Dev	2.781

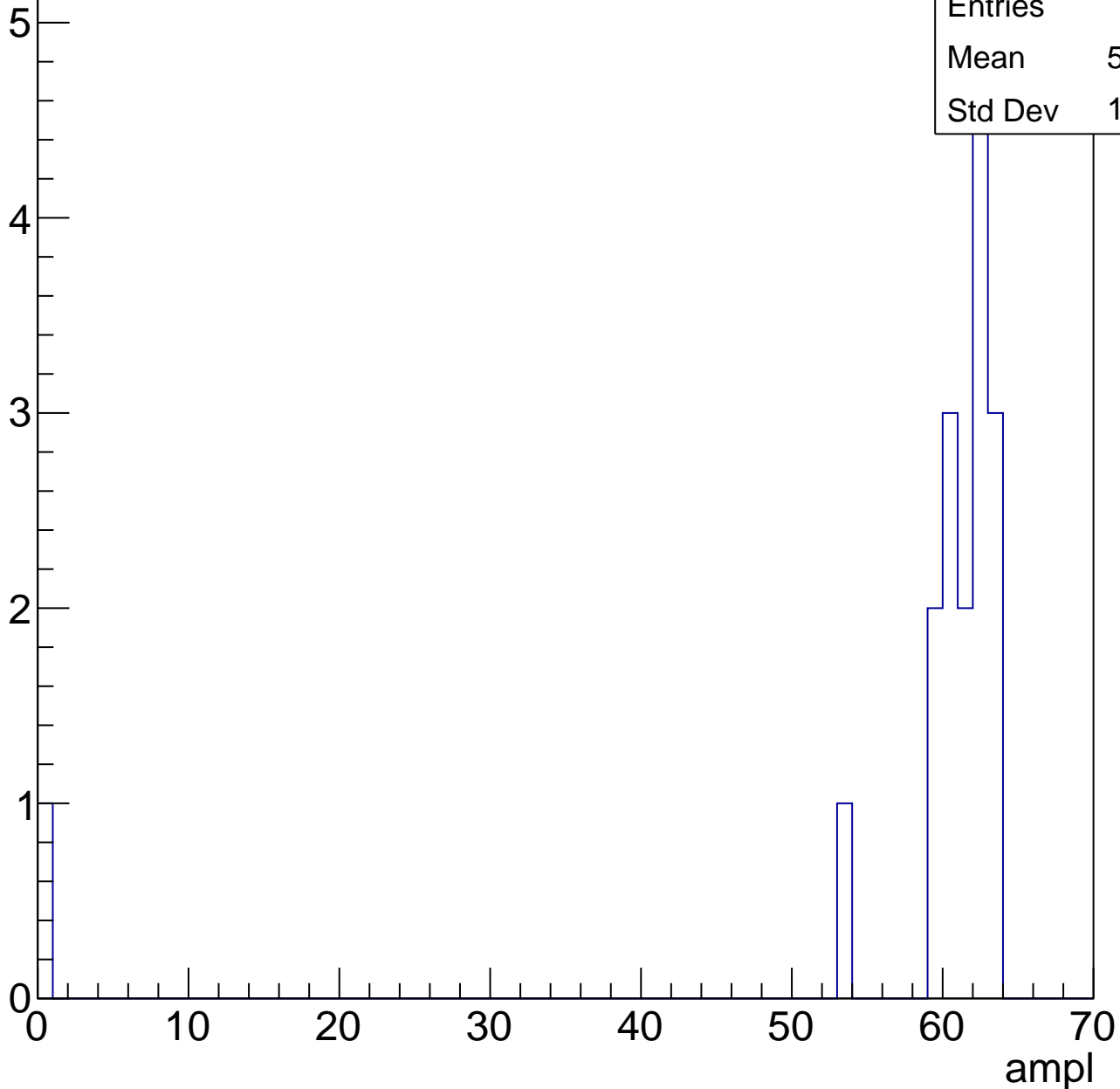


B1L103S, U6-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.18
Std Dev	14.48

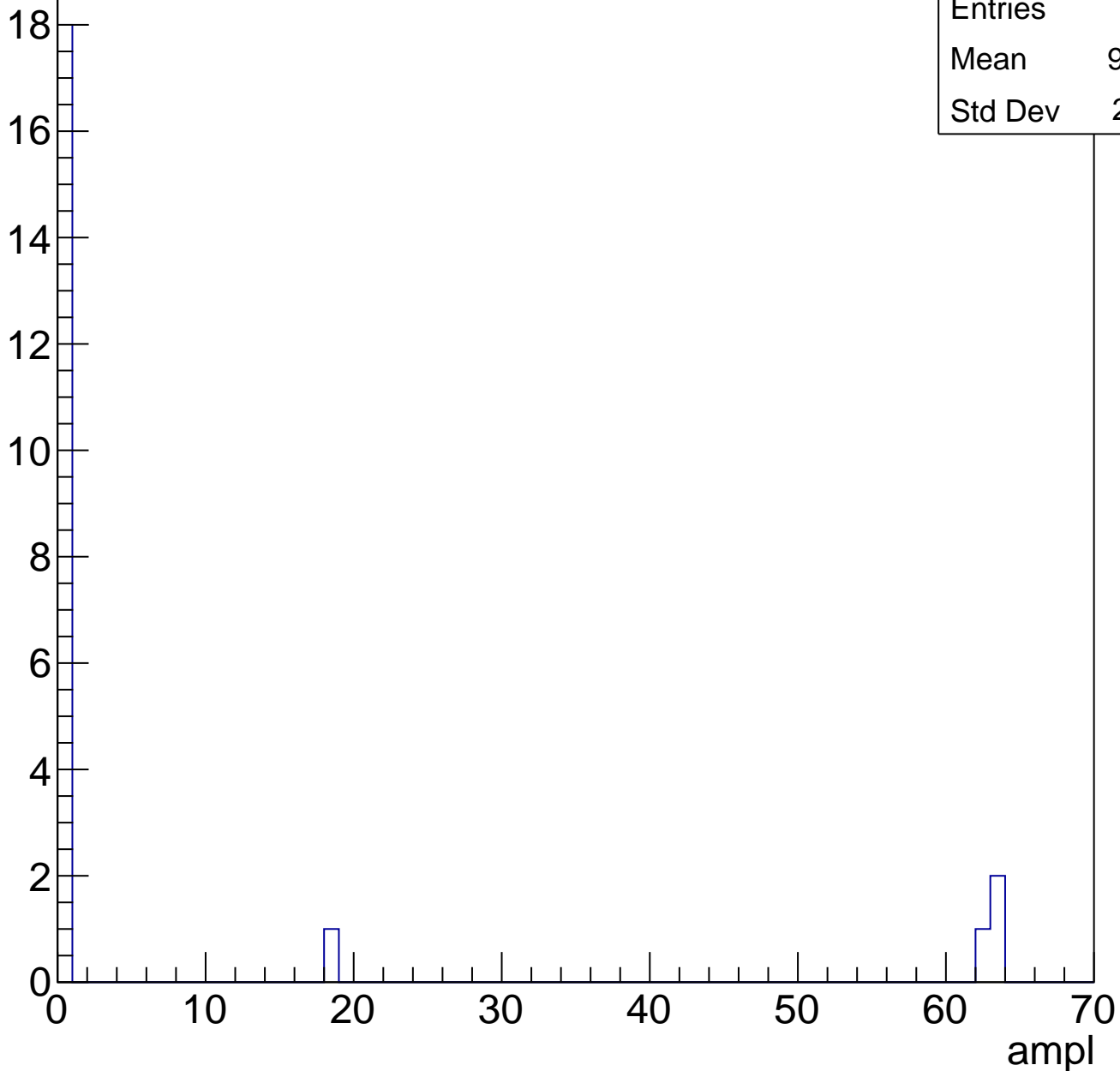


B1L103S, U6-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.364
Std Dev	21.51

Entry



B1L103S, U6-ch117, adc0

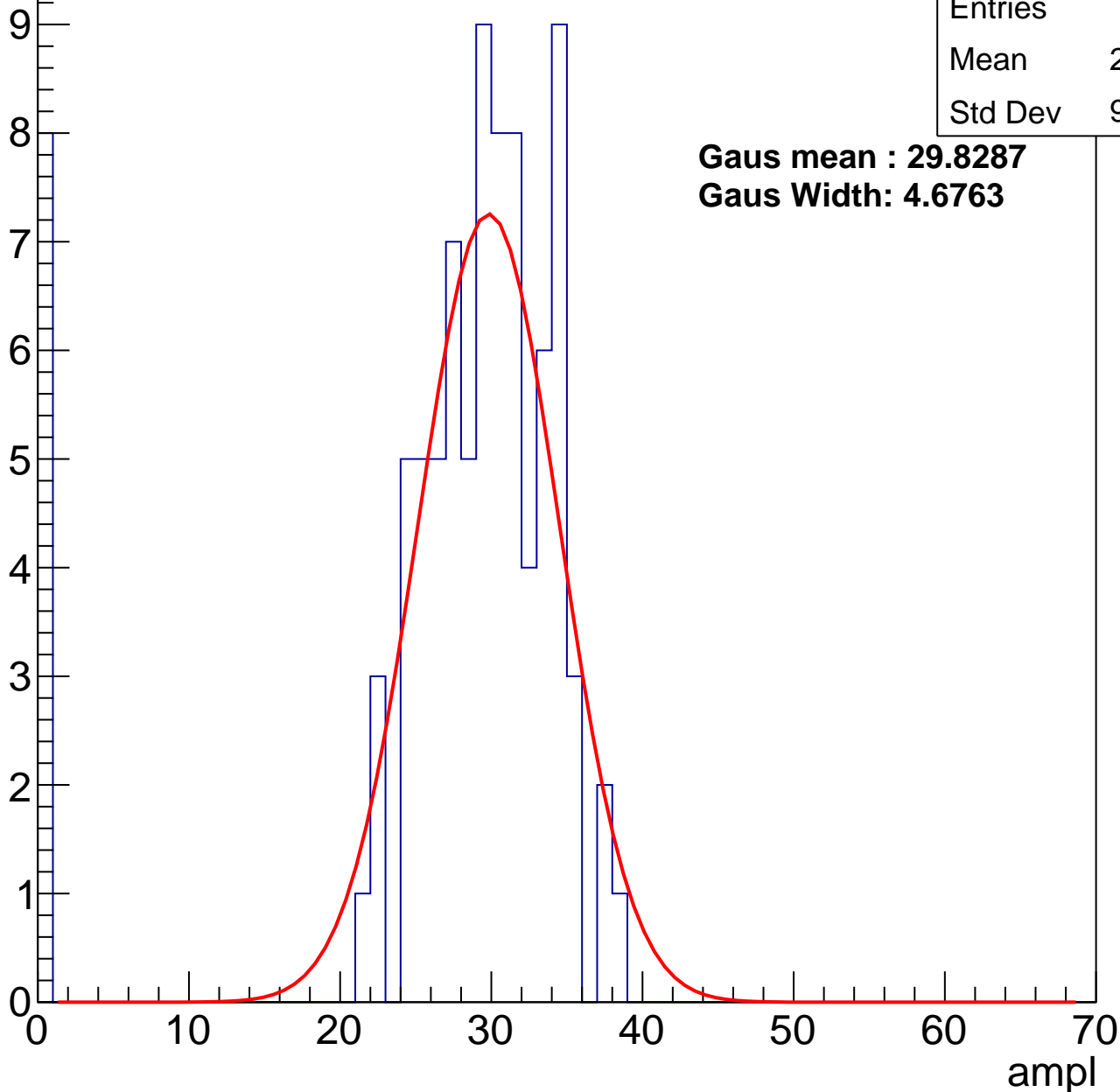
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	26.84
Std Dev	9.193

Gaus mean : 29.8287

Gaus Width: 4.6763



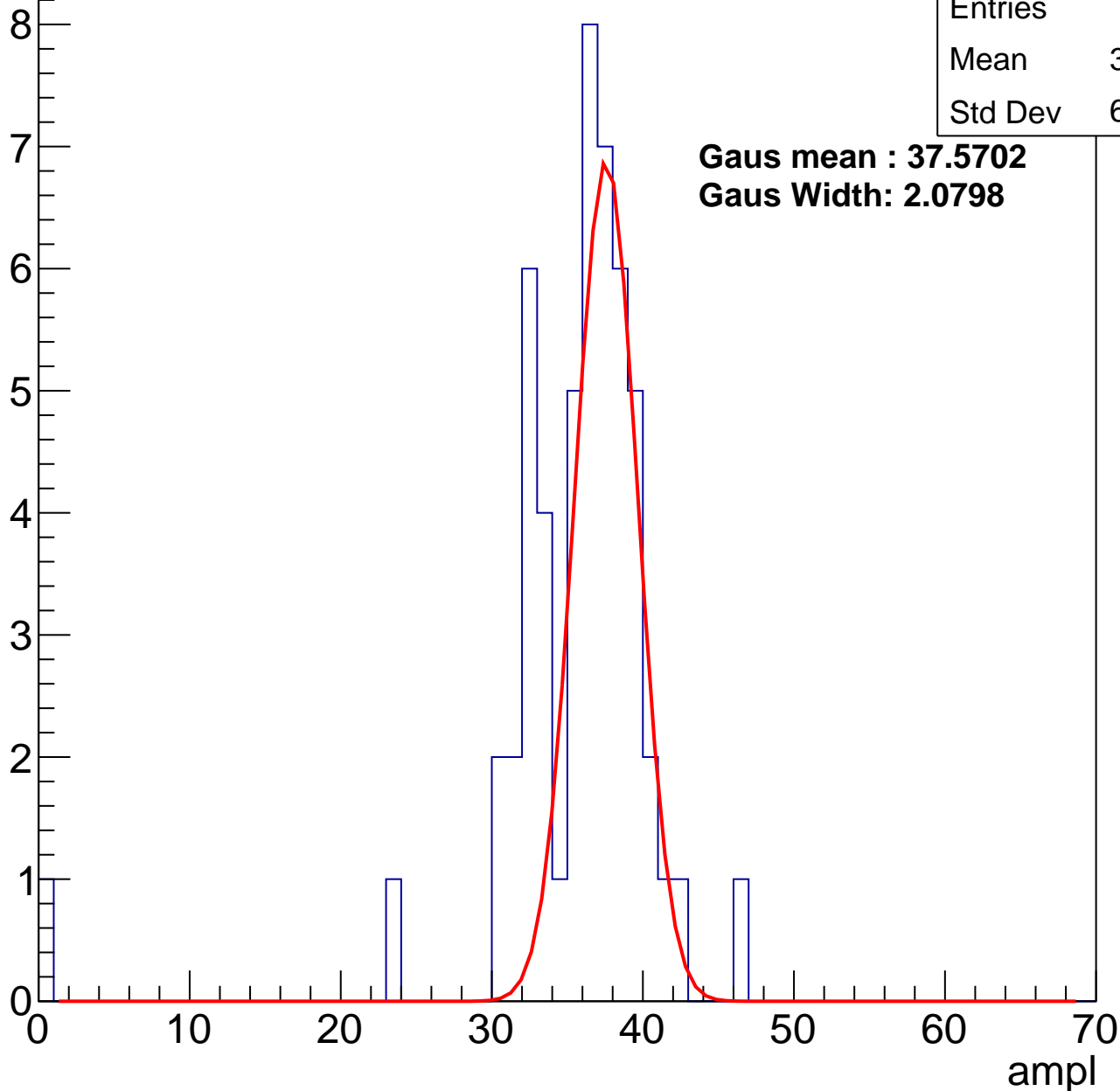
B1L103S, U6-ch117, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	35.04
Std Dev	6.062

Gaus mean : 37.5702
Gaus Width: 2.0798



B1L103S, U6-ch117, adc2

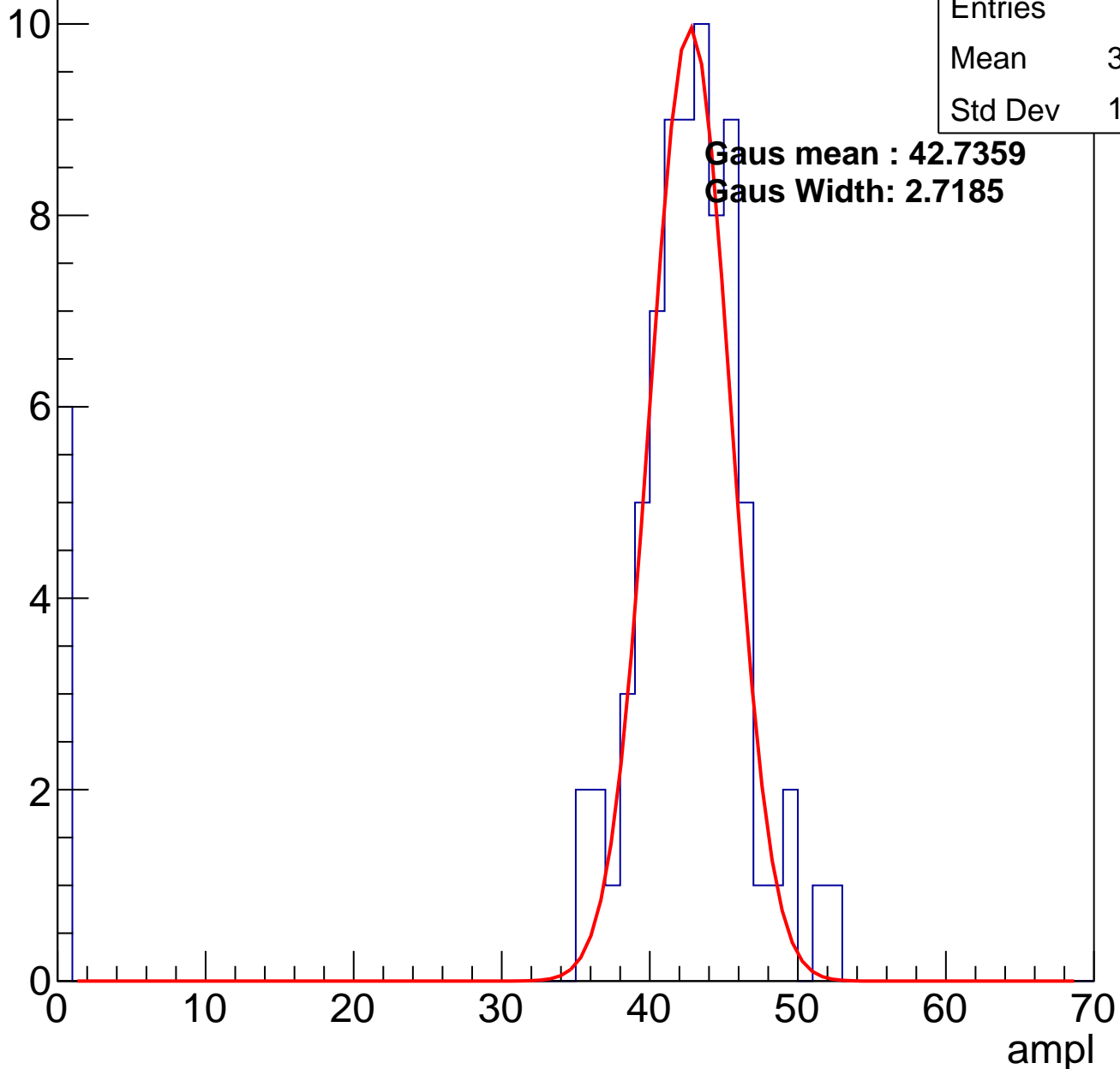
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	39.37
Std Dev	11.52

Gaus mean : 42.7359

Gaus Width: 2.7185

Entry

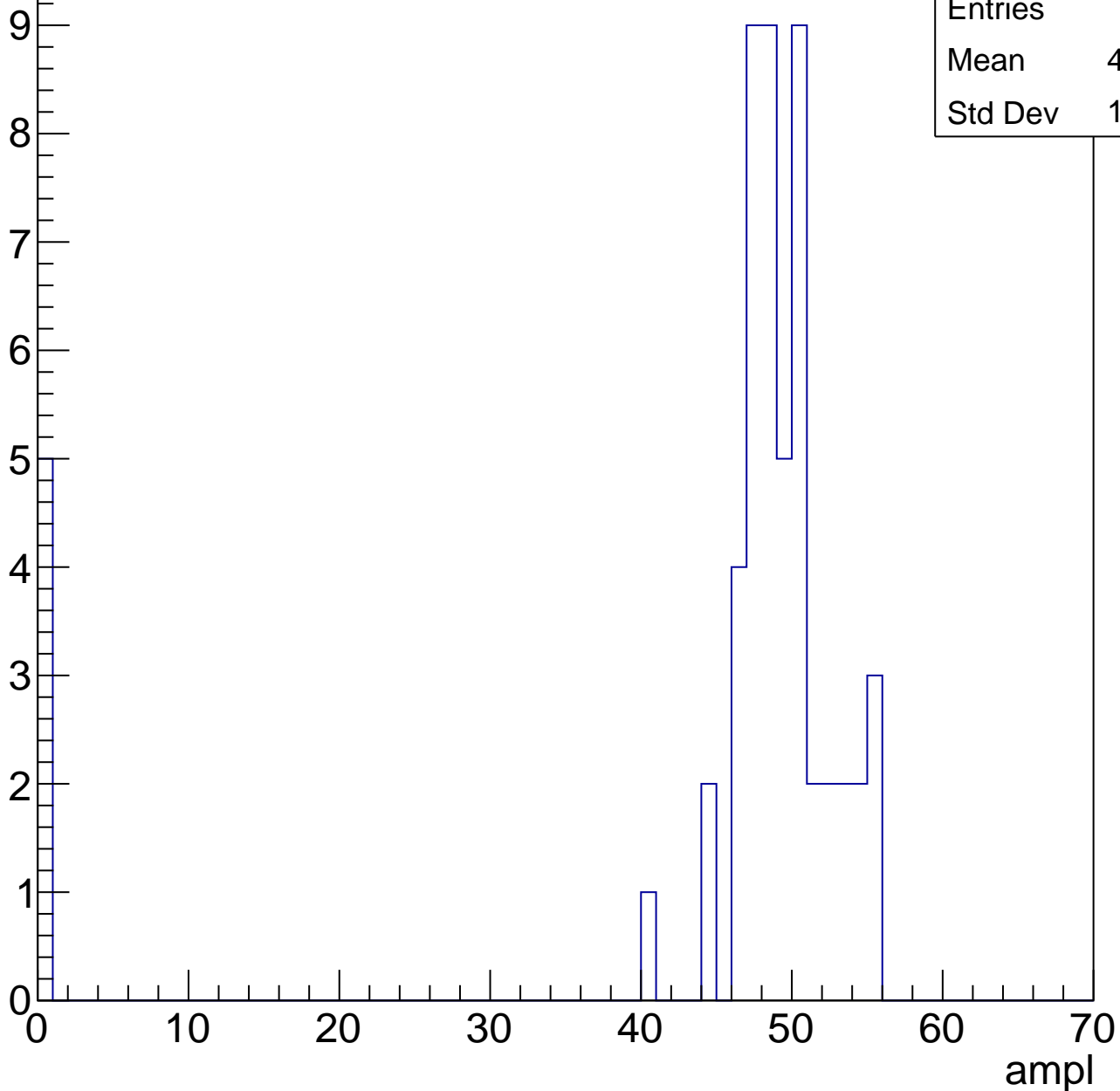


B1L103S, U6-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	44.49
Std Dev	14.35

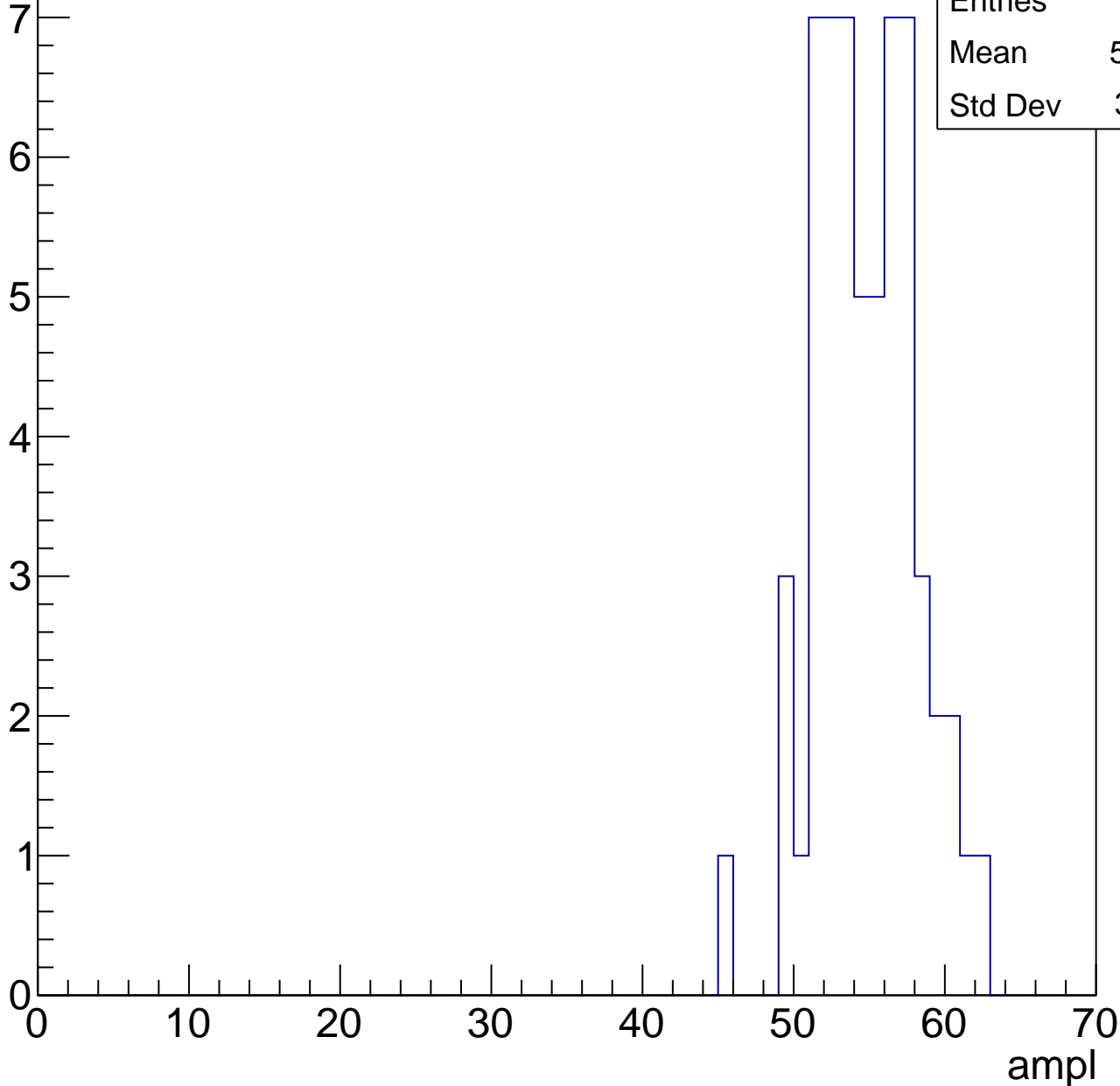


B1L103S, U6-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.32
Std Dev	3.301



B1L103S, U6-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries	48
Mean	58.98
Std Dev	2.618

ampl

0

10

20

30

40

50

60

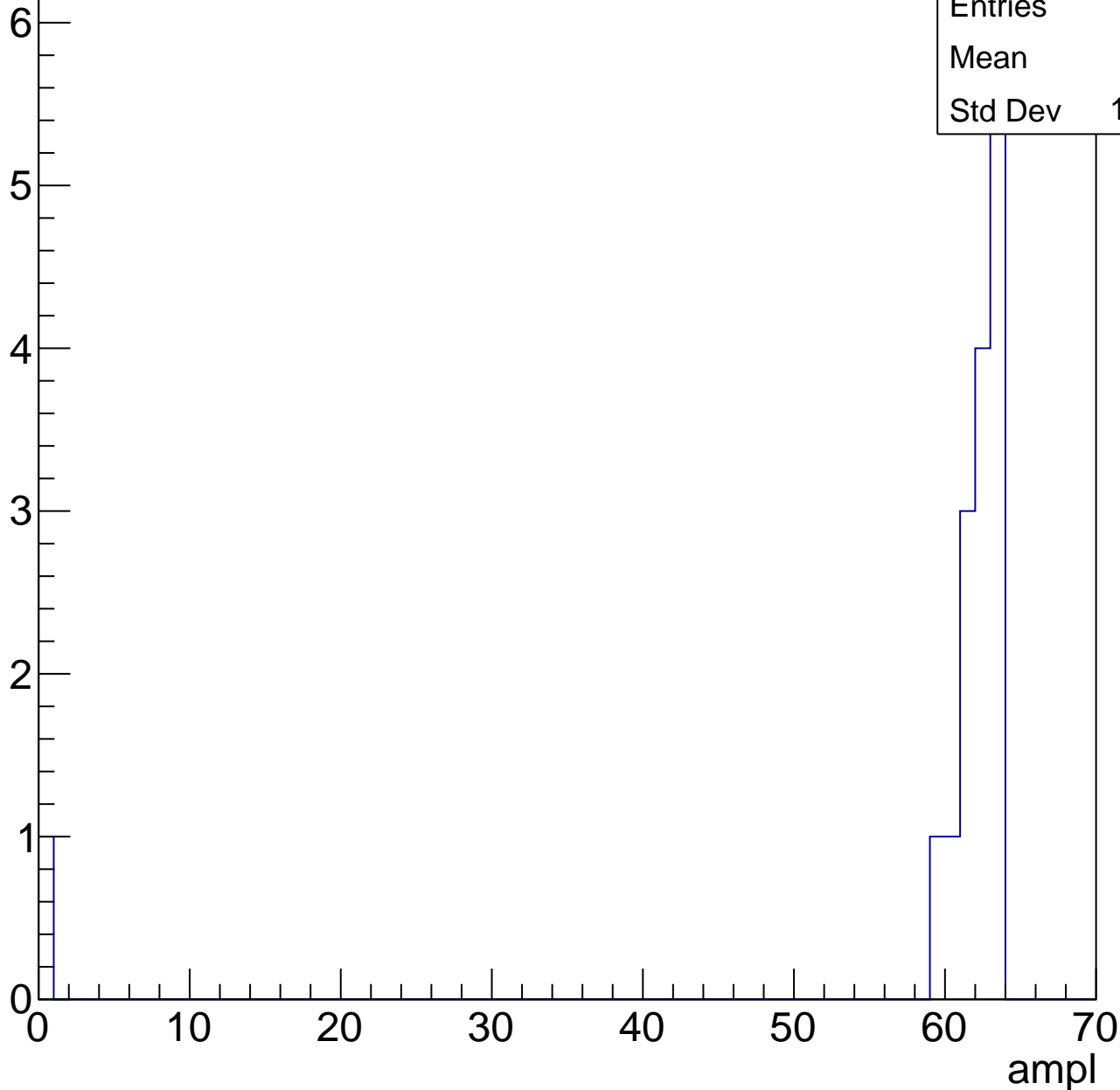
70

B1L103S, U6-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	58
Std Dev	15.02



B1L103S, U6-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch118, adc0

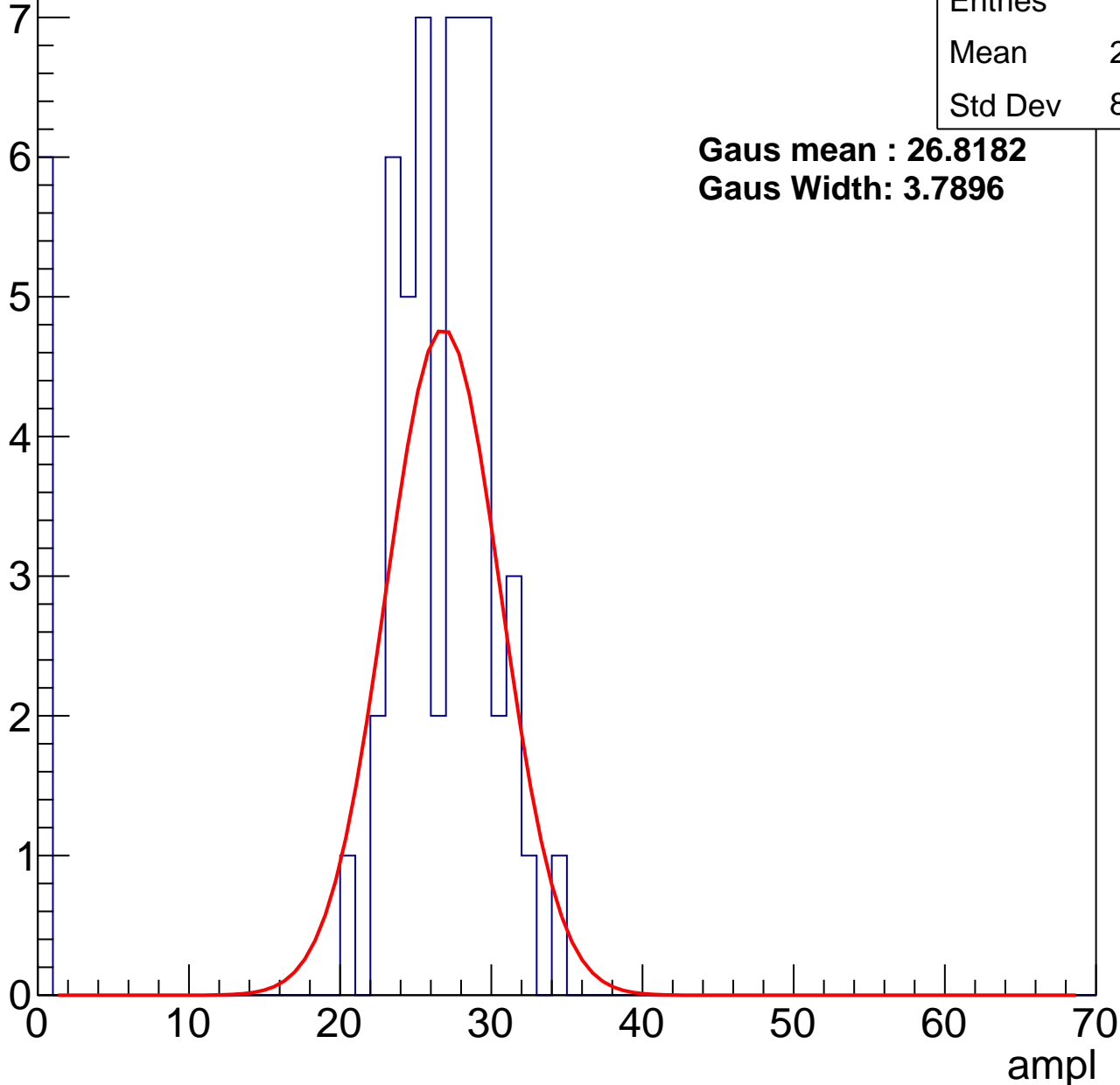
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	23.79
Std Dev	8.616

Gaus mean : 26.8182

Gaus Width: 3.7896



B1L103S, U6-ch118, adc1

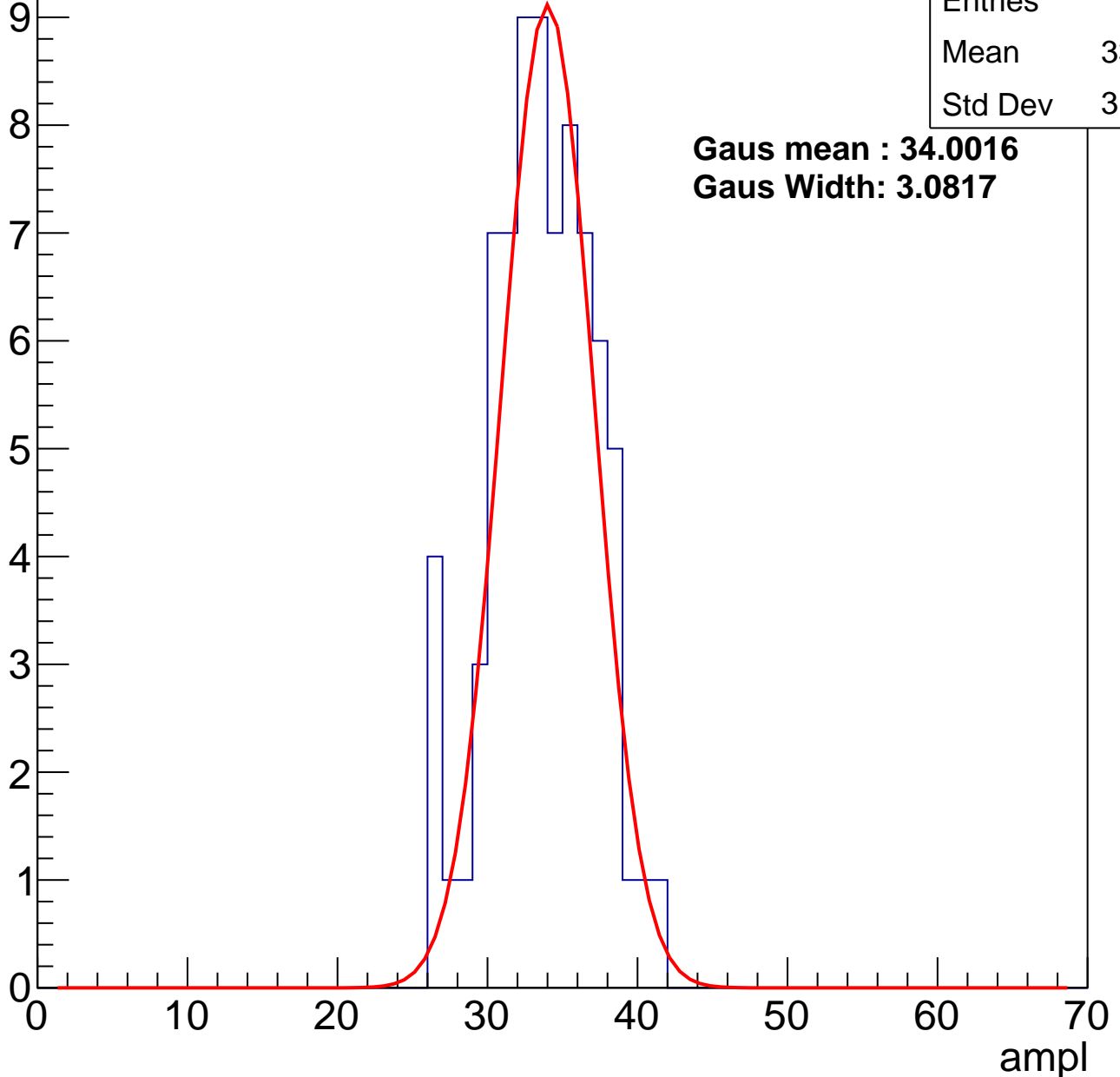
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	33.25
Std Dev	3.366

Gaus mean : 34.0016

Gaus Width: 3.0817



B1L103S, U6-ch118, adc2

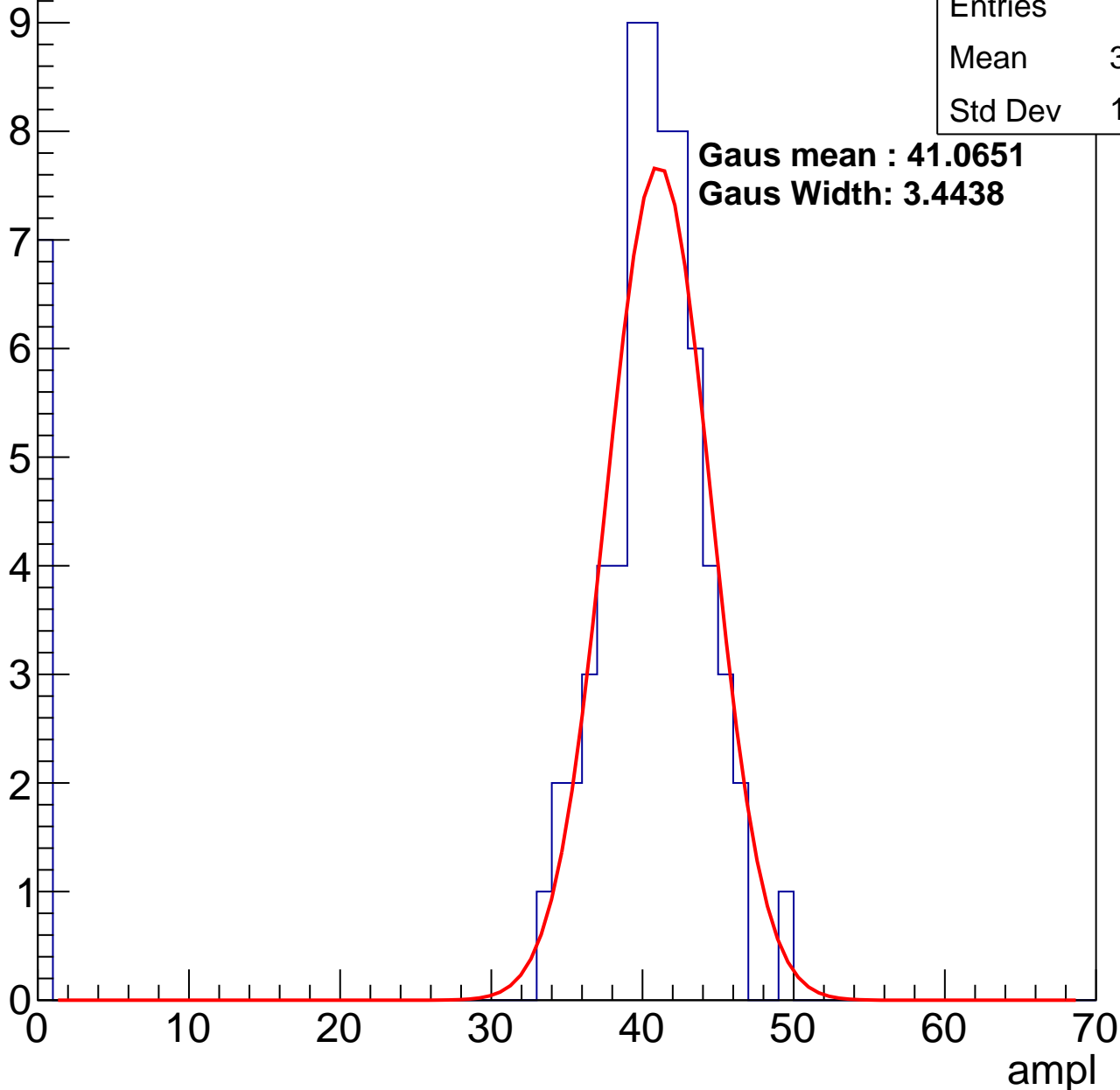
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.49
Std Dev	12.26

Gaus mean : 41.0651

Gaus Width: 3.4438

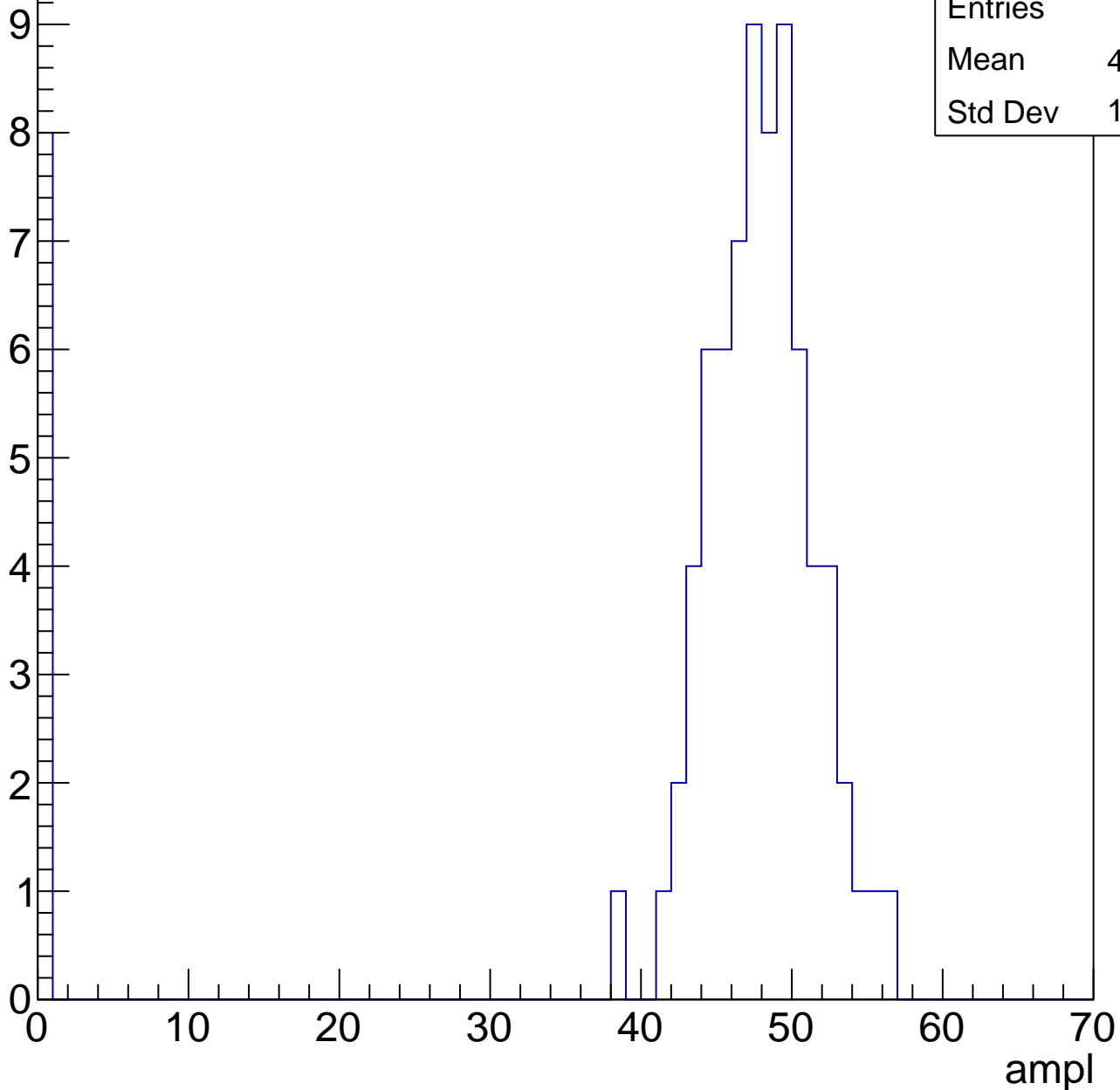


B1L103S, U6-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	42.77
Std Dev	14.62

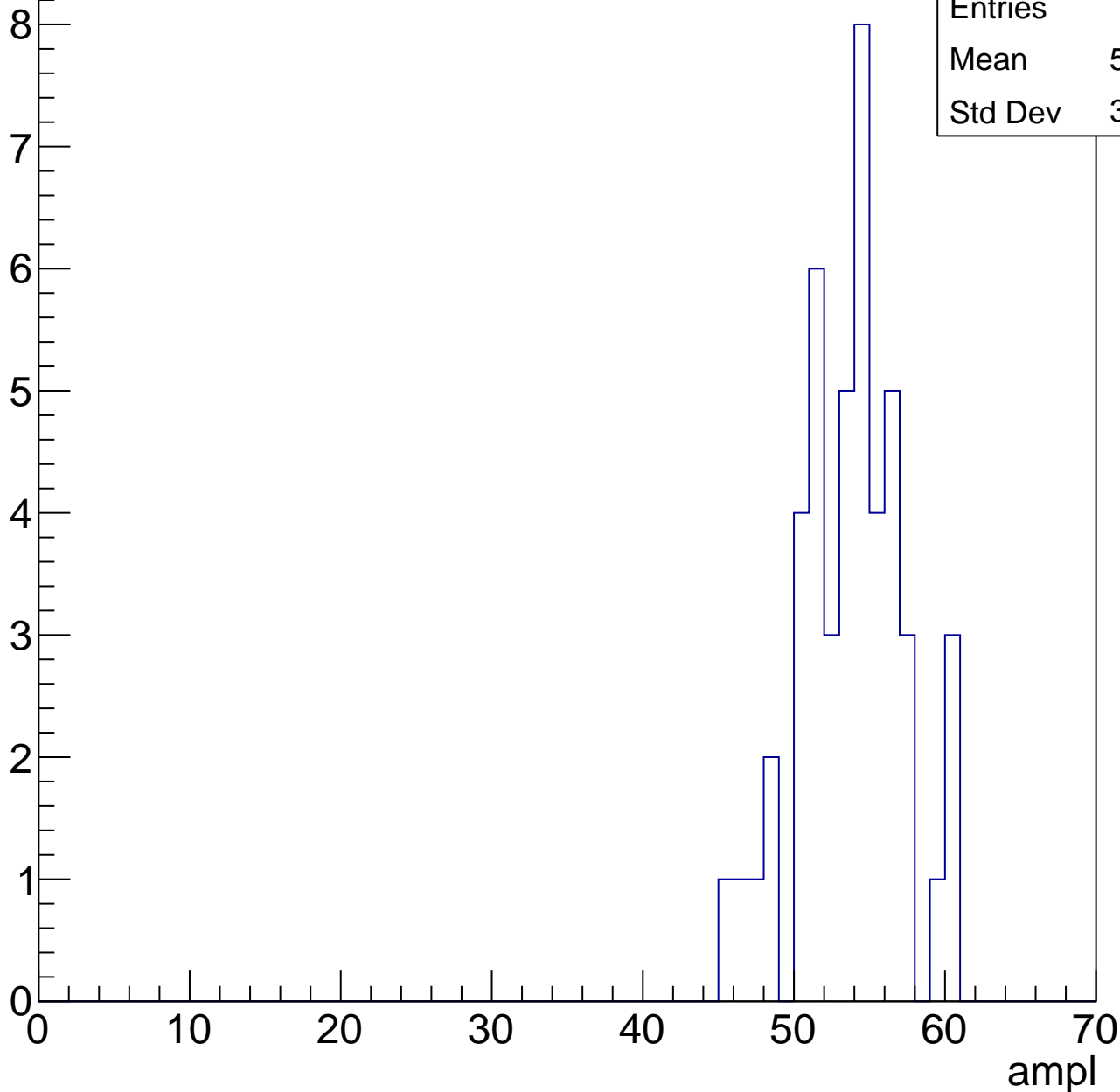


B1L103S, U6-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

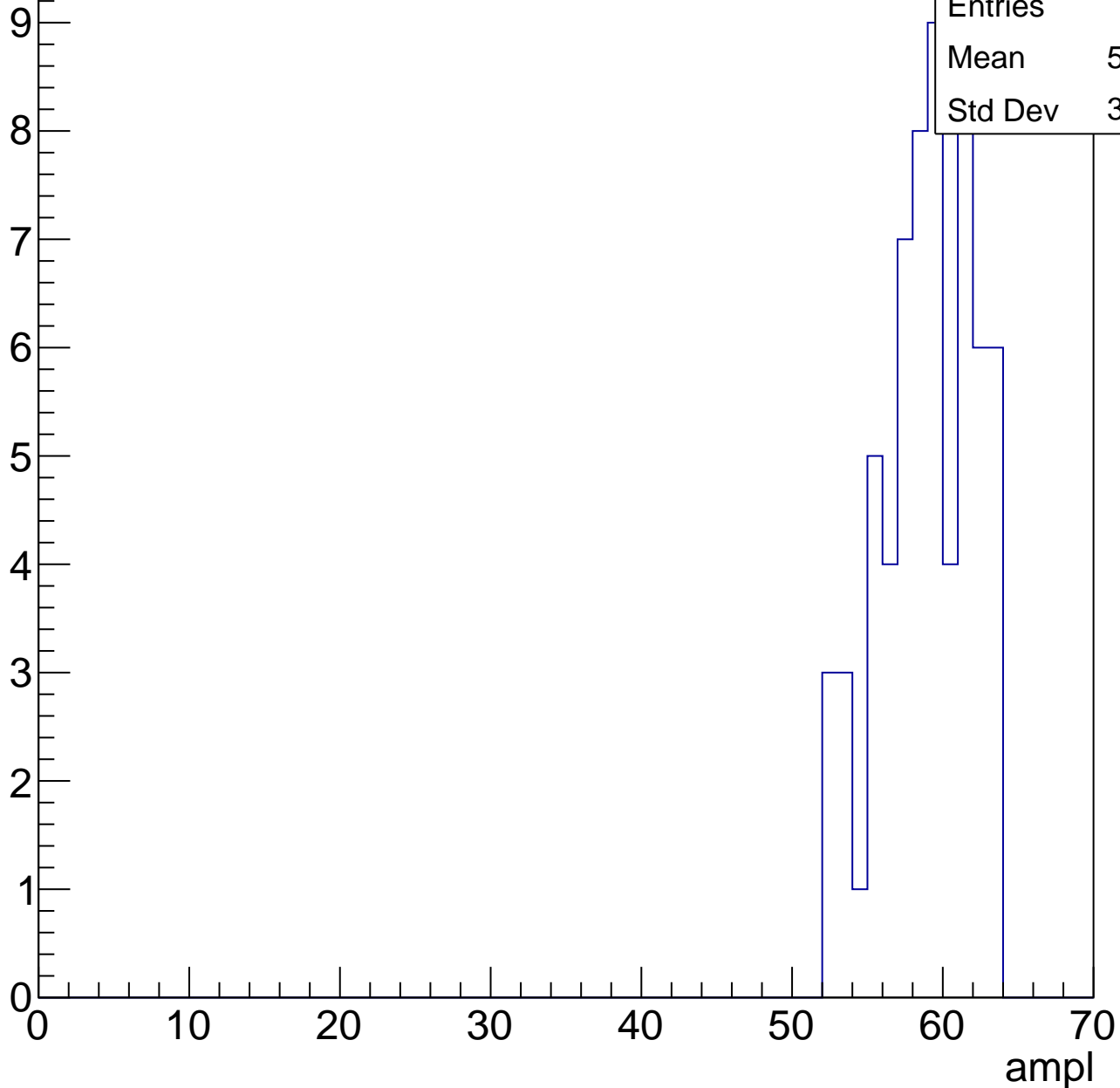
Entries	47
Mean	53.26
Std Dev	3.449



B1L103S, U6-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

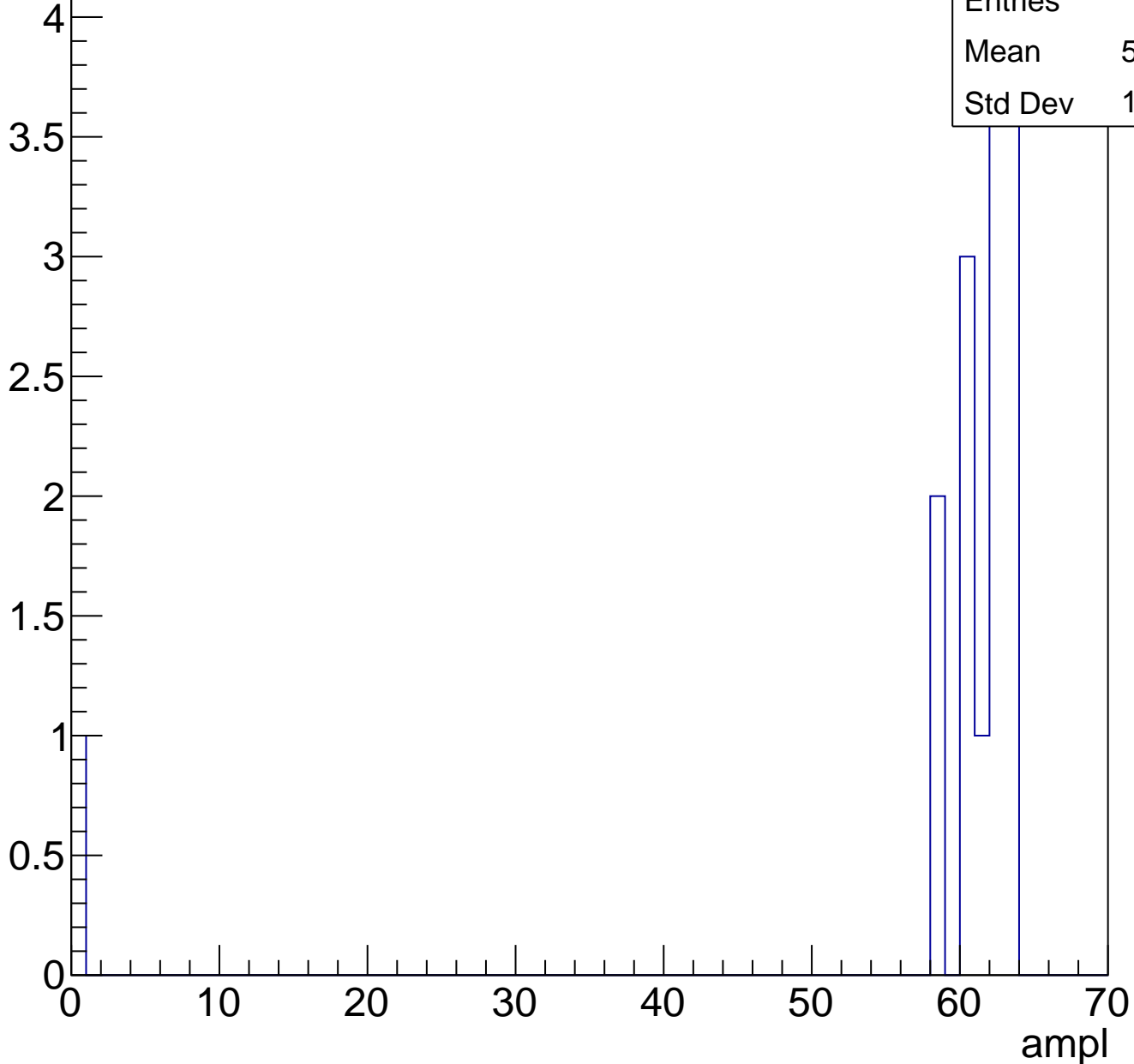
Entry



B1L103S, U6-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

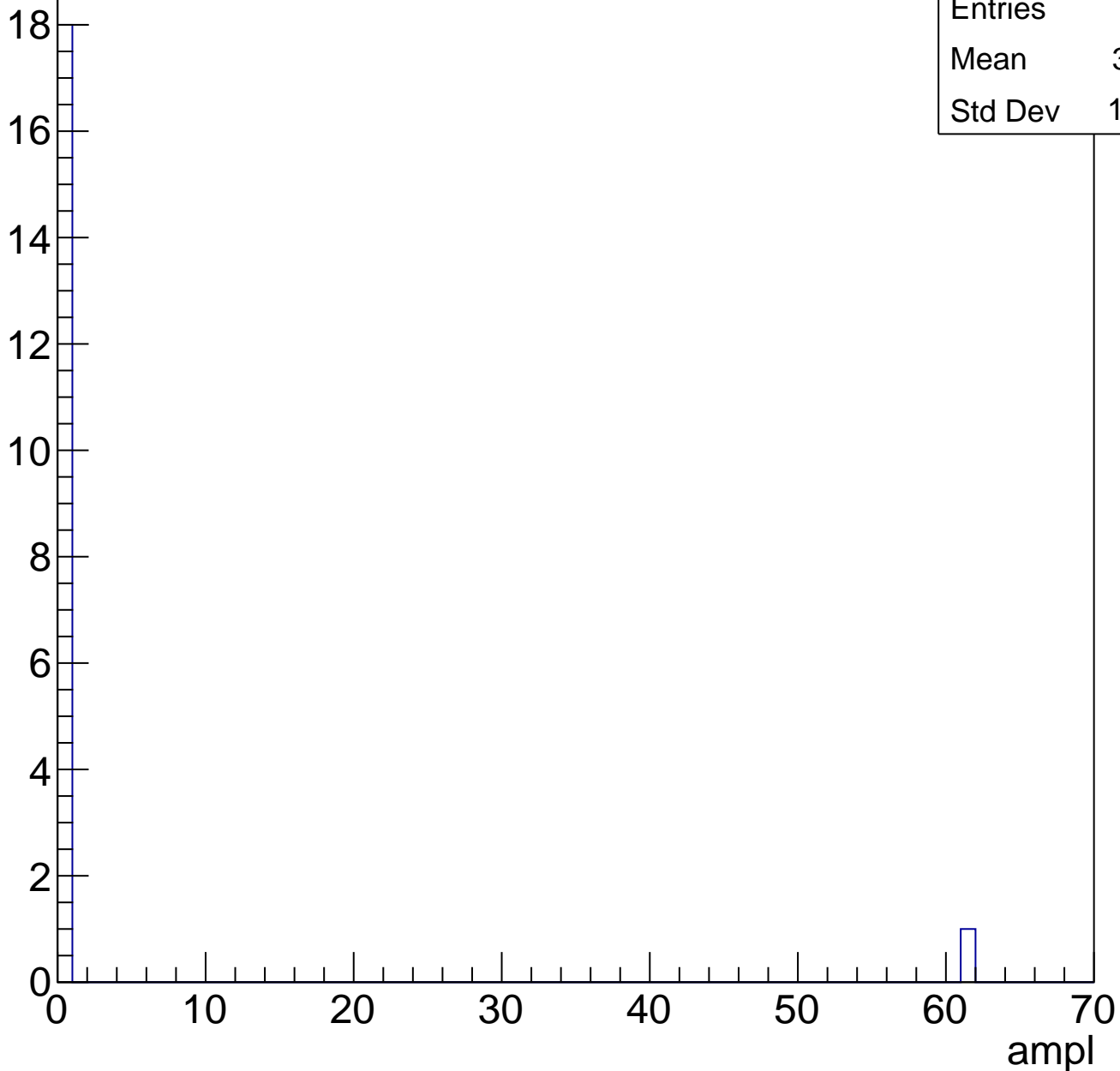


B1L103S, U6-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62

Entry



B1L103S, U6-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	23.12
Std Dev	11.67

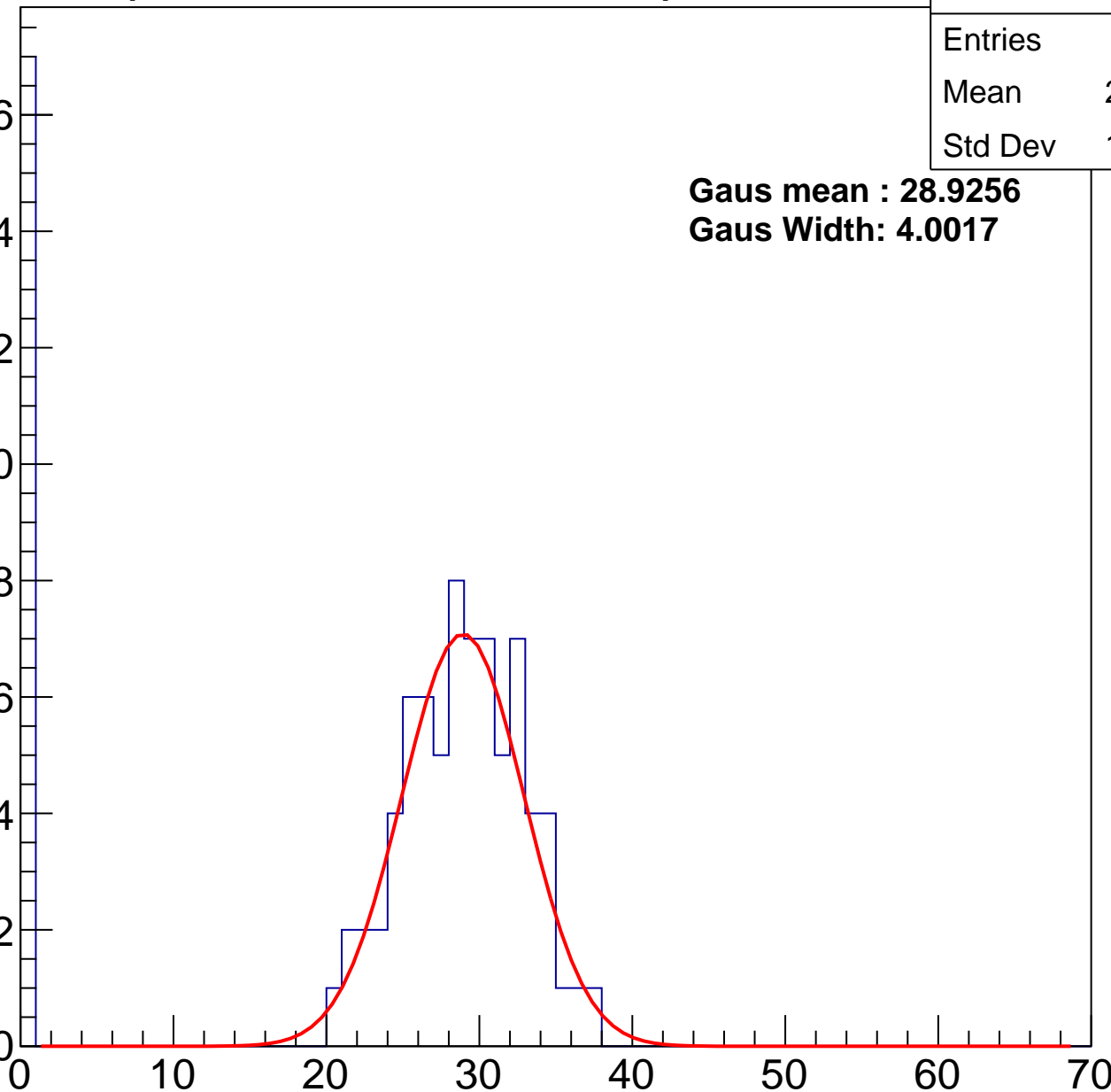
Gaus mean : 28.9256

Gaus Width: 4.0017

Entry

16
14
12
10
8
6
4
2
0

ampl



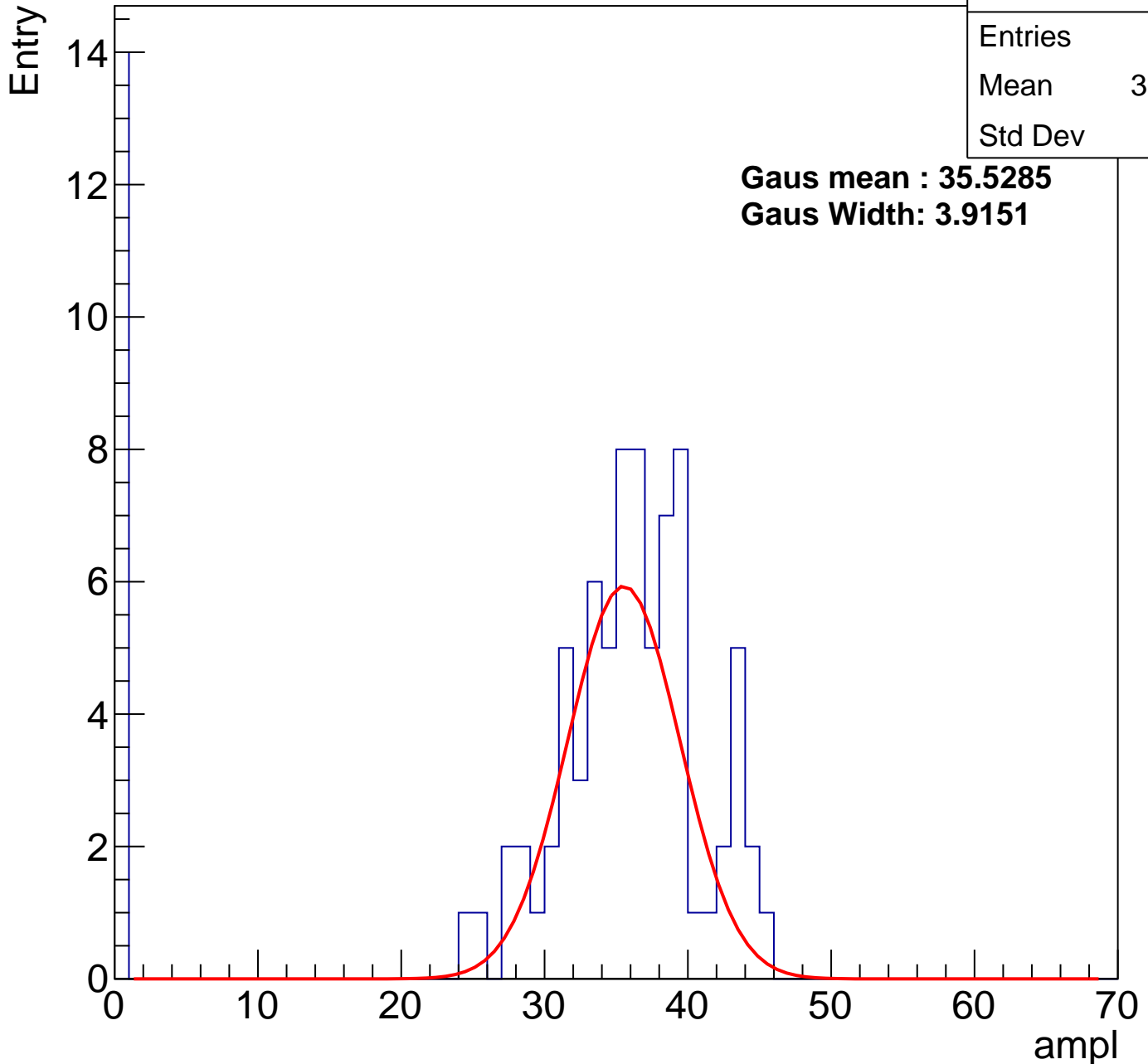
B1L103S, U6-ch119, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	30.12
Std Dev	13.6

Gaus mean : 35.5285

Gaus Width: 3.9151



B1L103S, U6-ch119, adc2

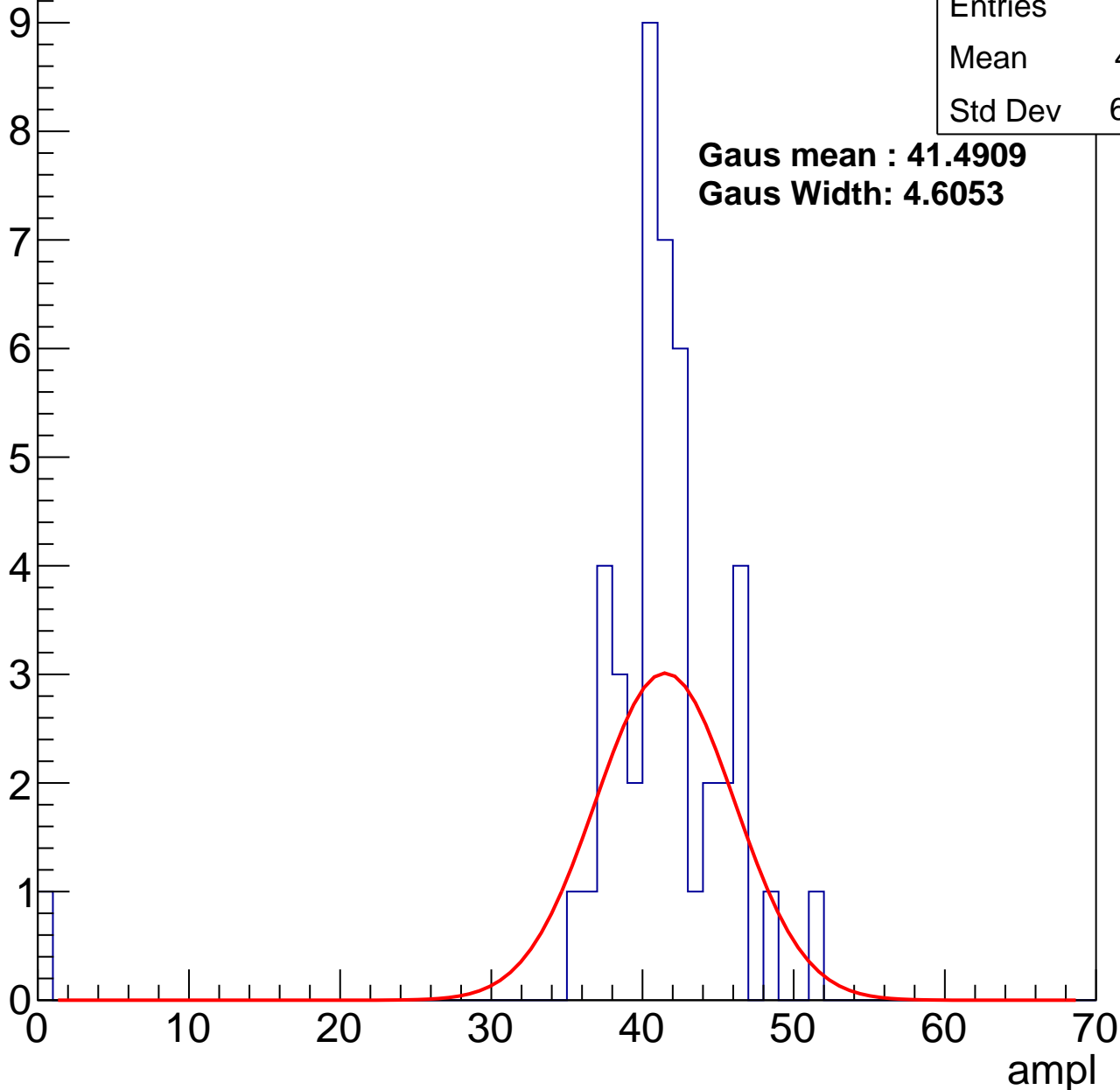
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	40.31
Std Dev	6.892

Gaus mean : 41.4909

Gaus Width: 4.6053



B1L103S, U6-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

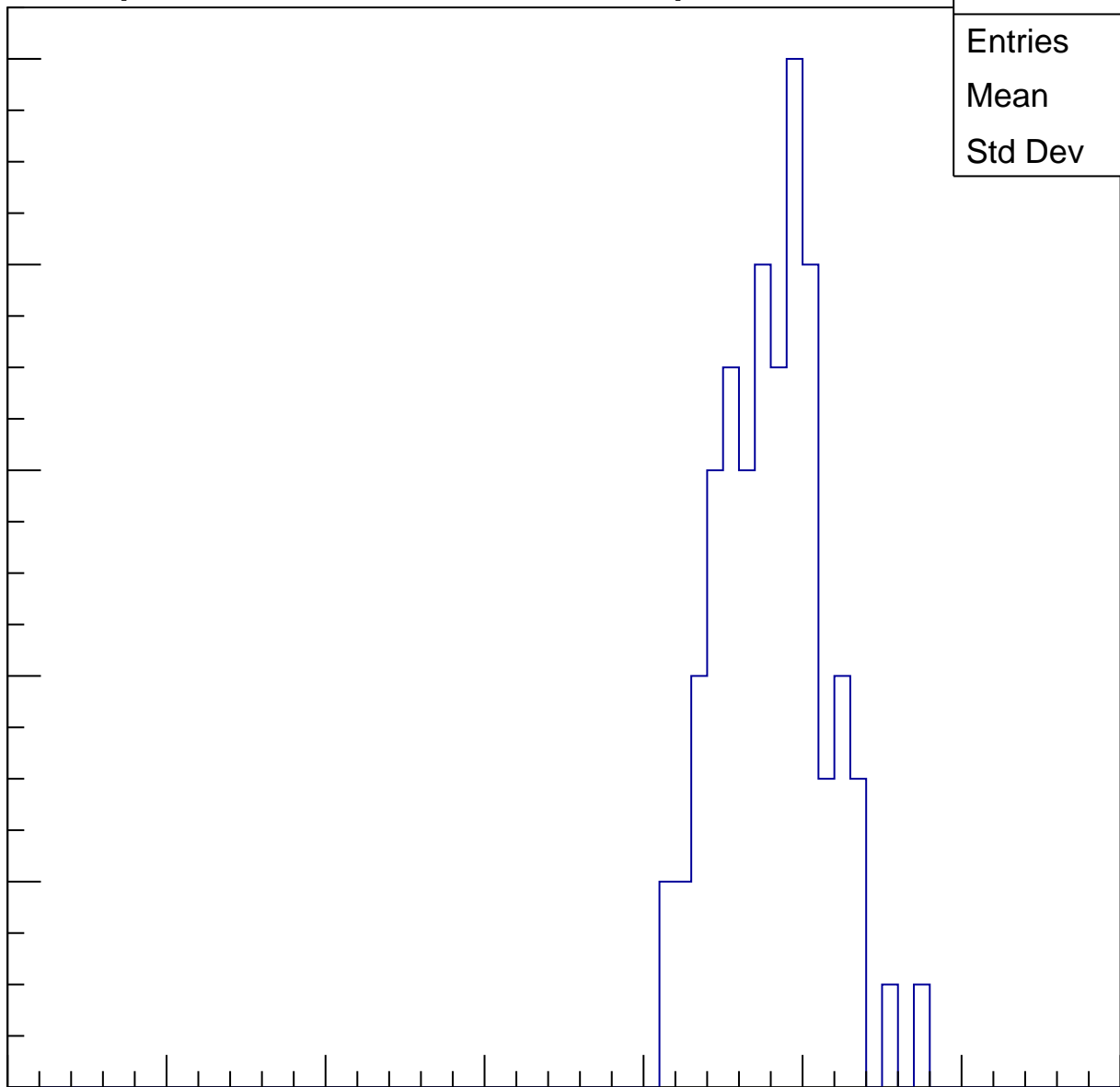
Entries	72
Mean	47.6
Std Dev	3.307

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

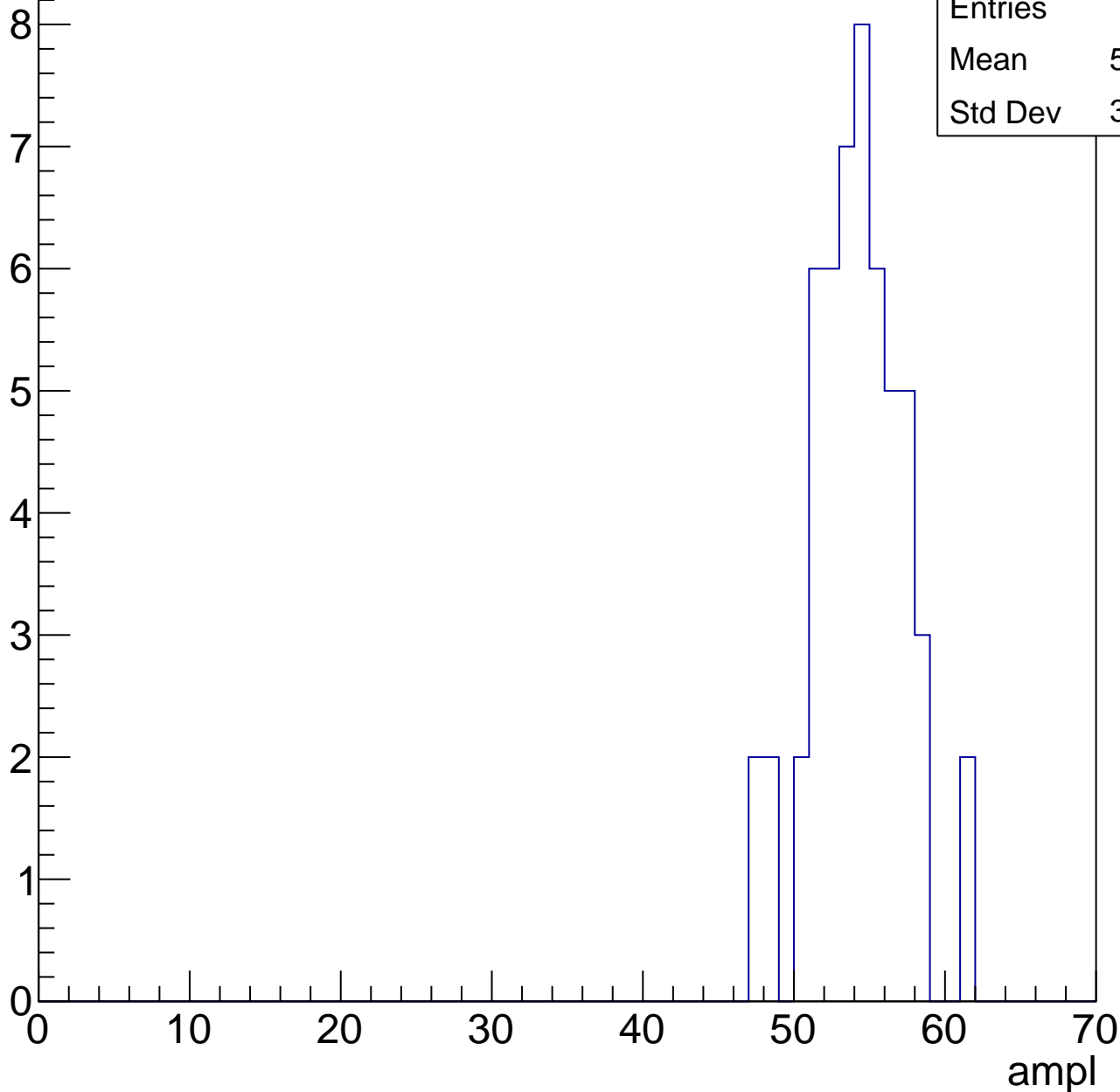


B1L103S, U6-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.74
Std Dev	3.044



B1L103S, U6-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	58.25
Std Dev	8.027

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L103S, U6-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	61.5
Std Dev	1.628

ampl

0 10 20 30 40 50 60 70

0 0.5 1 1.5 2 2.5 3 3.5 4

B1L103S, U6-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U6-ch120, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	21.47
Std Dev	12.05

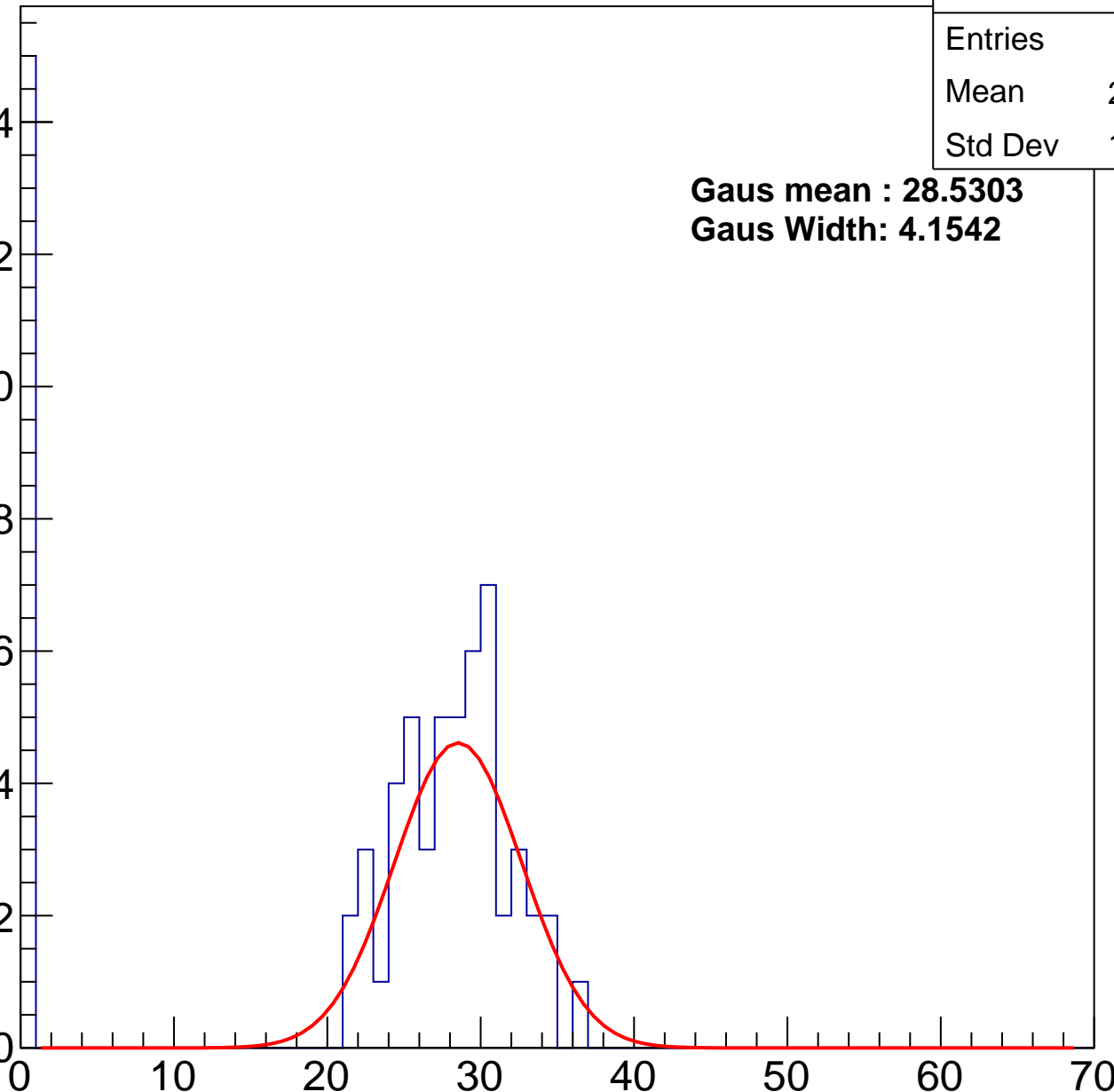
Gaus mean : 28.5303

Gaus Width: 4.1542

Entry

14
12
10
8
6
4
2
0

ampl



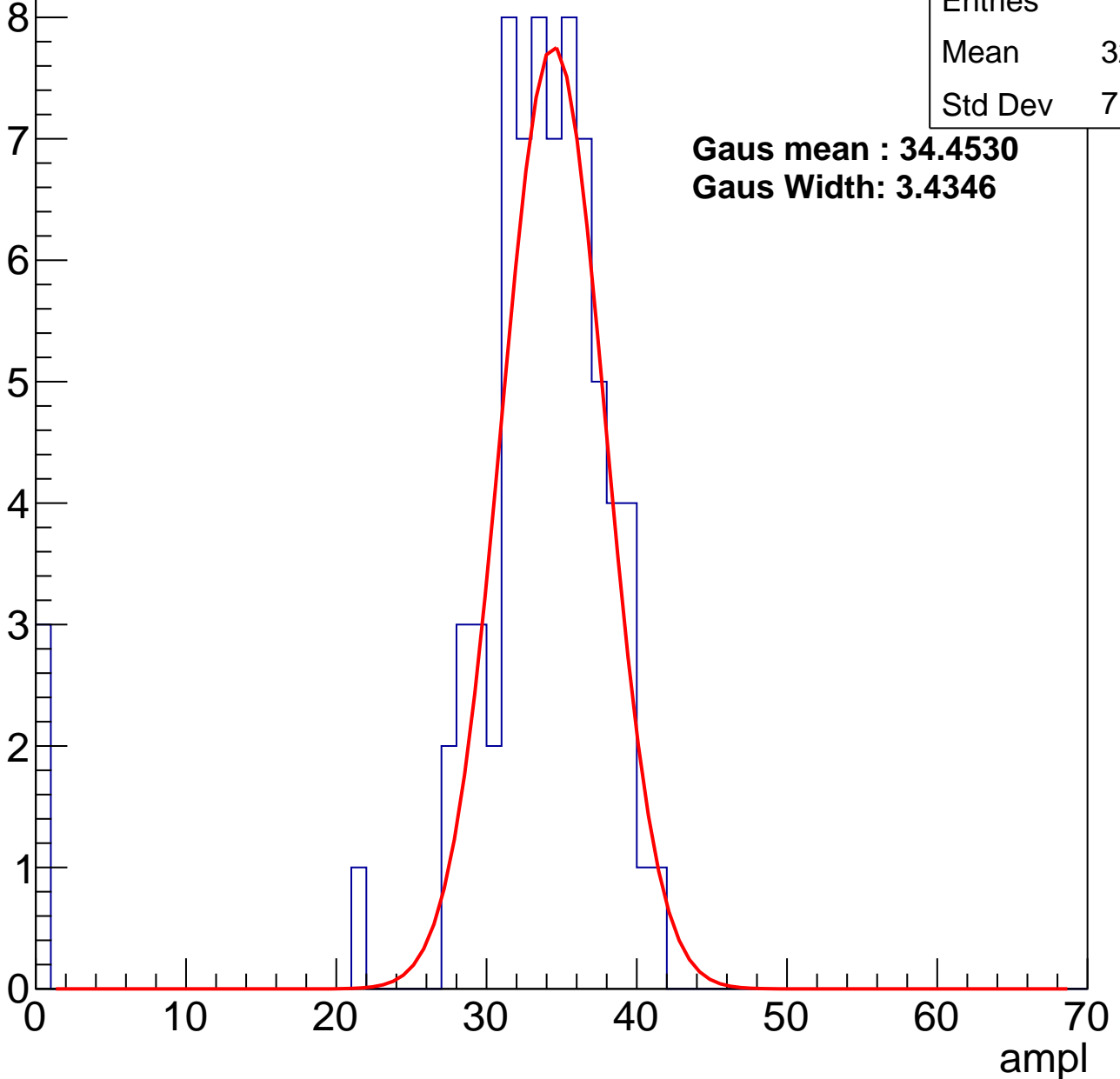
B1L103S, U6-ch120, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.24
Std Dev	7.497

Gaus mean : 34.4530
Gaus Width: 3.4346



B1L103S, U6-ch120, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	33.17
Std Dev	16.11

Gaus mean : 40.8444

Gaus Width: 3.2472

Entry

12

10

8

6

4

2

0

0

10

20

30

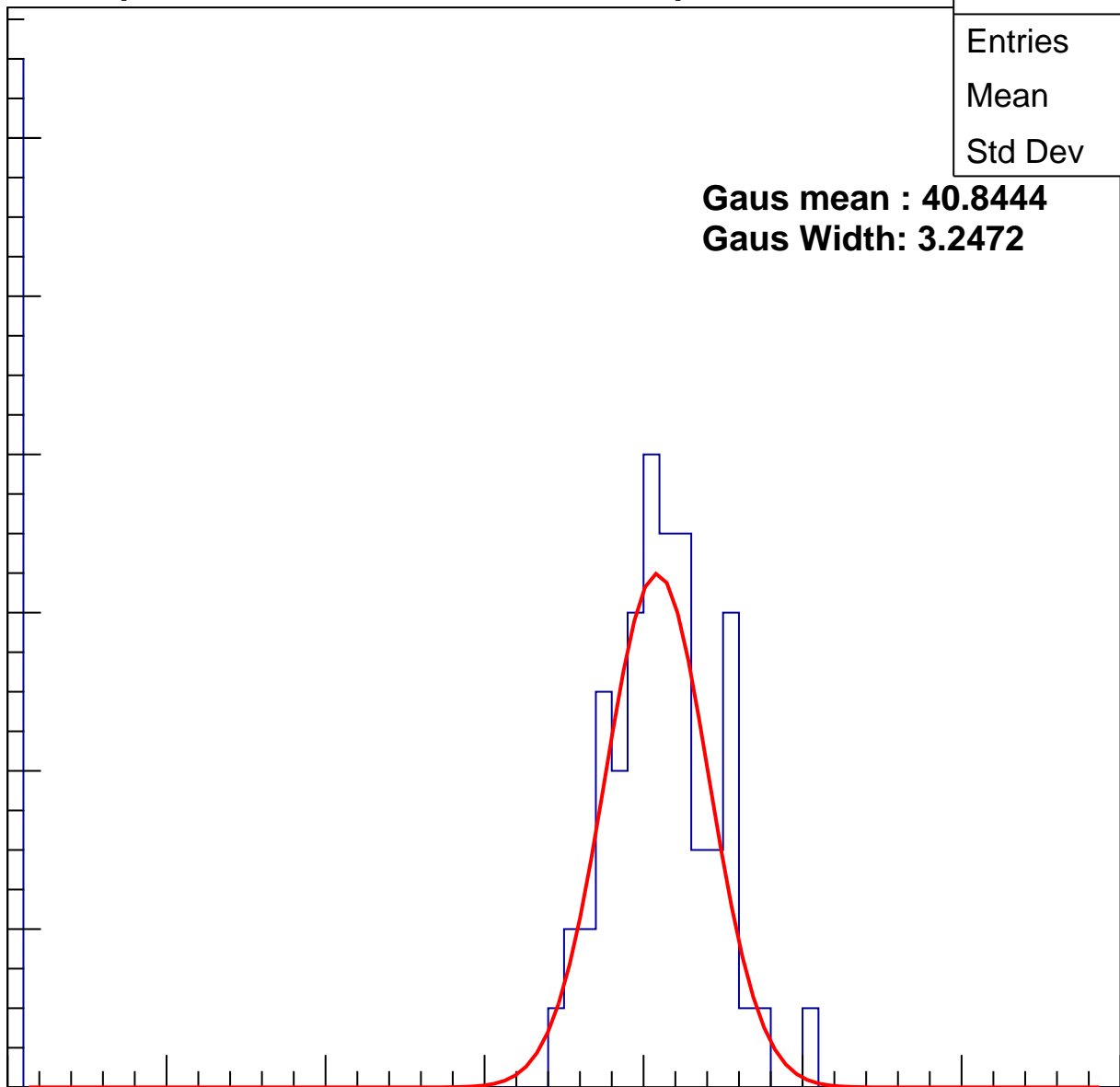
40

50

60

70

ampl

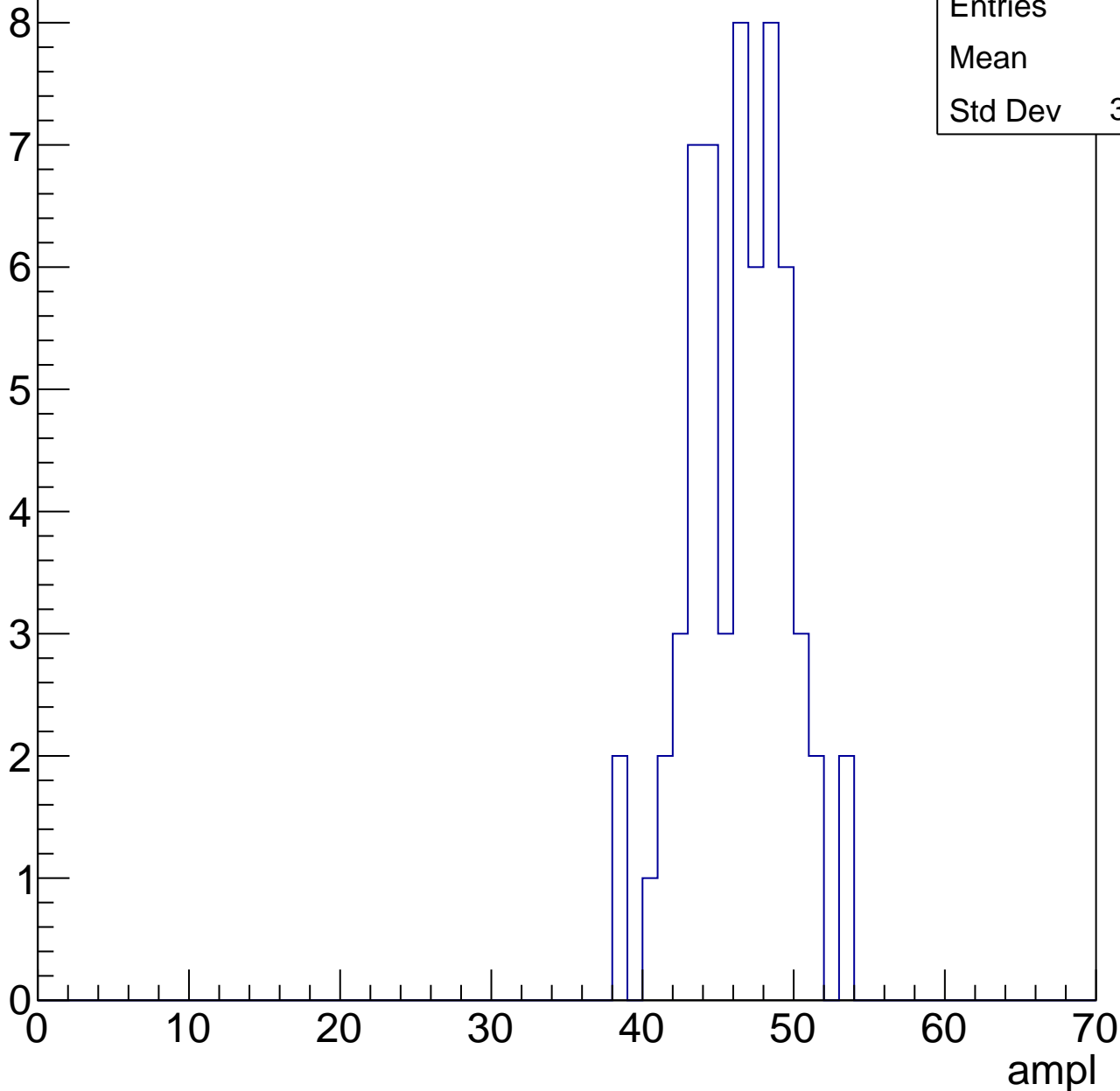


B1L103S, U6-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	45.9
Std Dev	3.275

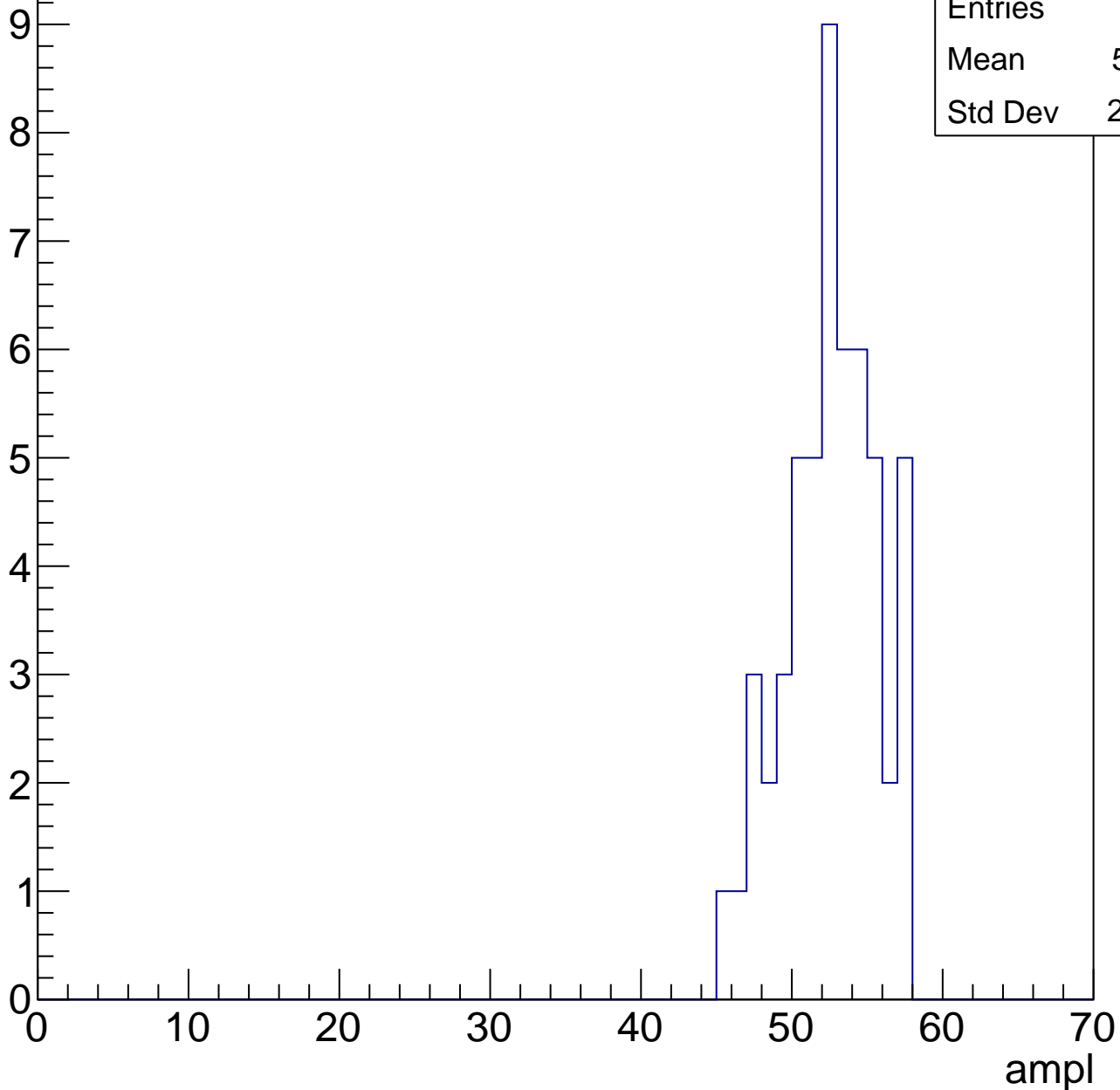


B1L103S, U6-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

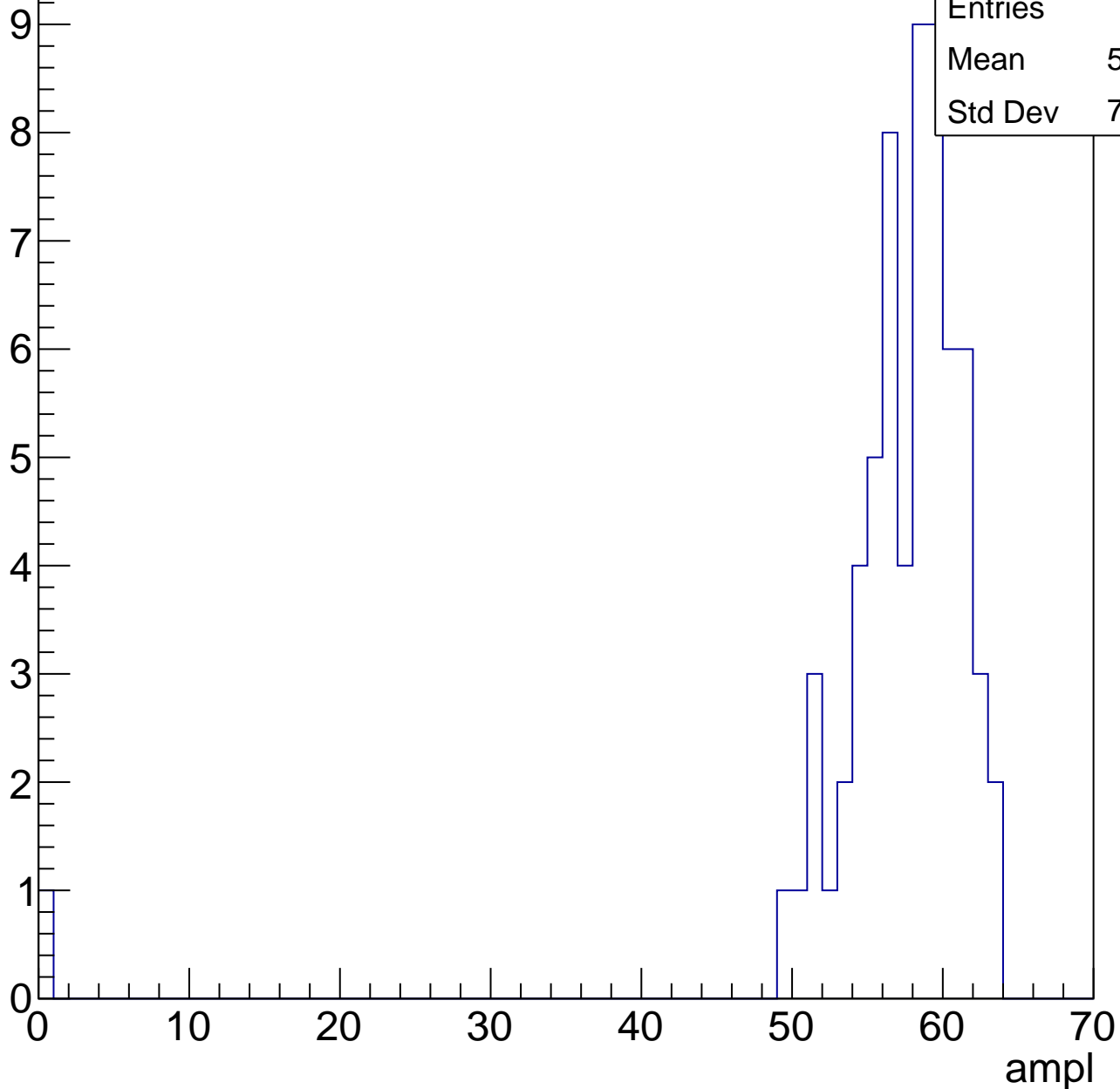
Entries	53
Mean	52.11
Std Dev	2.995



B1L103S, U6-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

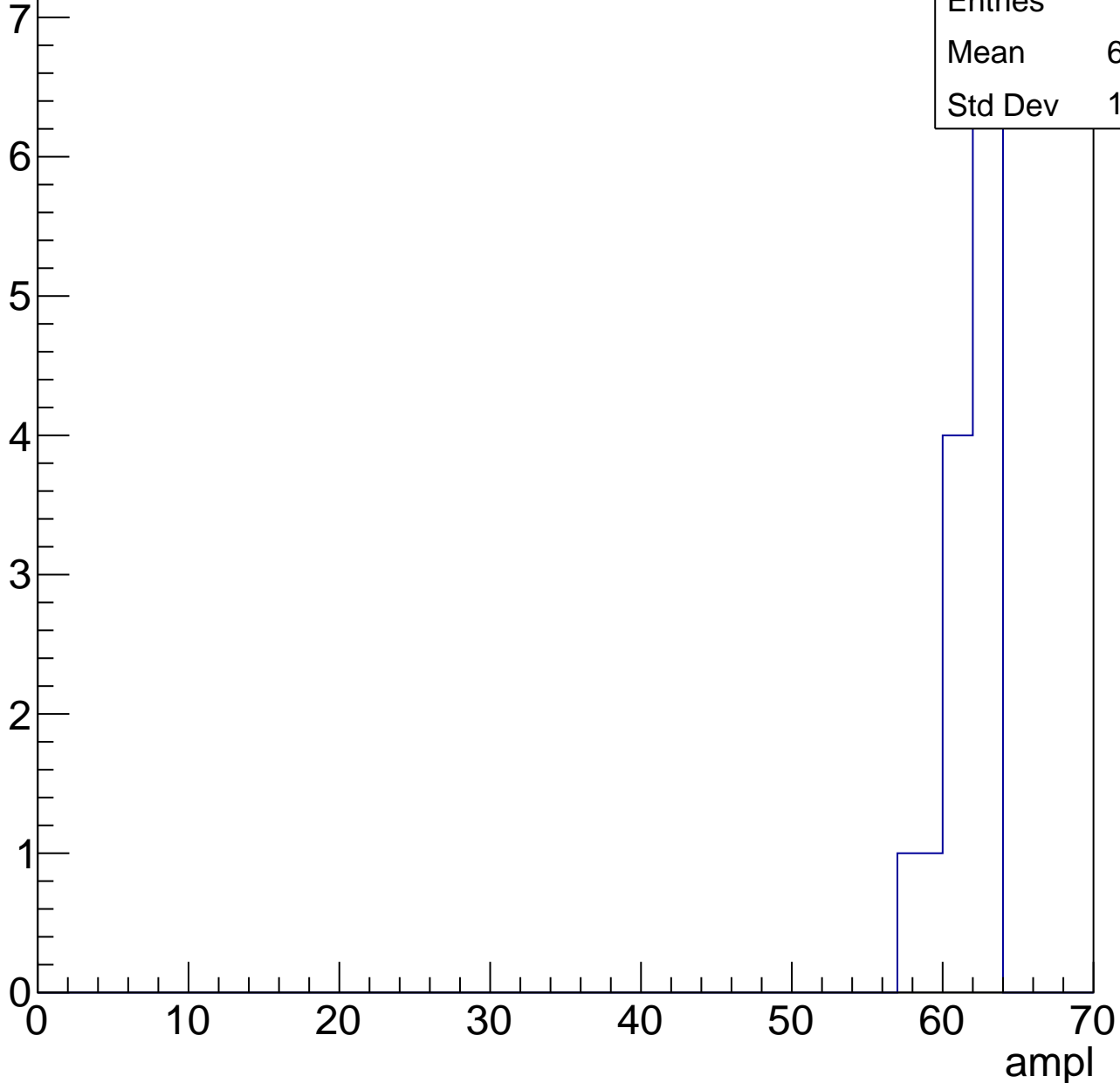


B1L103S, U6-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	61.32
Std Dev	1.618



B1L103S, U6-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U6-ch121, adc0

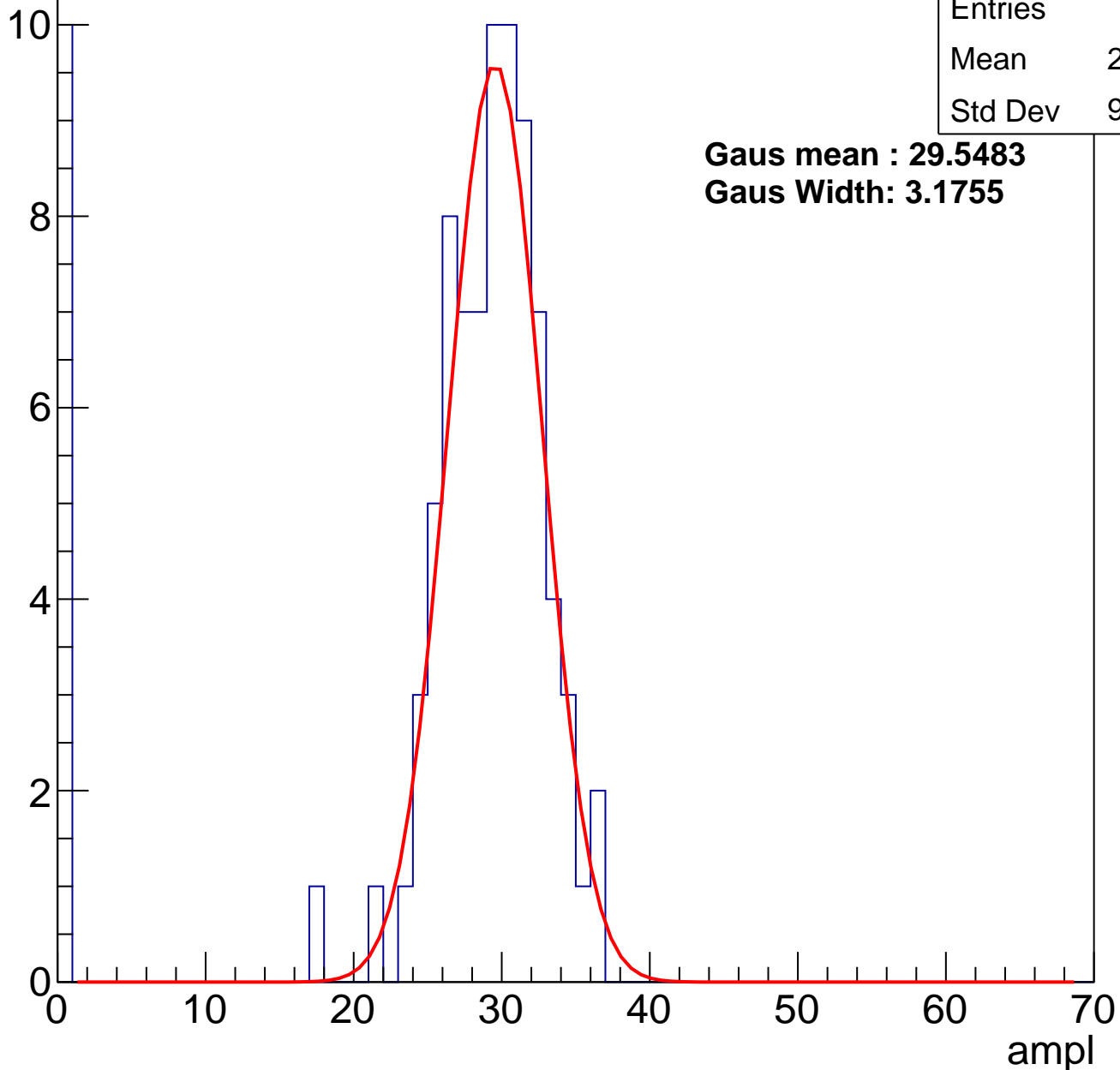
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.67
Std Dev	9.665

Gaus mean : 29.5483

Gaus Width: 3.1755

Entry



B1L103S, U6-ch121, adc1

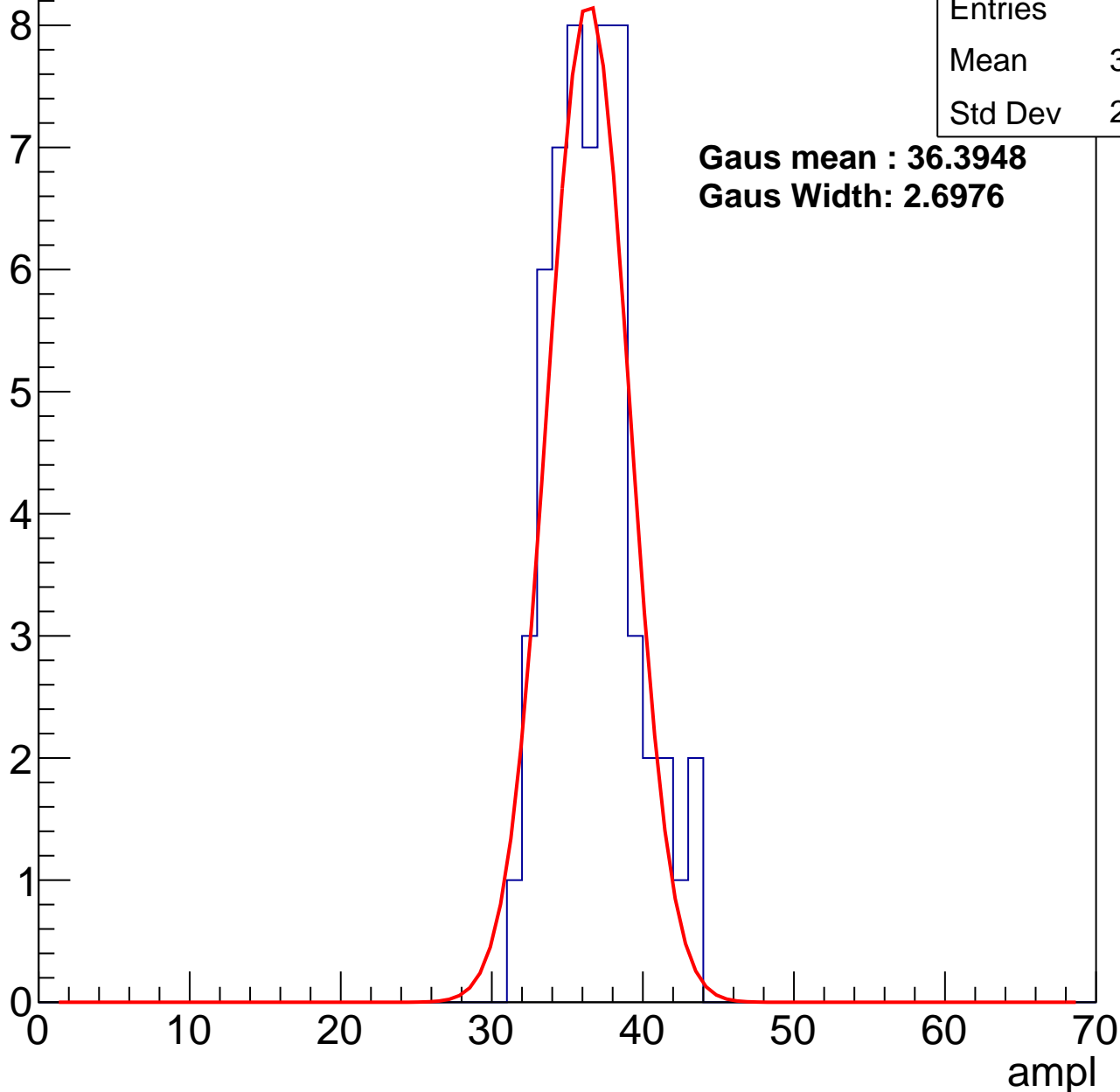
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	36.24
Std Dev	2.762

Gaus mean : 36.3948

Gaus Width: 2.6976



B1L103S, U6-ch121, adc2

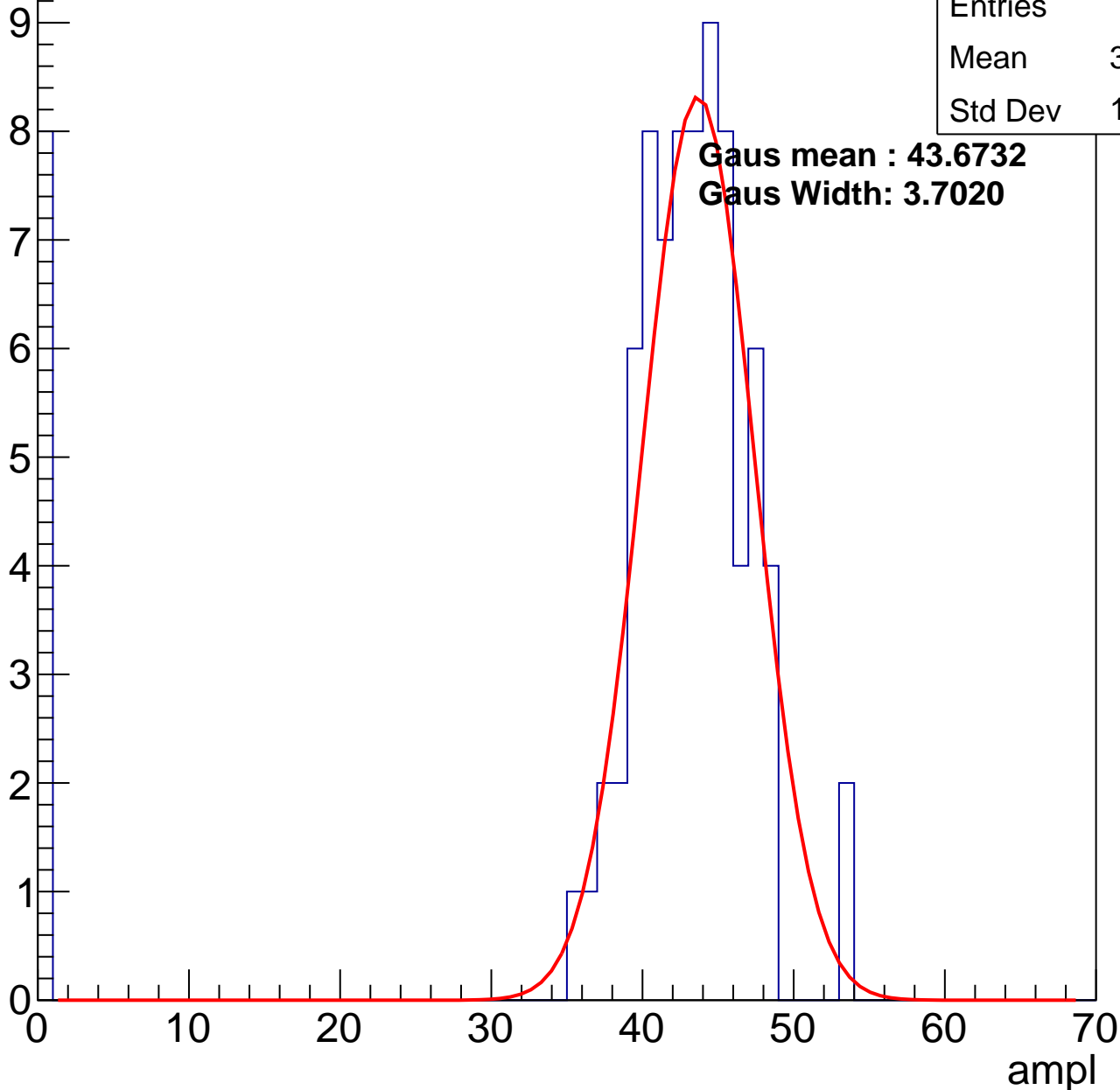
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	38.83
Std Dev	13.02

Gaus mean : 43.6732

Gaus Width: 3.7020

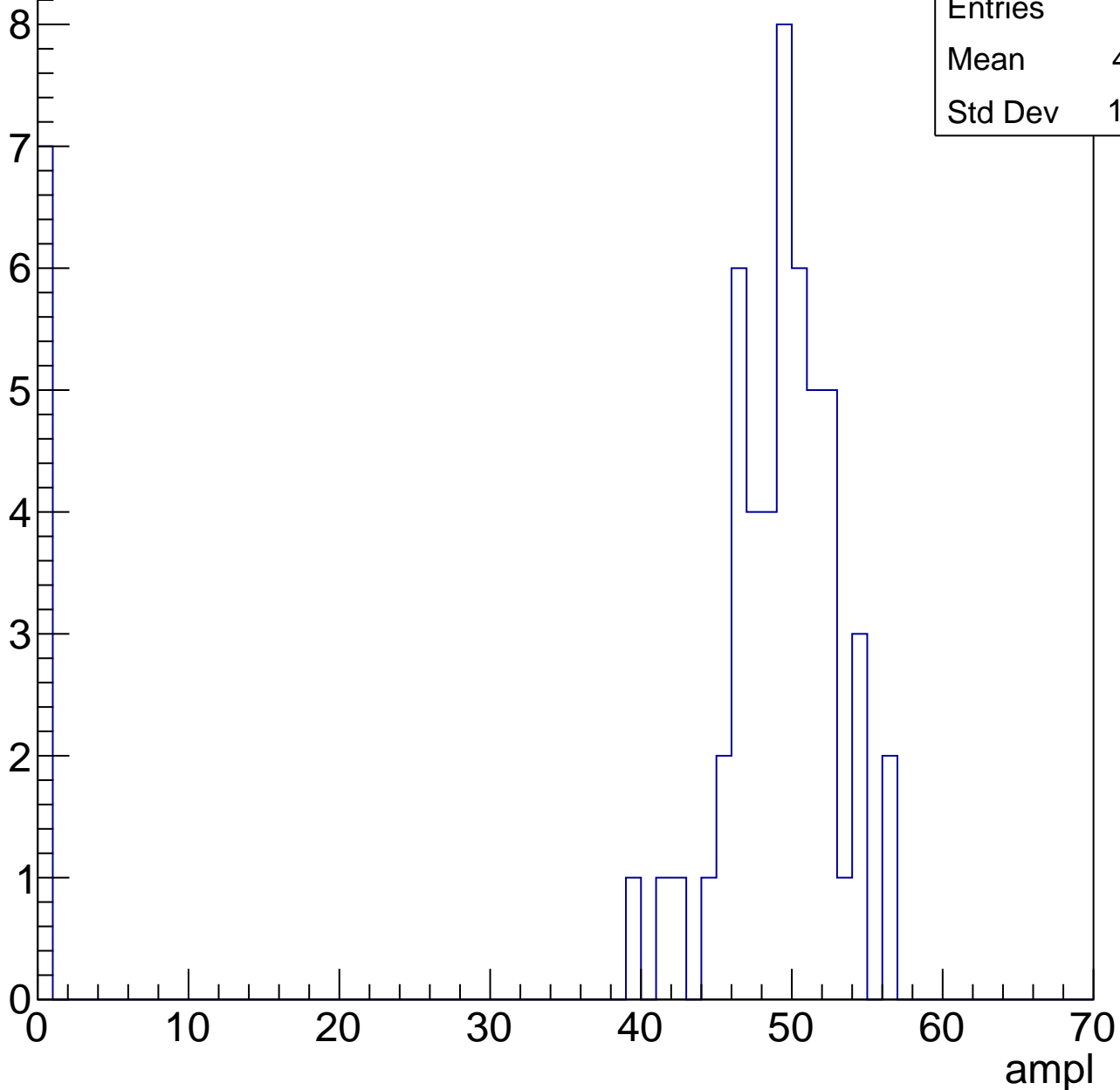


B1L103S, U6-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	42.91
Std Dev	16.38

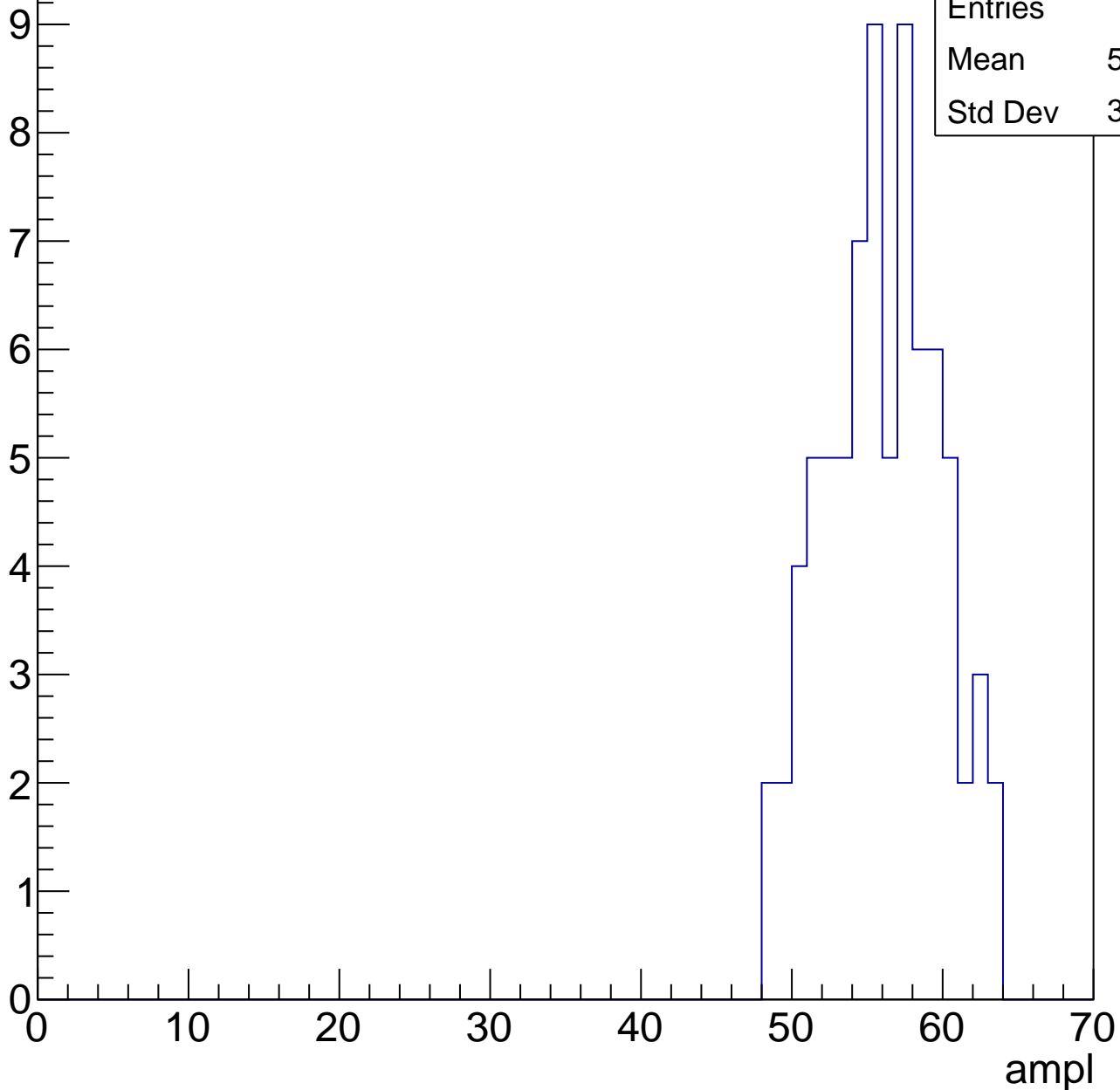


B1L103S, U6-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	55.53
Std Dev	3.716

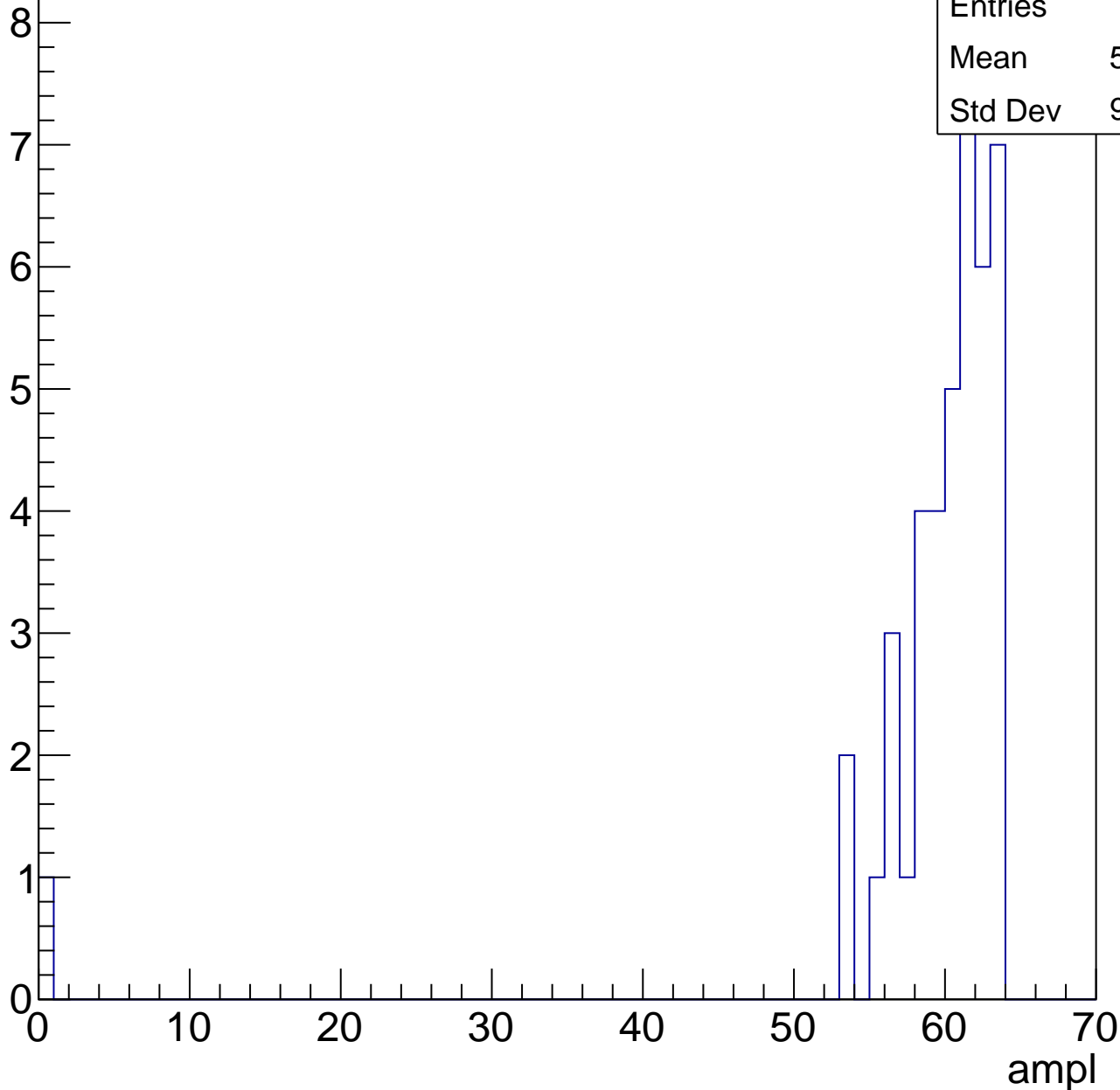


B1L103S, U6-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.45
Std Dev	9.505



B1L103S, U6-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U6-ch122, adc0

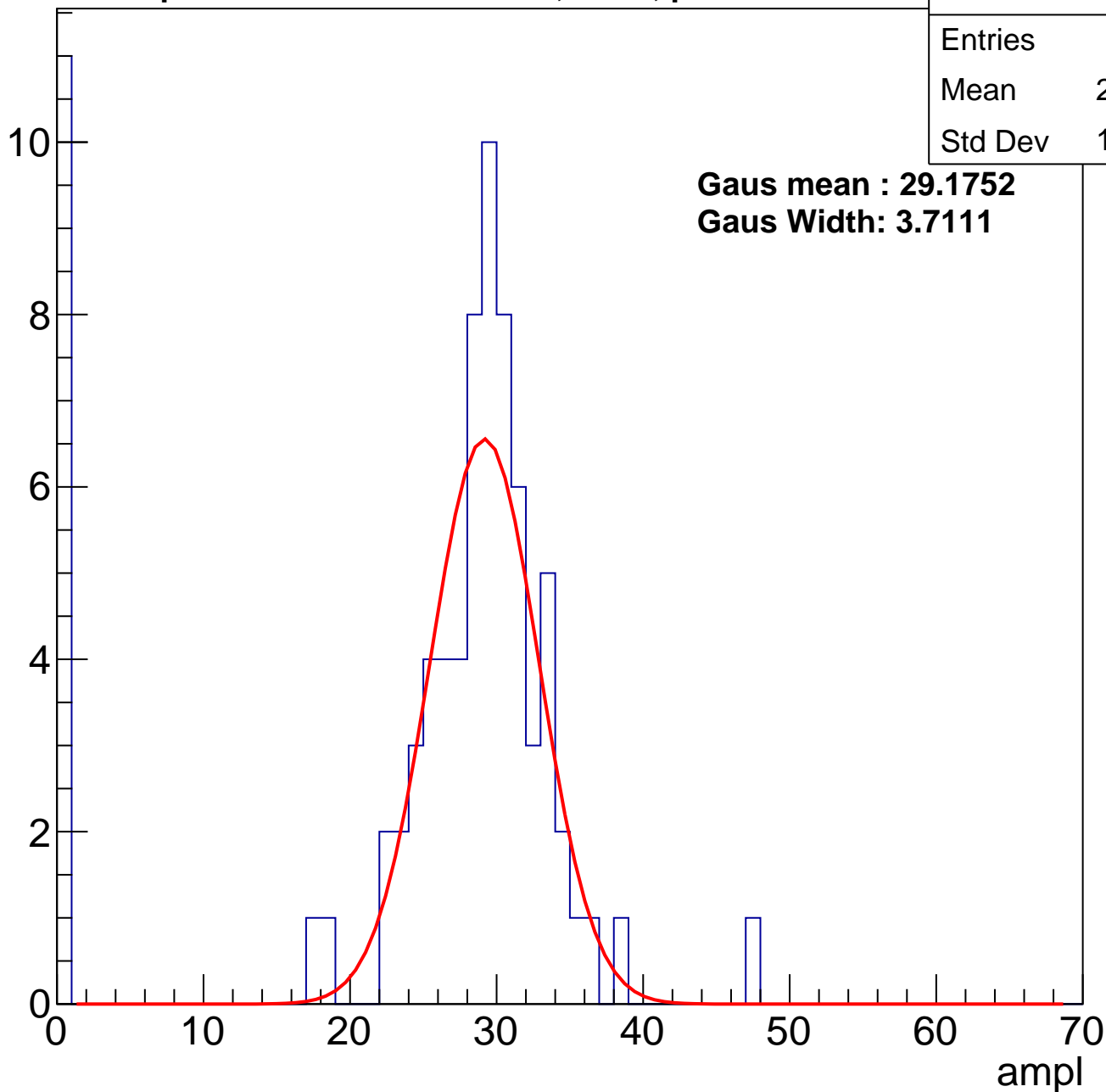
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	24.79
Std Dev	10.85

Gaus mean : 29.1752

Gaus Width: 3.7111

Entry



B1L103S, U6-ch122, adc1

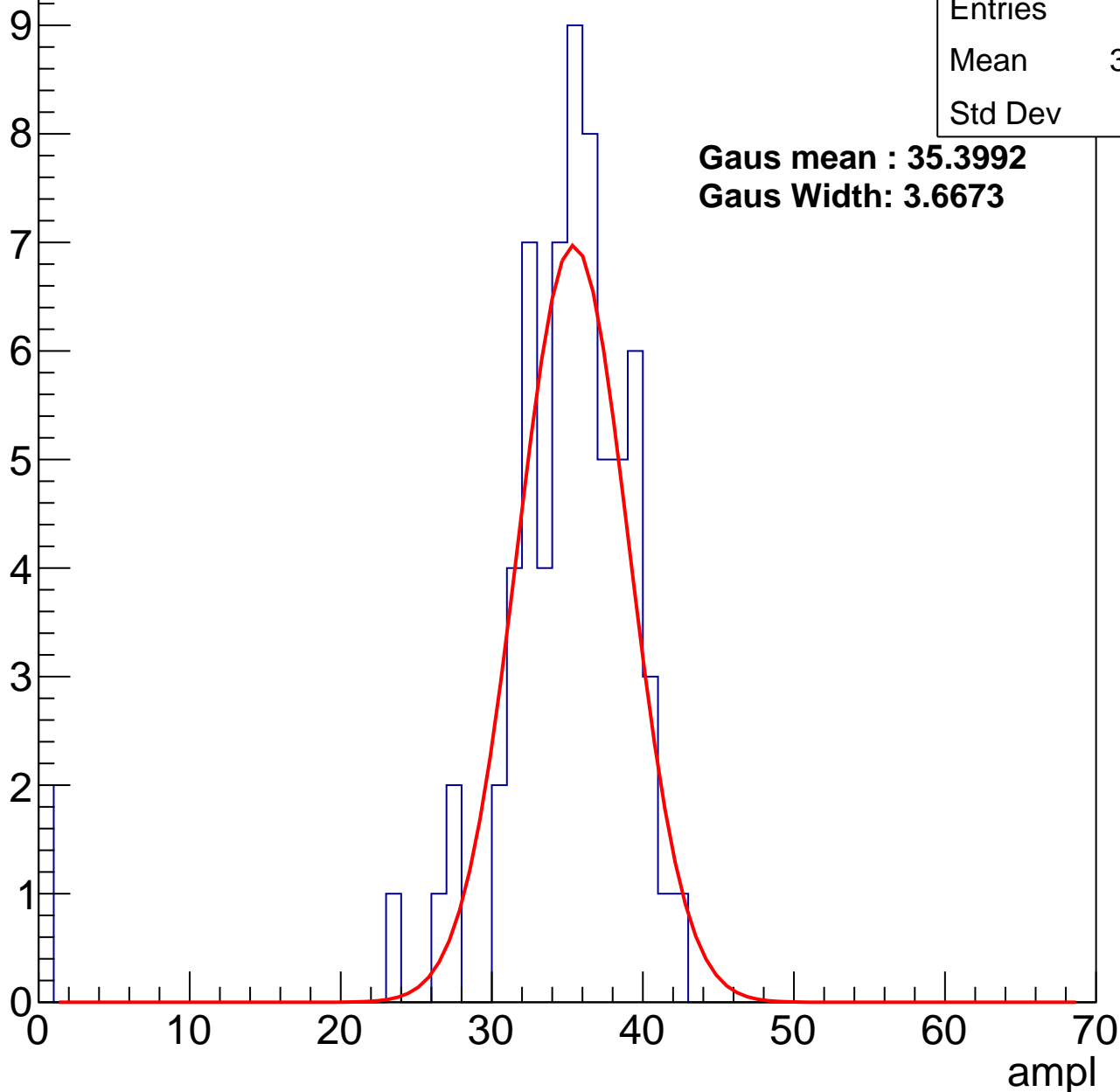
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.76
Std Dev	6.89

Gaus mean : 35.3992

Gaus Width: 3.6673



B1L103S, U6-ch122, adc2

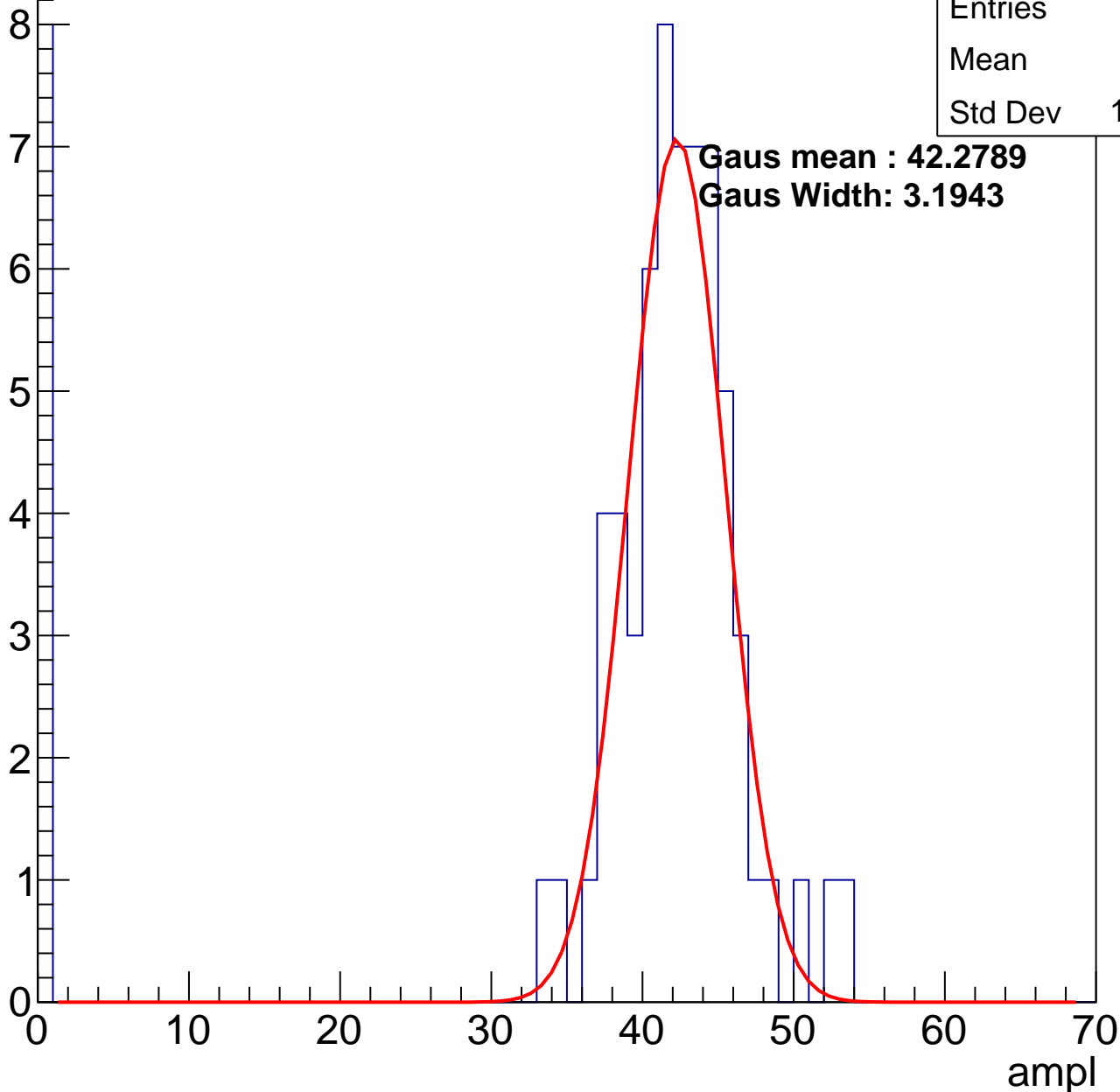
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.2
Std Dev	13.82

Gaus mean : 42.2789

Gaus Width: 3.1943

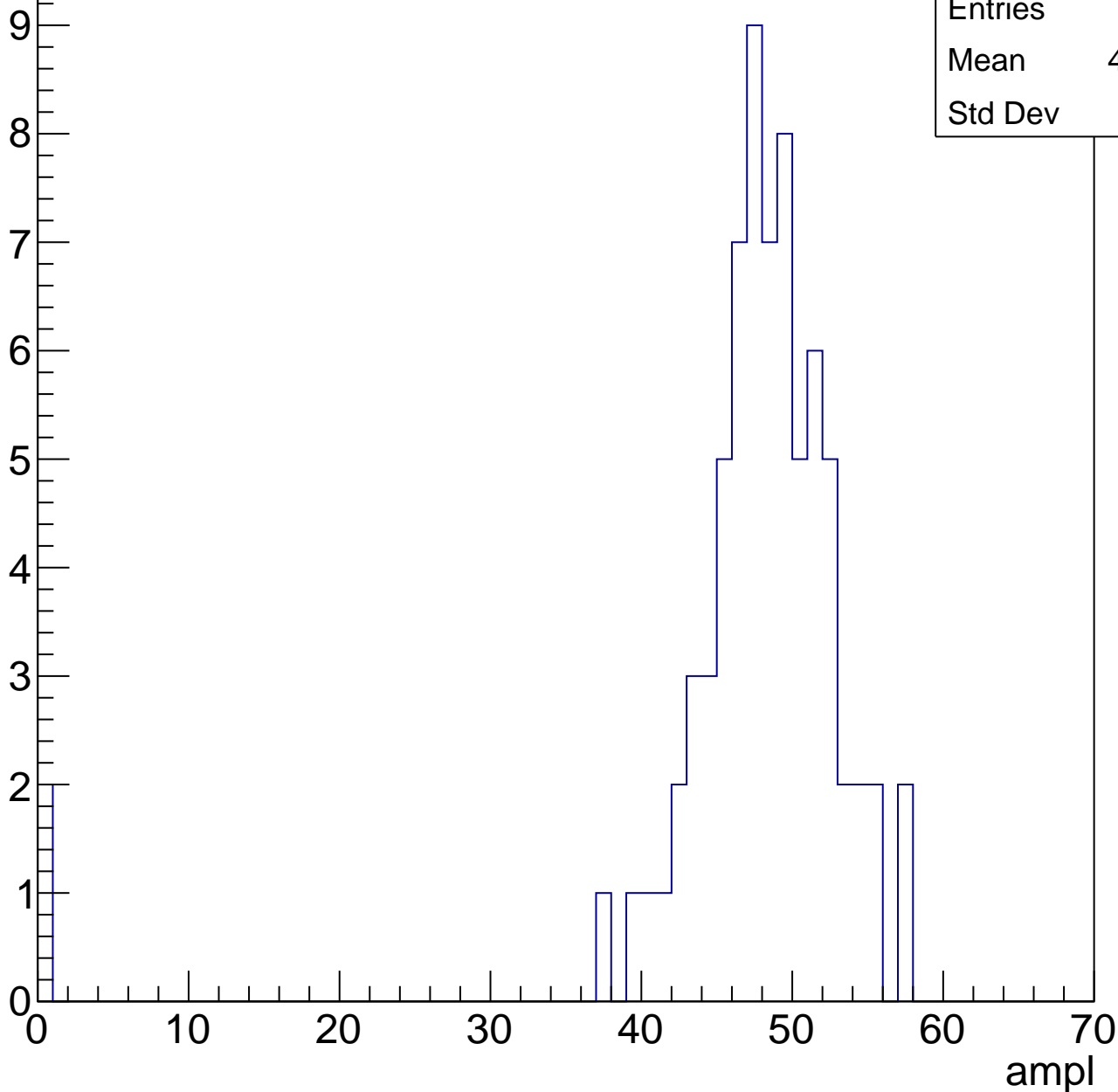


B1L103S, U6-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	46.68
Std Dev	8.71

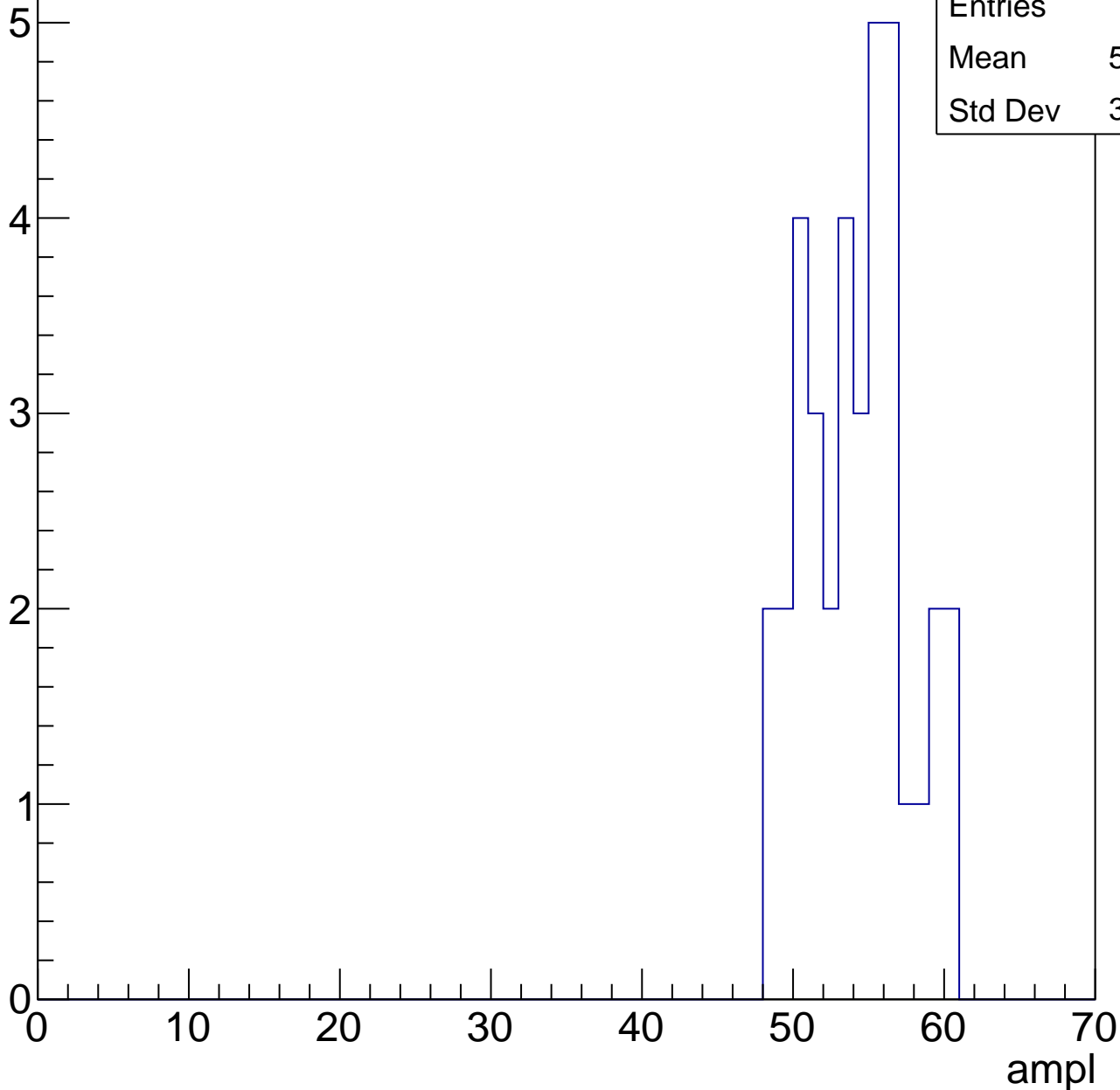


B1L103S, U6-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	53.69
Std Dev	3.307

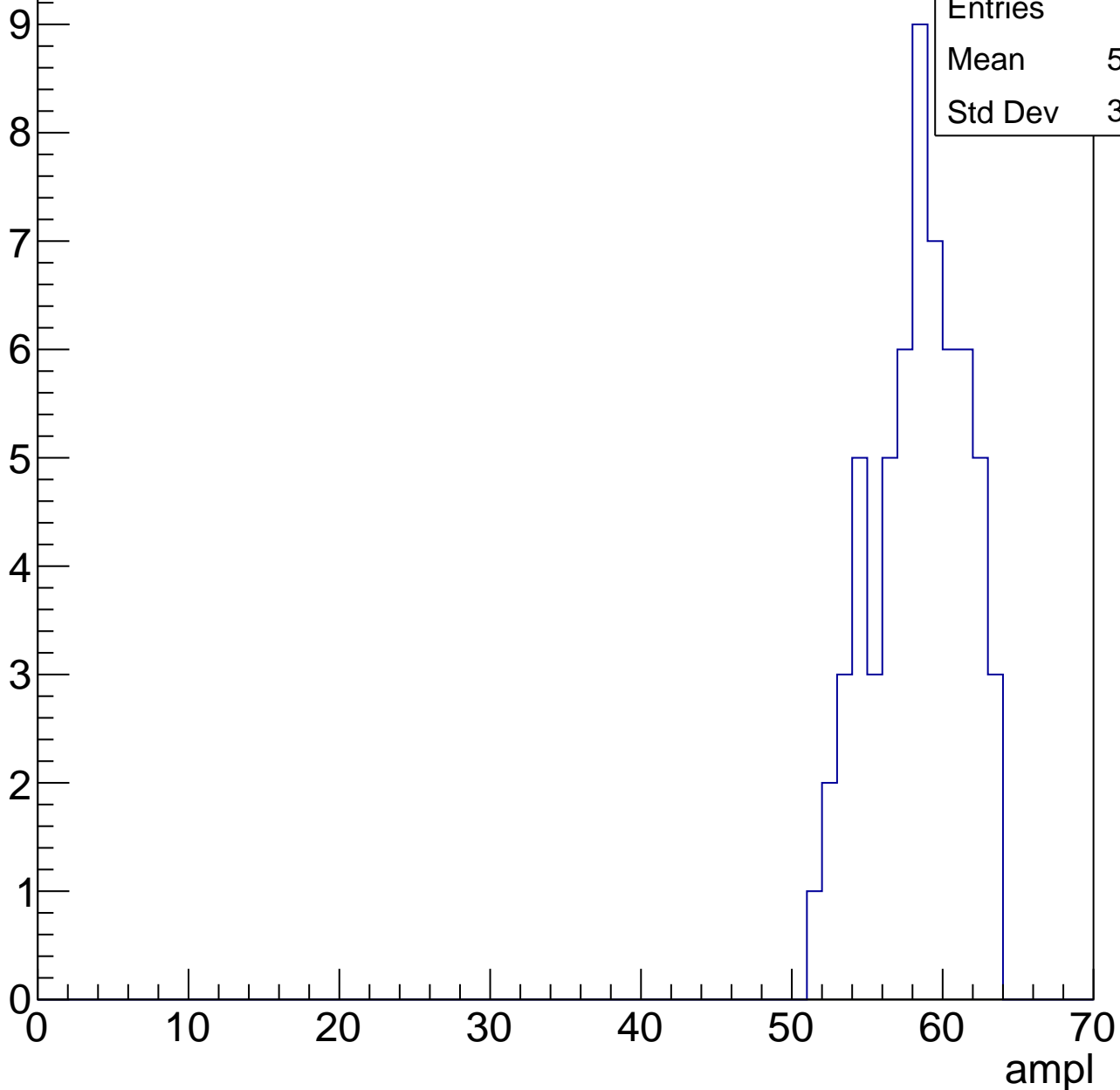


B1L103S, U6-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.89
Std Dev	3.052

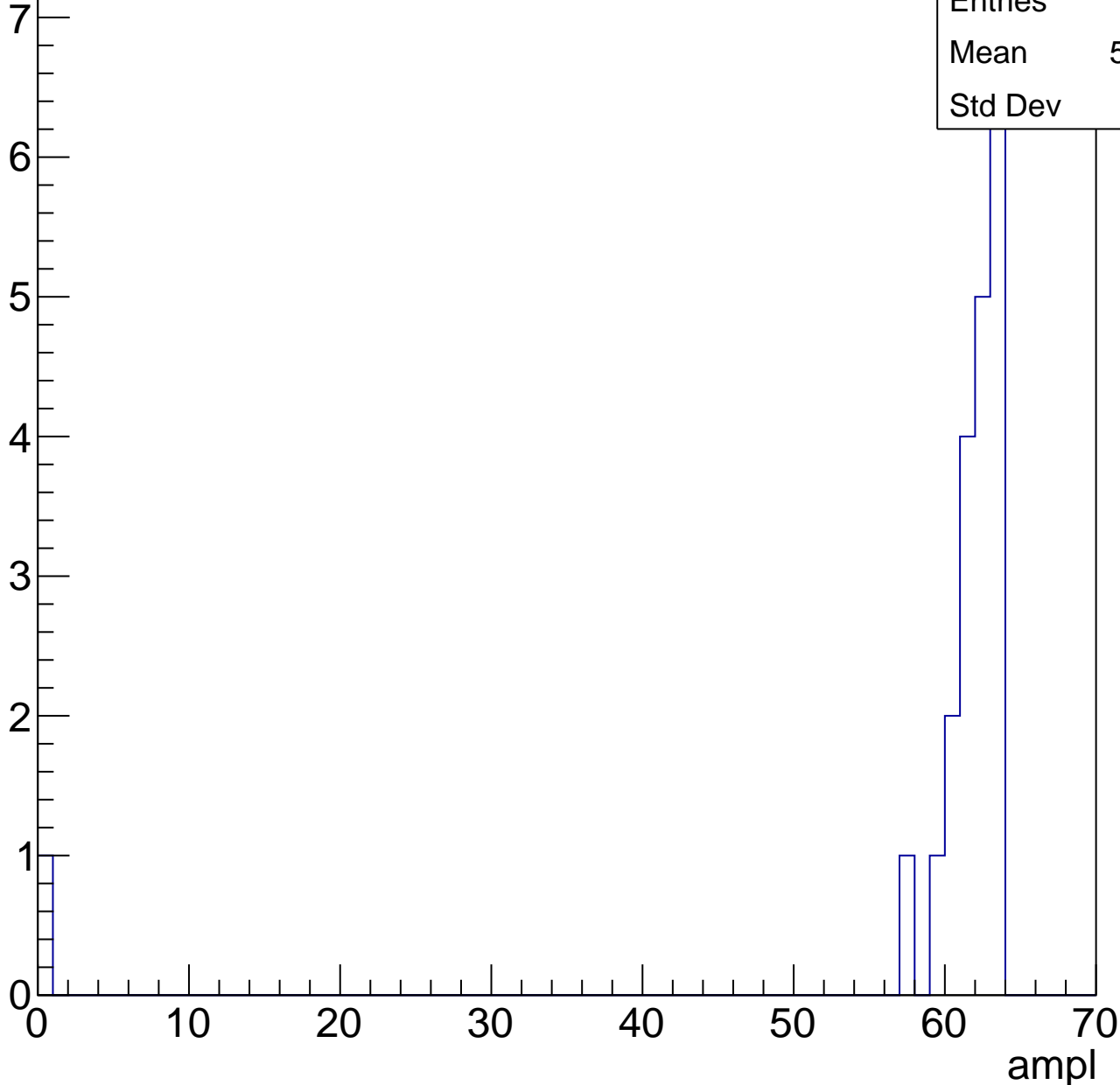


B1L103S, U6-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.62
Std Dev	13.2



B1L103S, U6-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch123, adc0

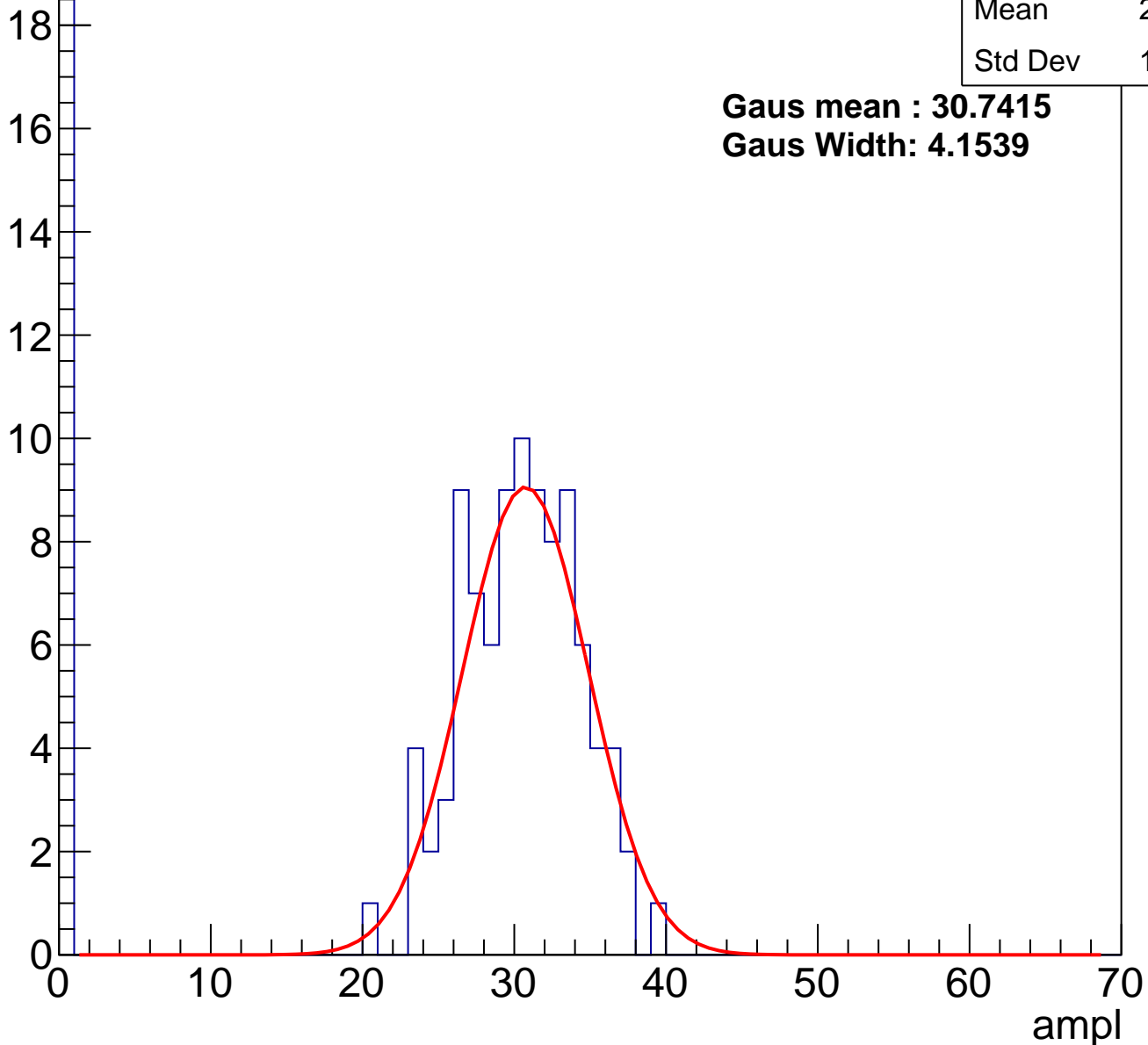
calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	24.96
Std Dev	11.73

Gaus mean : 30.7415

Gaus Width: 4.1539

Entry



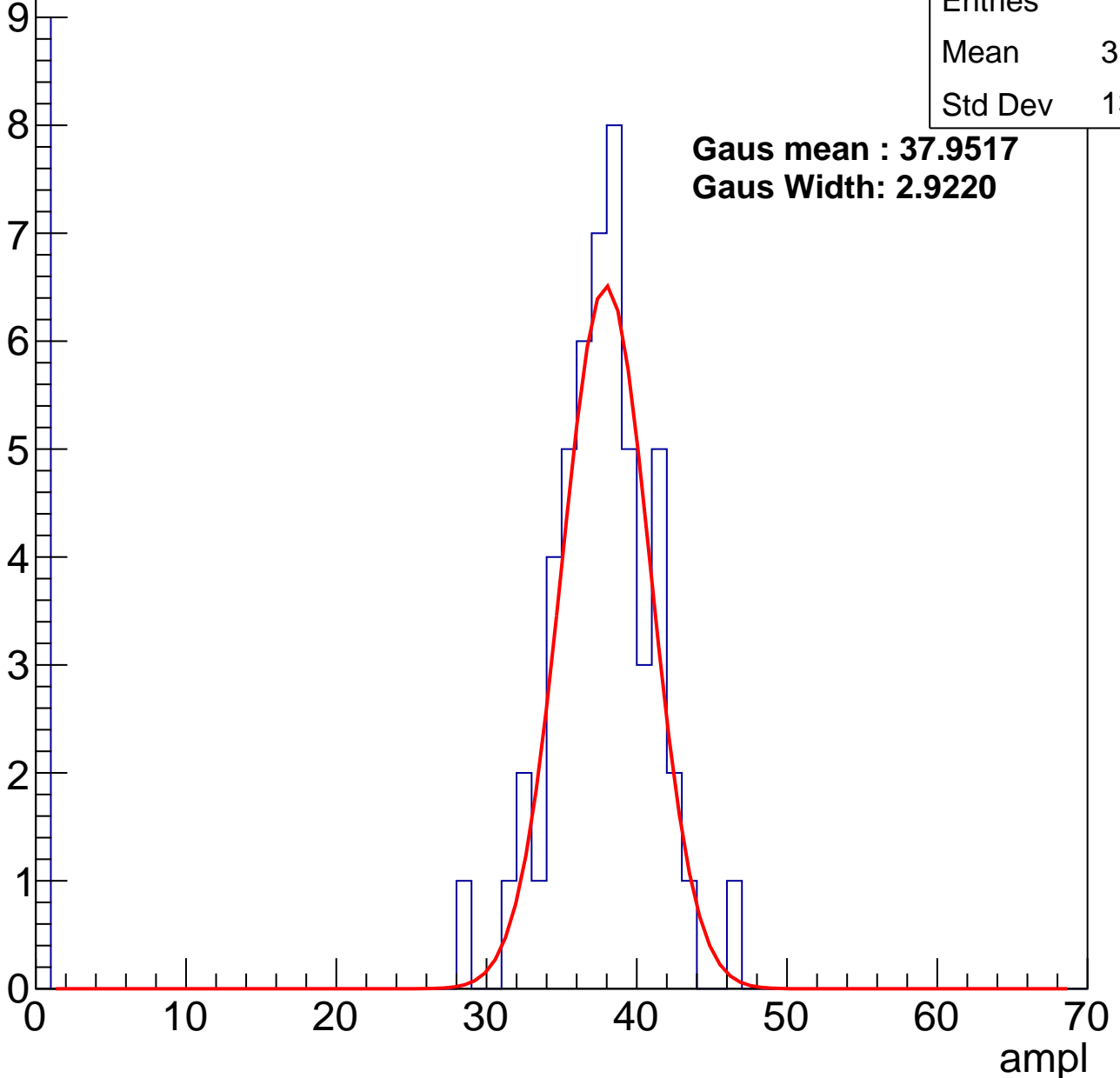
B1L103S, U6-ch123, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	31.79
Std Dev	13.56

Gaus mean : 37.9517
Gaus Width: 2.9220



B1L103S, U6-ch123, adc2

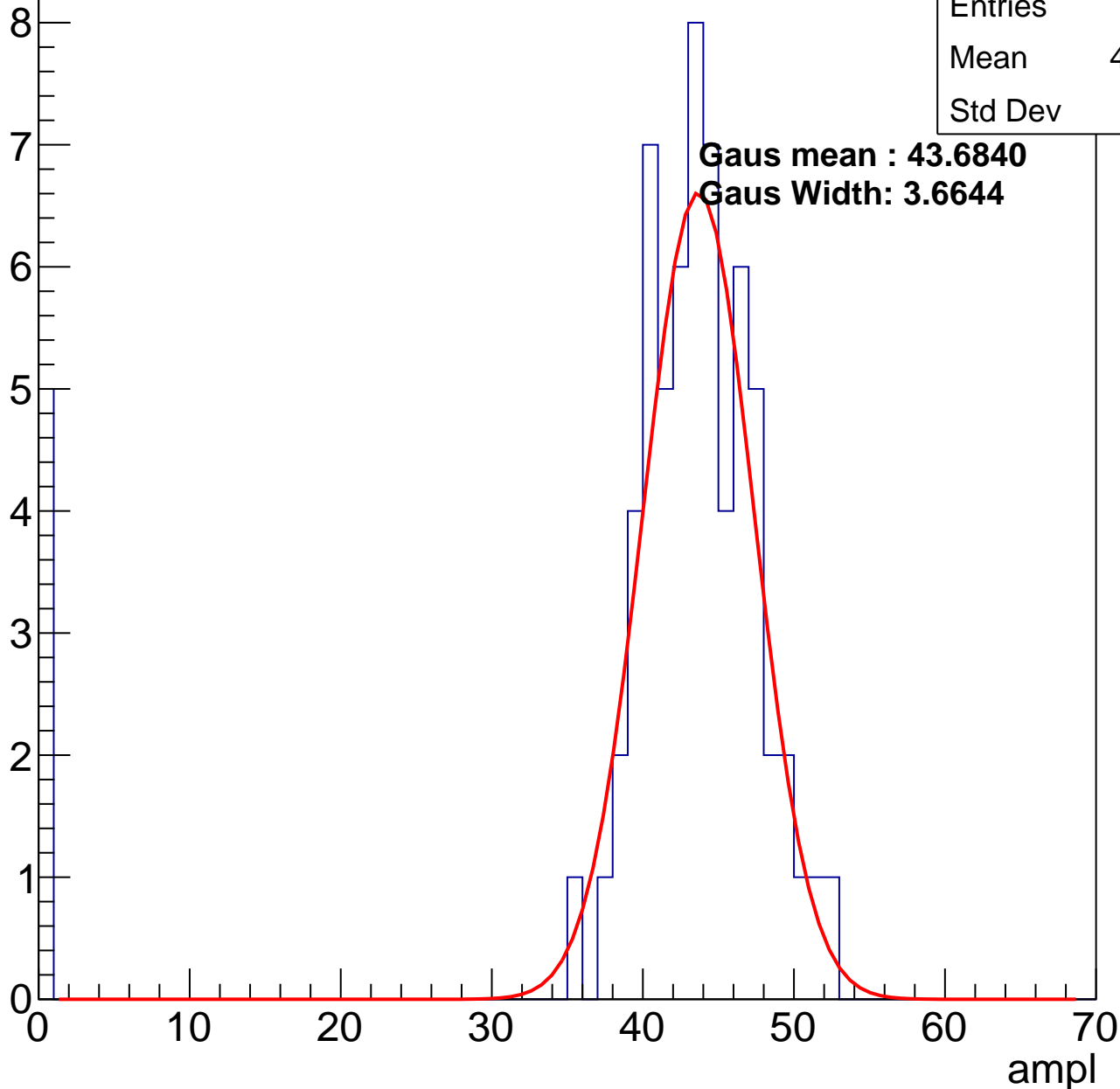
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.16
Std Dev	11.8

Gaus mean : 43.6840

Gaus Width: 3.6644

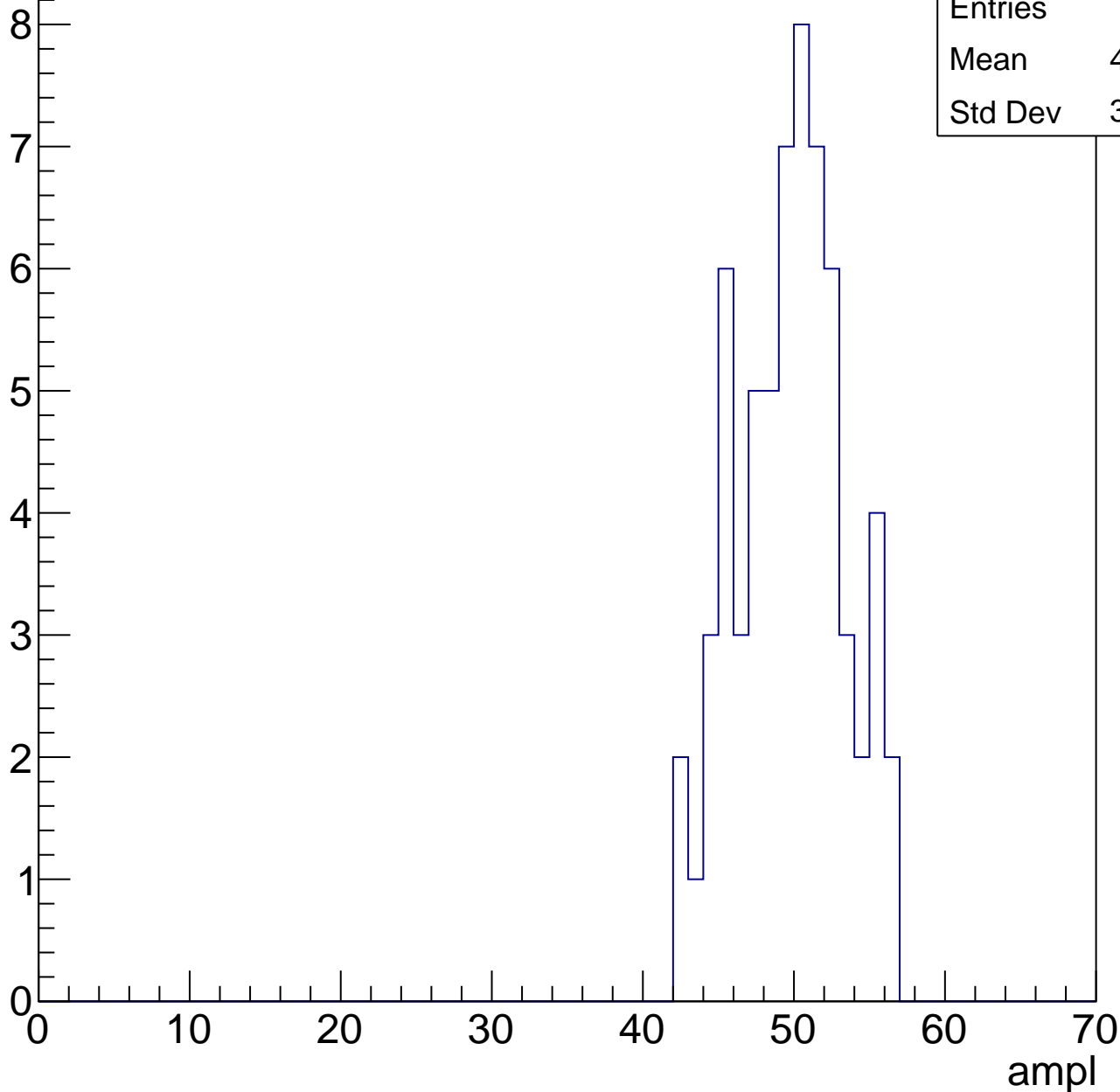


B1L103S, U6-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.27
Std Dev	3.497

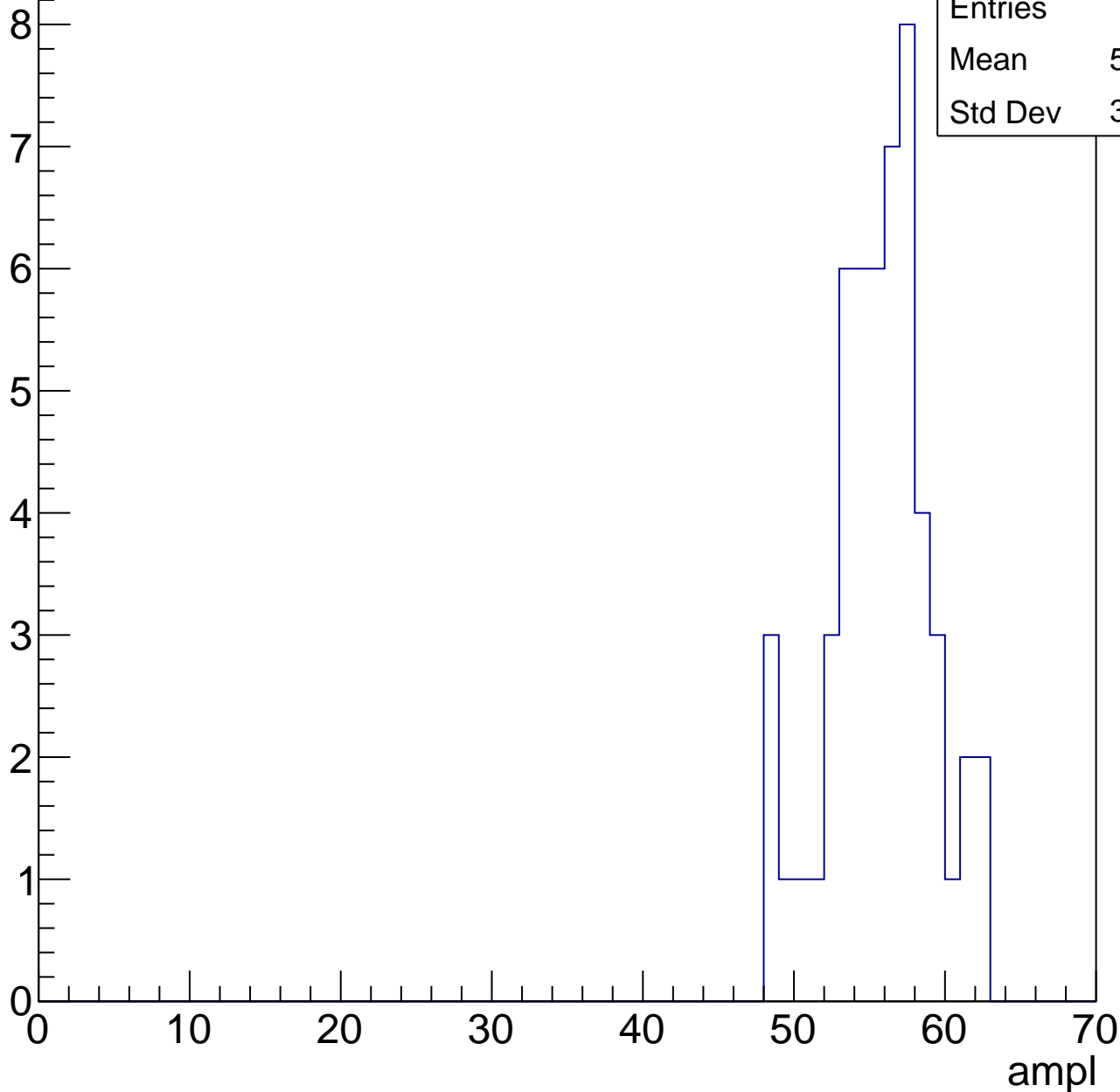


B1L103S, U6-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.28
Std Dev	3.319

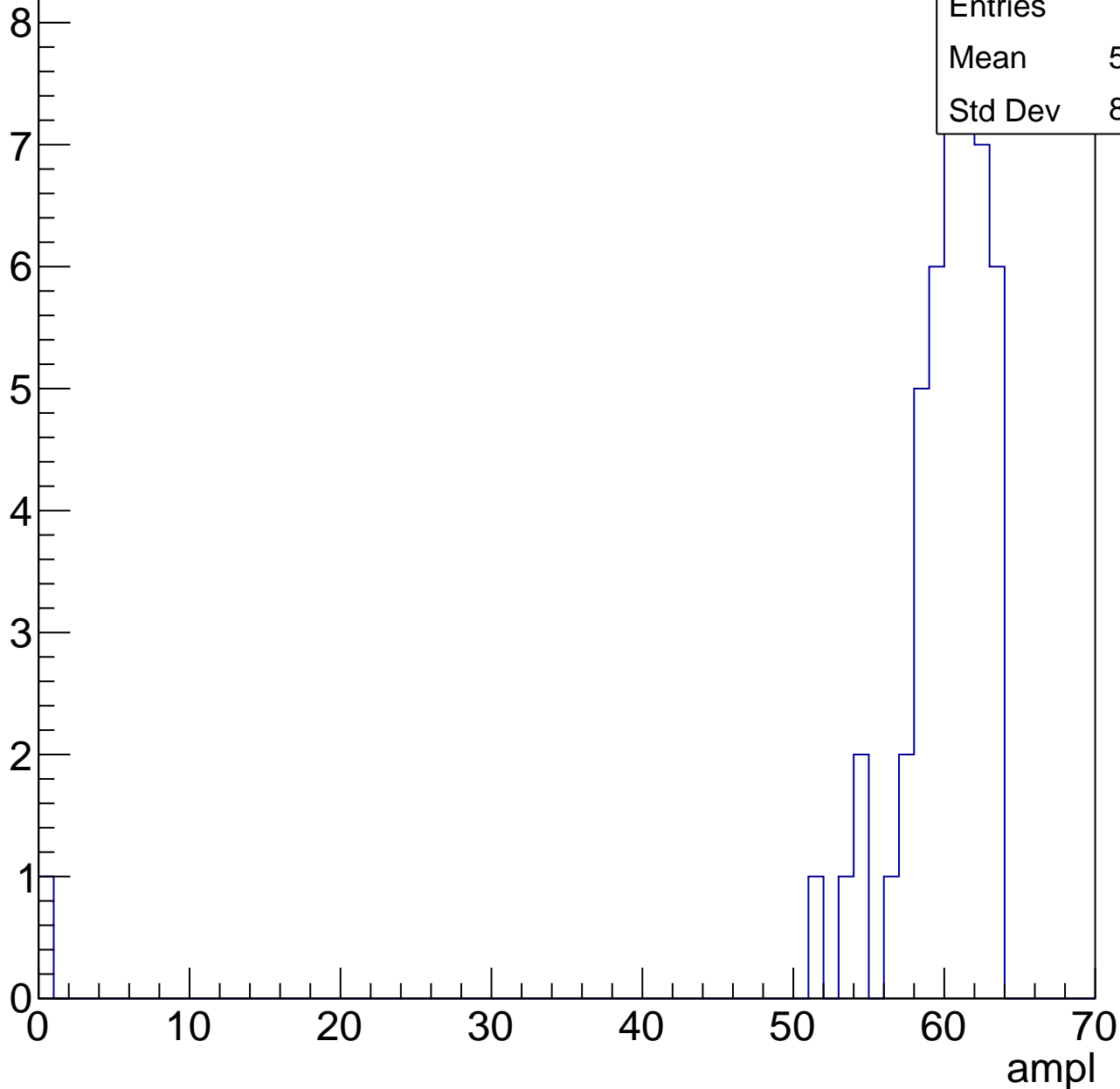


B1L103S, U6-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

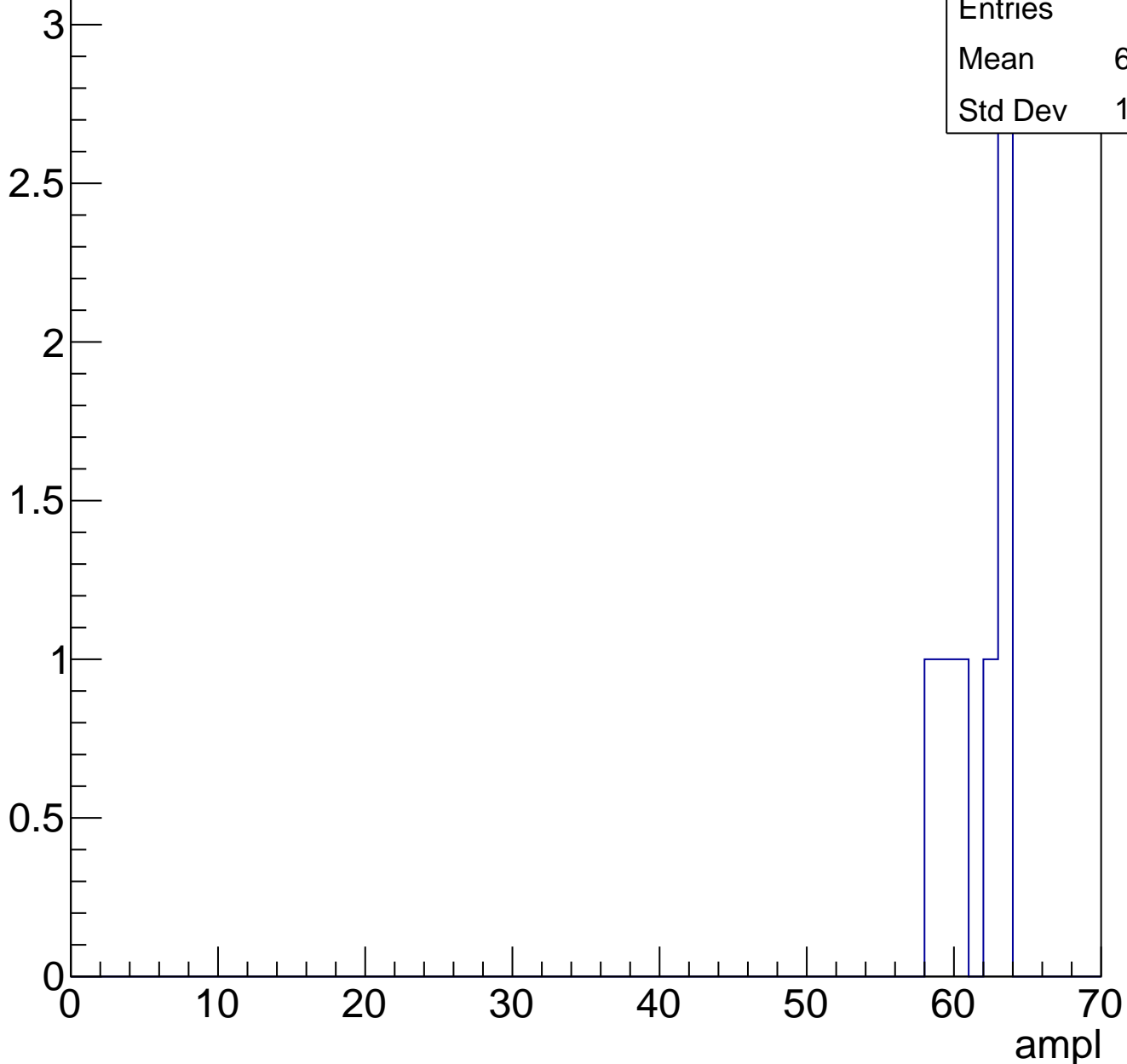
Entries	48
Mean	58.46
Std Dev	8.942



B1L103S, U6-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U6-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U6-ch124, adc0

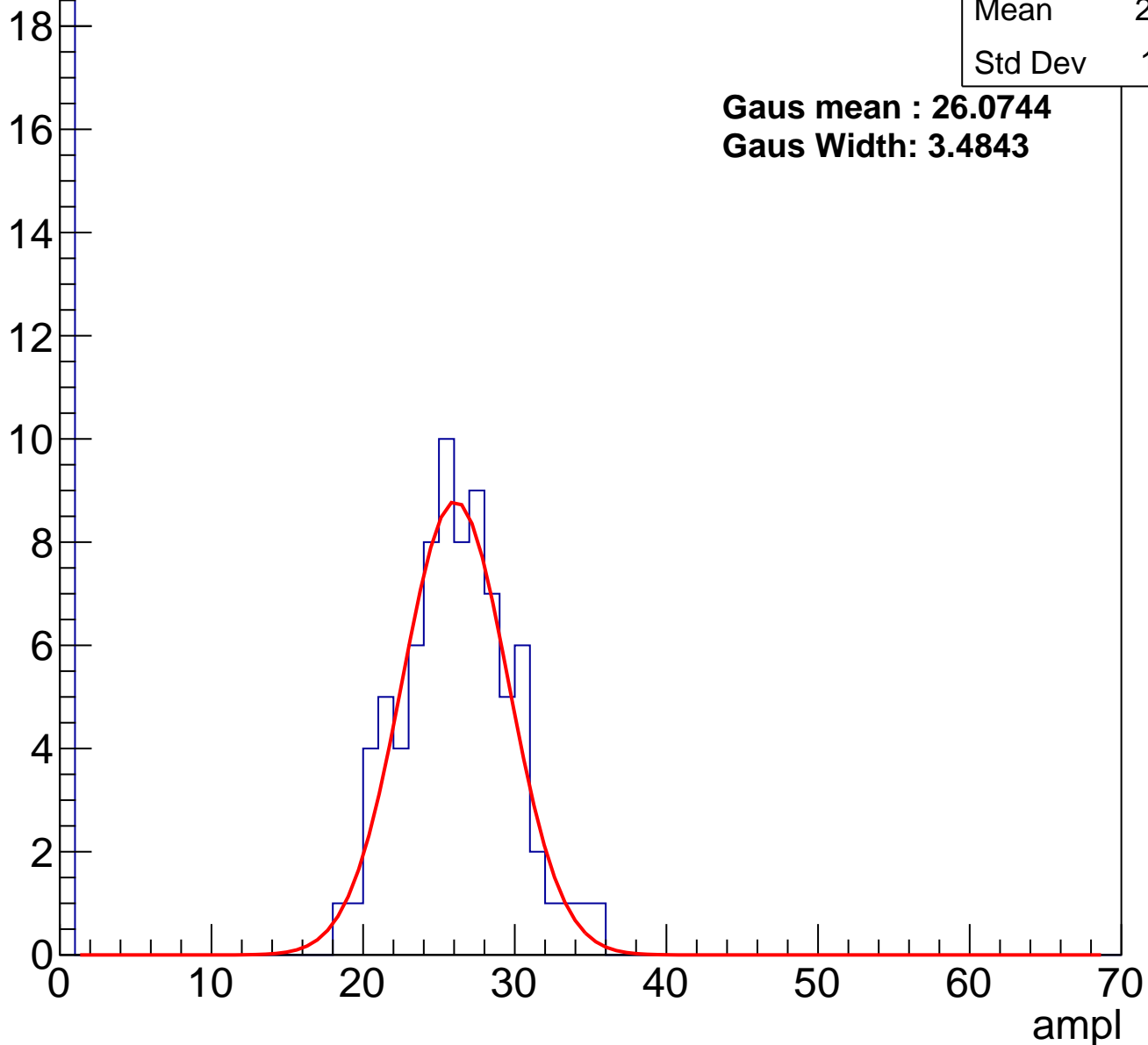
calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	20.79
Std Dev	10.61

Gaus mean : 26.0744

Gaus Width: 3.4843

Entry



B1L103S, U6-ch124, adc1

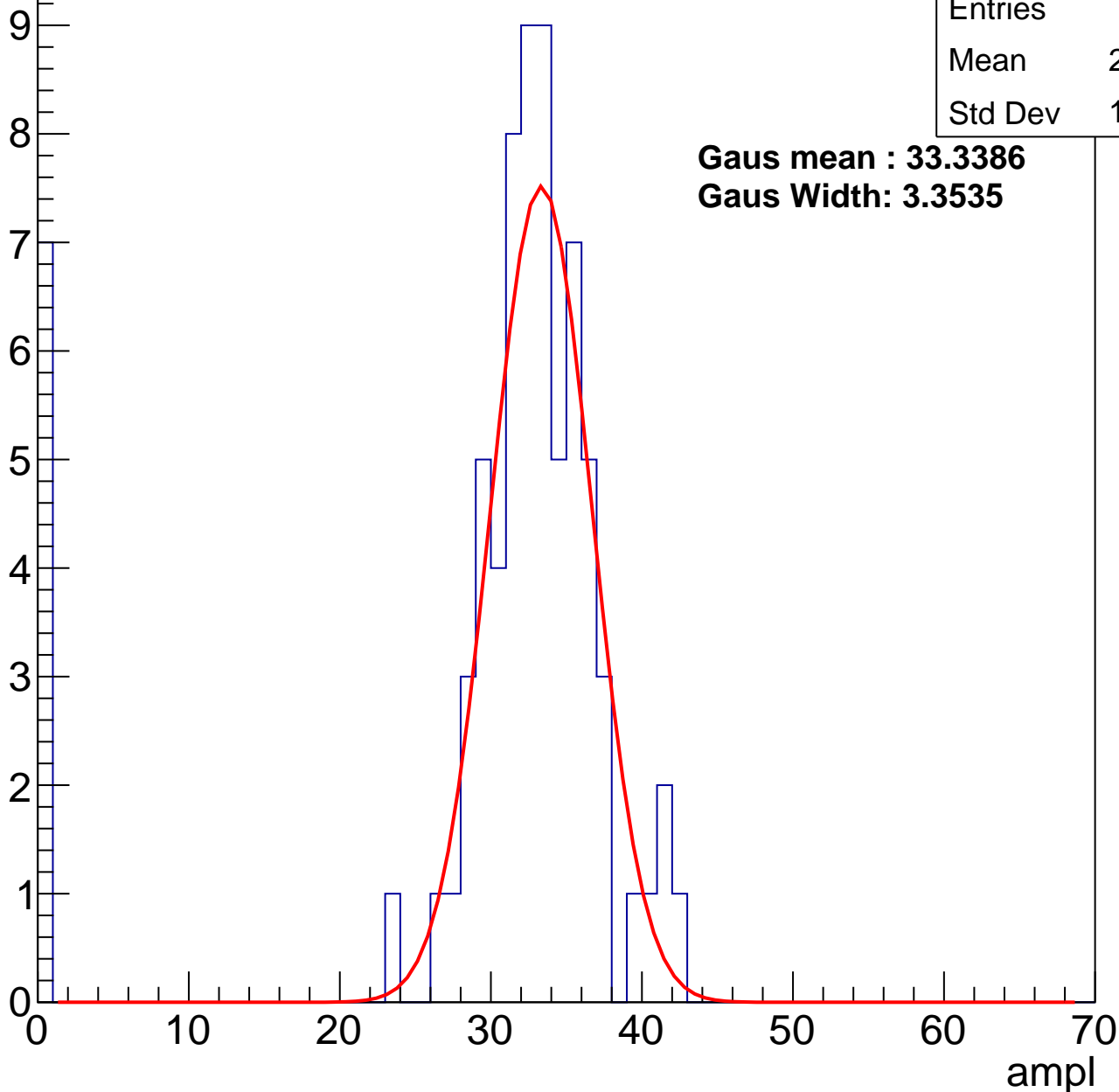
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	29.68
Std Dev	10.24

Gaus mean : 33.3386

Gaus Width: 3.3535



B1L103S, U6-ch124, adc2

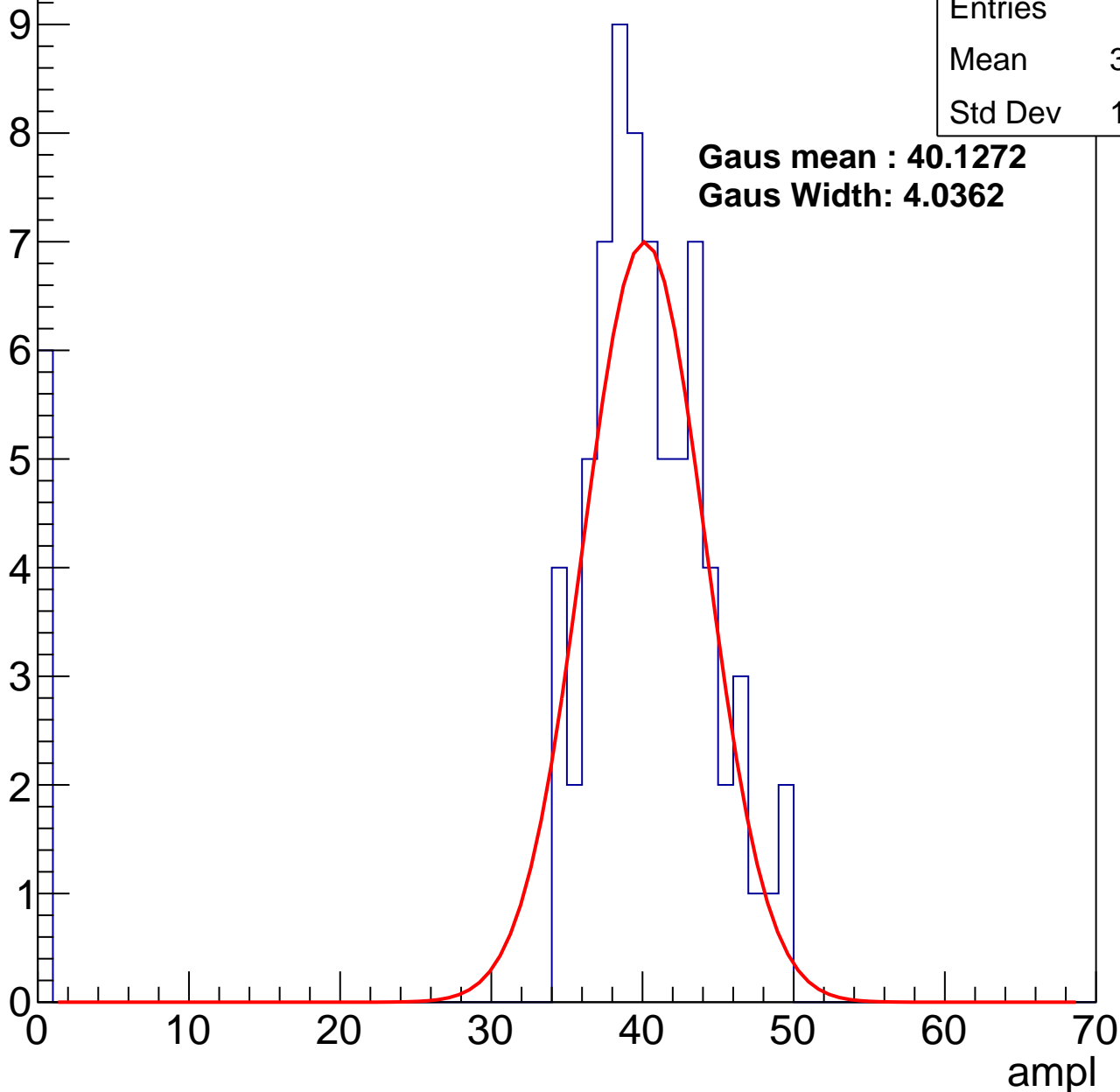
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	37.08
Std Dev	11.27

Gaus mean : 40.1272

Gaus Width: 4.0362

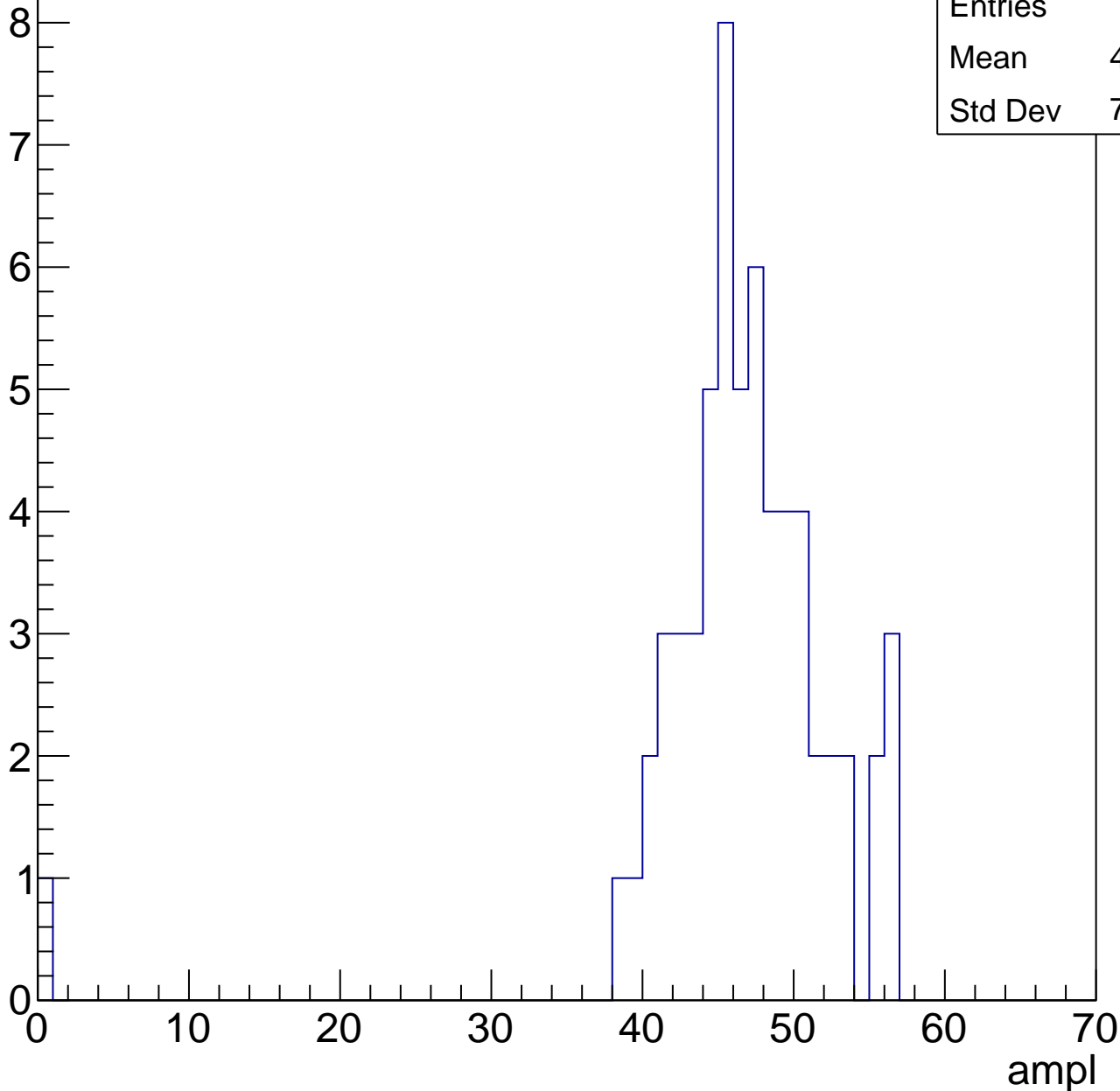


B1L103S, U6-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	45.98
Std Dev	7.336

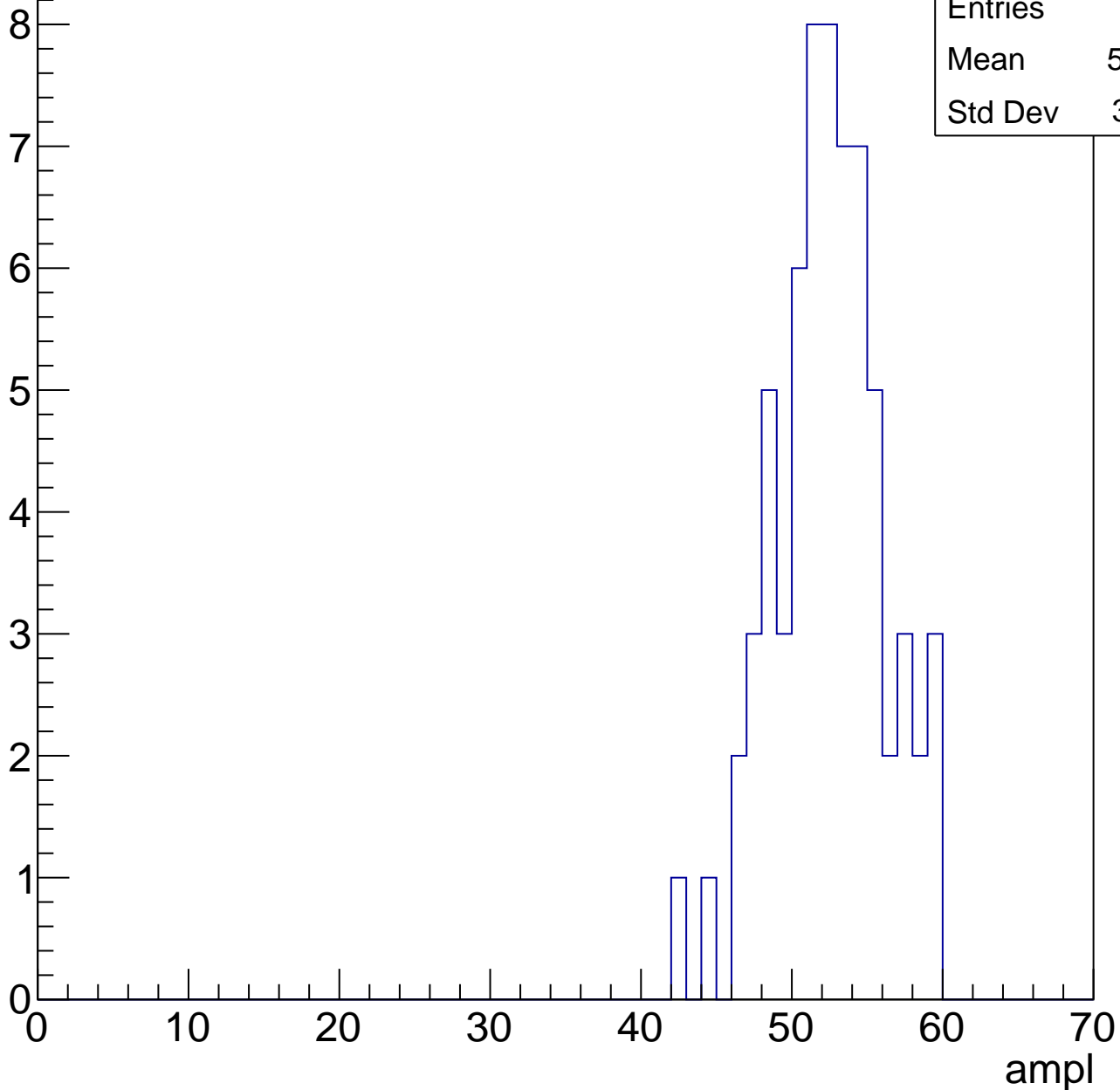


B1L103S, U6-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

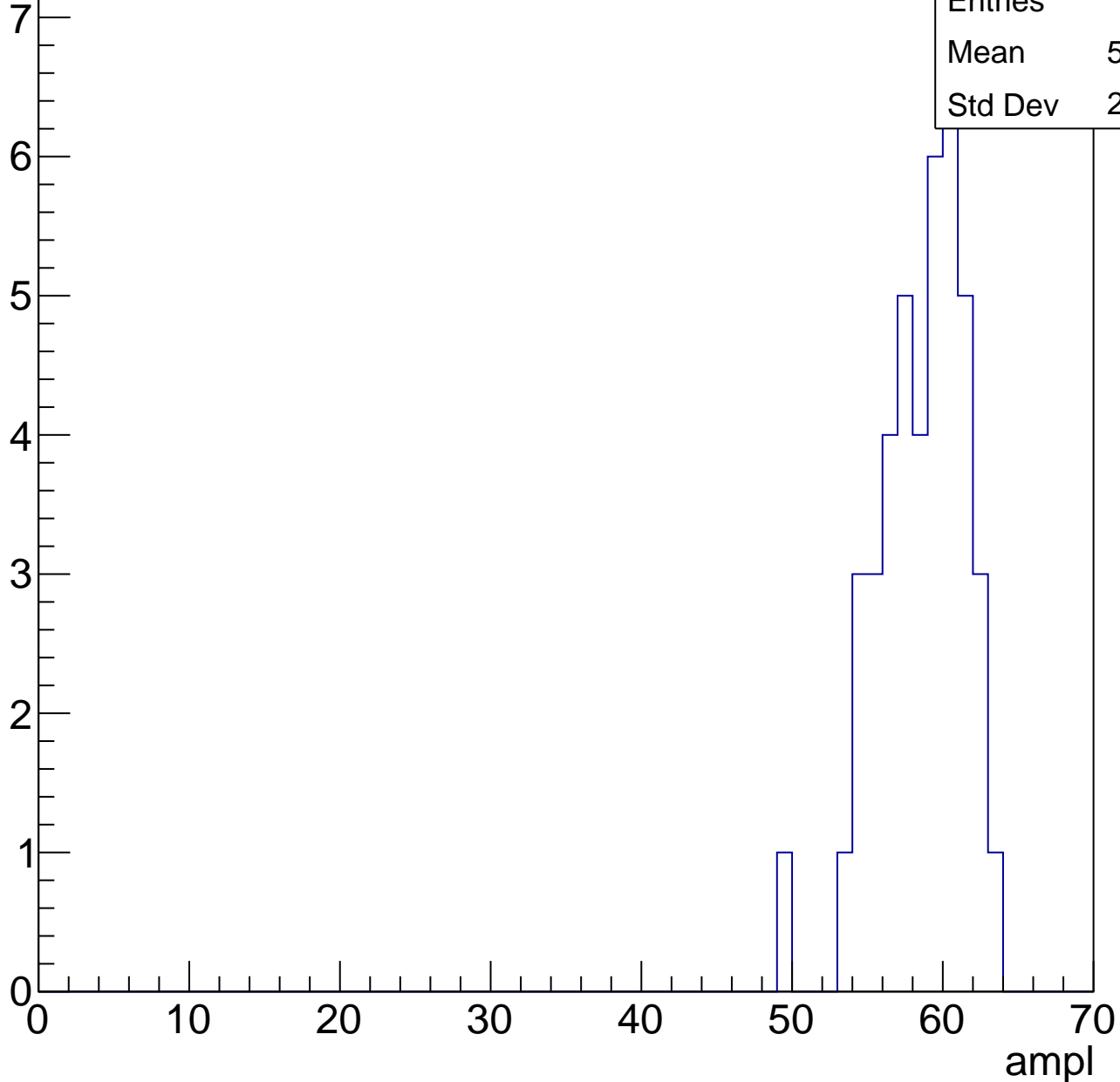
Entries	66
Mean	51.97
Std Dev	3.601



B1L103S, U6-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

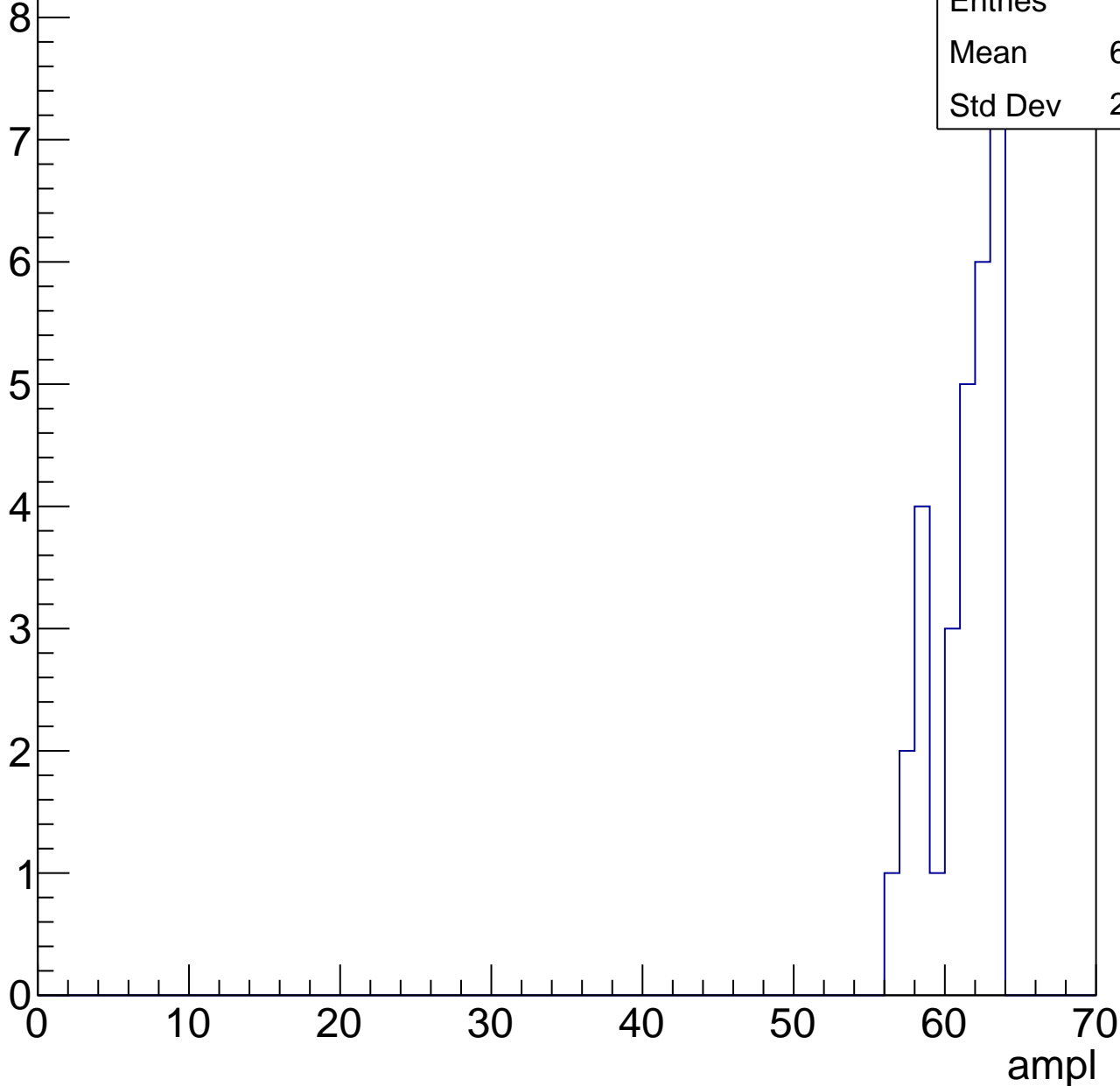


B1L103S, U6-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	60.73
Std Dev	2.128



B1L103S, U6-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

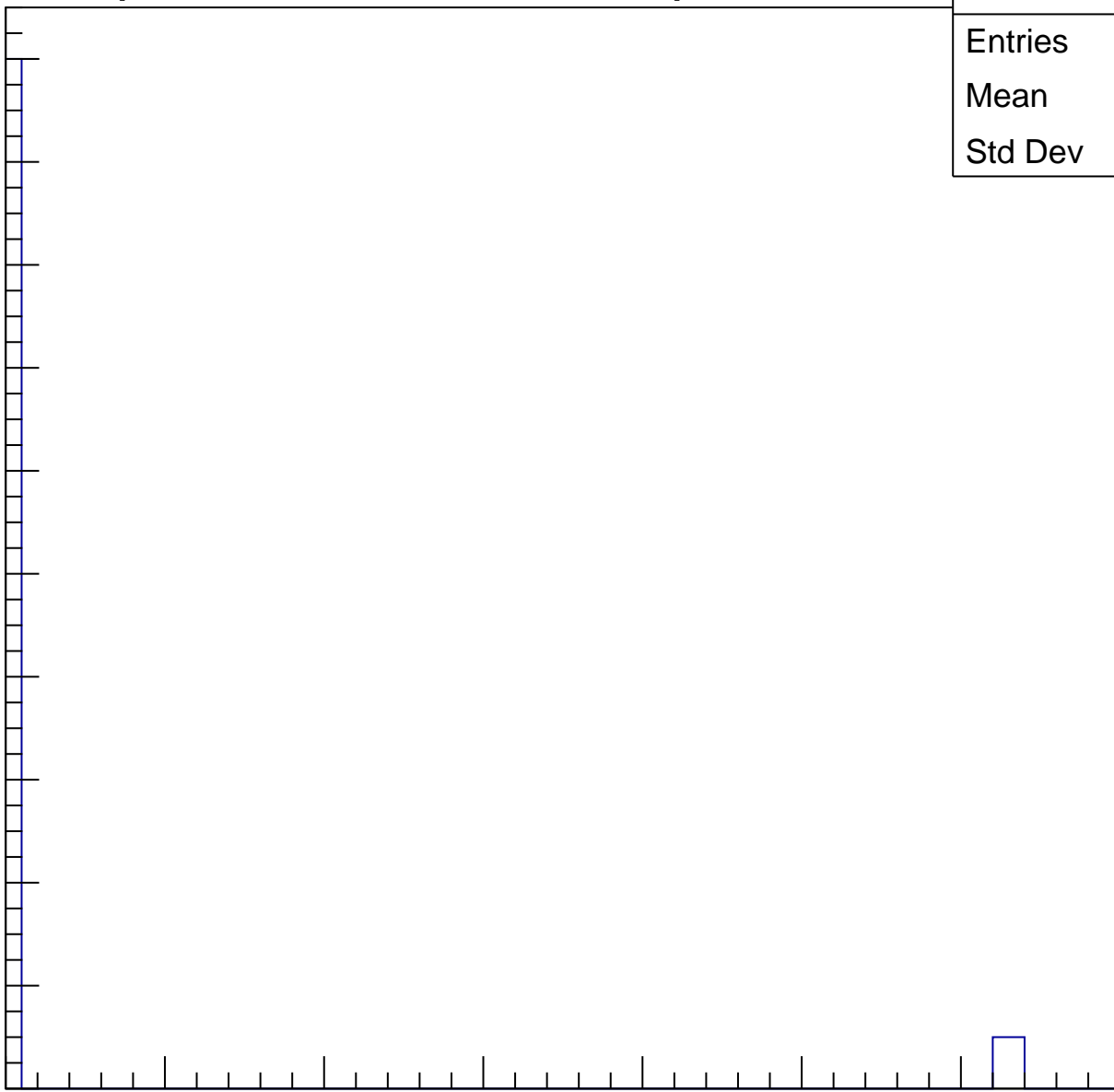
Entries	22
Mean	5.682
Std Dev	17.97

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U6-ch125, adc0

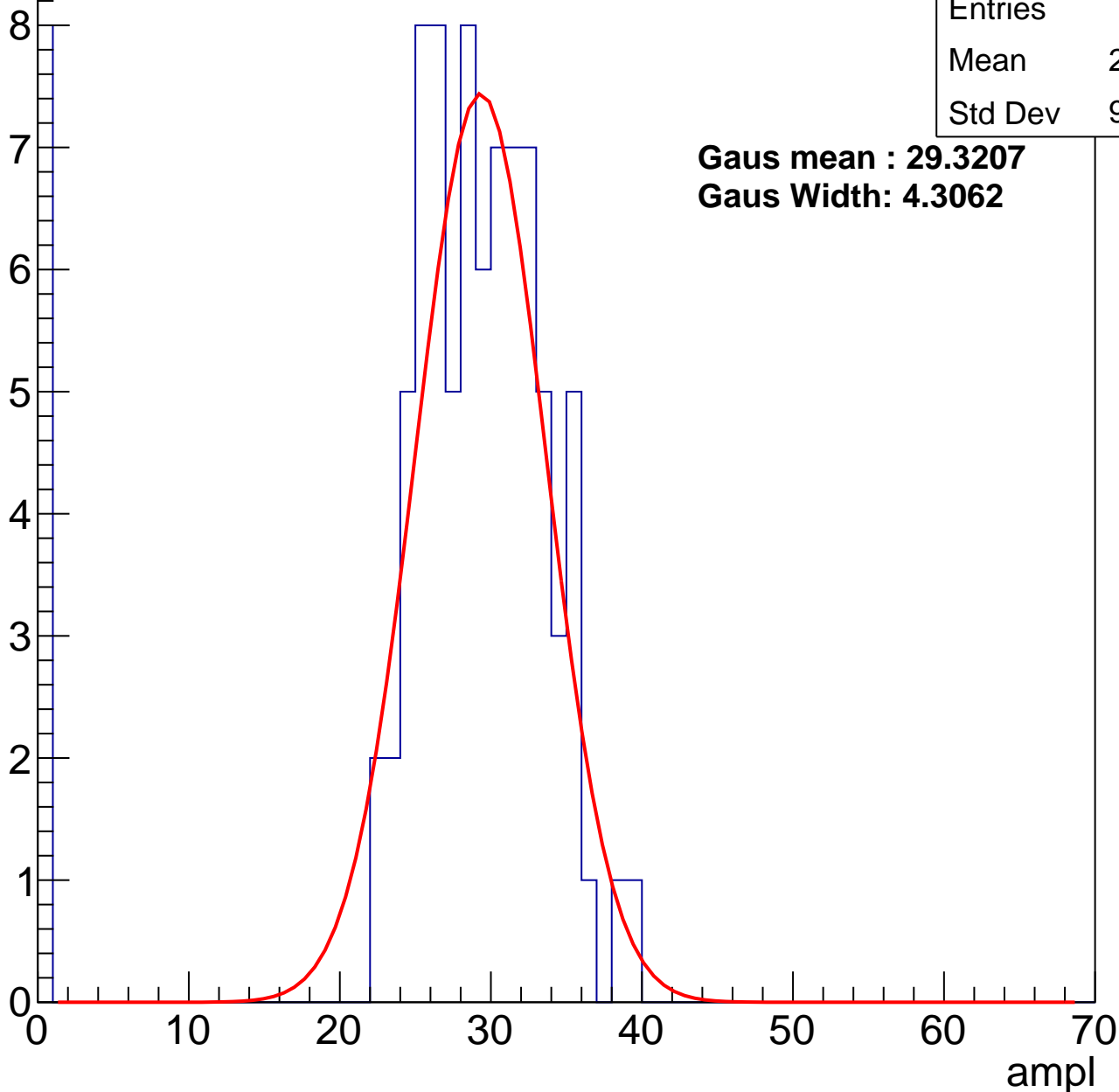
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	26.48
Std Dev	9.088

Gaus mean : 29.3207

Gaus Width: 4.3062



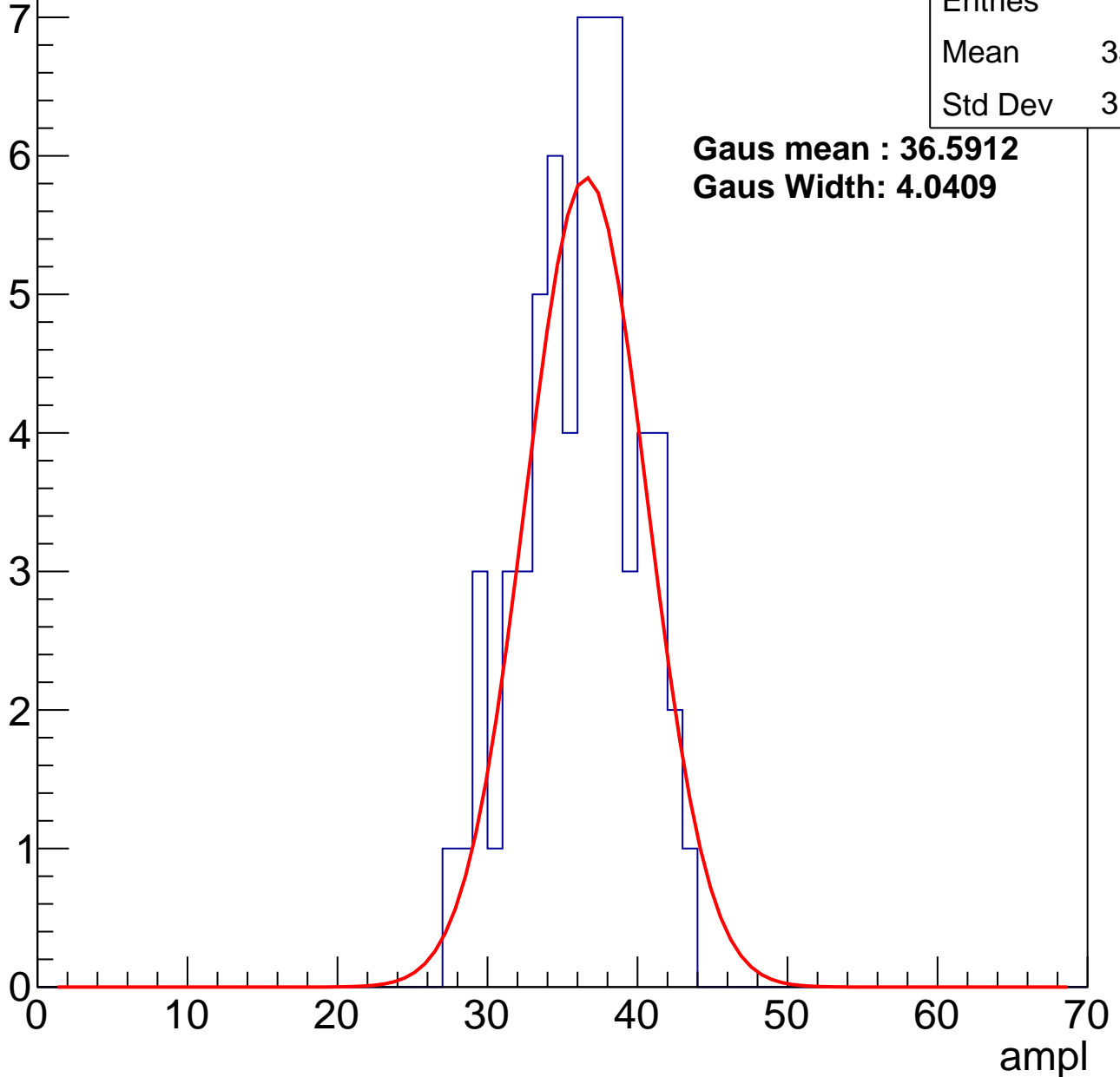
B1L103S, U6-ch125, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	35.73
Std Dev	3.738

Gaus mean : 36.5912
Gaus Width: 4.0409



B1L103S, U6-ch125, adc2

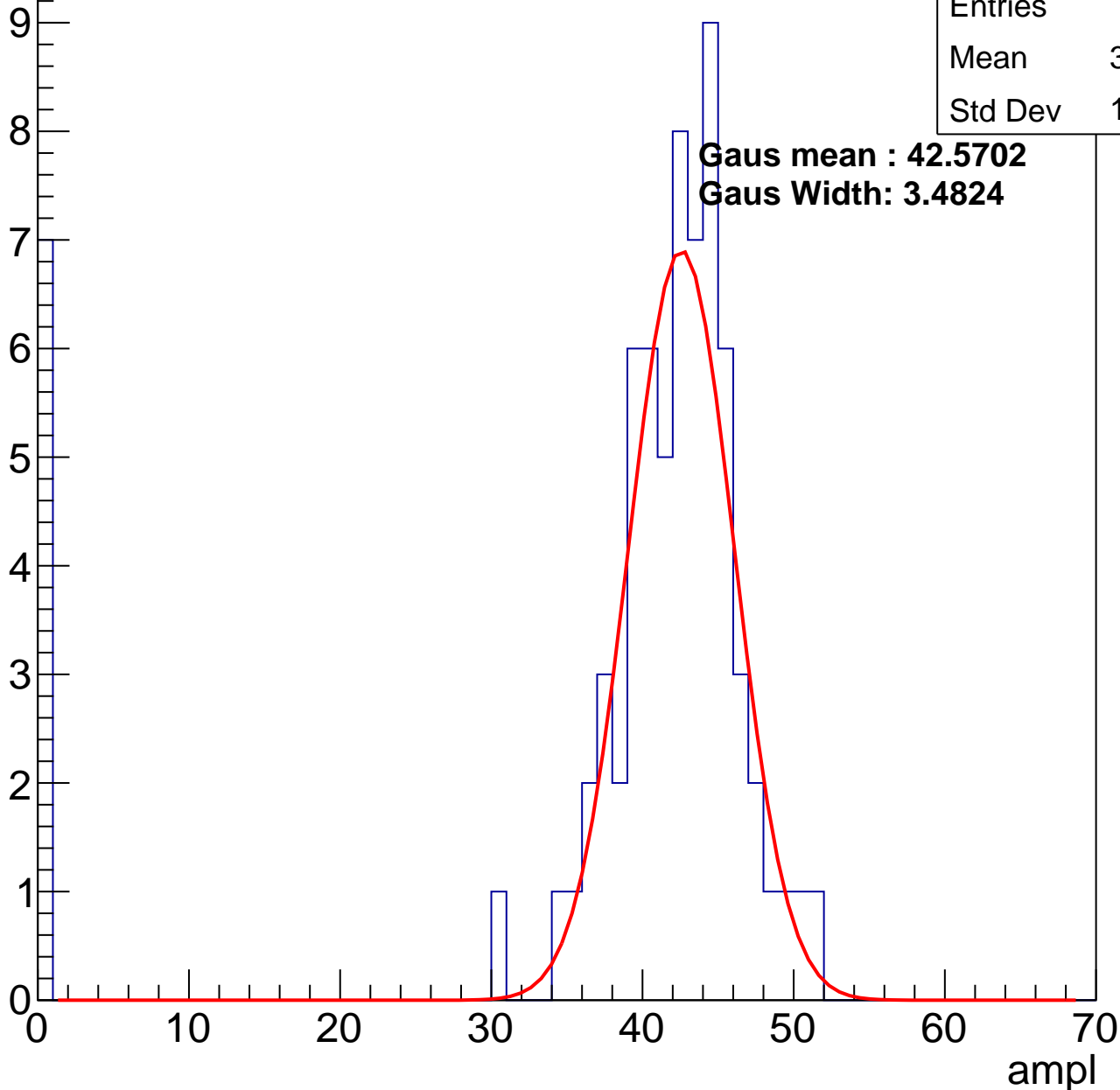
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	37.95
Std Dev	12.87

Gaus mean : 42.5702

Gaus Width: 3.4824

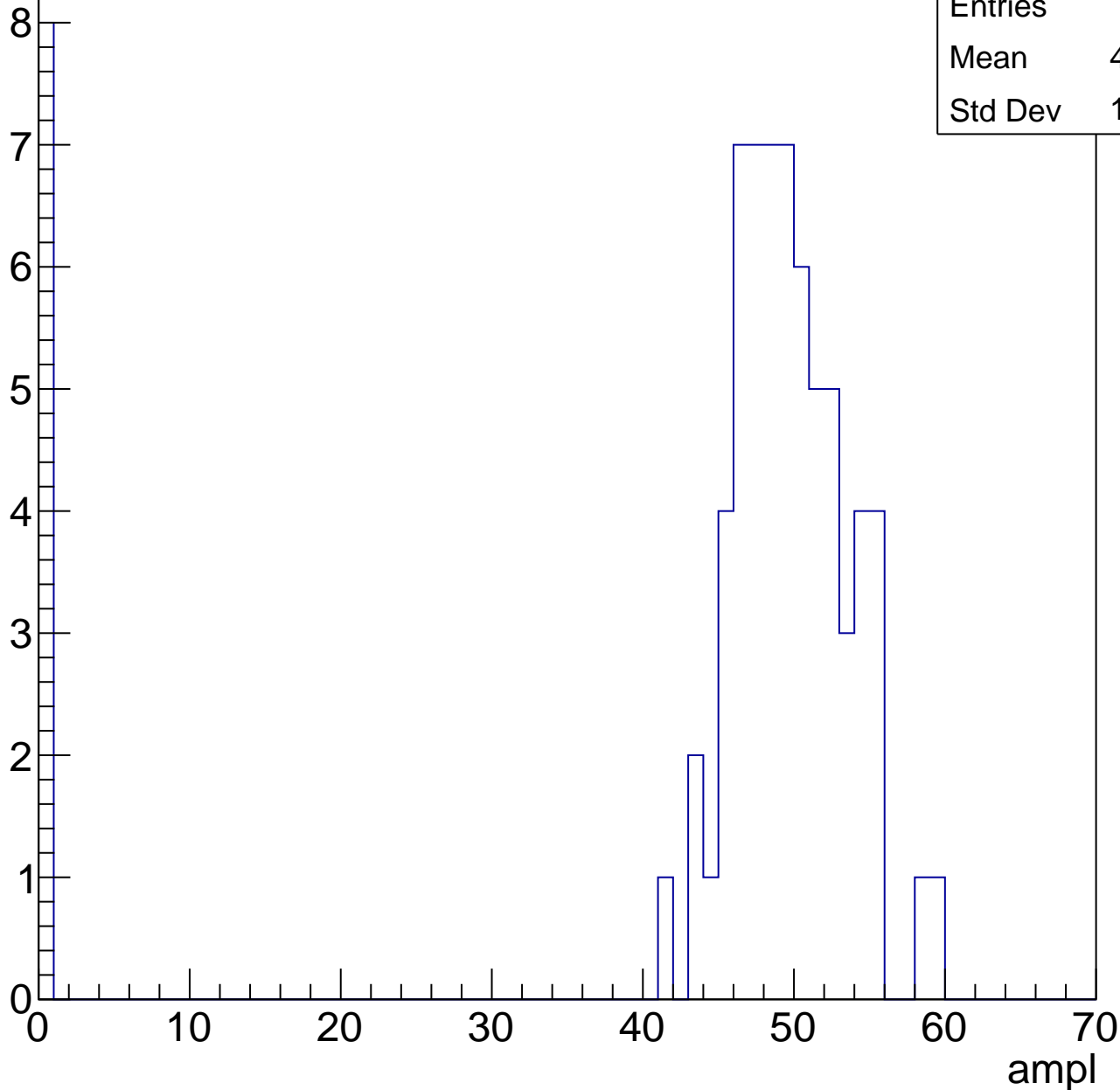


B1L103S, U6-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

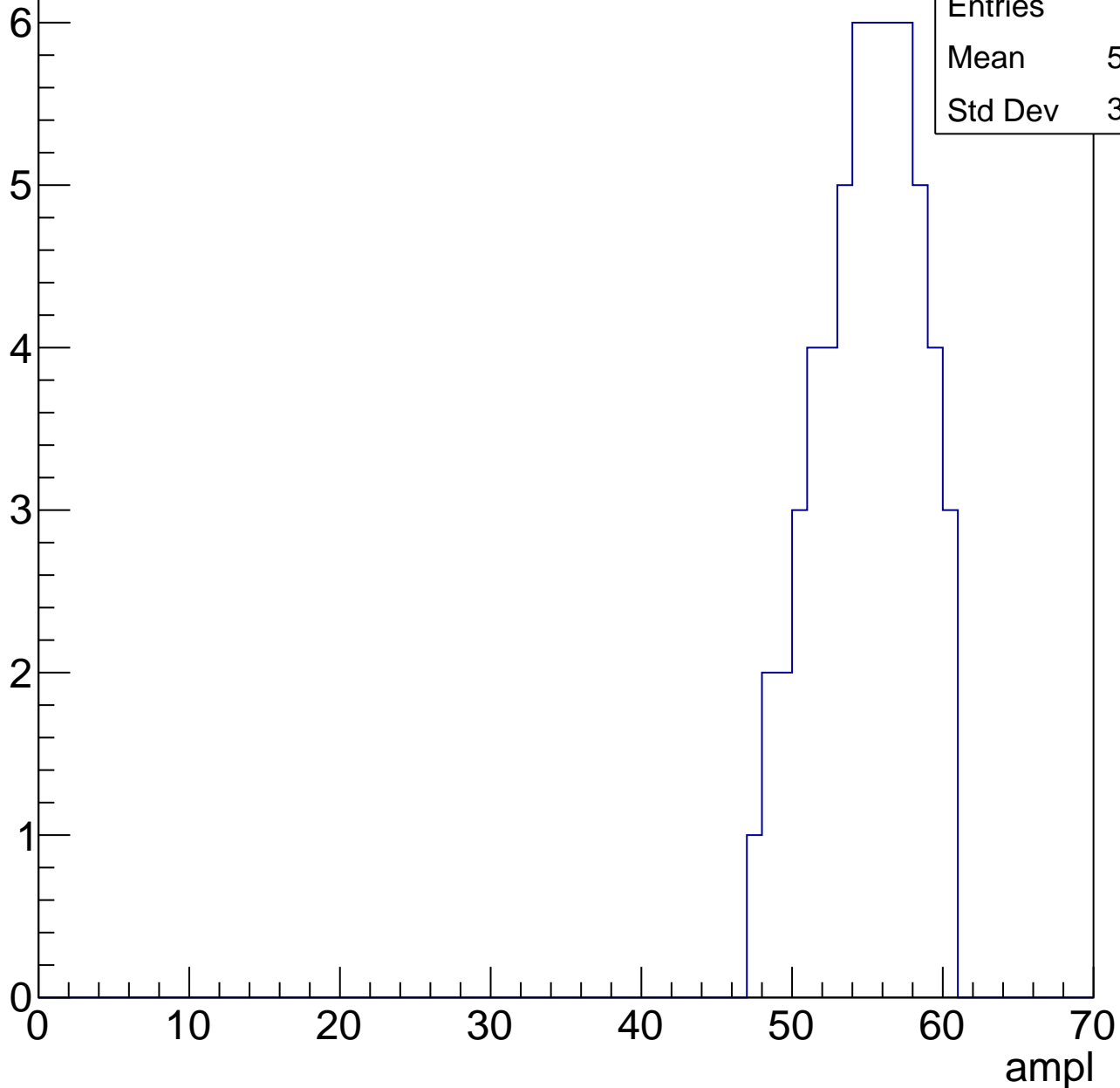
Entries	73
Mean	43.95
Std Dev	15.79



B1L103S, U6-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



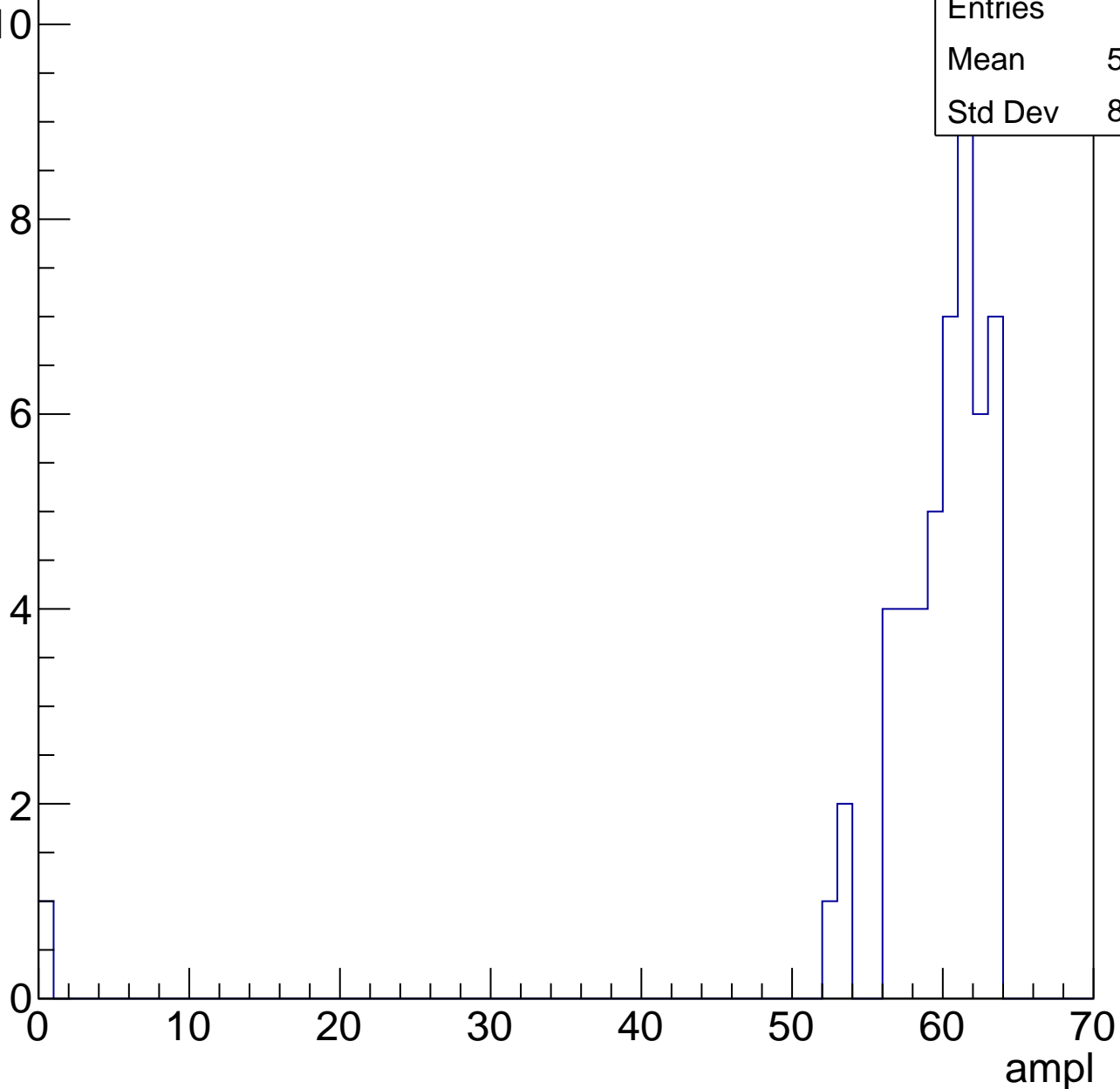
Entries	57
Mean	54.49
Std Dev	3.336

B1L103S, U6-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

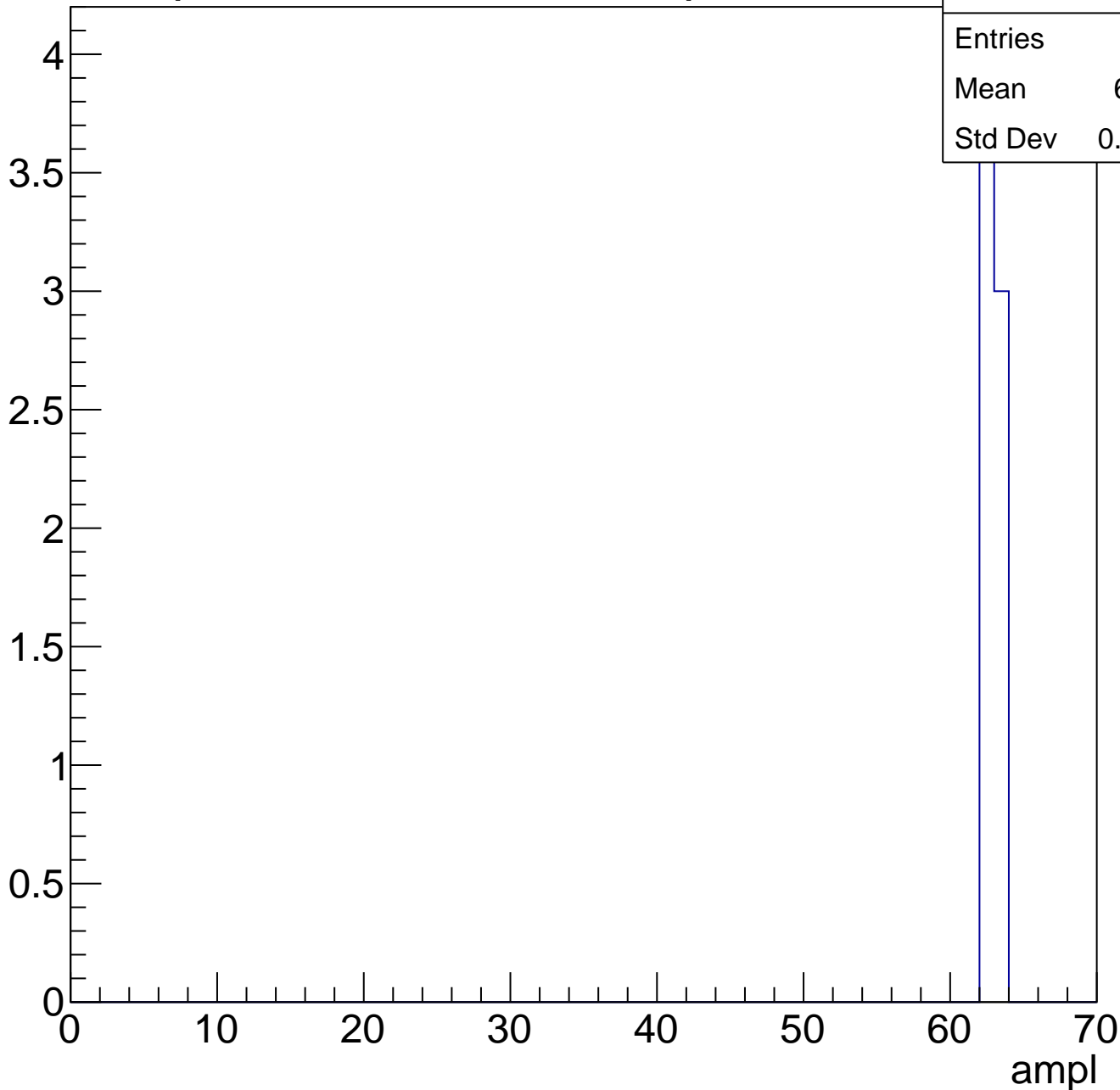
Entries	51
Mean	58.43
Std Dev	8.694



B1L103S, U6-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

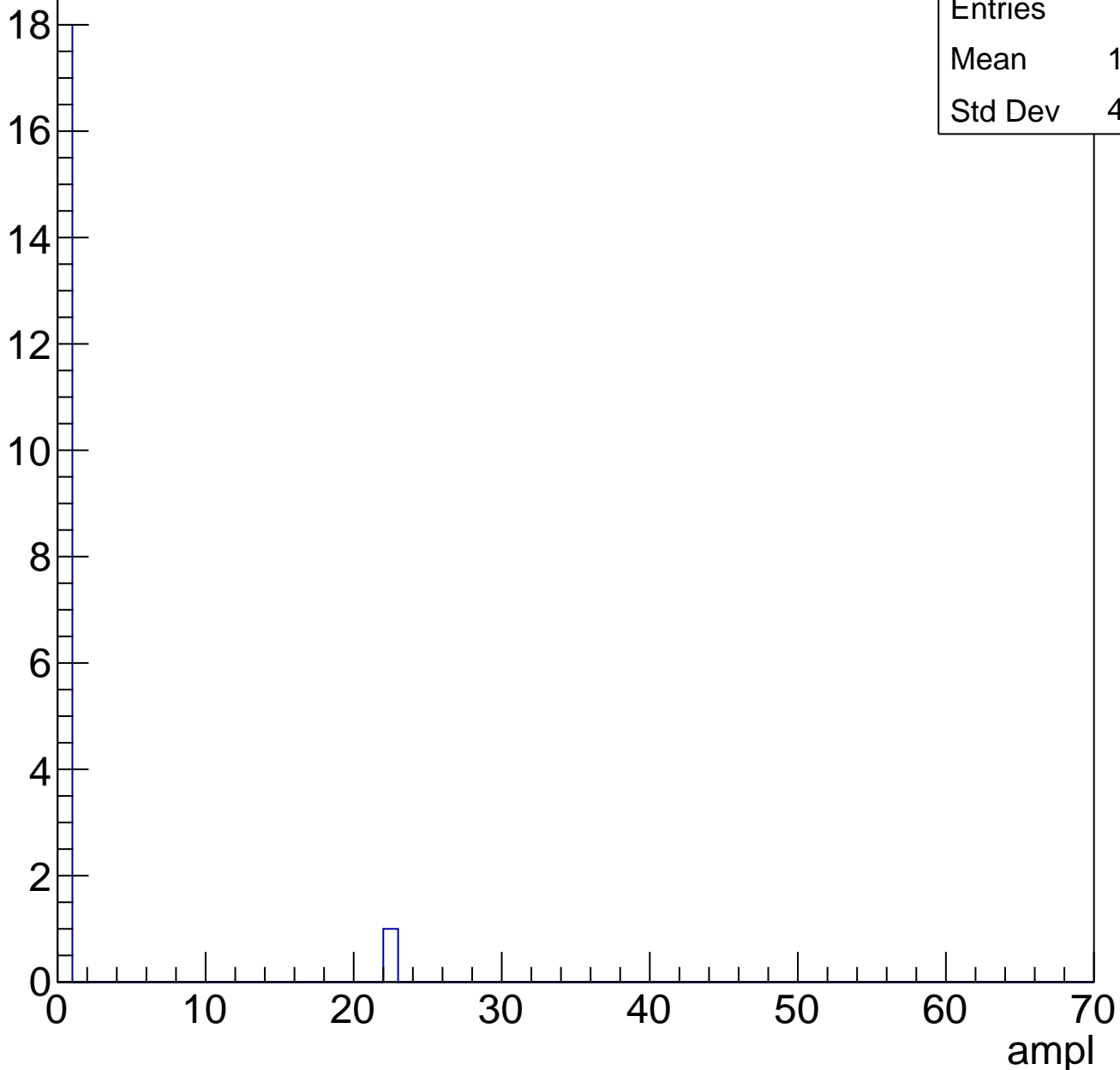


B1L103S, U6-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U6-ch126, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	18.8
Std Dev	10.94

Gaus mean : 24.9557

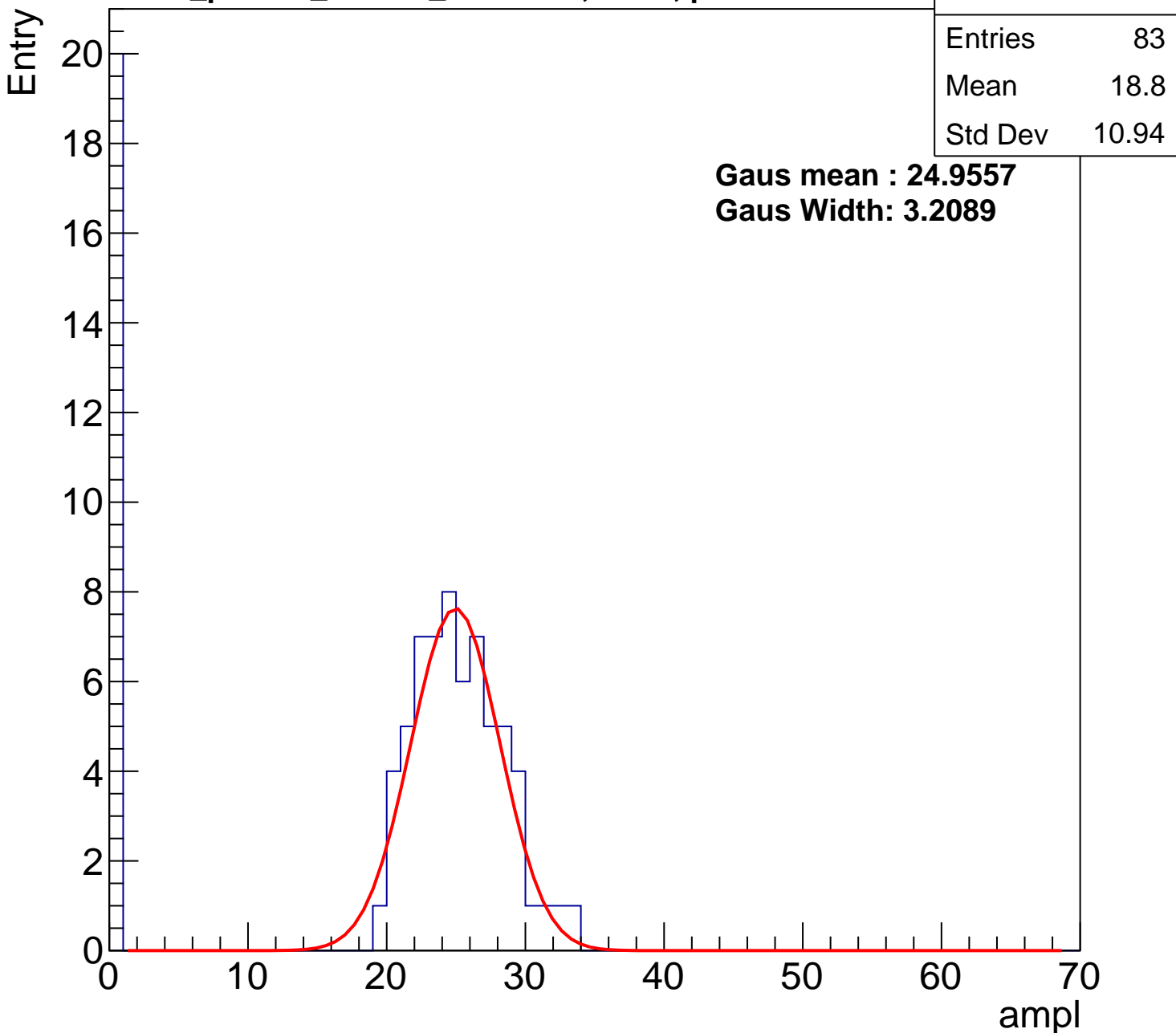
Gaus Width: 3.2089

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch126, adc1

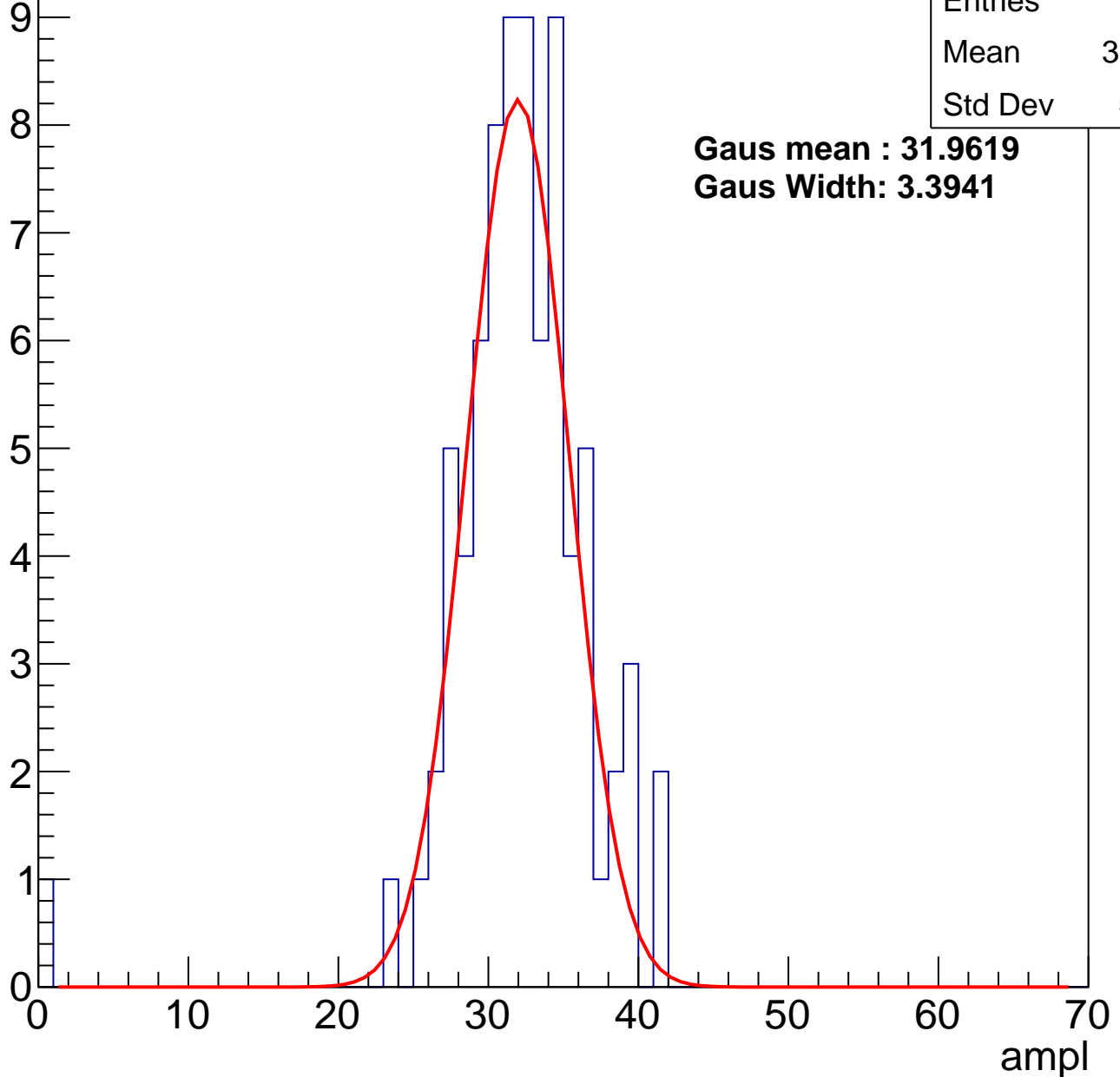
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.59
Std Dev	5.14

Gaus mean : 31.9619

Gaus Width: 3.3941



B1L103S, U6-ch126, adc2

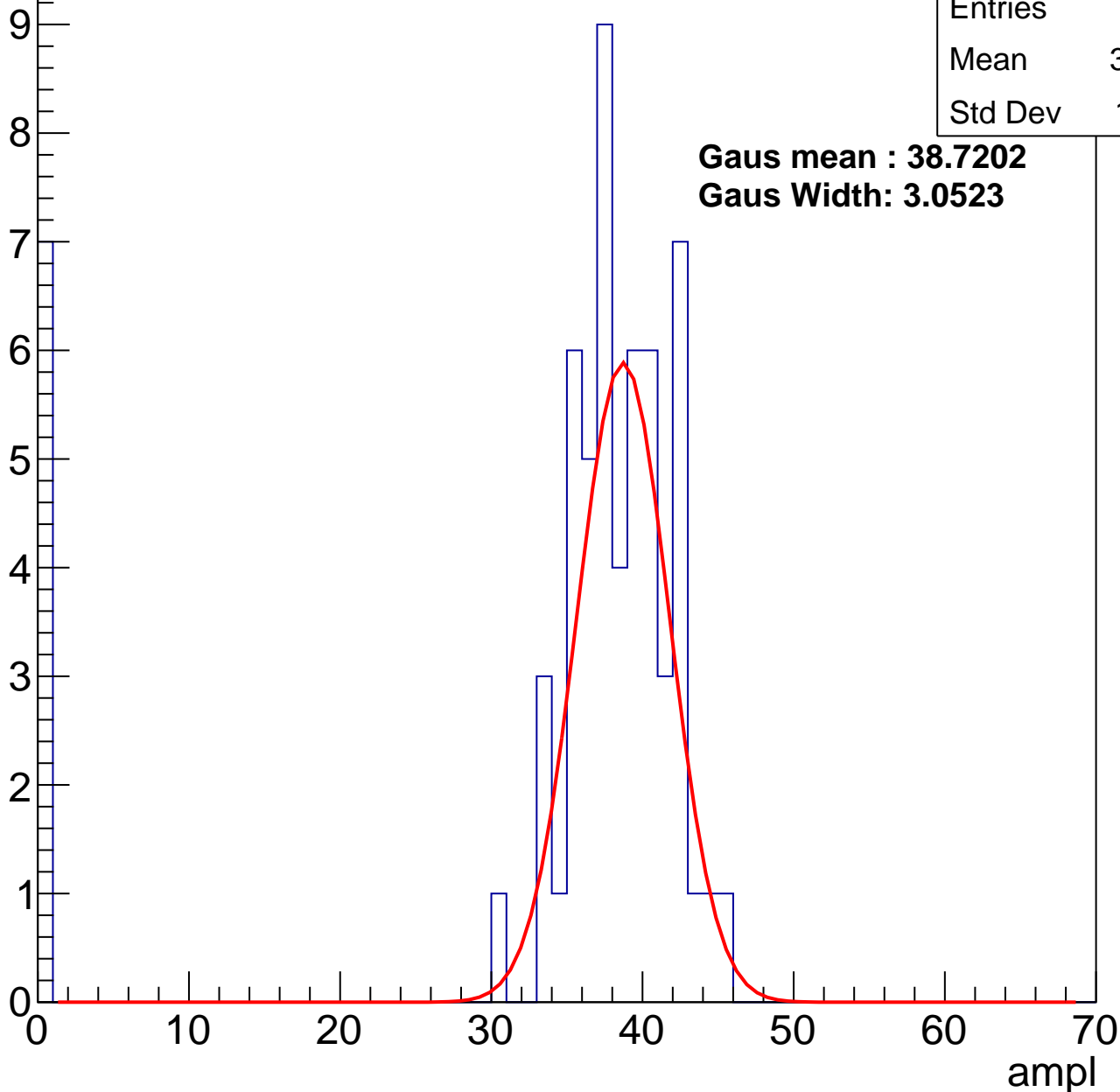
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33.79
Std Dev	12.51

Gaus mean : 38.7202

Gaus Width: 3.0523

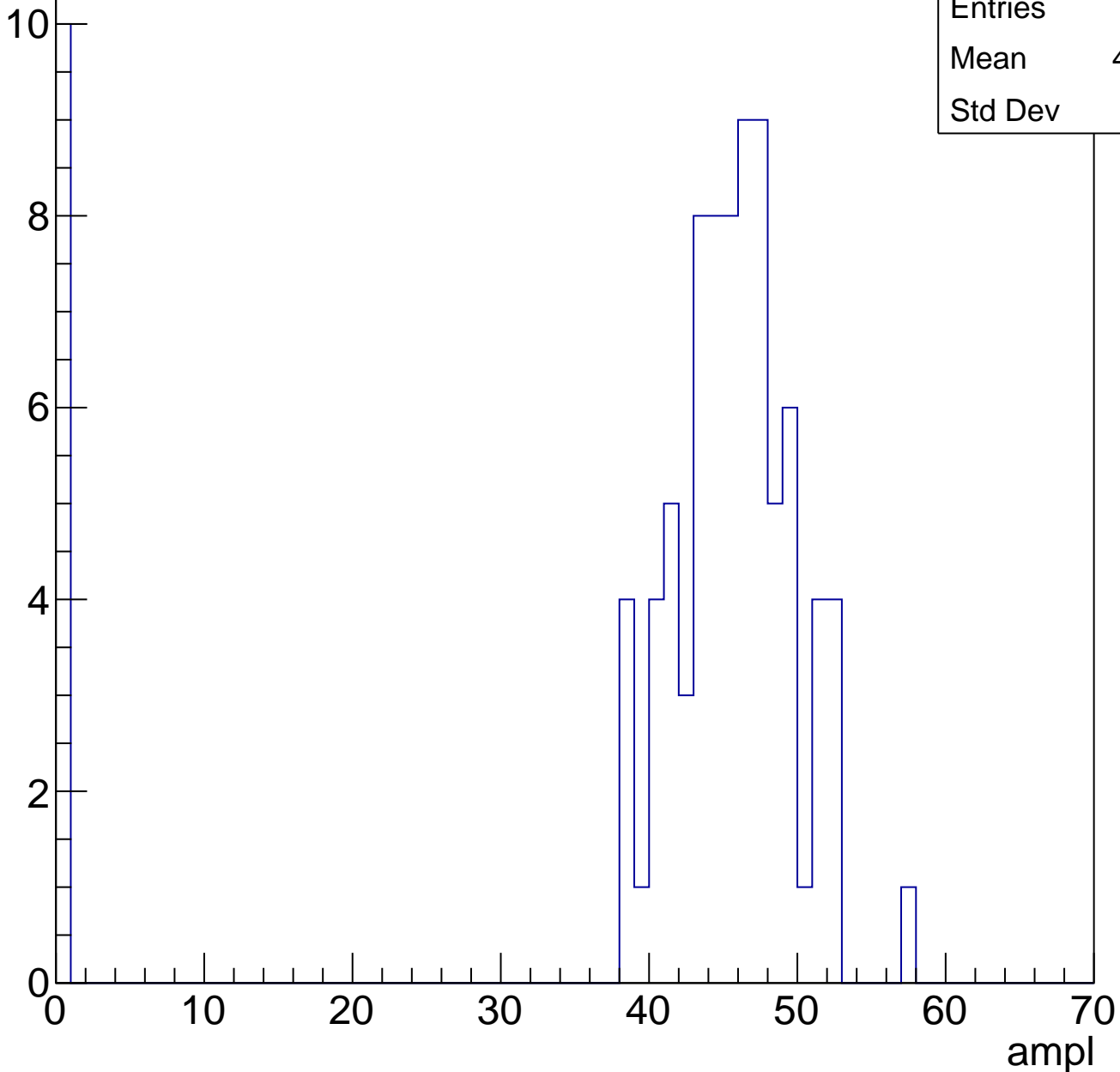


B1L103S, U6-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	40.31
Std Dev	14.7

Entry

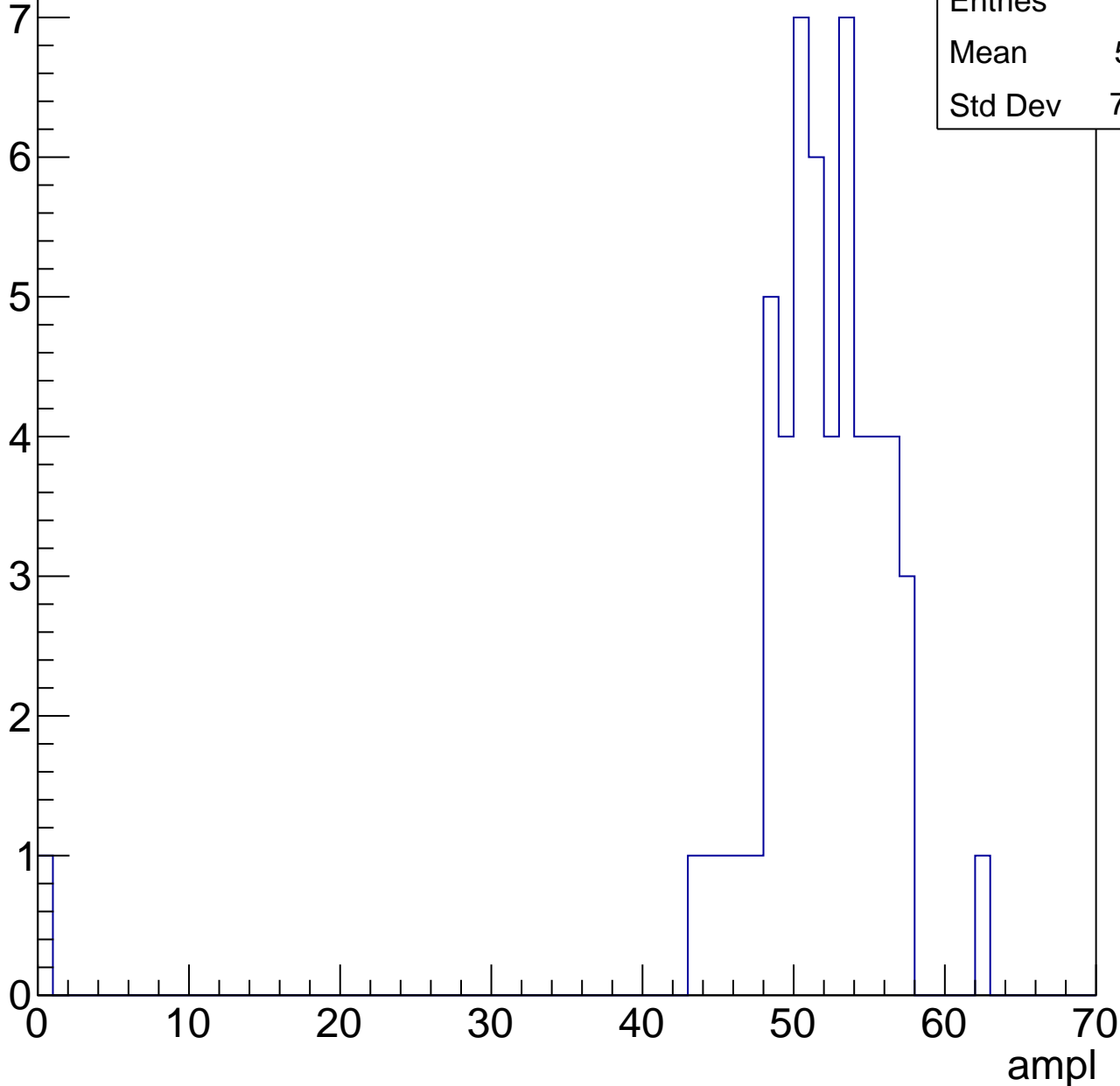


B1L103S, U6-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	50.71
Std Dev	7.764

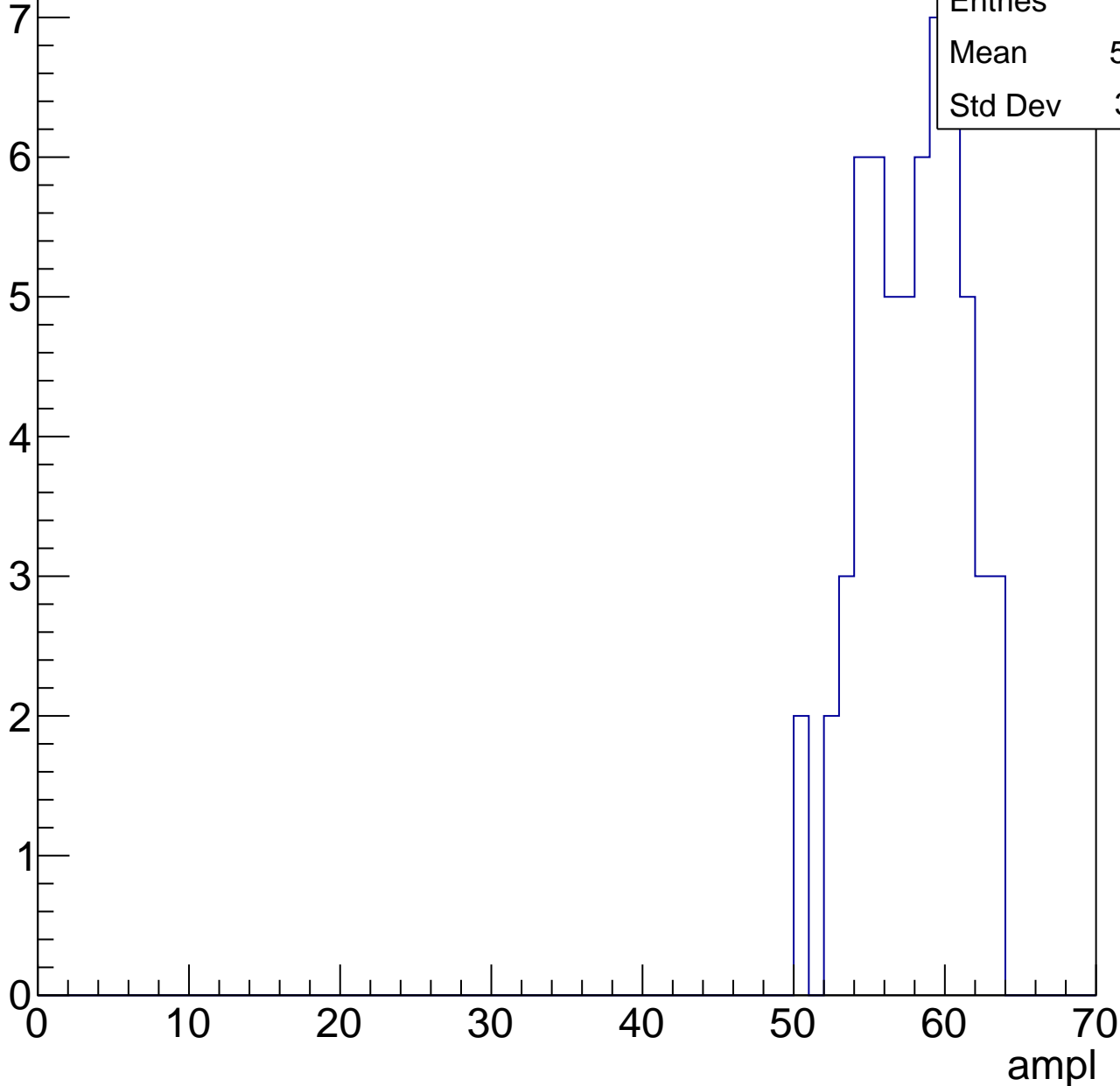


B1L103S, U6-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.38
Std Dev	3.241

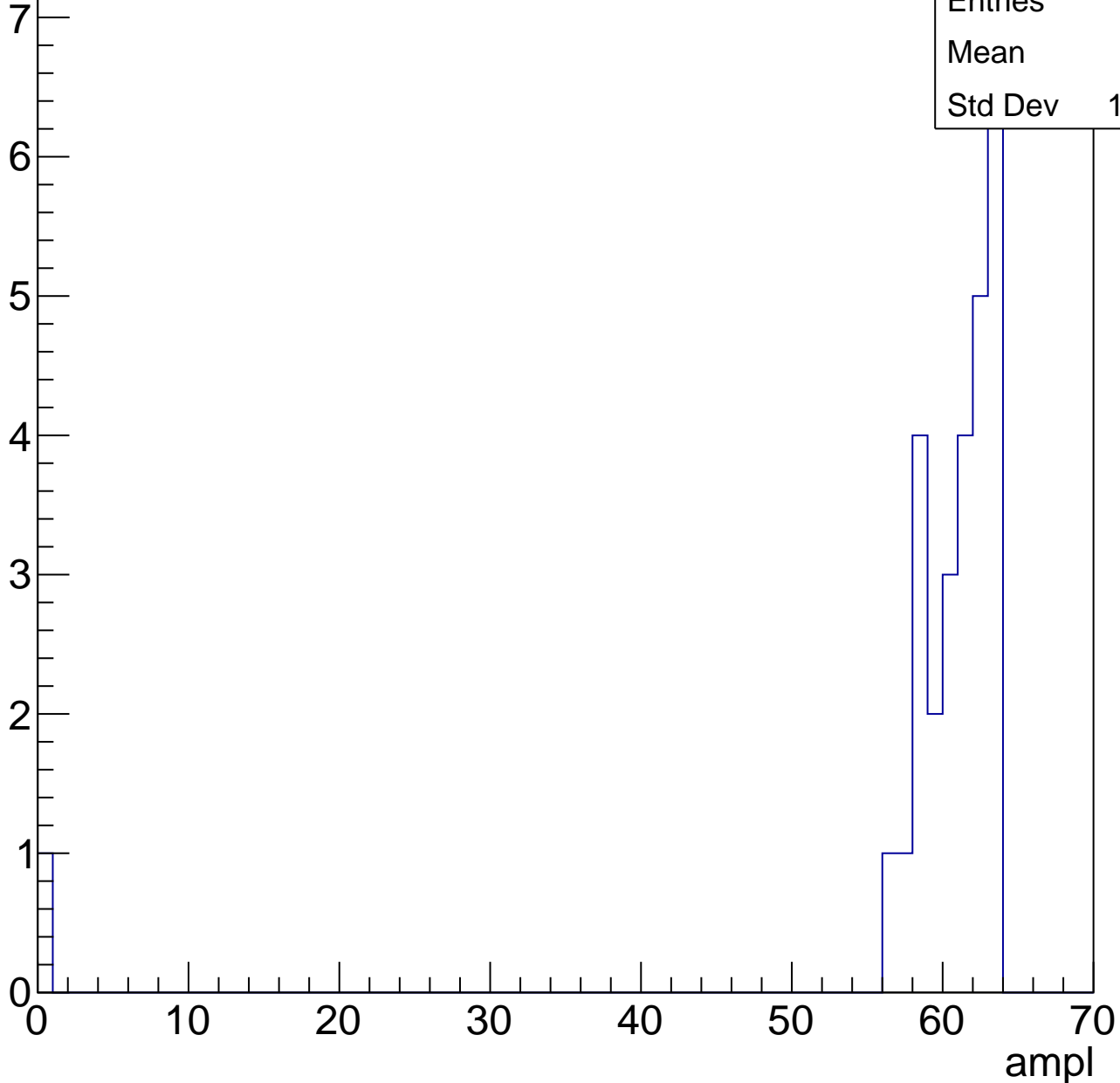


B1L103S, U6-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.5
Std Dev	11.44

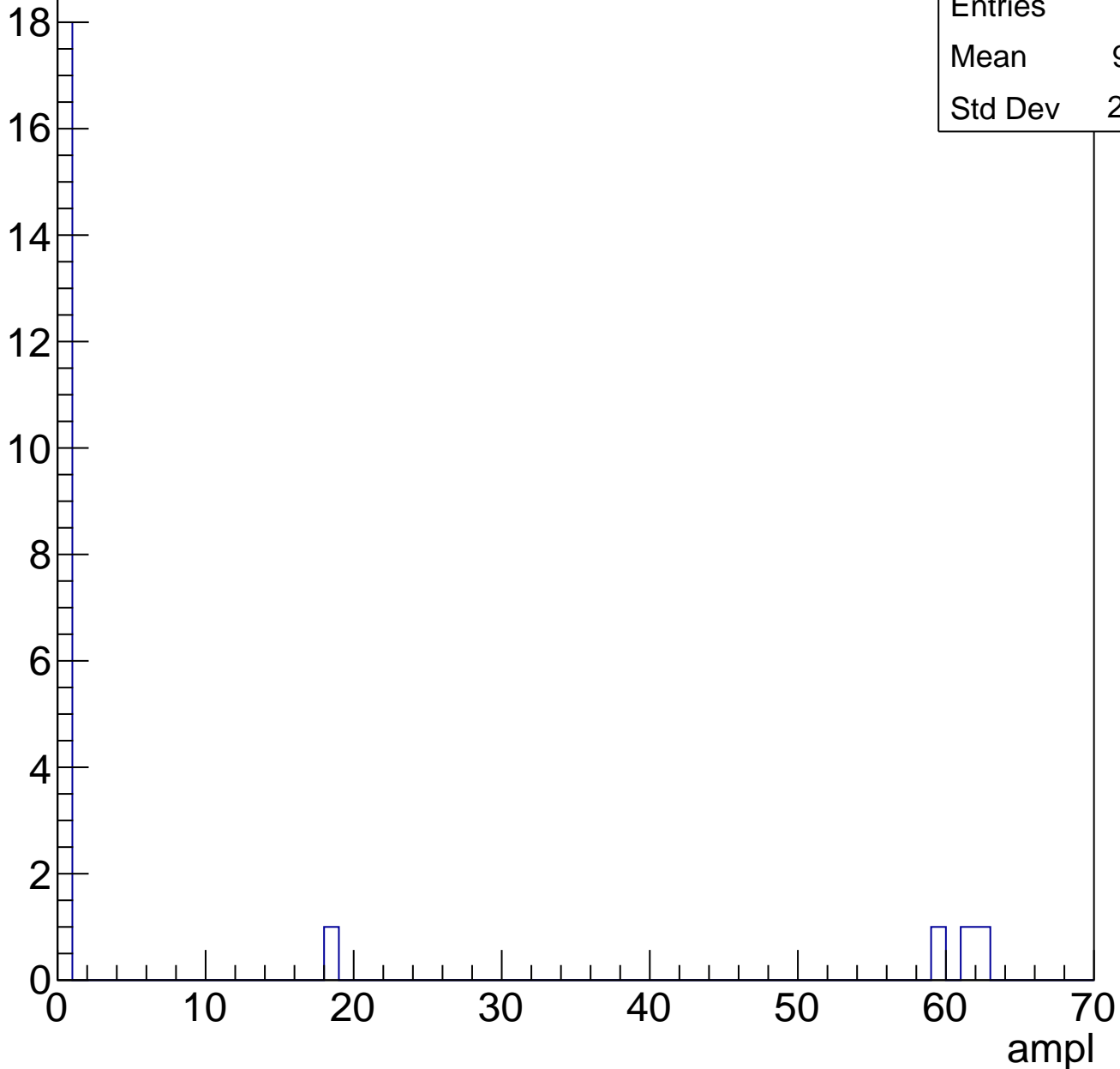


B1L103S, U6-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.091
Std Dev	20.84

Entry



B1L103S, U6-ch127, adc0

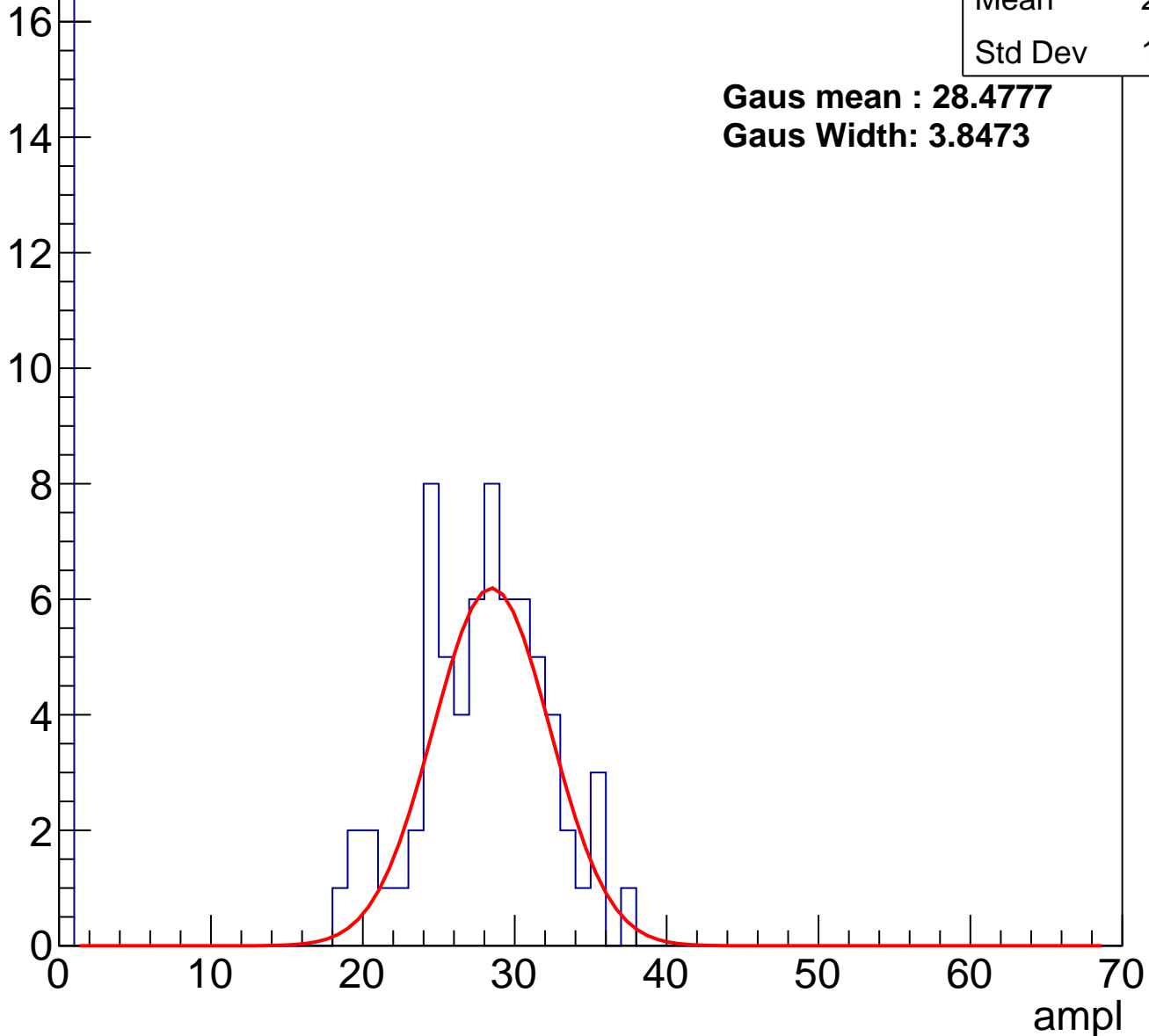
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.01
Std Dev	11.61

Gaus mean : 28.4777

Gaus Width: 3.8473

Entry



B1L103S, U6-ch127, adc1

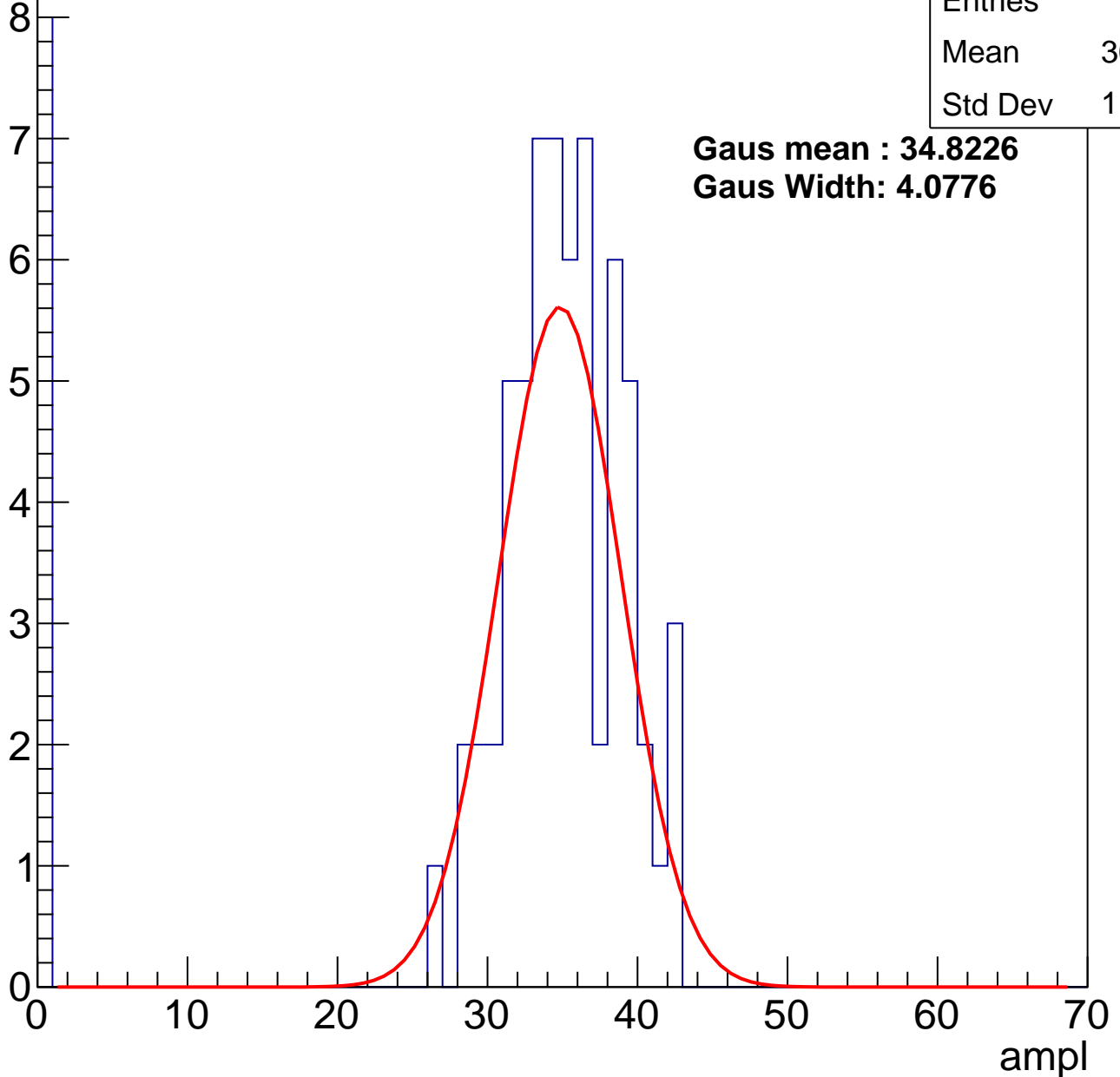
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.85
Std Dev	11.52

Gaus mean : 34.8226

Gaus Width: 4.0776



B1L103S, U6-ch127, adc2

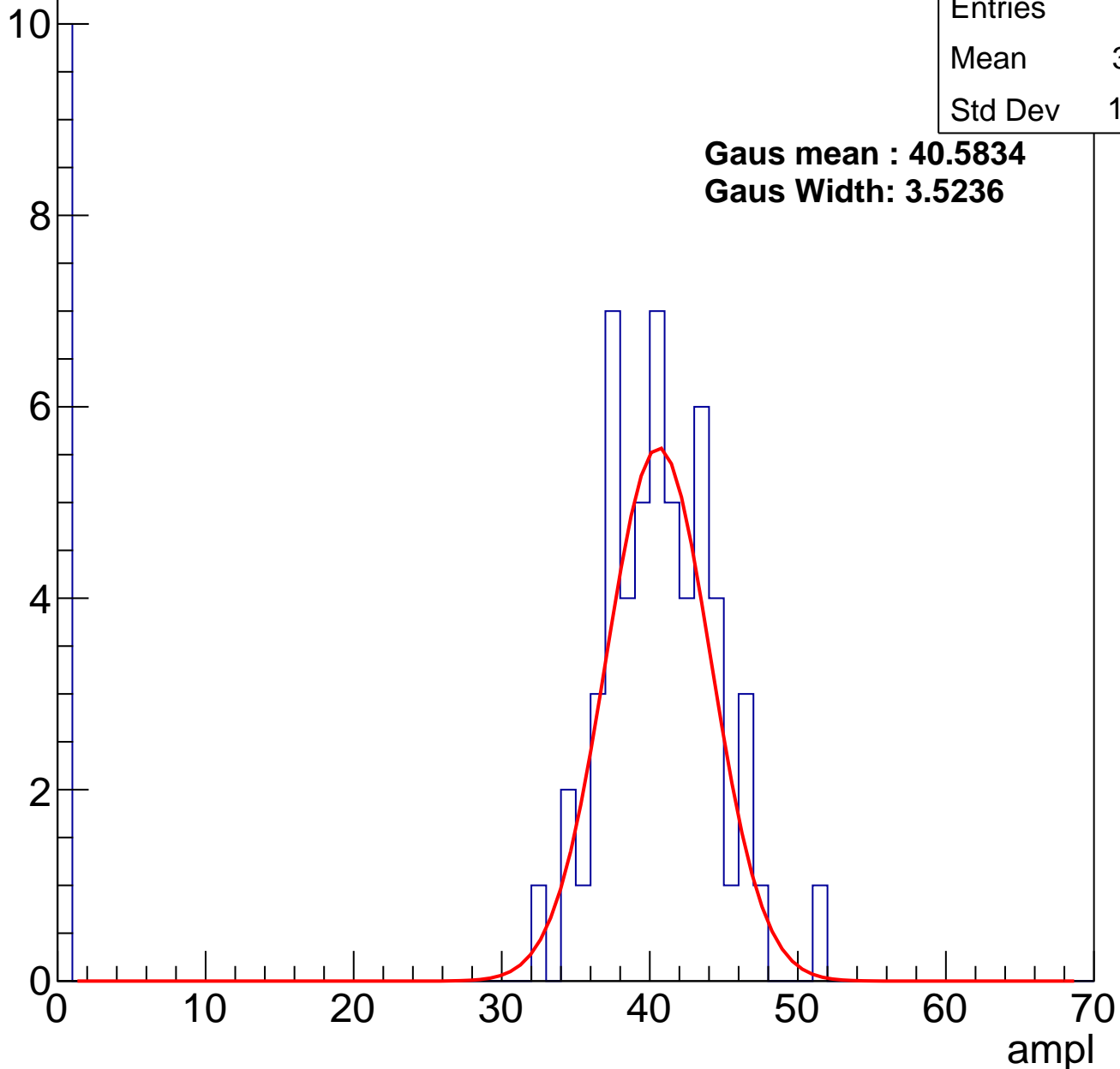
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	34.11
Std Dev	14.92

Gaus mean : 40.5834

Gaus Width: 3.5236

Entry

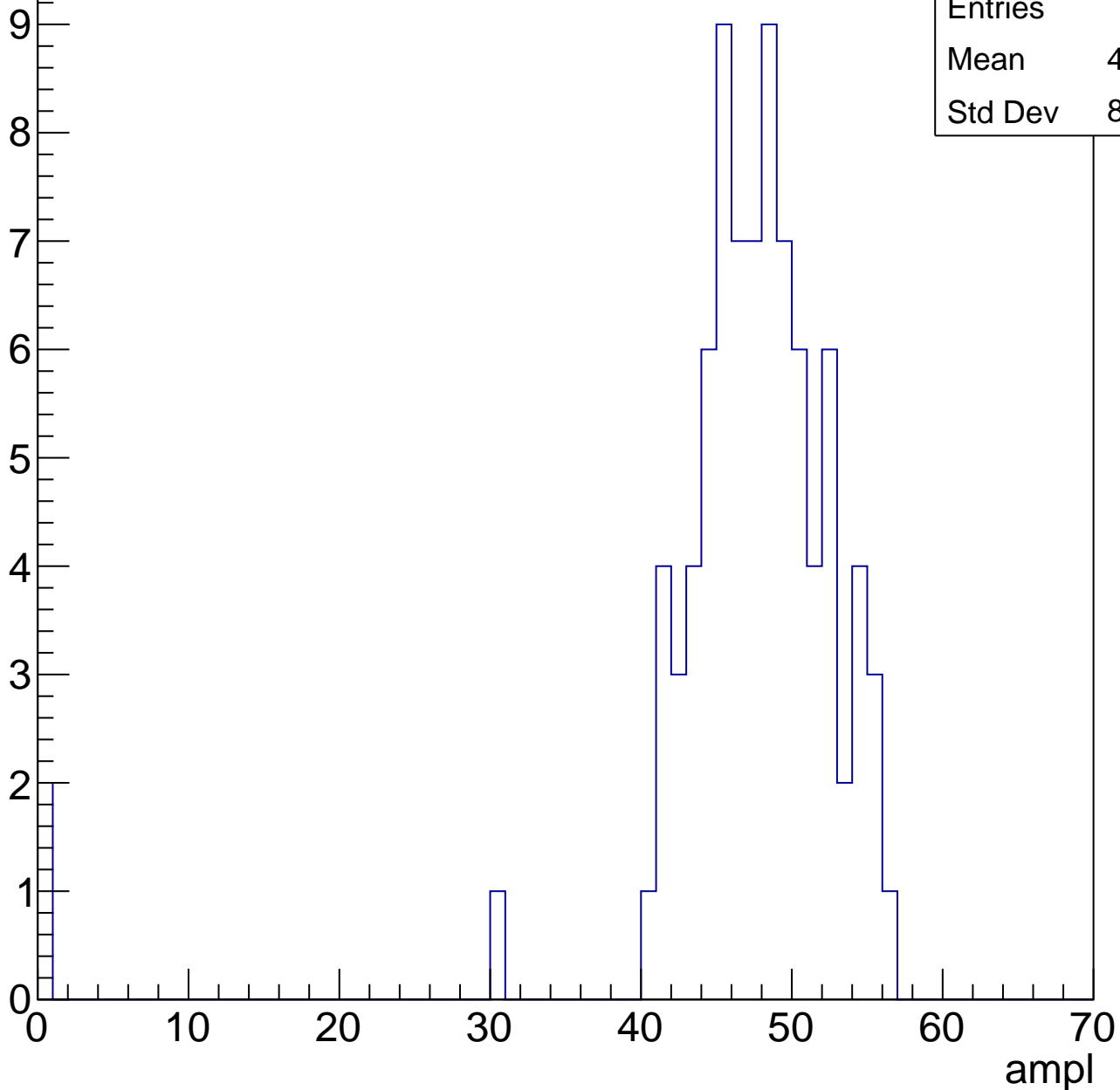


B1L103S, U6-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	46.35
Std Dev	8.315

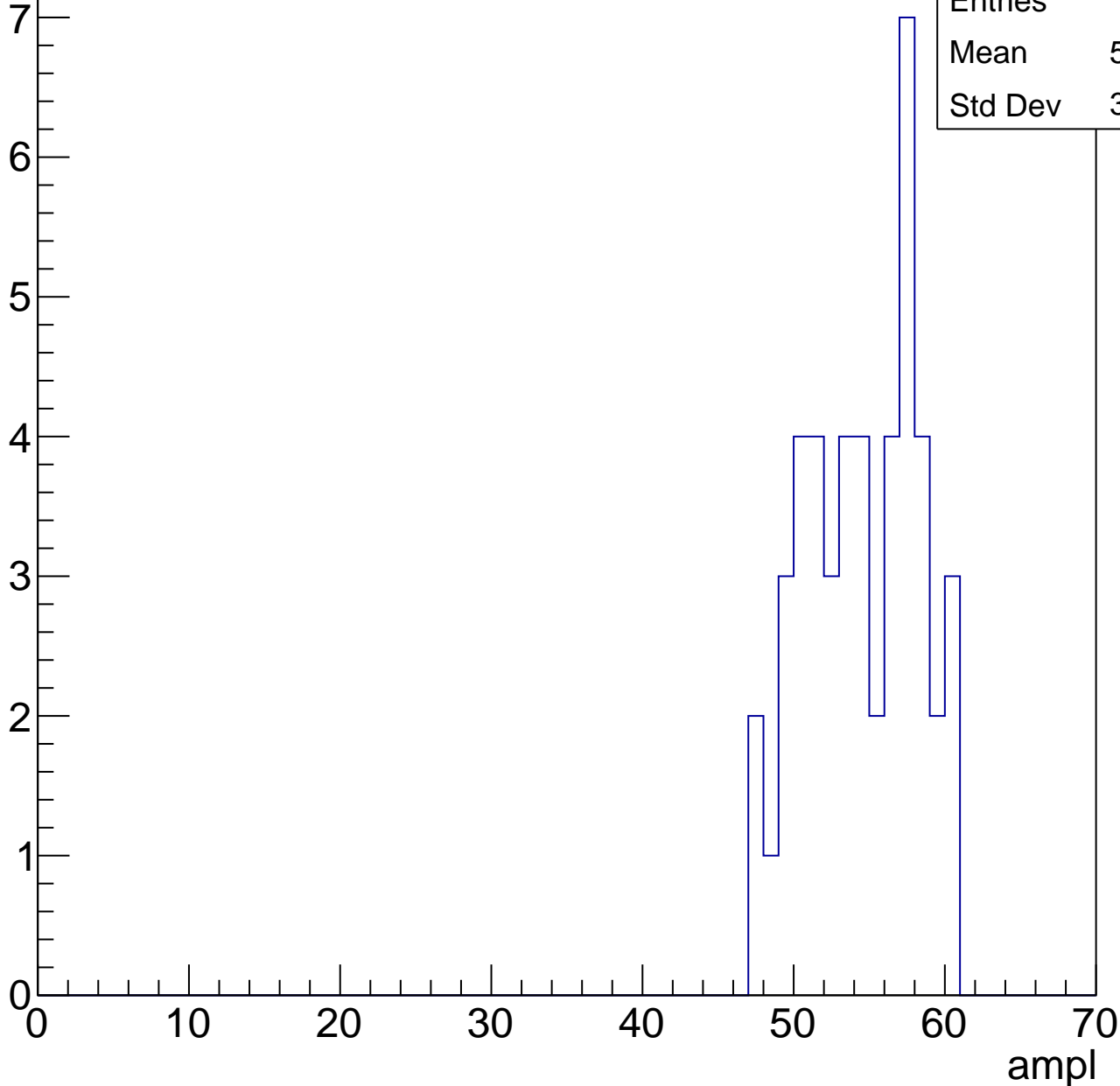


B1L103S, U6-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	54.04
Std Dev	3.655

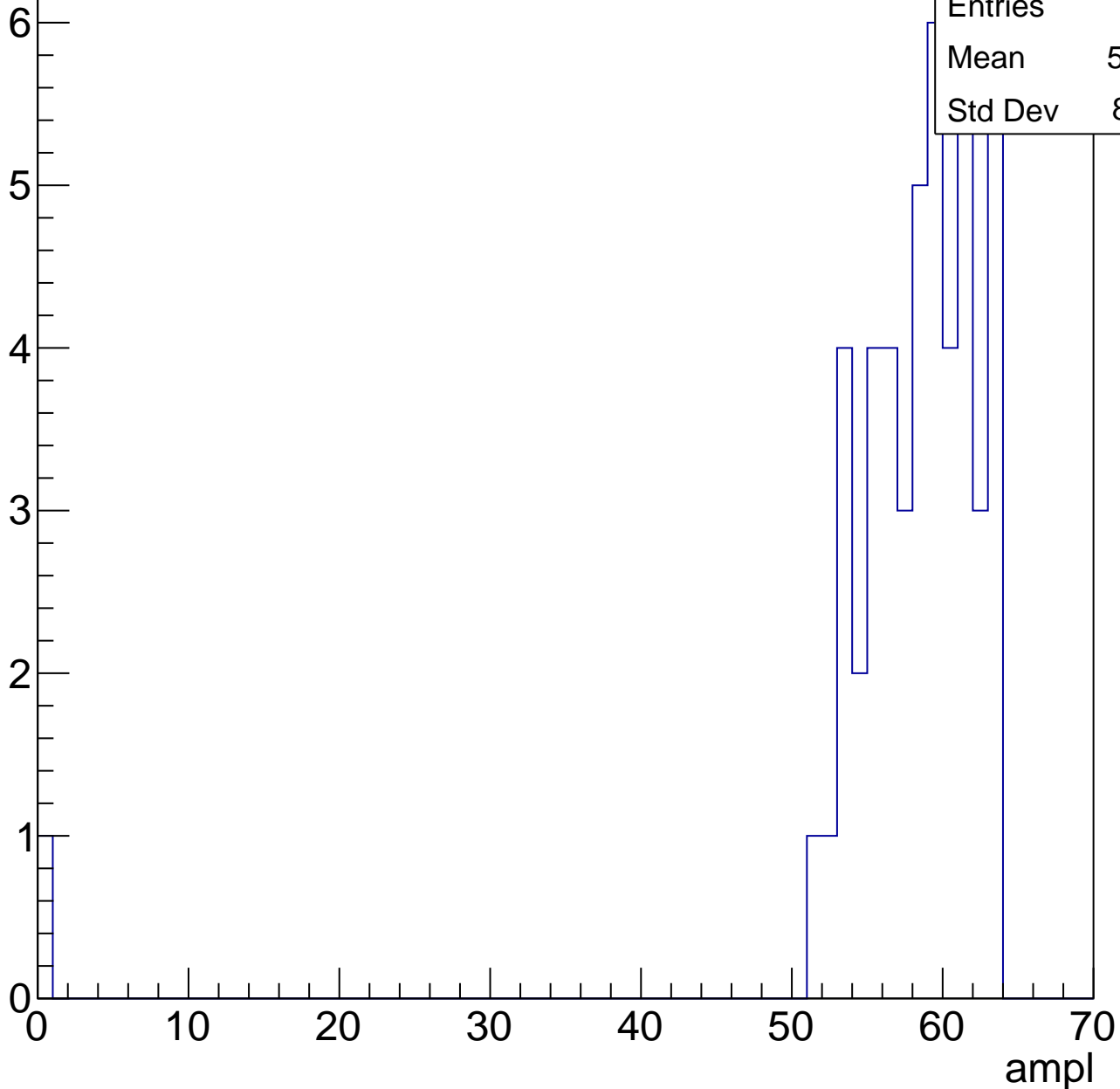


B1L103S, U6-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.04
Std Dev	8.791

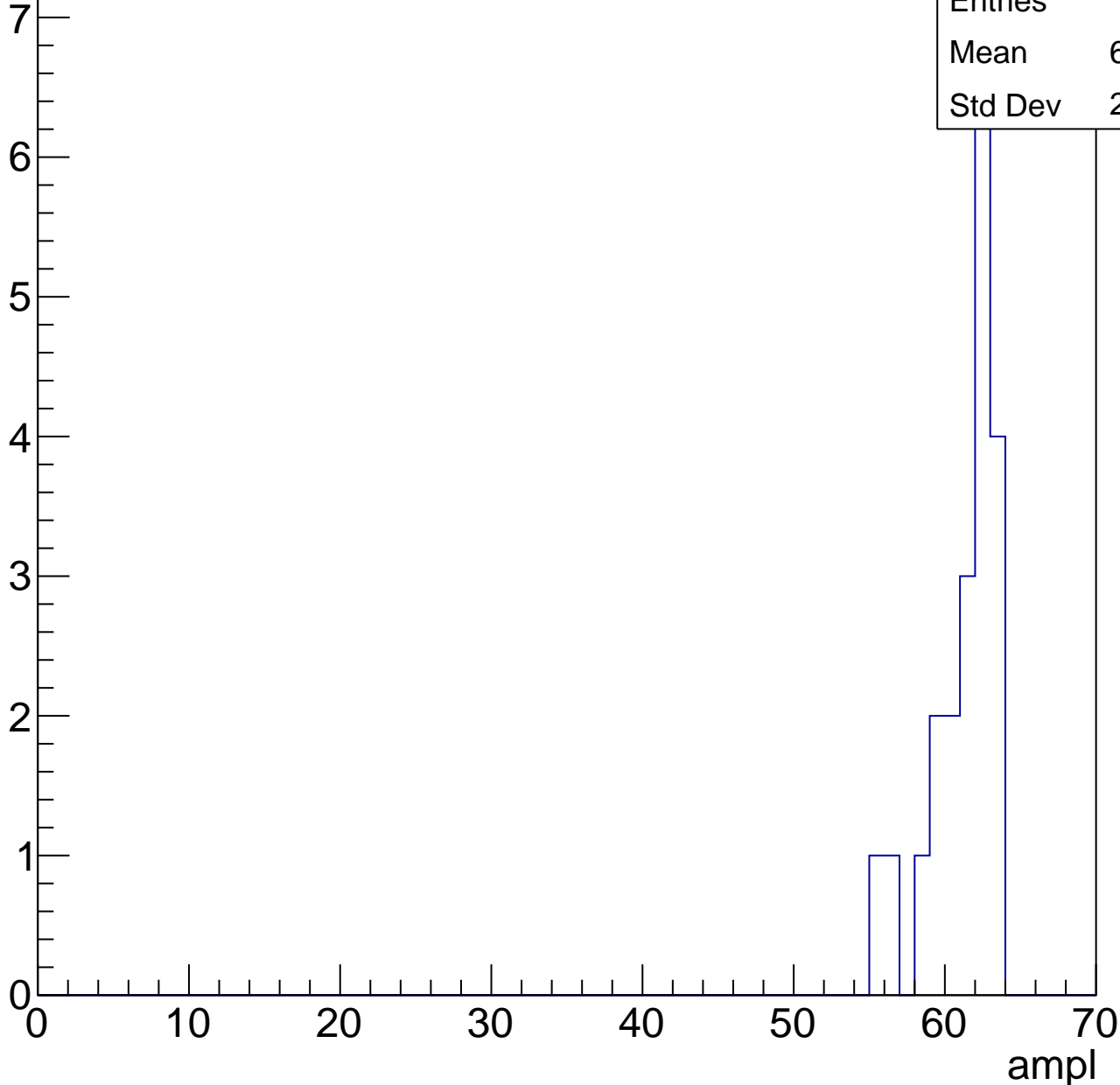


B1L103S, U6-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

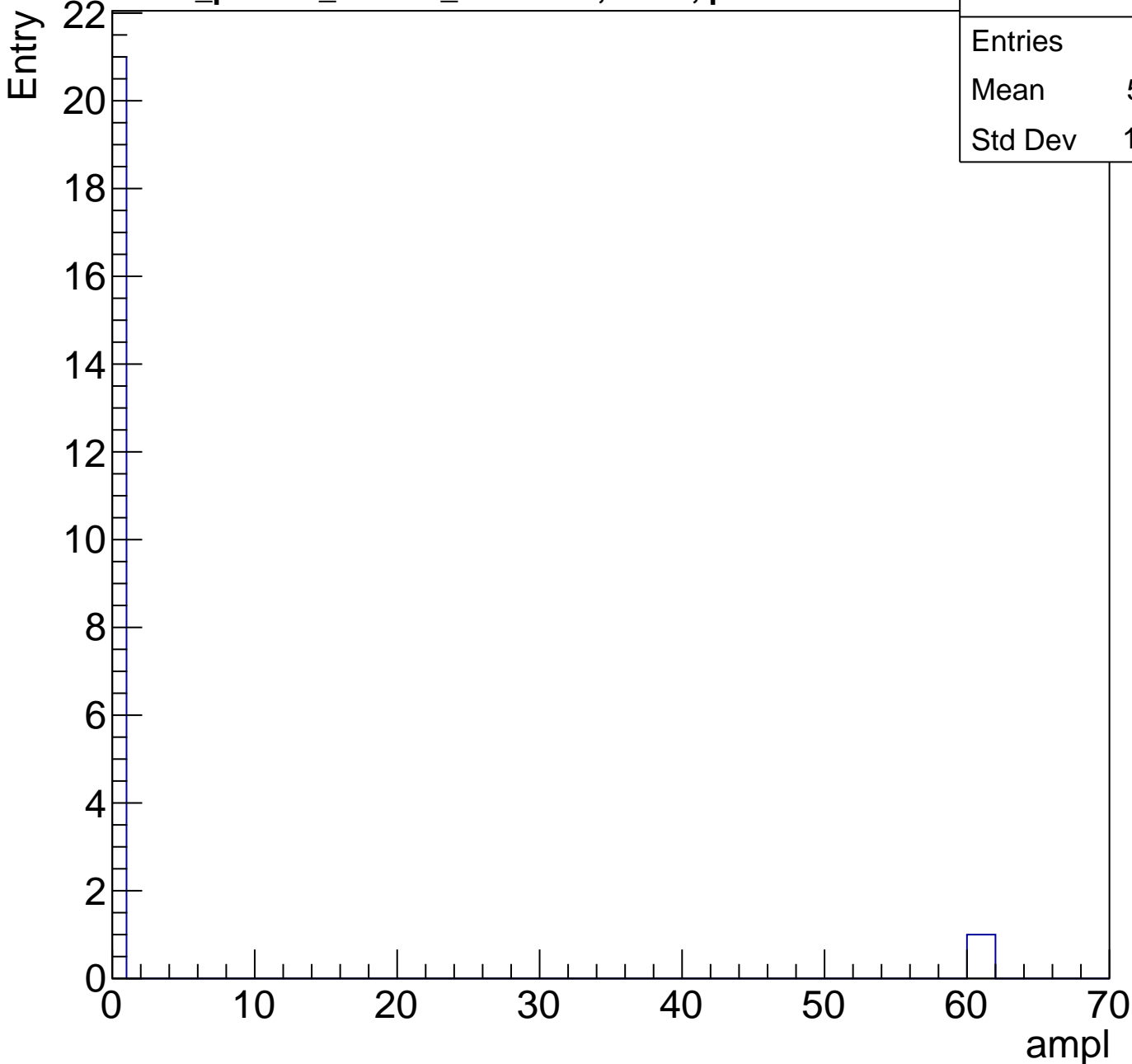
Entries	21
Mean	60.76
Std Dev	2.202



B1L103S, U6-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.261
Std Dev	17.05



B1L103S, U6-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.261
Std Dev	17.05

