

B1L101S, U17-ch0, adc0

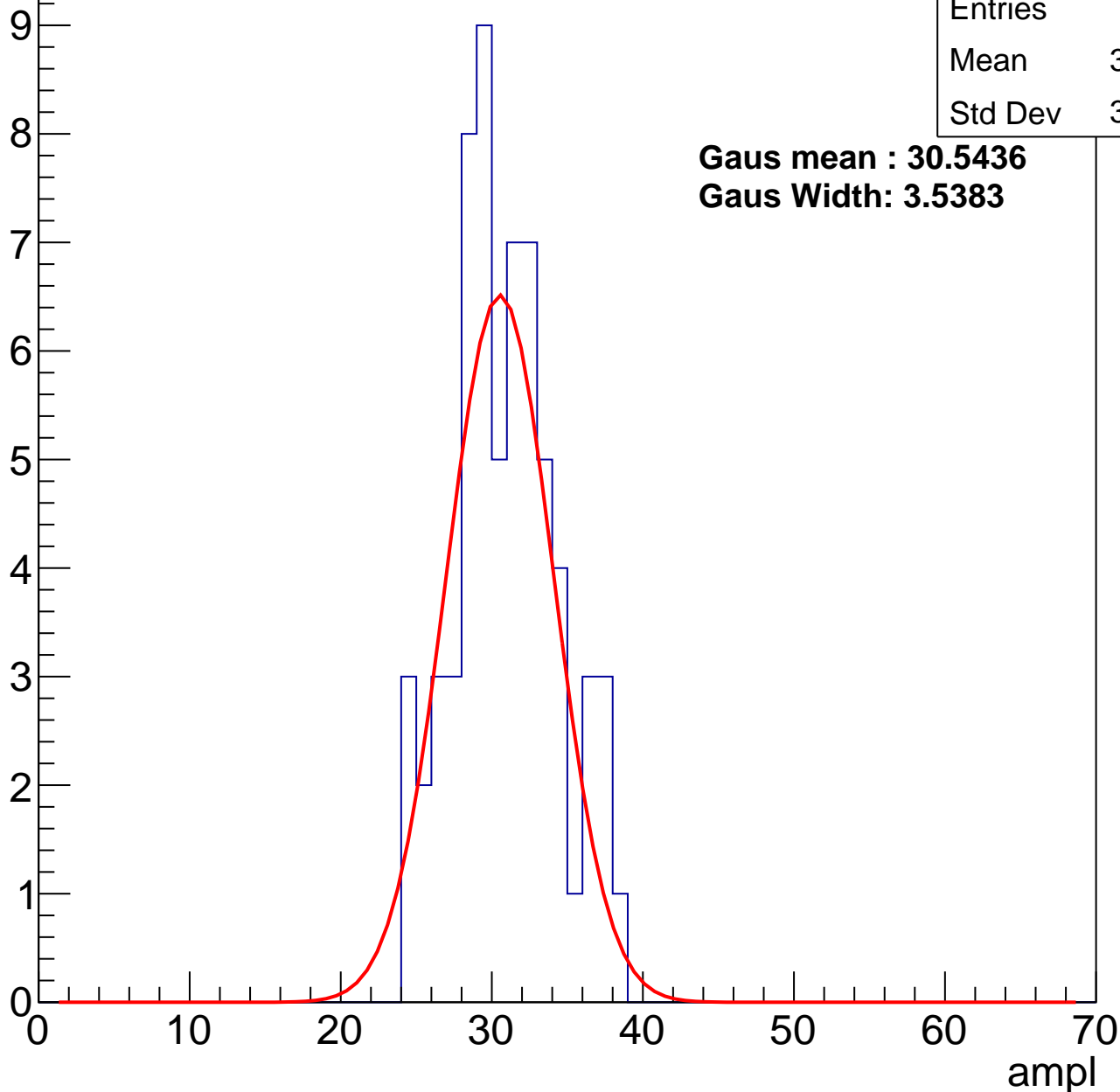
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.47
Std Dev	3.419

Gaus mean : 30.5436

Gaus Width: 3.5383



B1L101S, U17-ch0, adc1

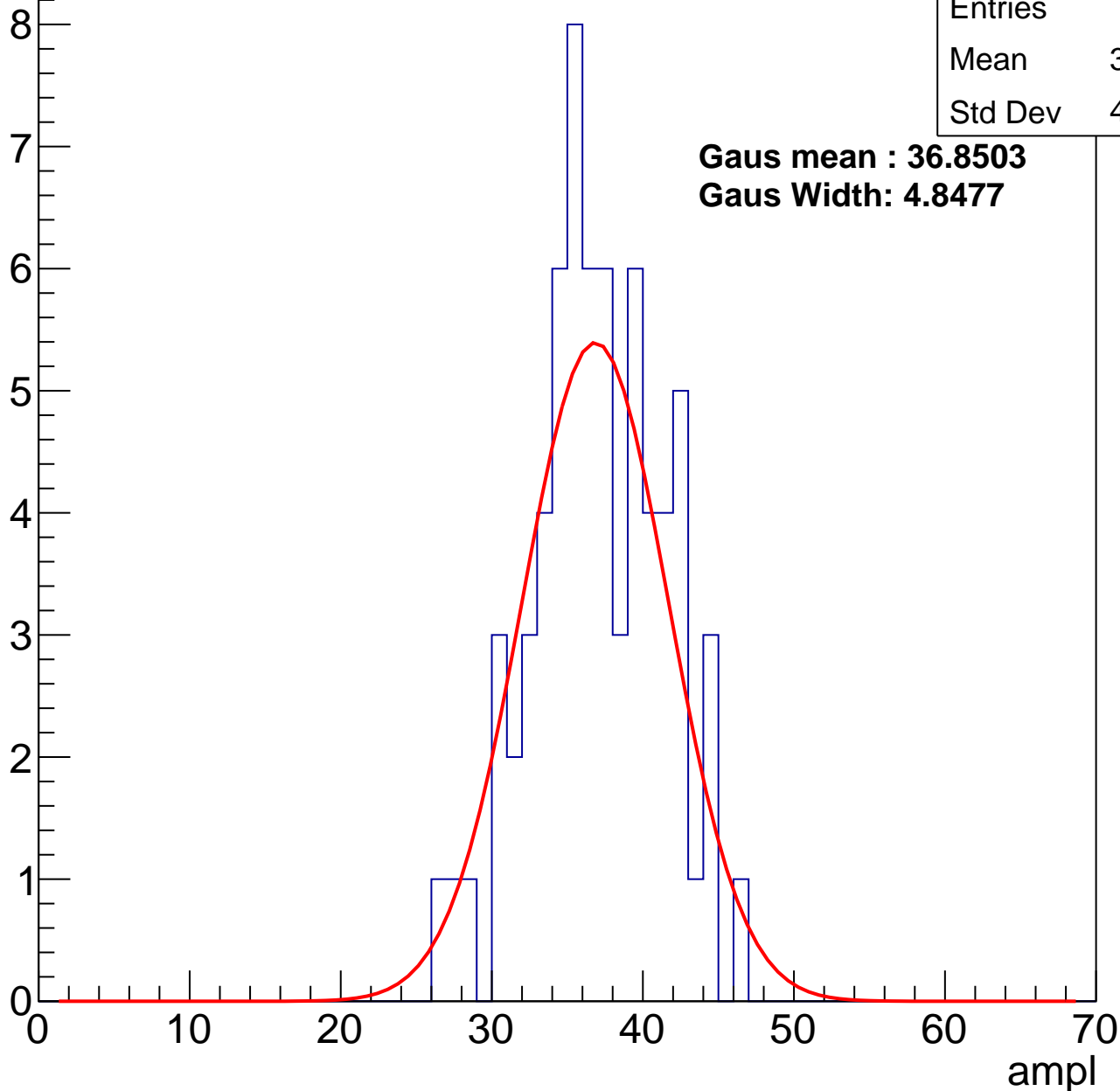
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.56
Std Dev	4.296

Gaus mean : 36.8503

Gaus Width: 4.8477



B1L101S, U17-ch0, adc2

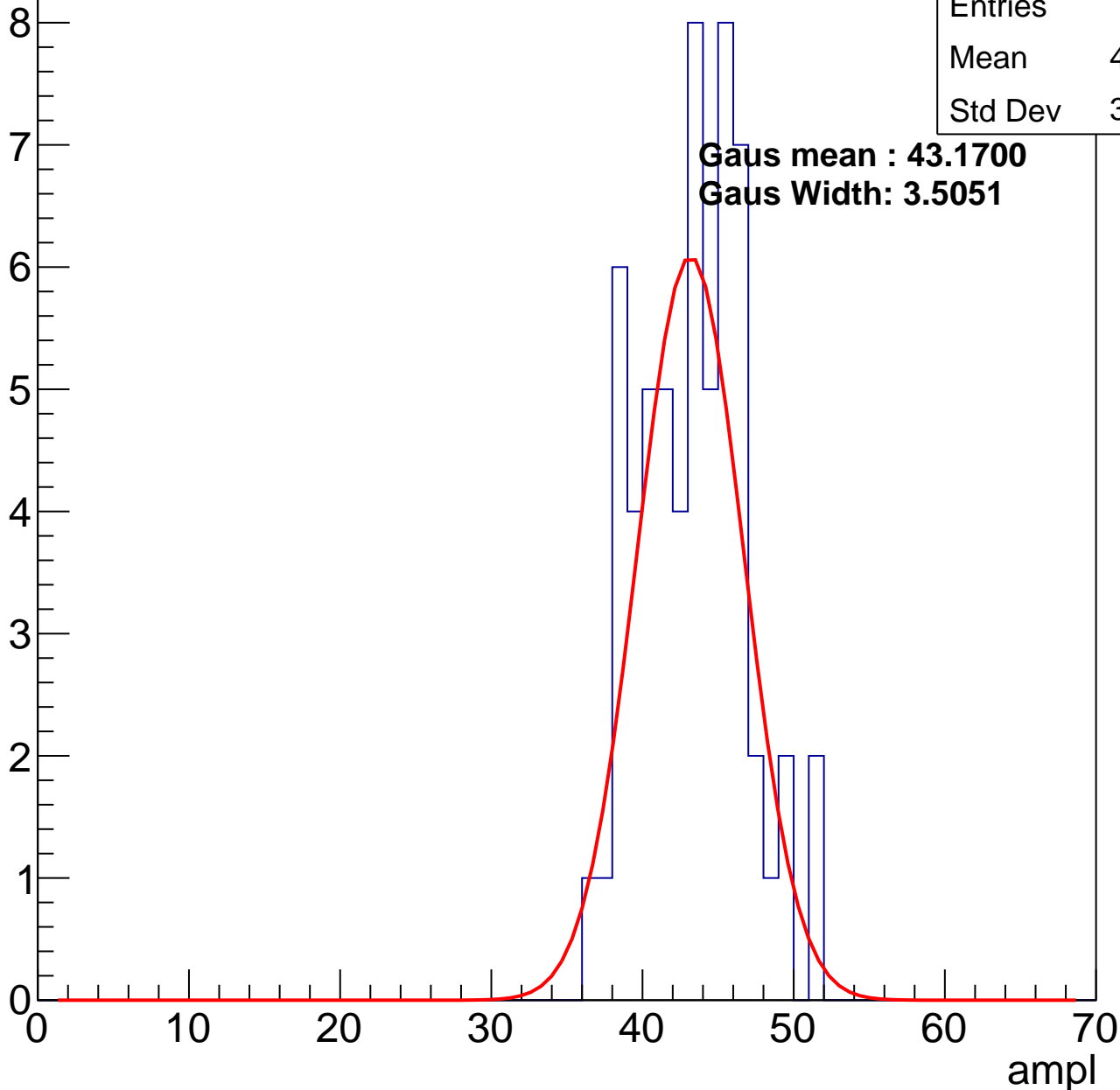
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.92
Std Dev	3.447

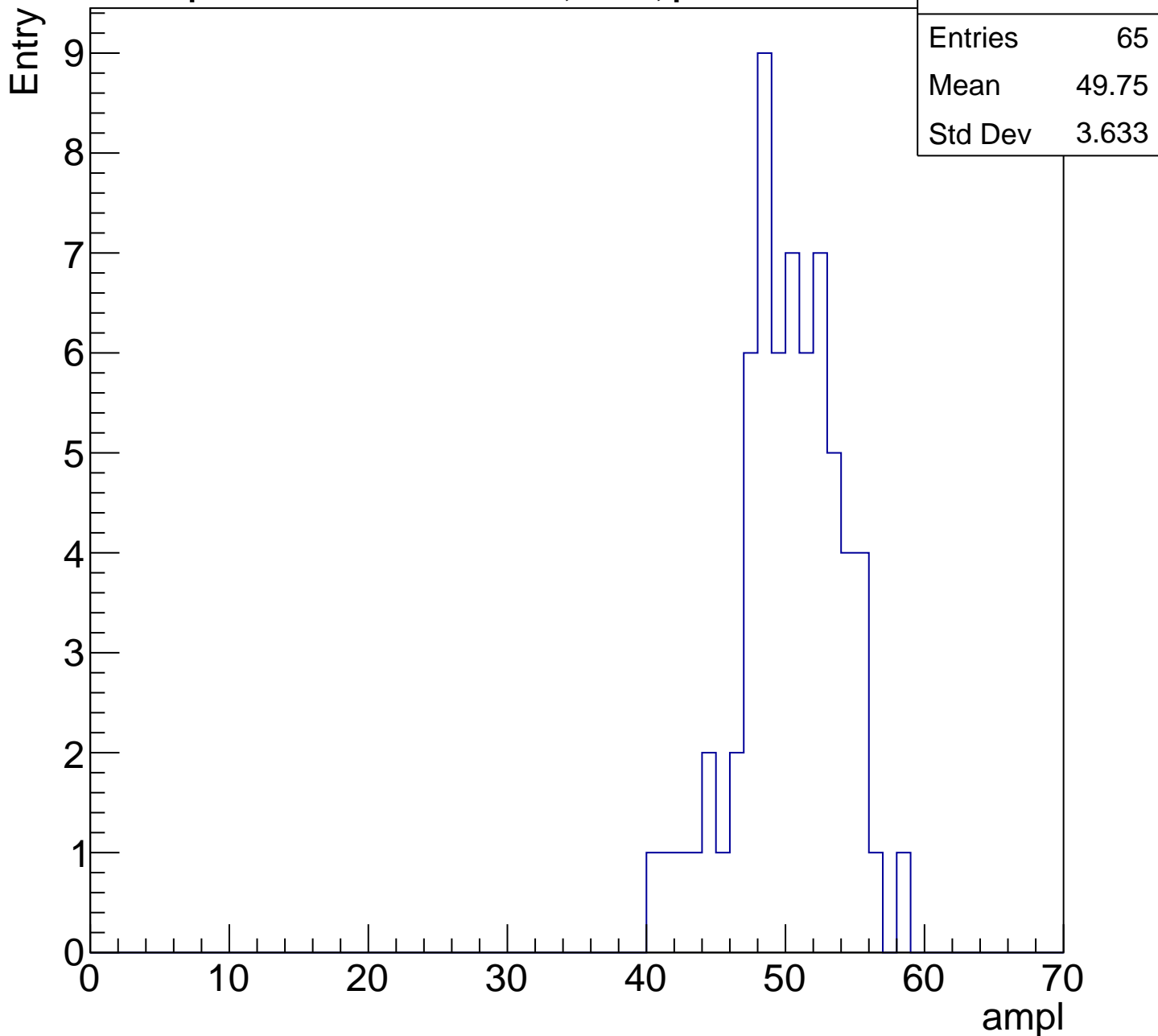
Gaus mean : 43.1700

Gaus Width: 3.5051



B1L101S, U17-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

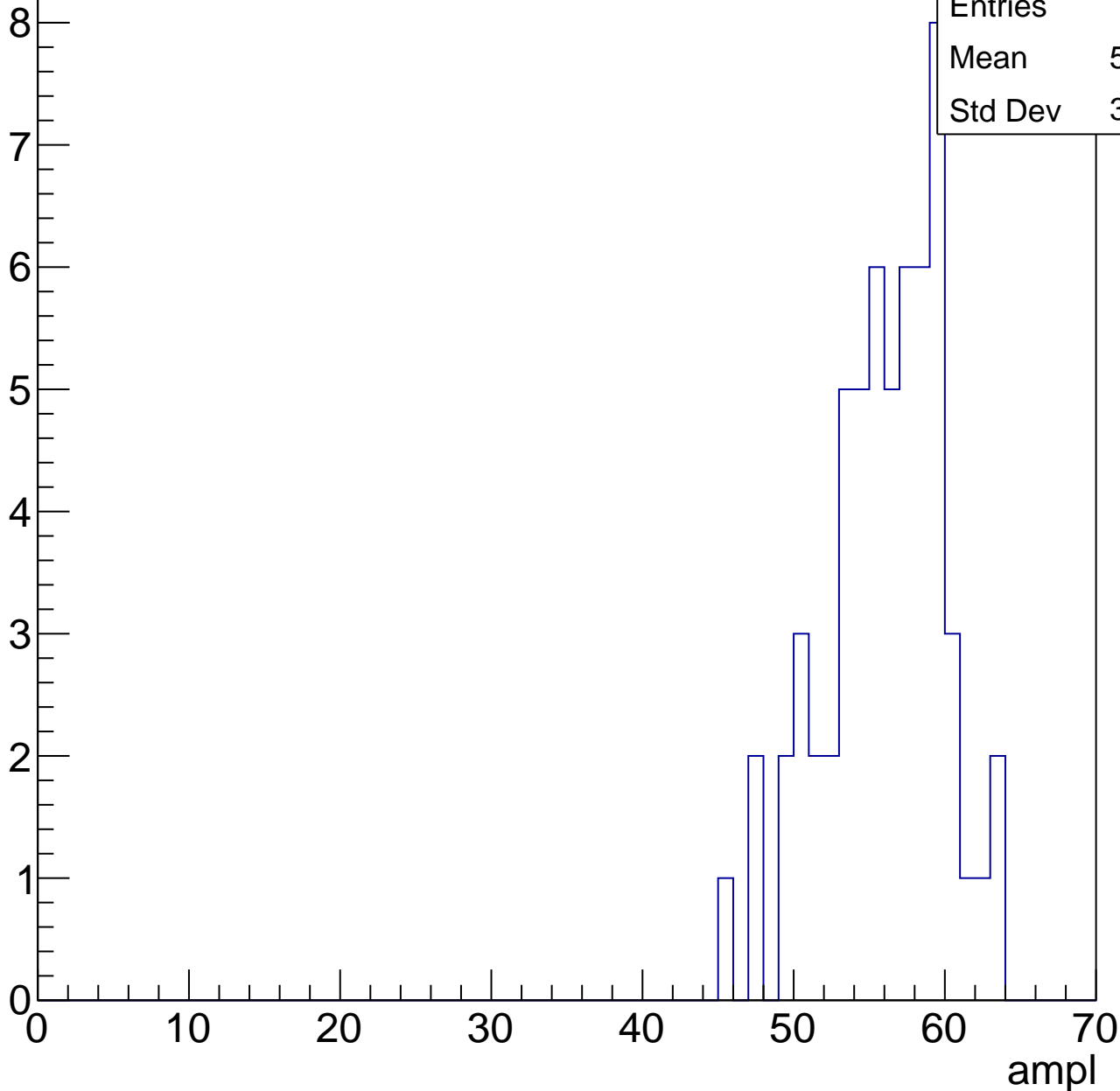


B1L101S, U17-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.48
Std Dev	3.948

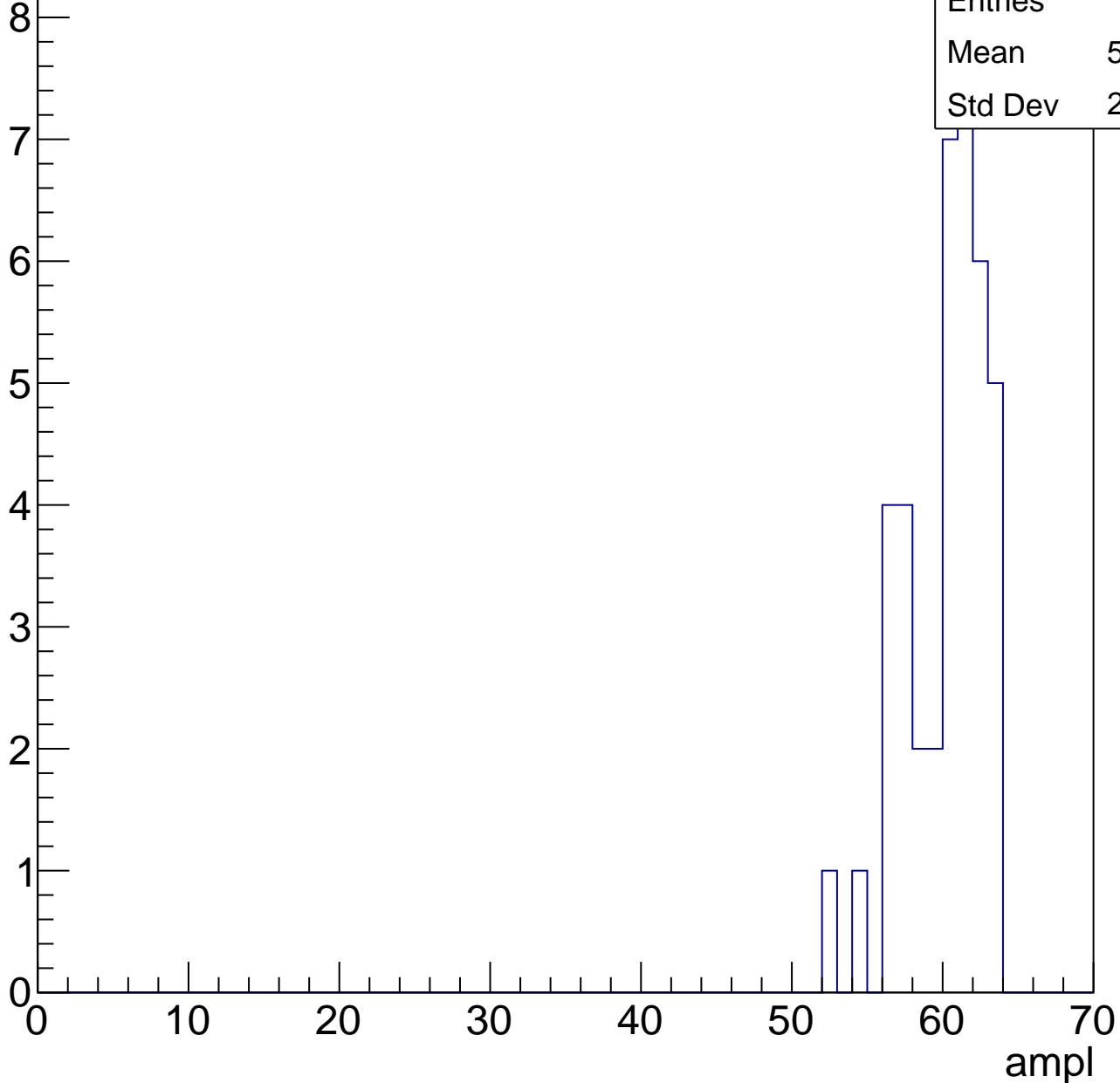


B1L101S, U17-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

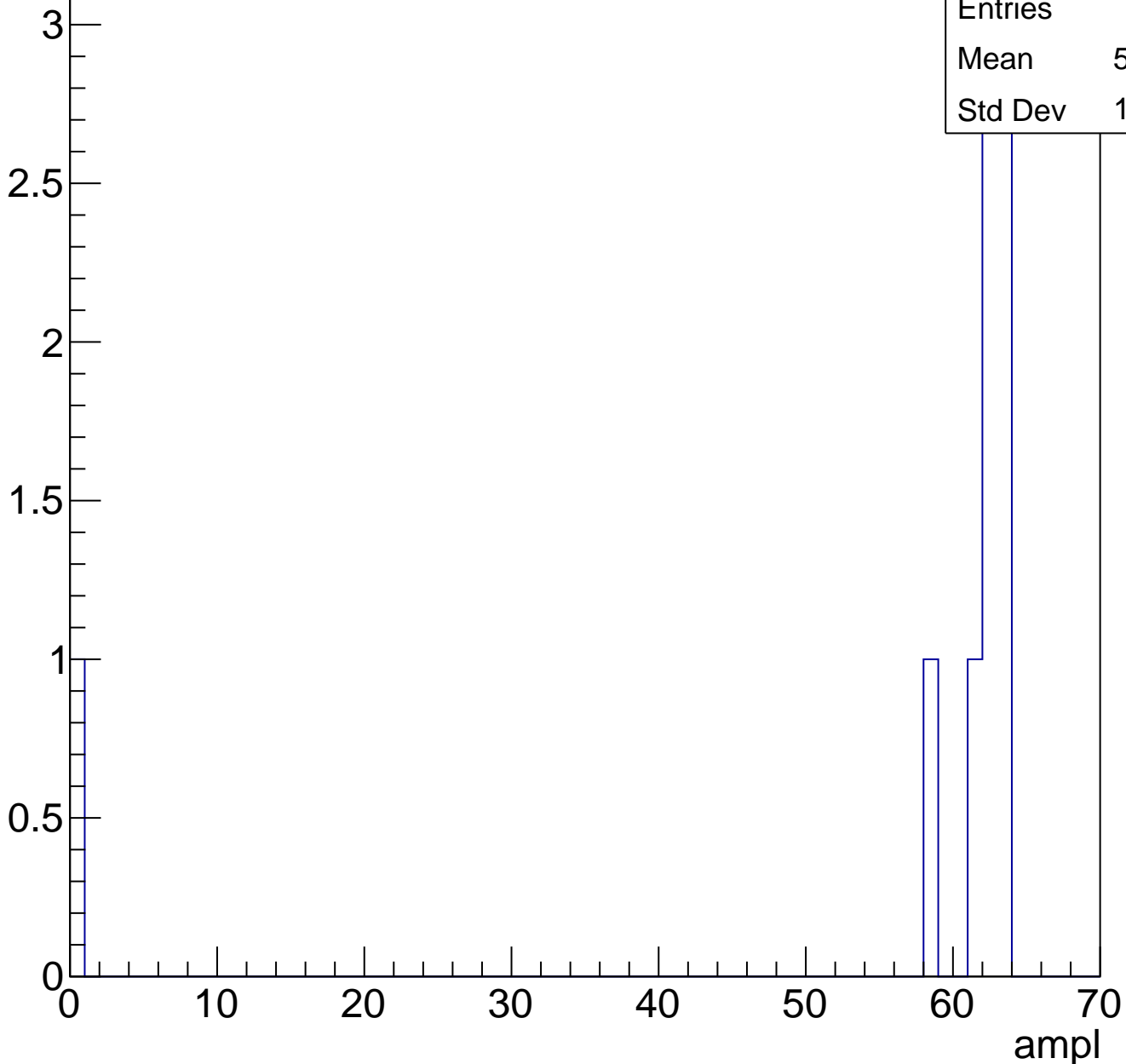
Entries	40
Mean	59.67
Std Dev	2.659



B1L101S, U17-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch1, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	30.09
Std Dev	5.014

Gaus mean : 30.0989

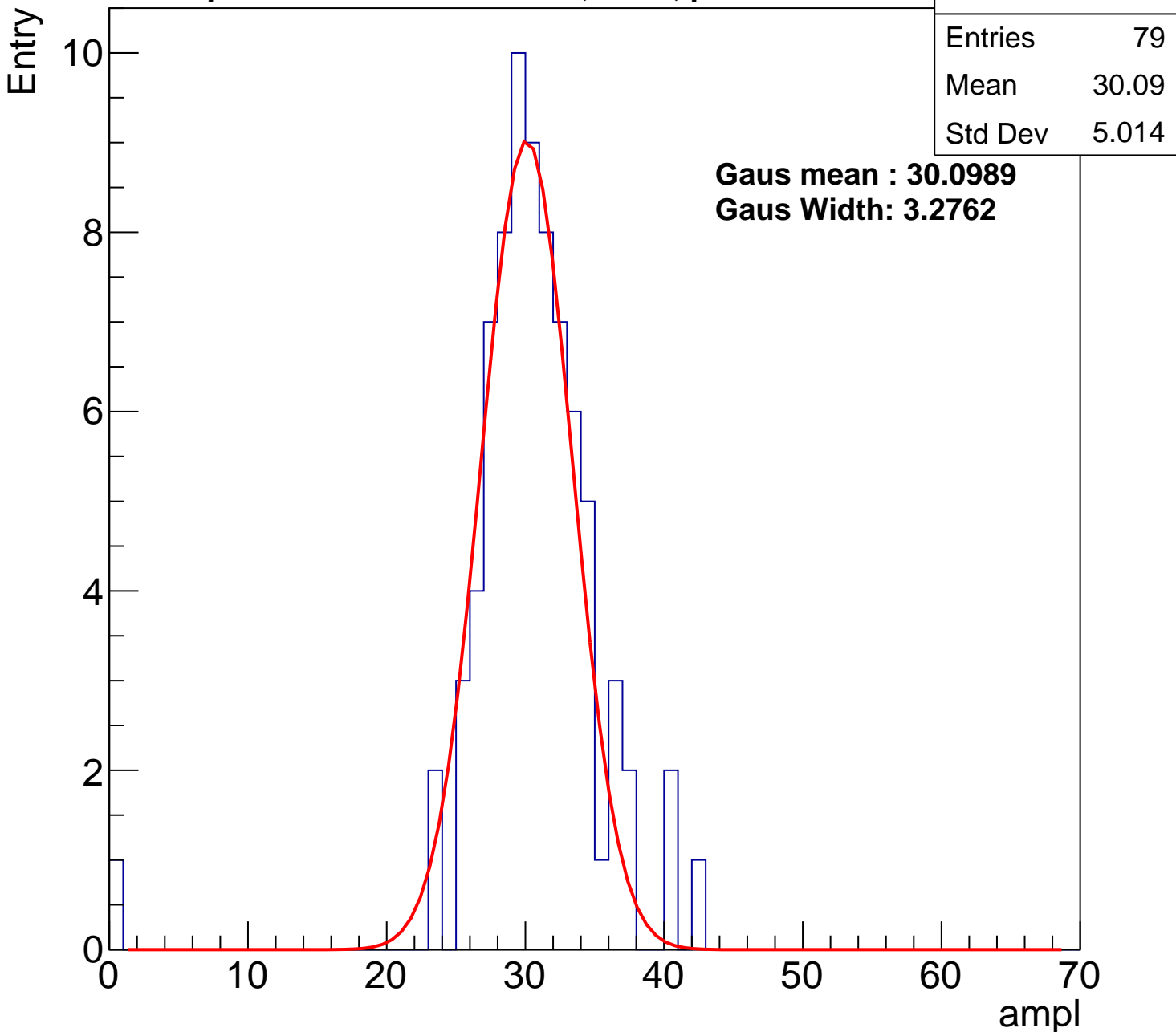
Gaus Width: 3.2762

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



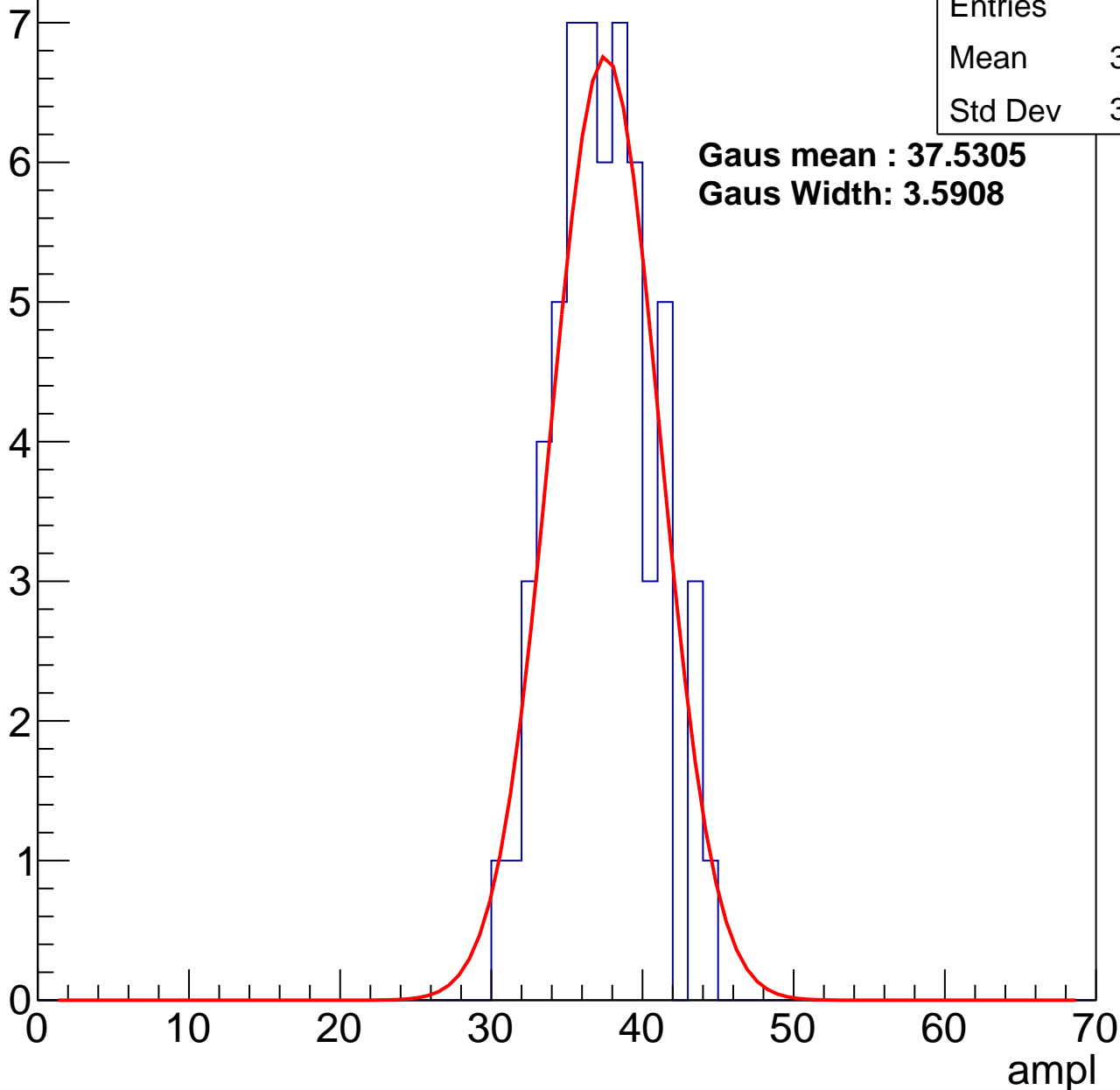
B1L101S, U17-ch1, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.88
Std Dev	3.184

Gaus mean : 37.5305
Gaus Width: 3.5908



B1L101S, U17-ch1, adc2

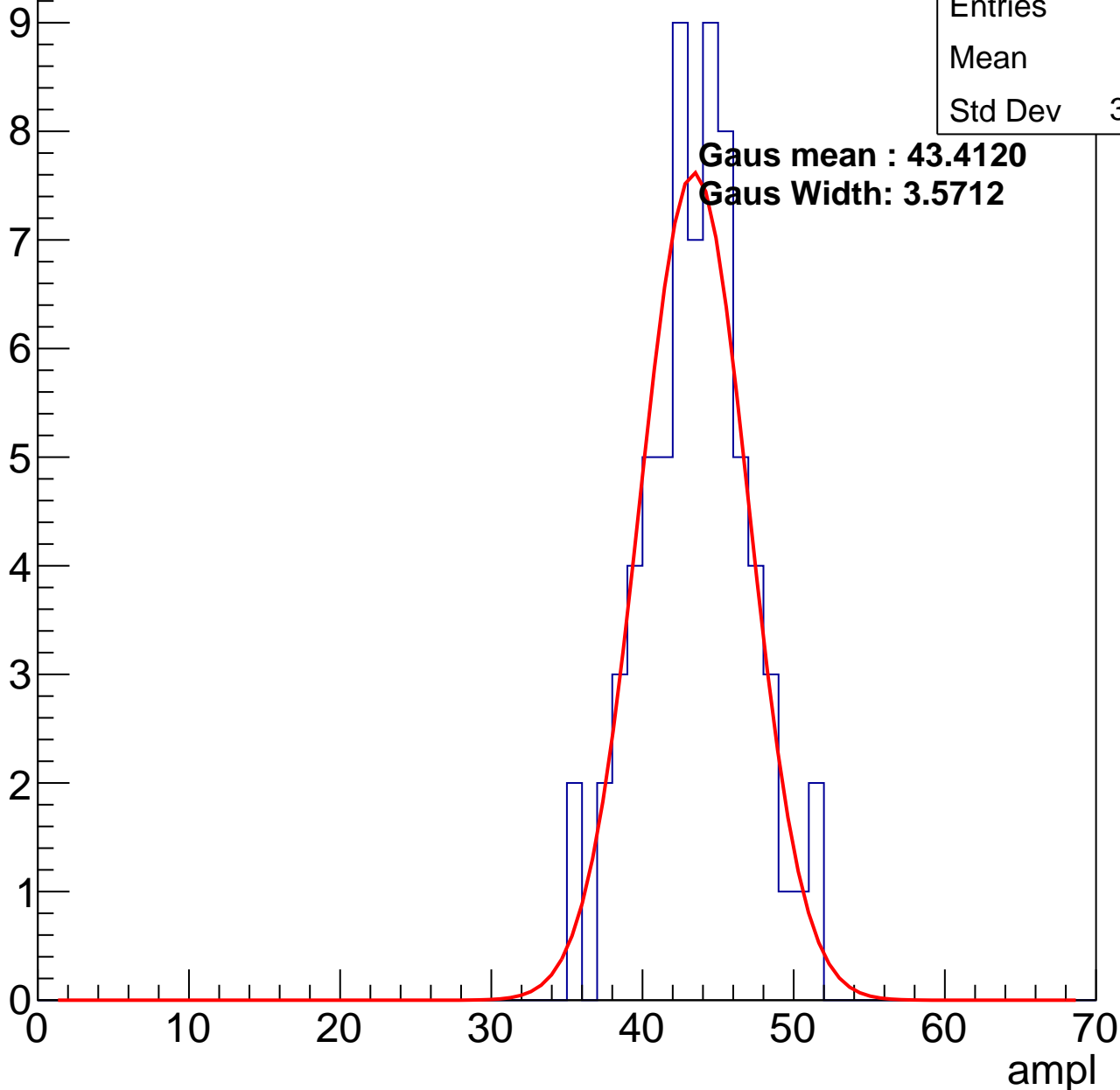
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.1
Std Dev	3.485

Gaus mean : 43.4120

Gaus Width: 3.5712

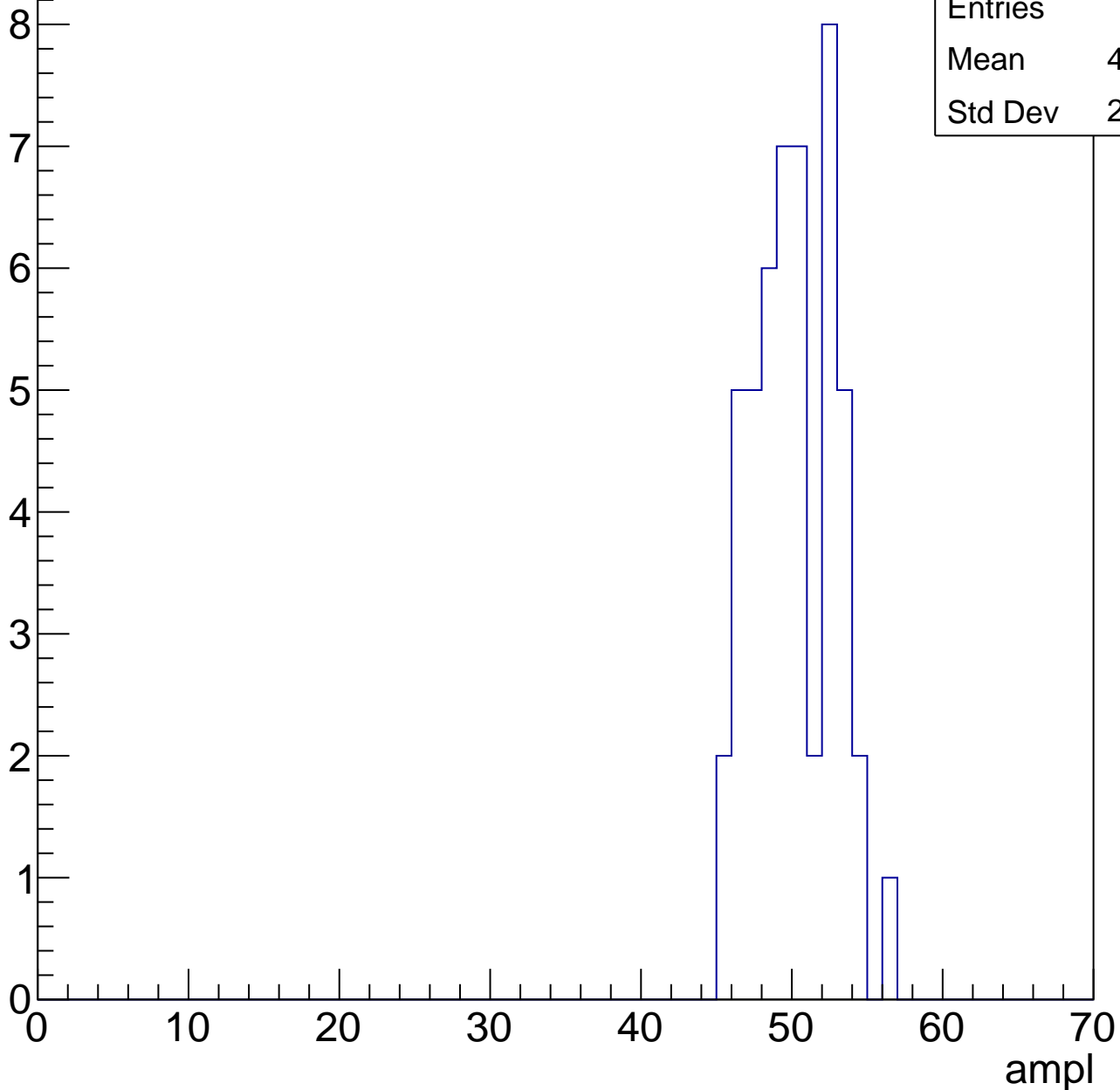


B1L101S, U17-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

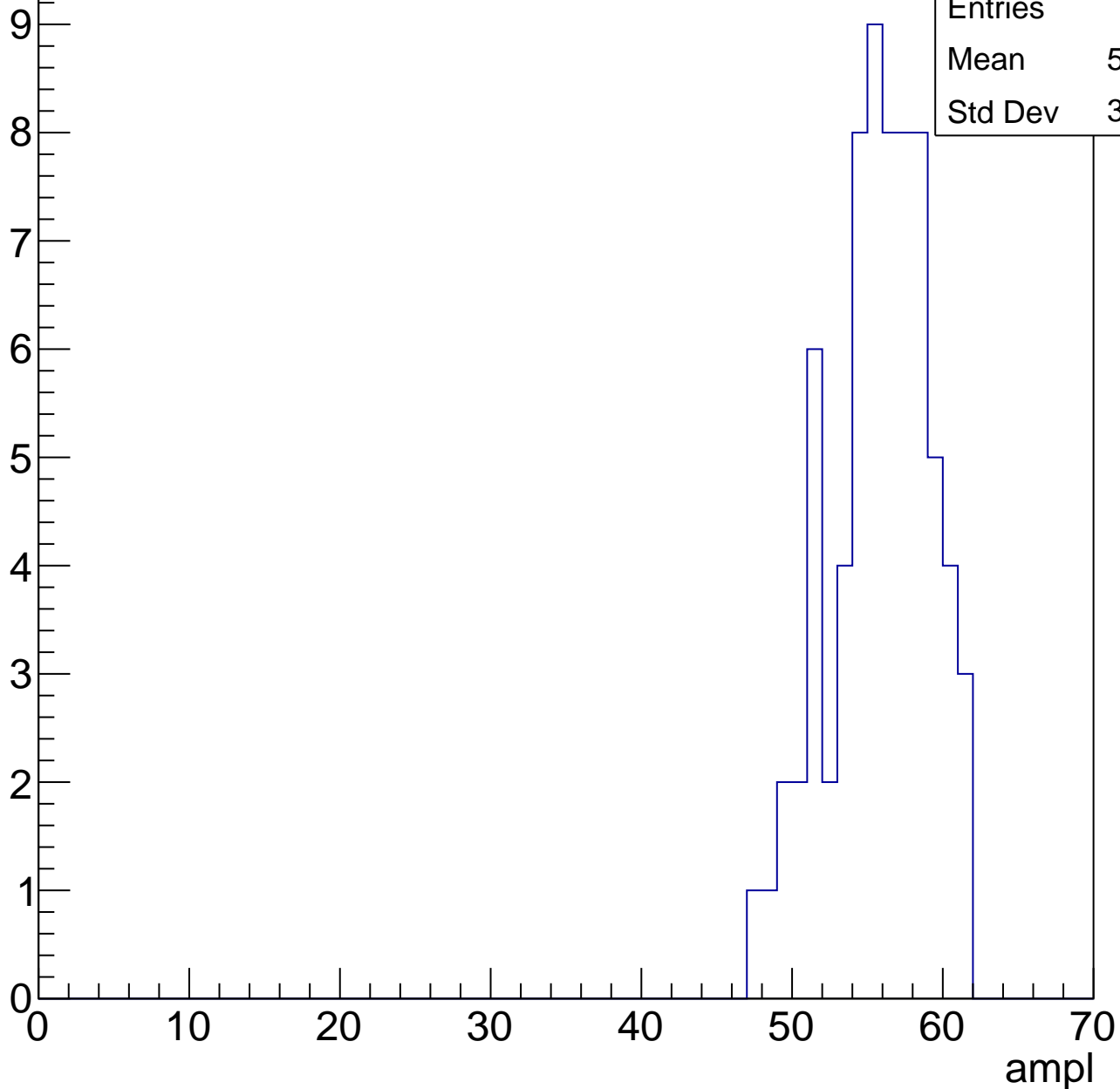
Entries	50
Mean	49.66
Std Dev	2.635



B1L101S, U17-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

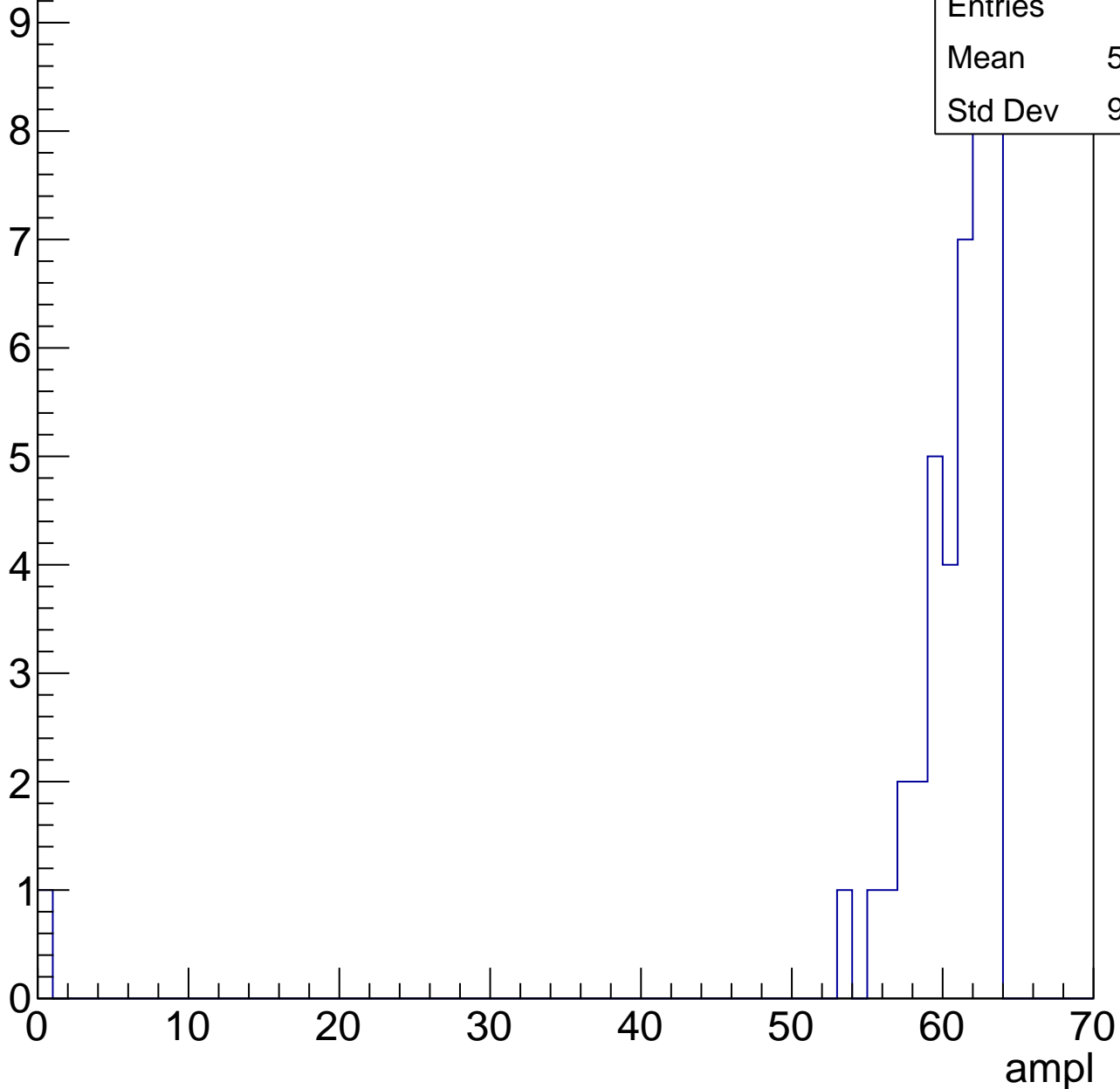


B1L101S, U17-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.98
Std Dev	9.618



B1L101S, U17-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L101S, U17-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch2, adc0

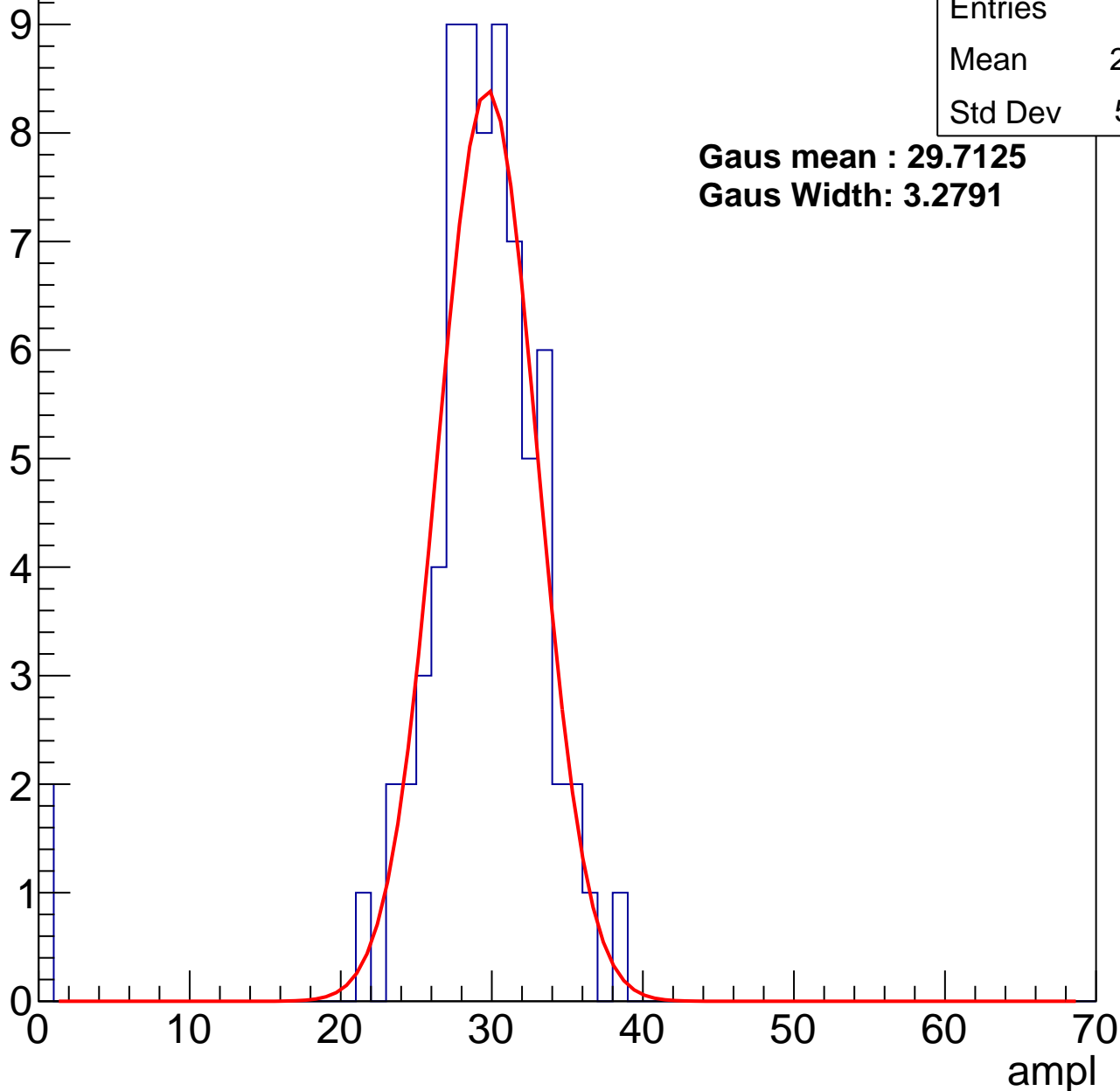
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.47
Std Dev	5.741

Gaus mean : 29.7125

Gaus Width: 3.2791



B1L101S, U17-ch2, adc1

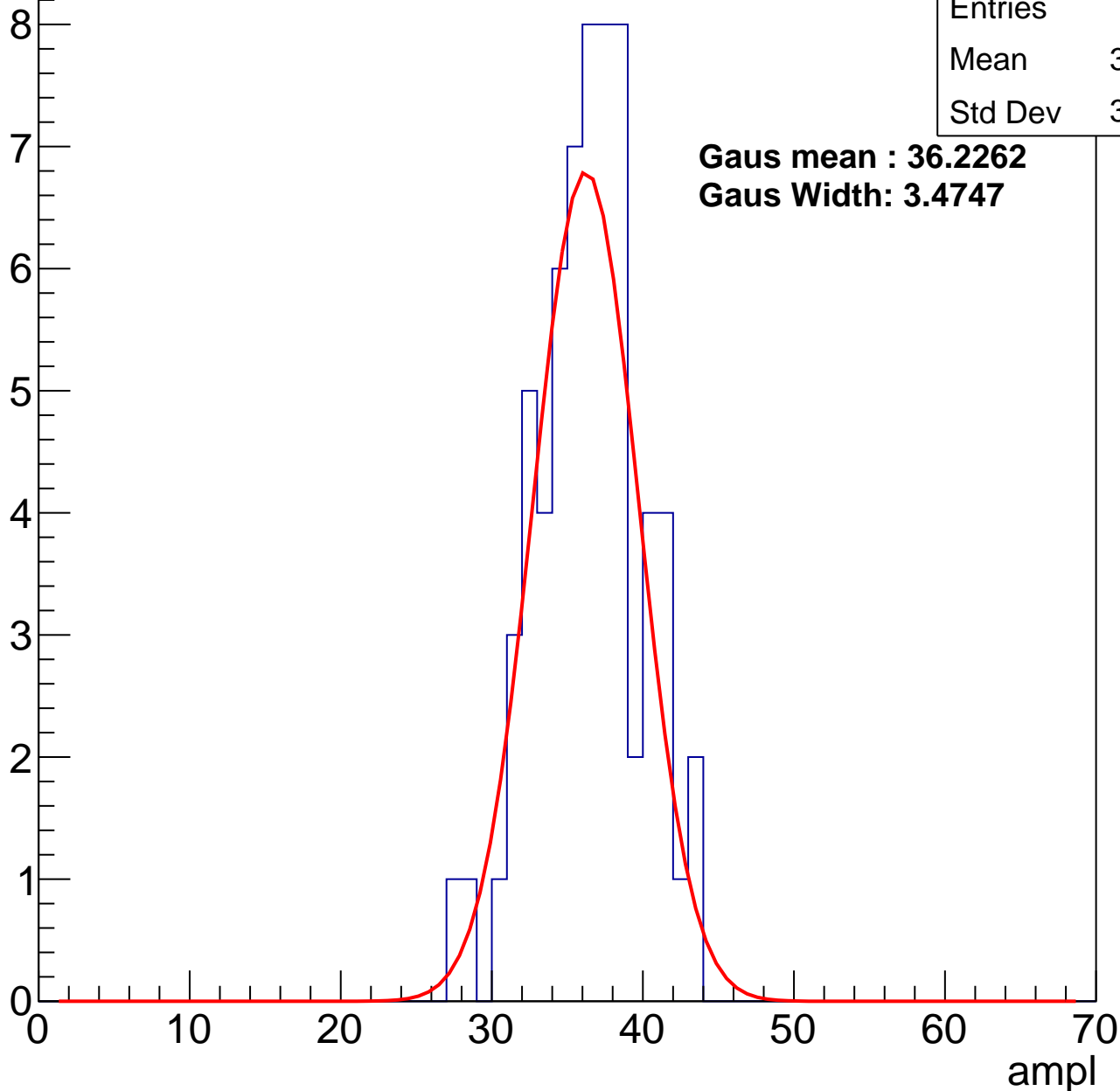
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	35.95
Std Dev	3.417

Gaus mean : 36.2262

Gaus Width: 3.4747



B1L101S, U17-ch2, adc2

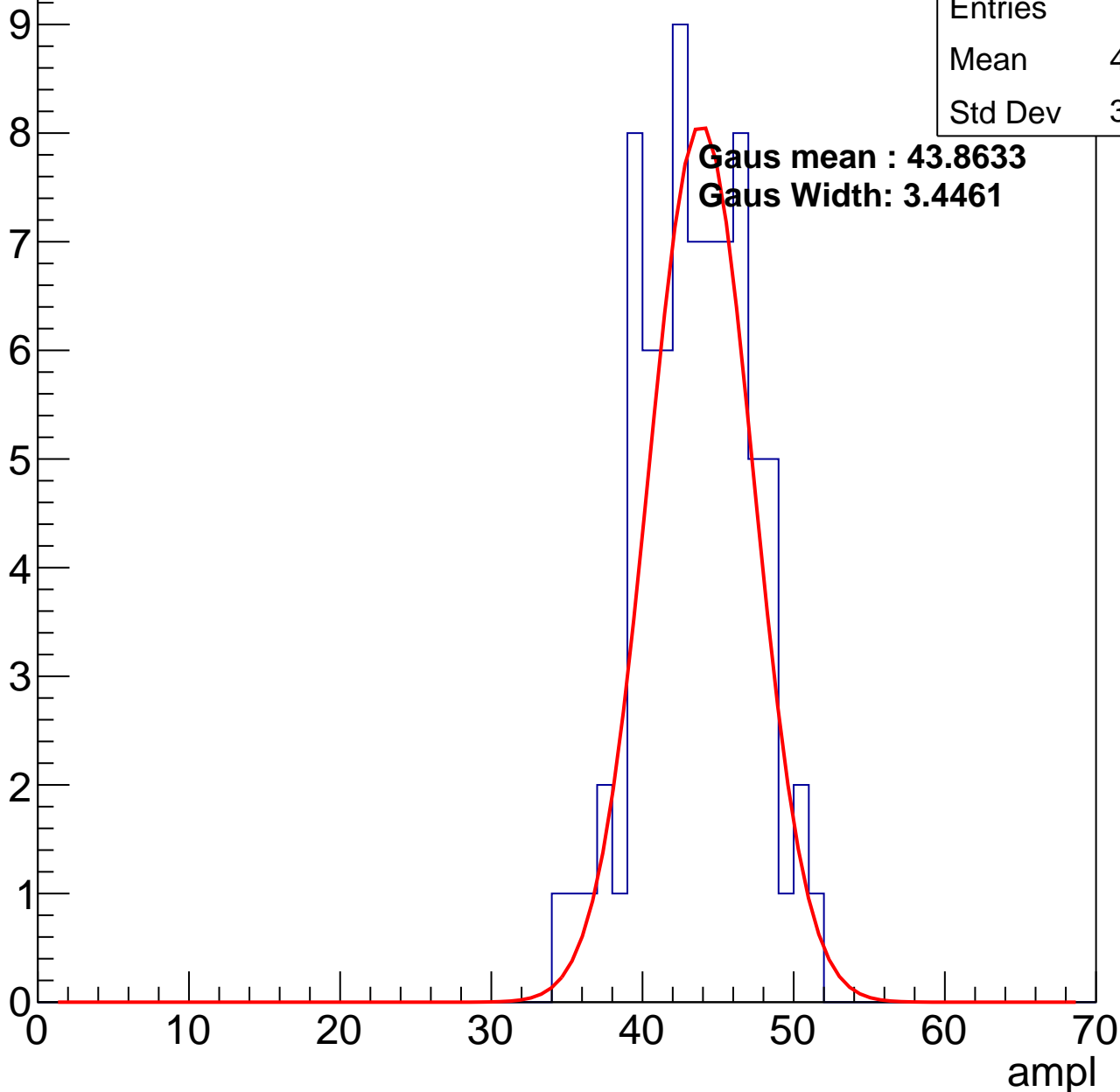
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.08
Std Dev	3.608

Gaus mean : 43.8633

Gaus Width: 3.4461

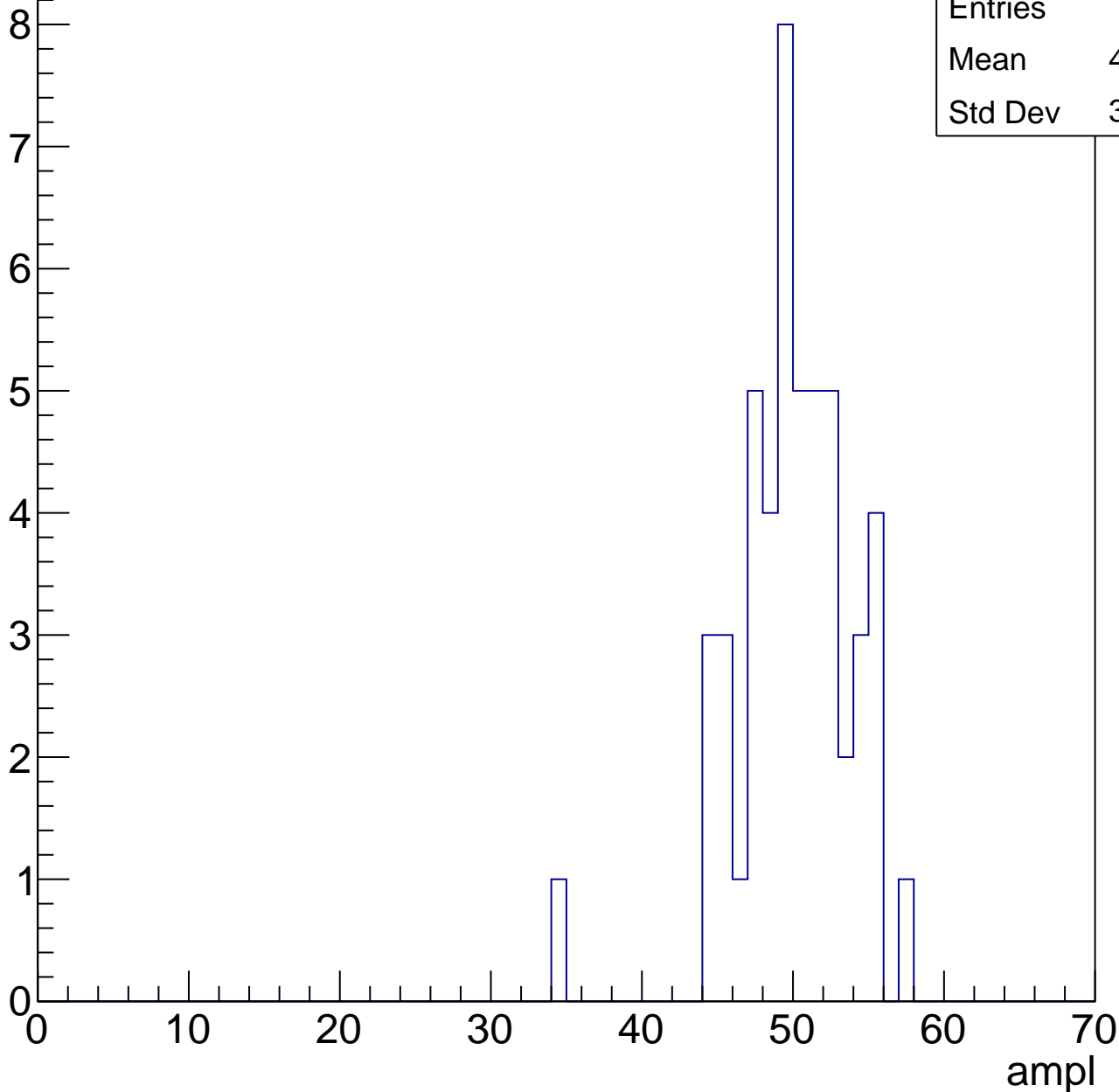


B1L101S, U17-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

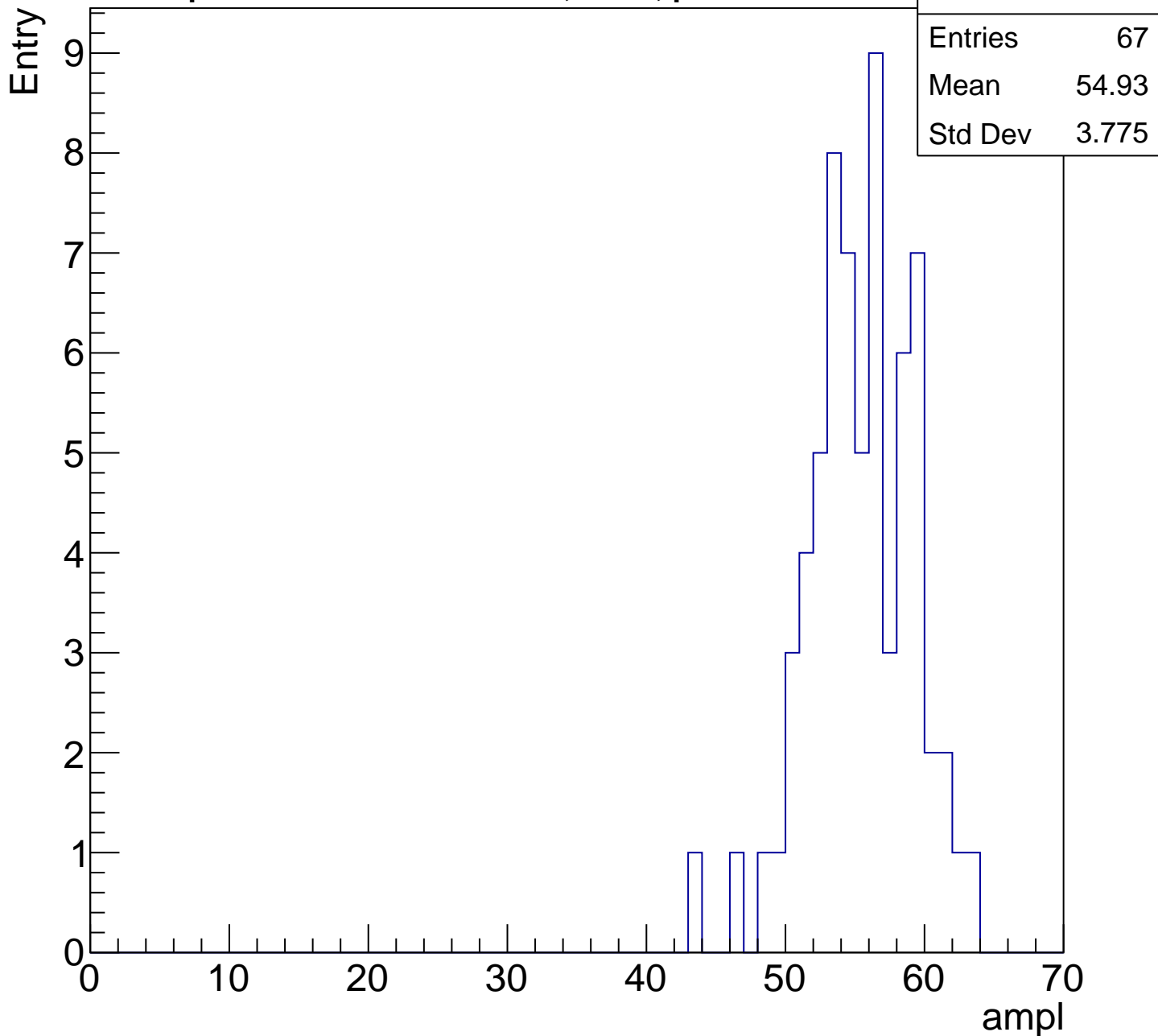
Entry

Entries	50
Mean	49.52
Std Dev	3.879



B1L101S, U17-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

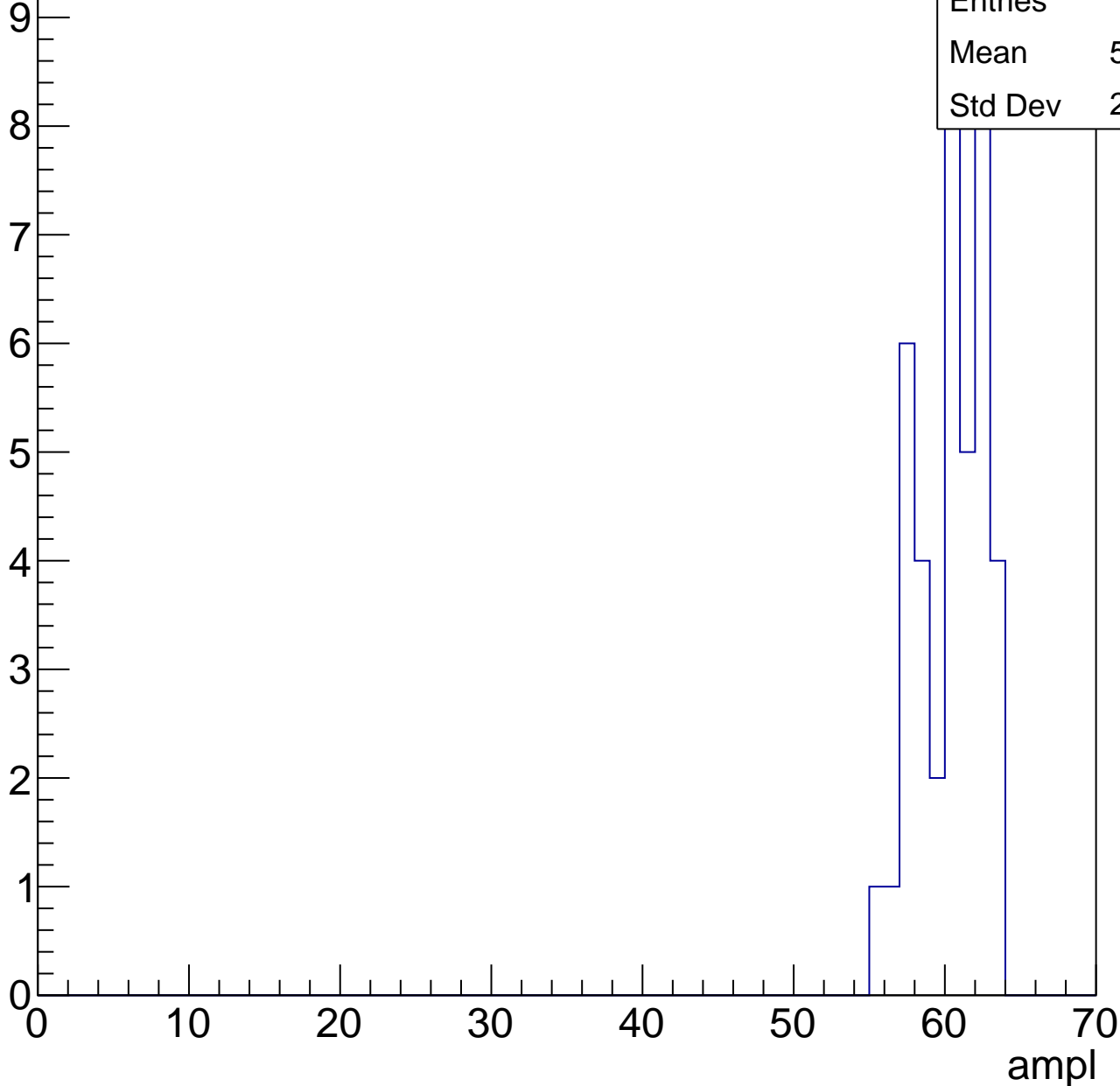


B1L101S, U17-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

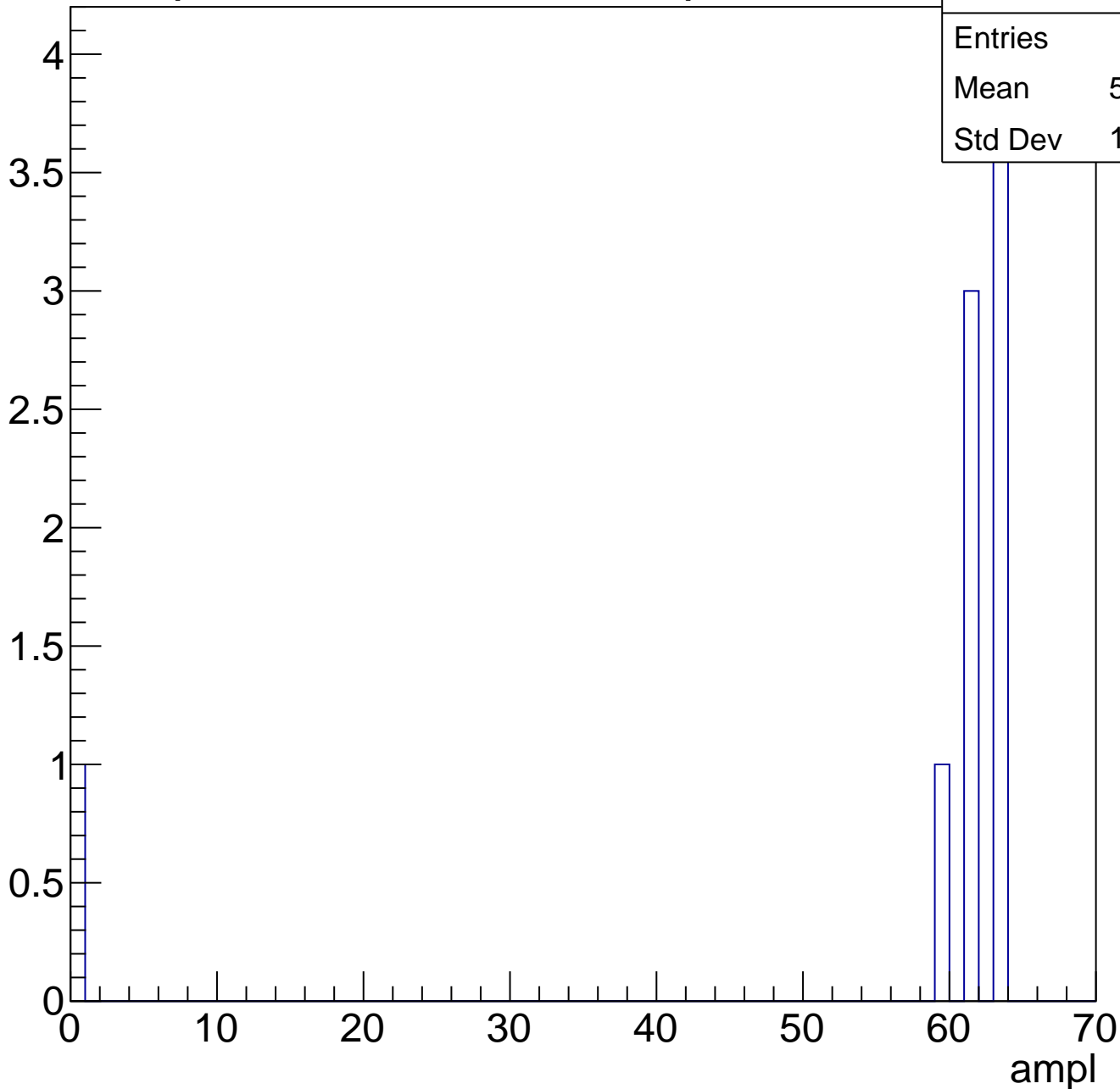
Entries	40
Mean	59.95
Std Dev	2.179



B1L101S, U17-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch3, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	27.27
Std Dev	4.537

Gaus mean : 28.4793

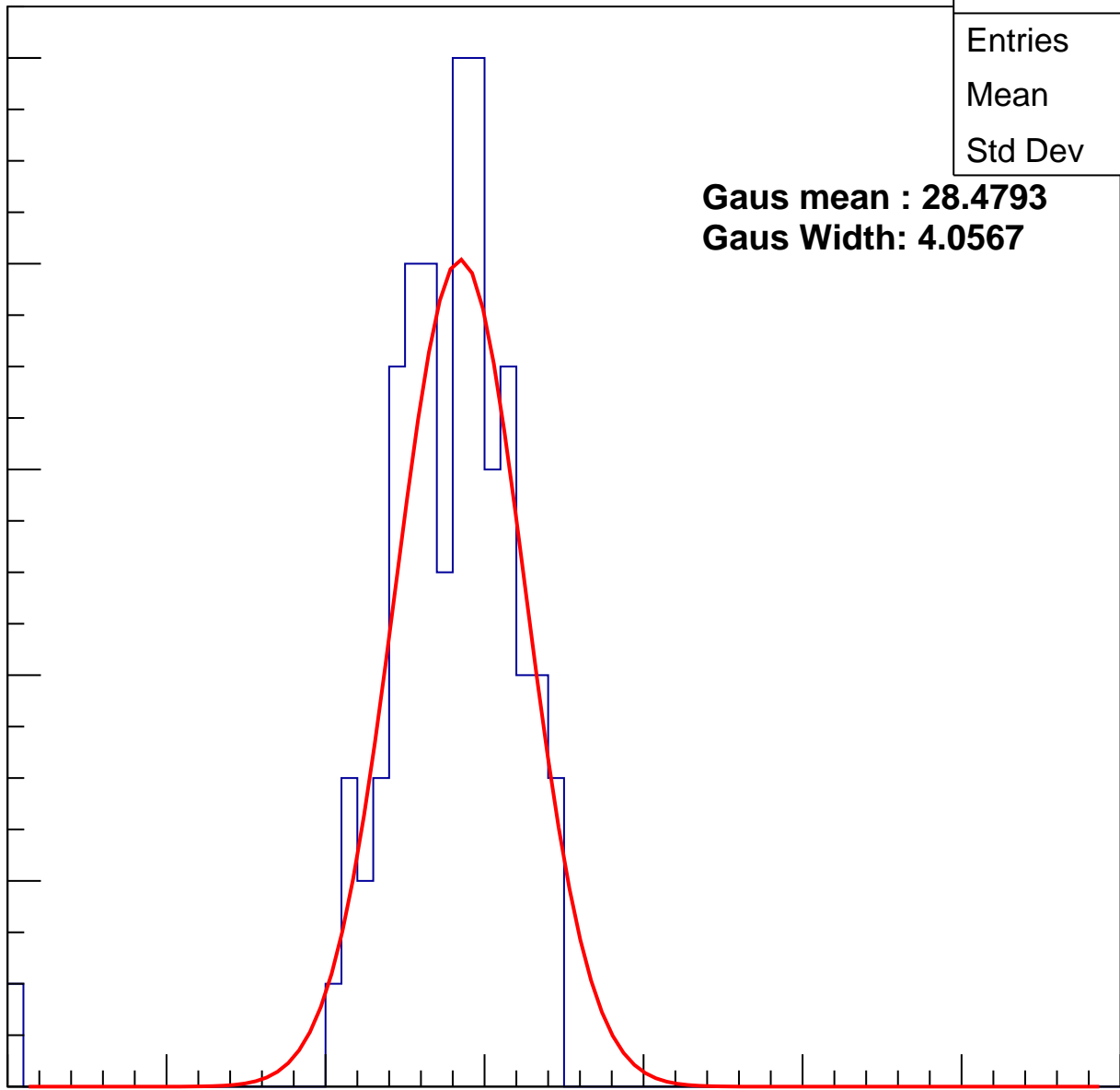
Gaus Width: 4.0567

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch3, adc1

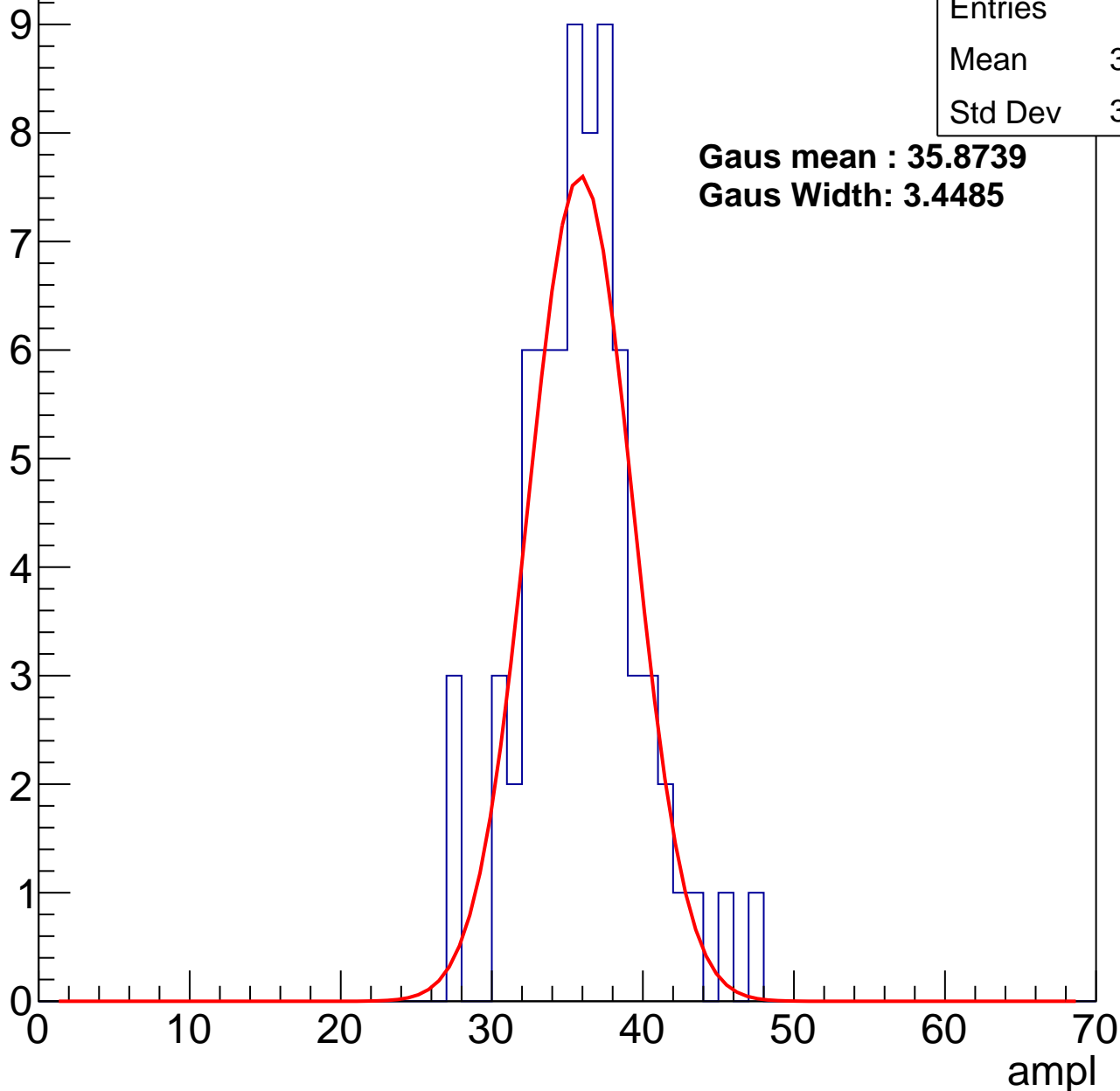
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.53
Std Dev	3.805

Gaus mean : 35.8739

Gaus Width: 3.4485



B1L101S, U17-ch3, adc2

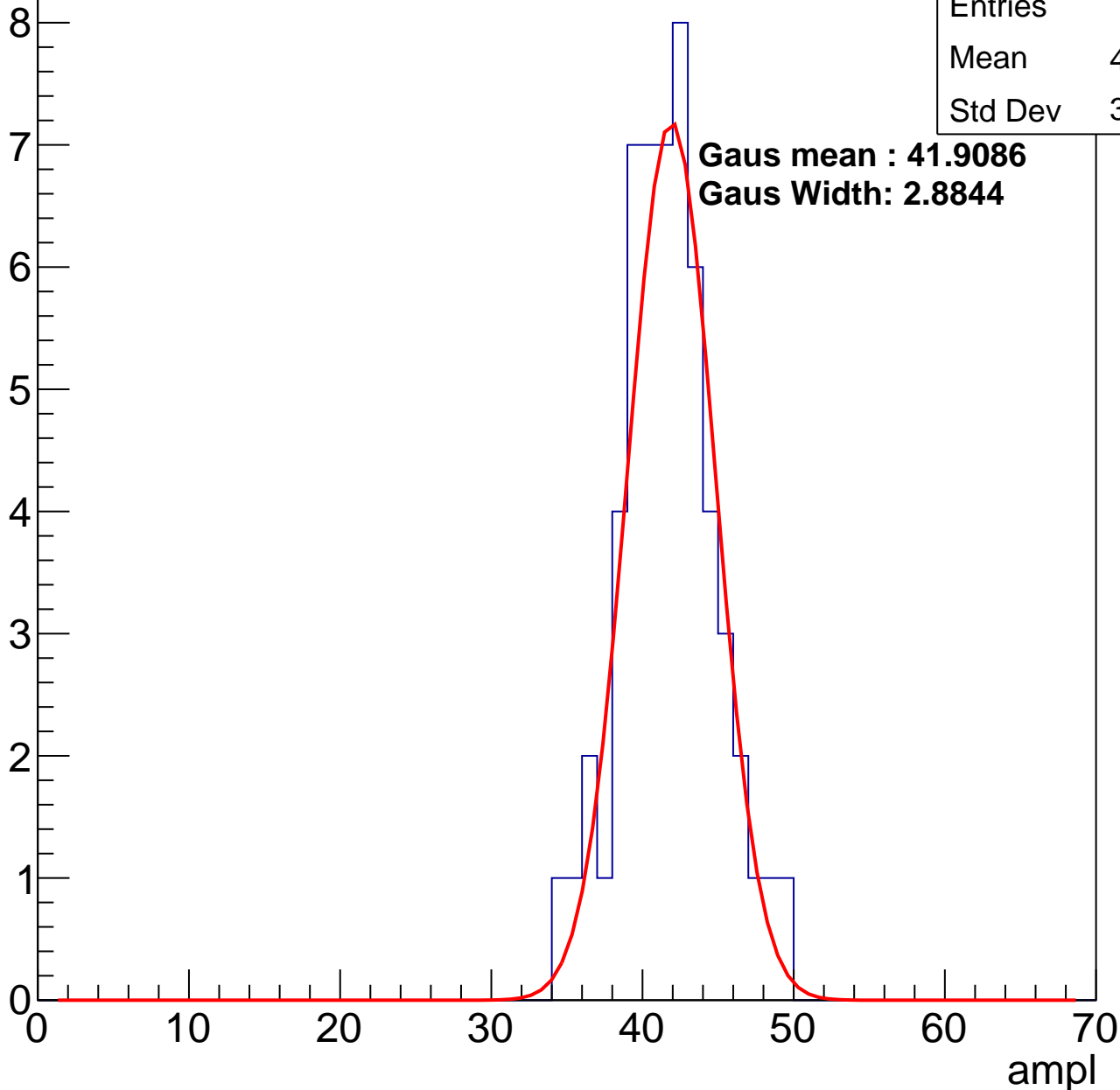
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.27
Std Dev	3.085

Gaus mean : 41.9086

Gaus Width: 2.8844

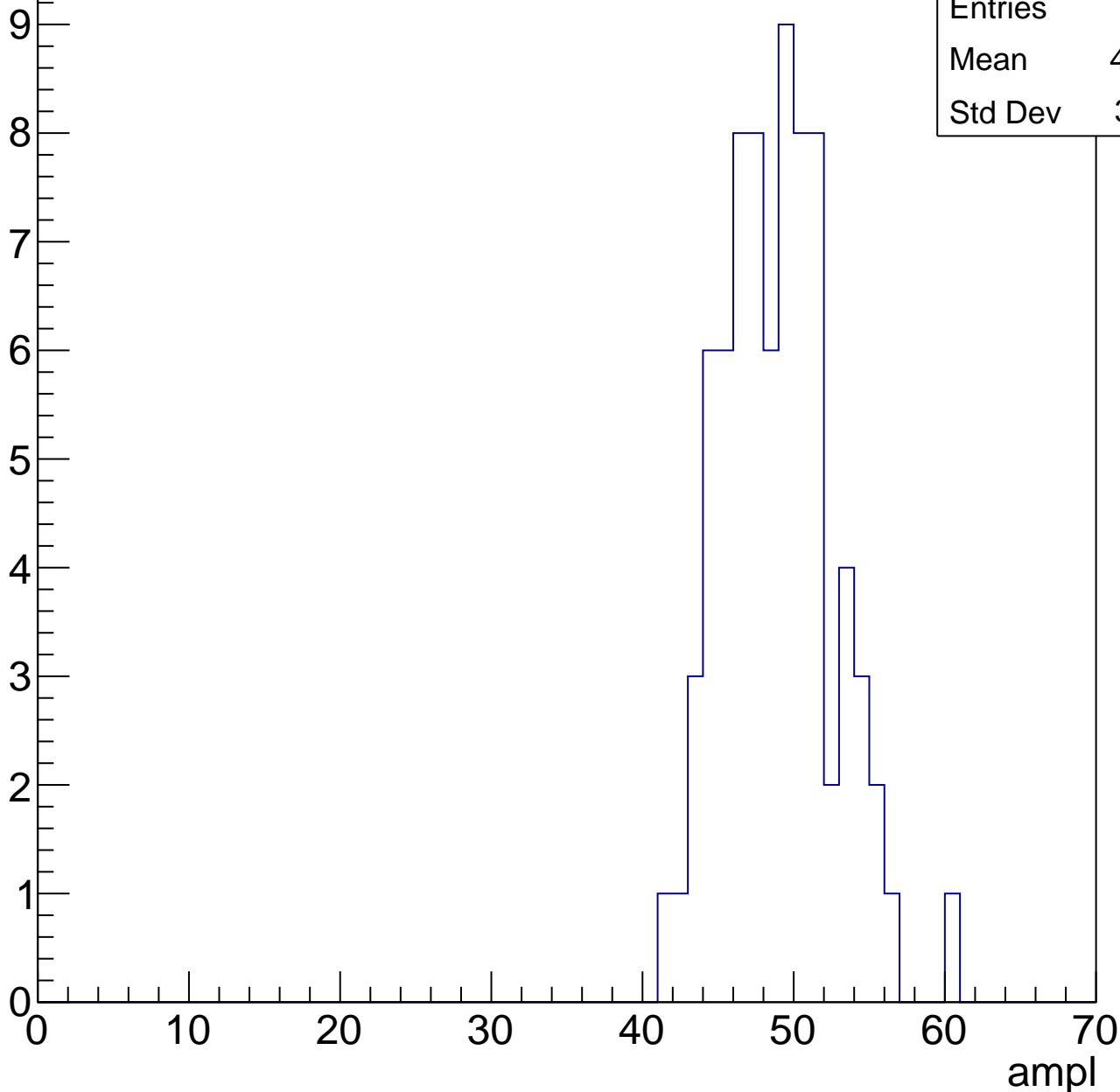


B1L101S, U17-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

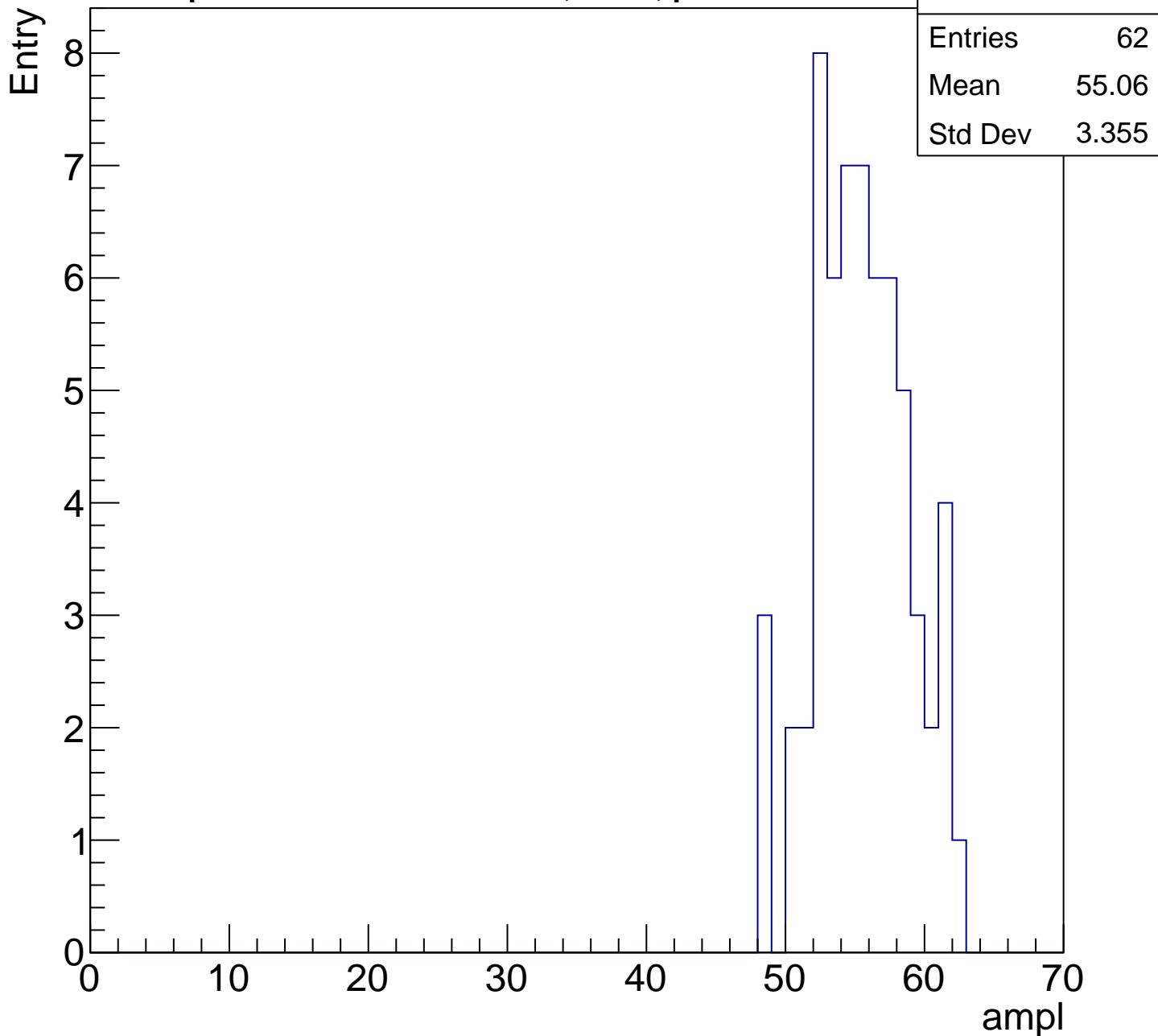
Entry

Entries	77
Mean	48.45
Std Dev	3.591



B1L101S, U17-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

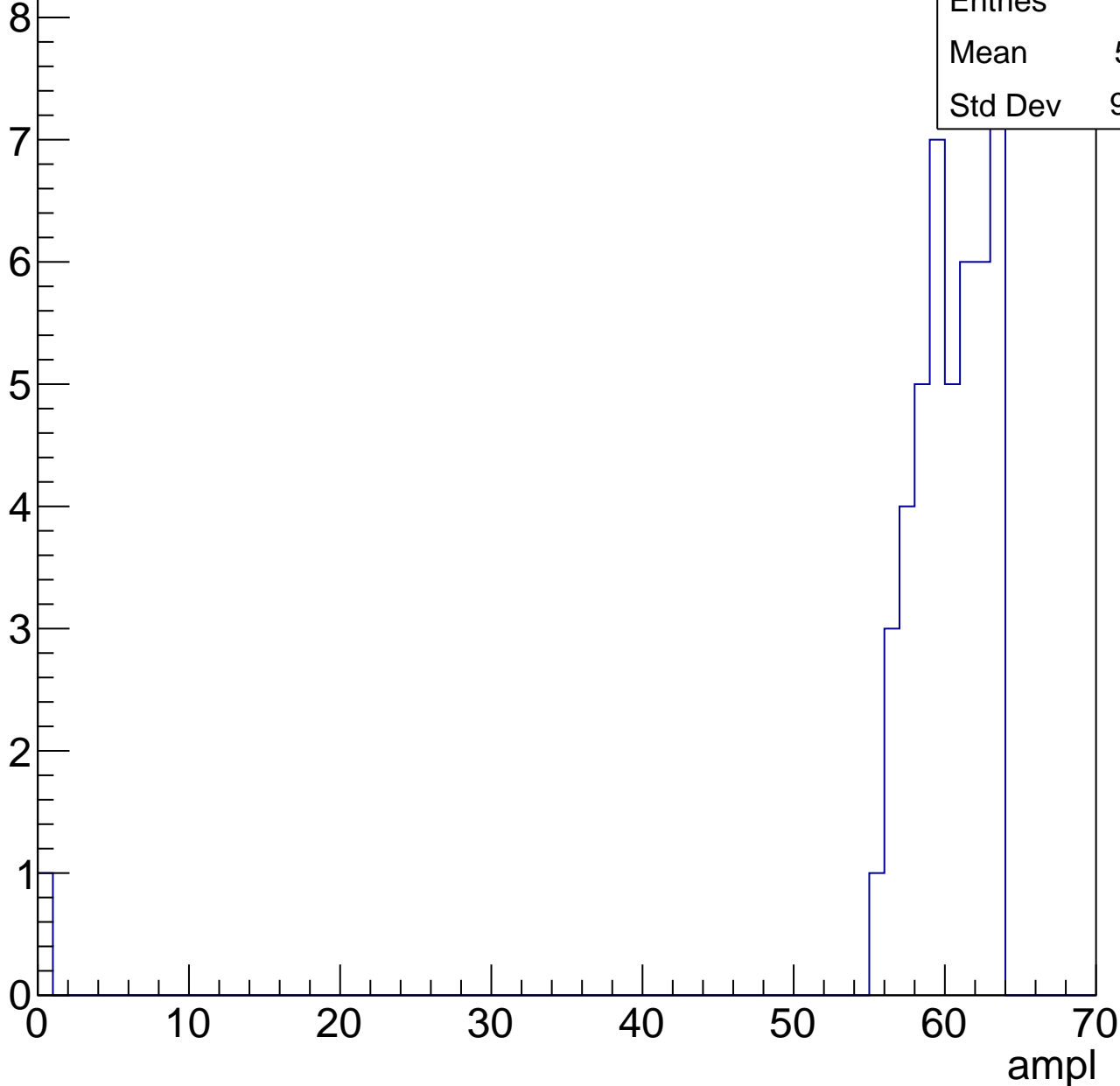


B1L101S, U17-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

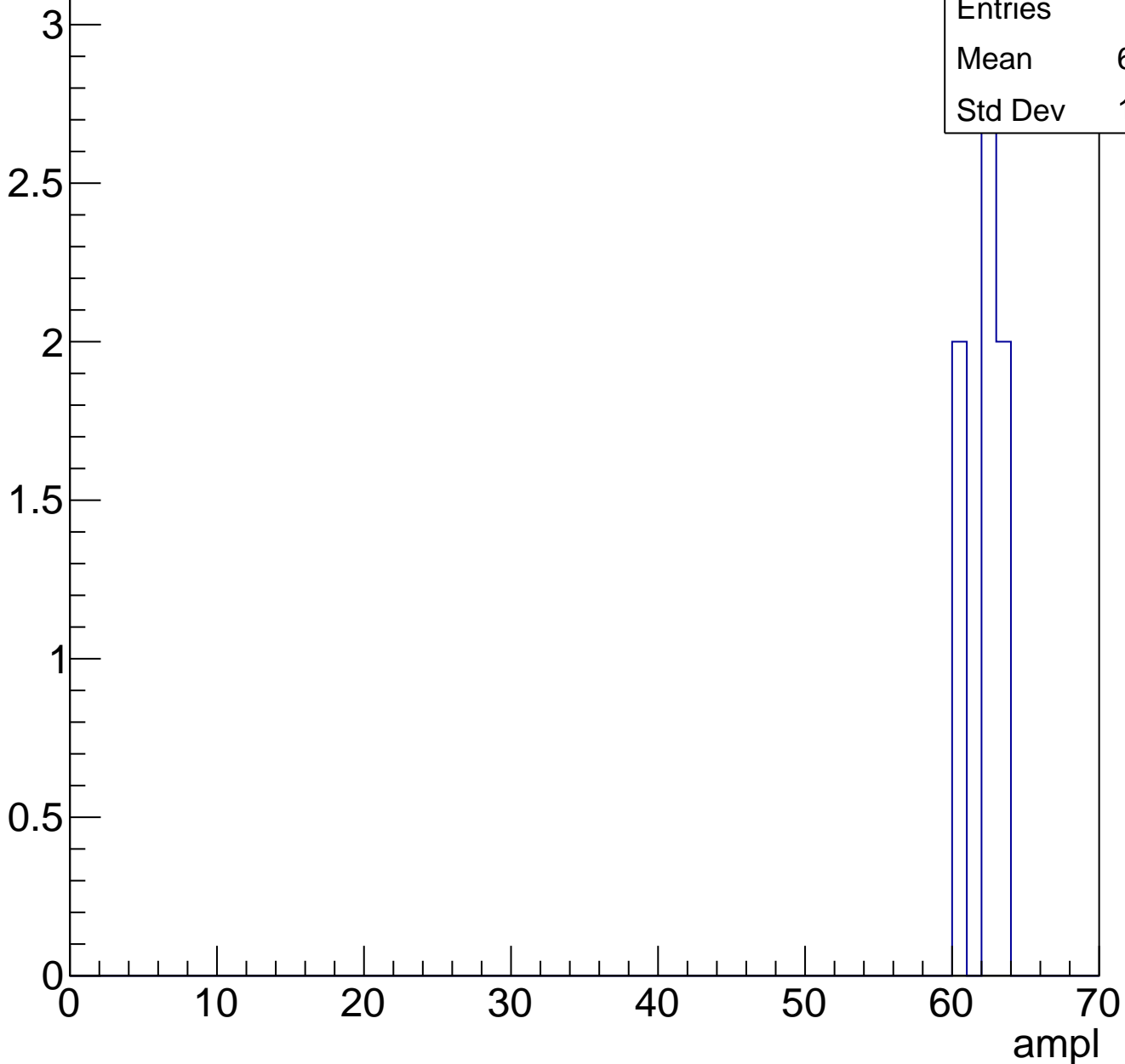
Entries	46
Mean	58.61
Std Dev	9.028



B1L101S, U17-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	10
Std Dev	10

B1L101S, U17-ch4, adc0

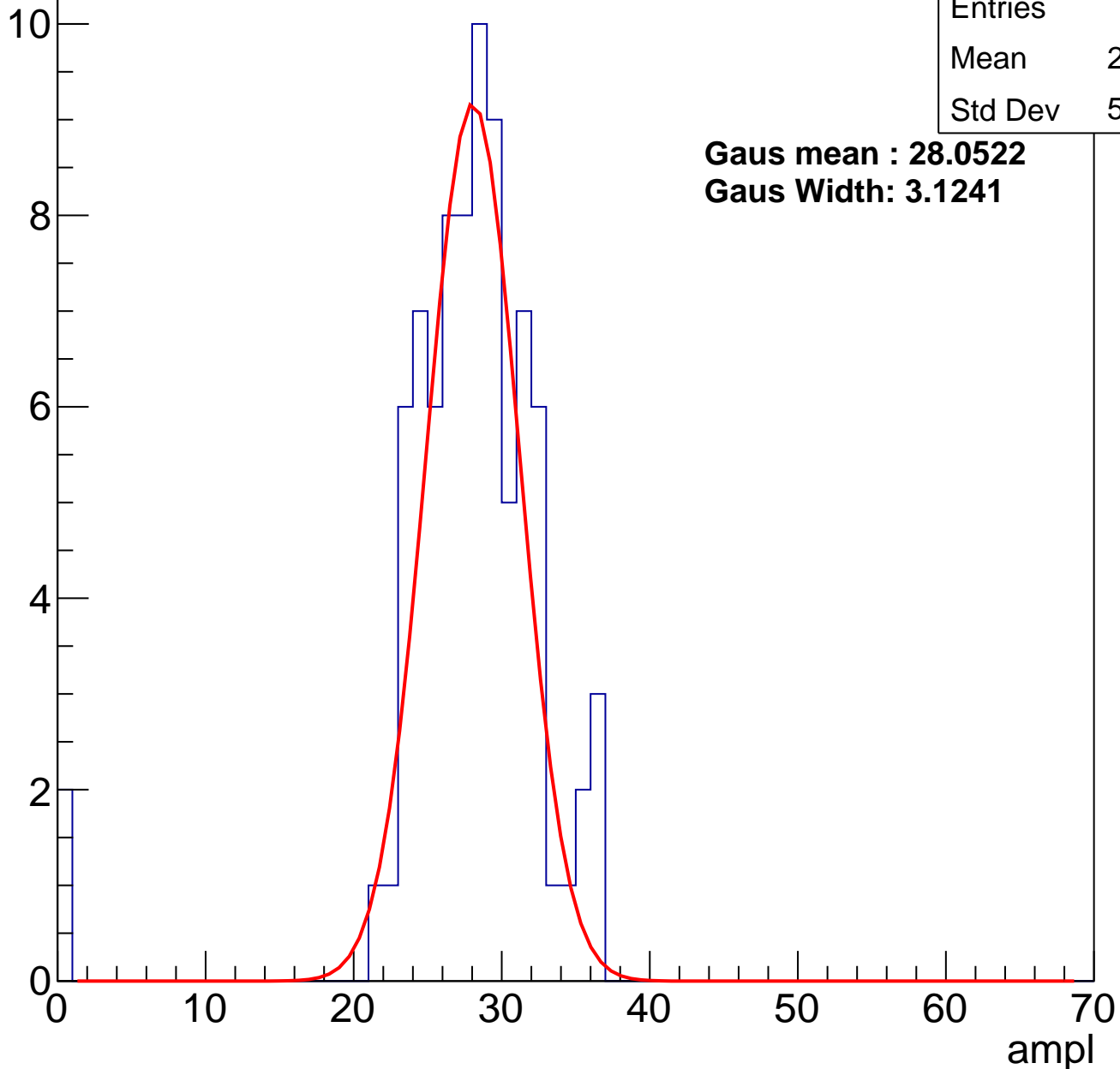
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	27.33
Std Dev	5.499

Gaus mean : 28.0522

Gaus Width: 3.1241

Entry



B1L101S, U17-ch4, adc1

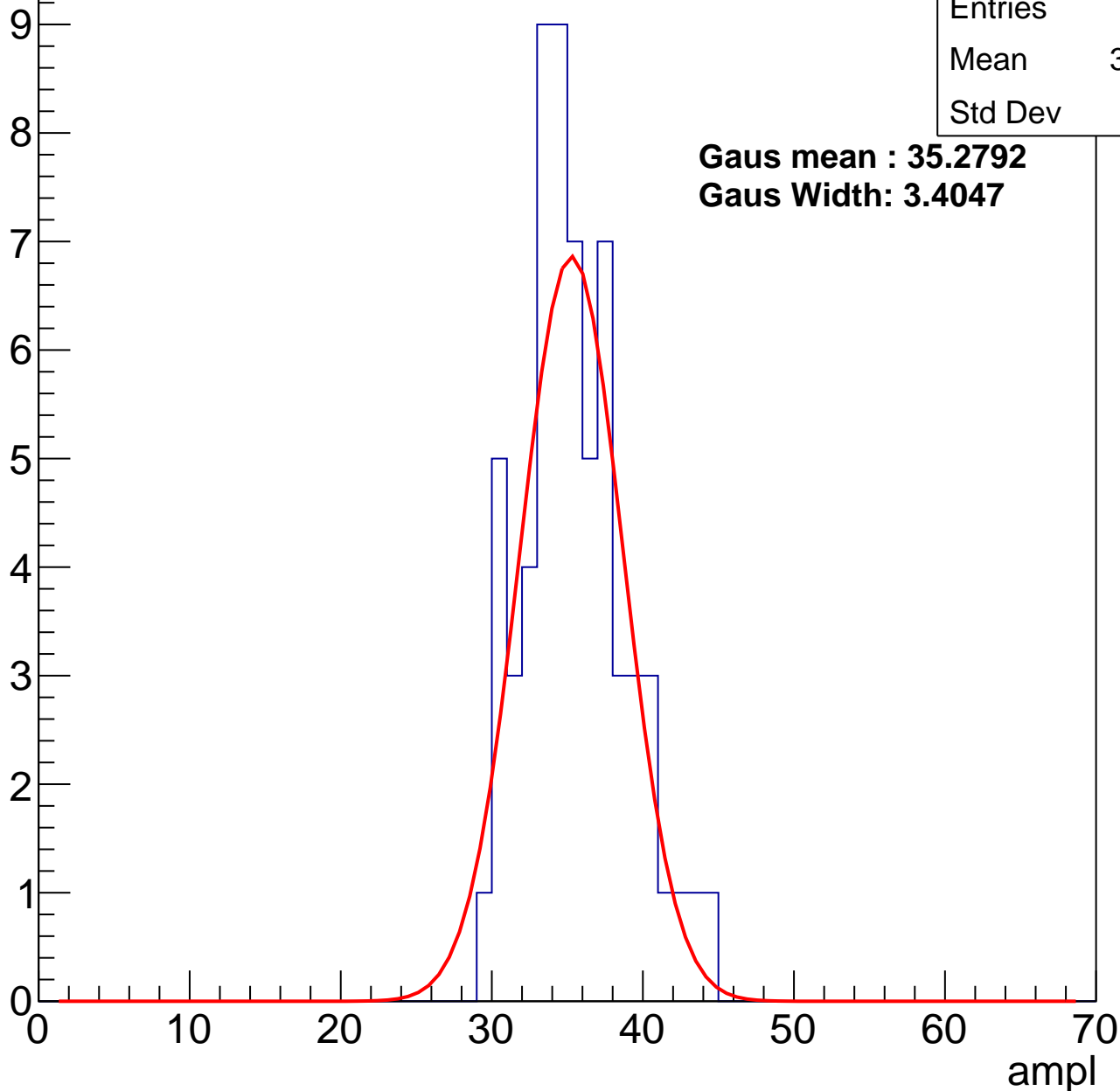
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.05
Std Dev	3.34

Gaus mean : 35.2792

Gaus Width: 3.4047



B1L101S, U17-ch4, adc2

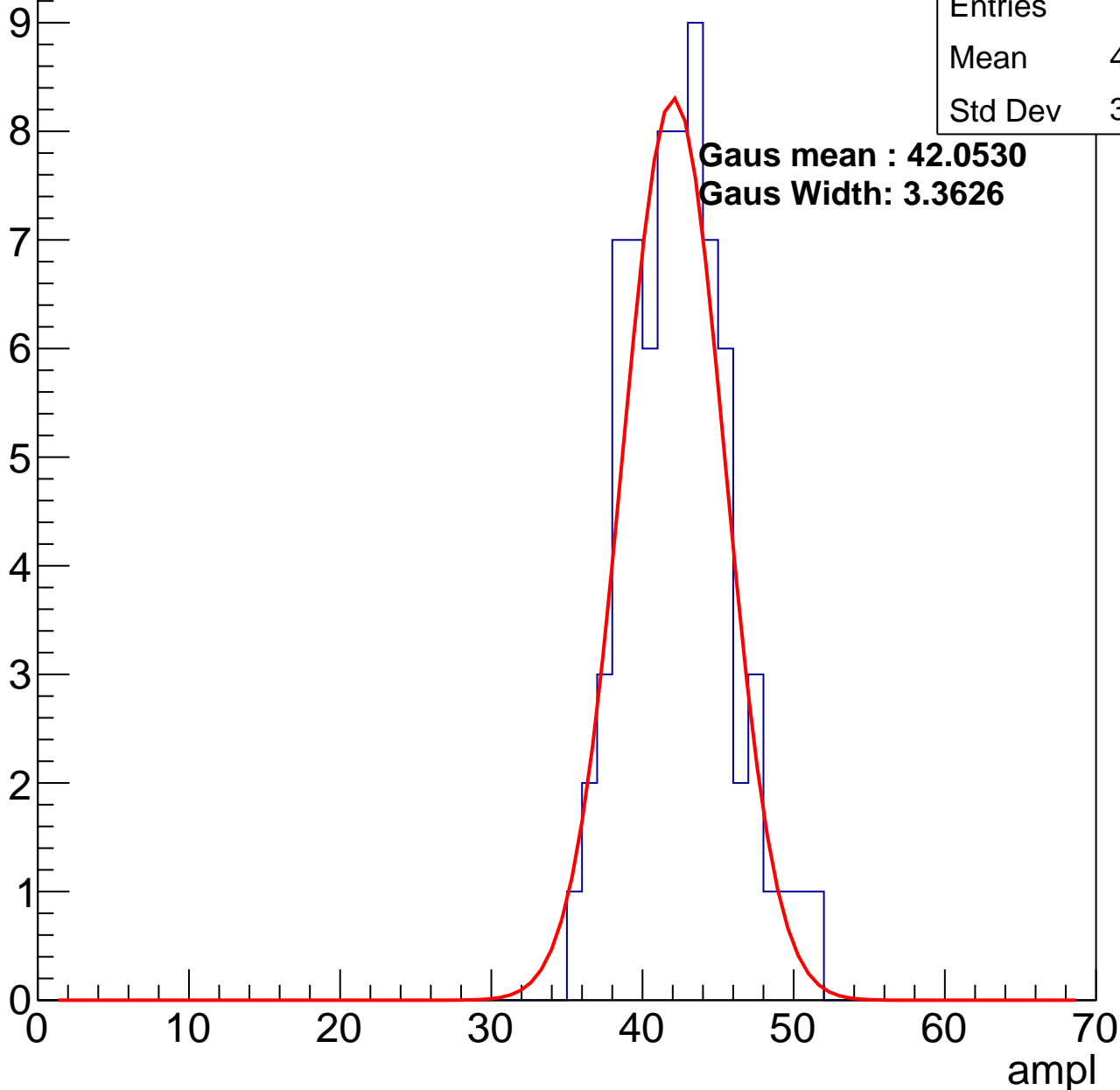
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.88
Std Dev	3.372

Gaus mean : 42.0530

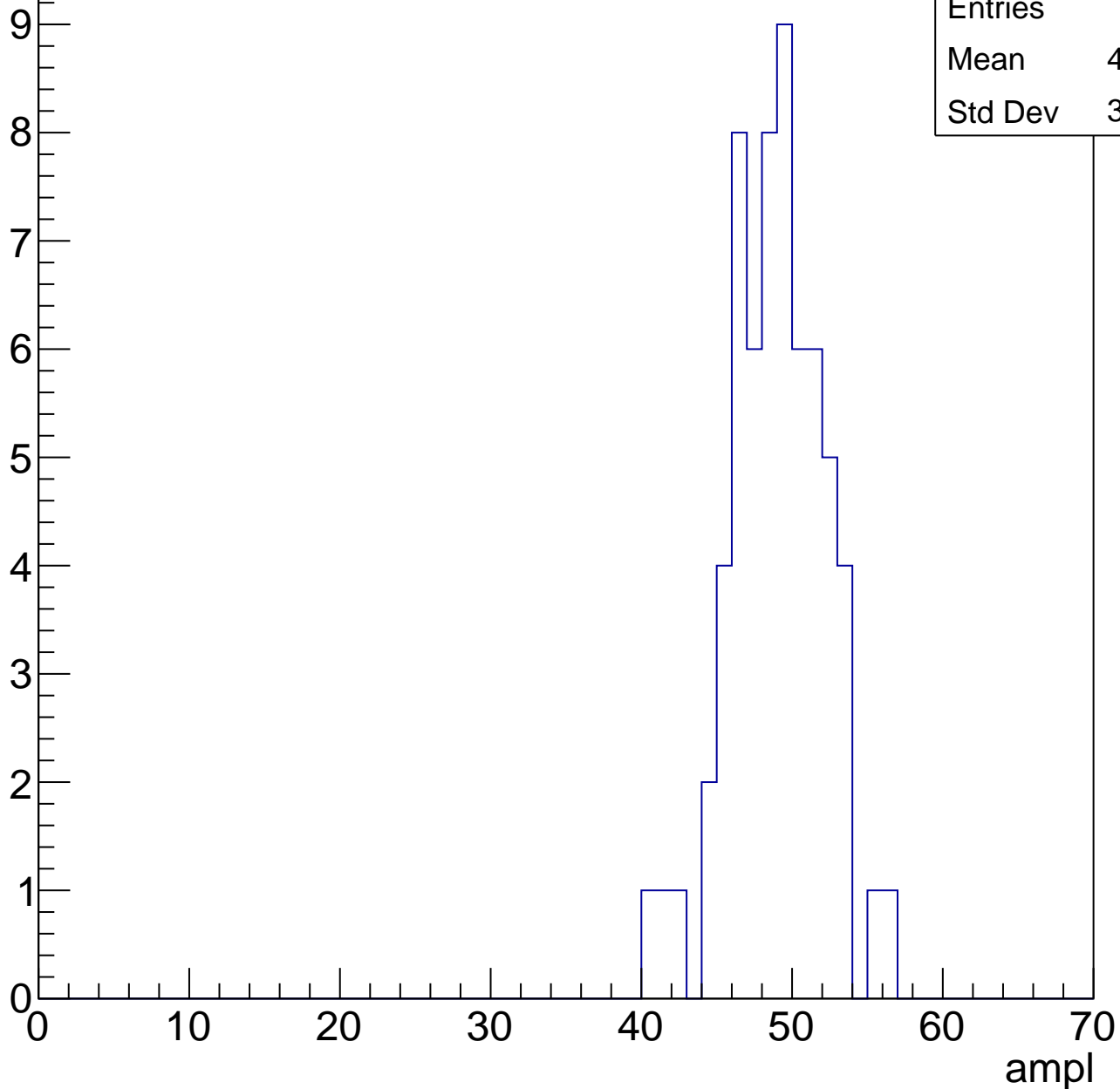
Gaus Width: 3.3626



B1L101S, U17-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



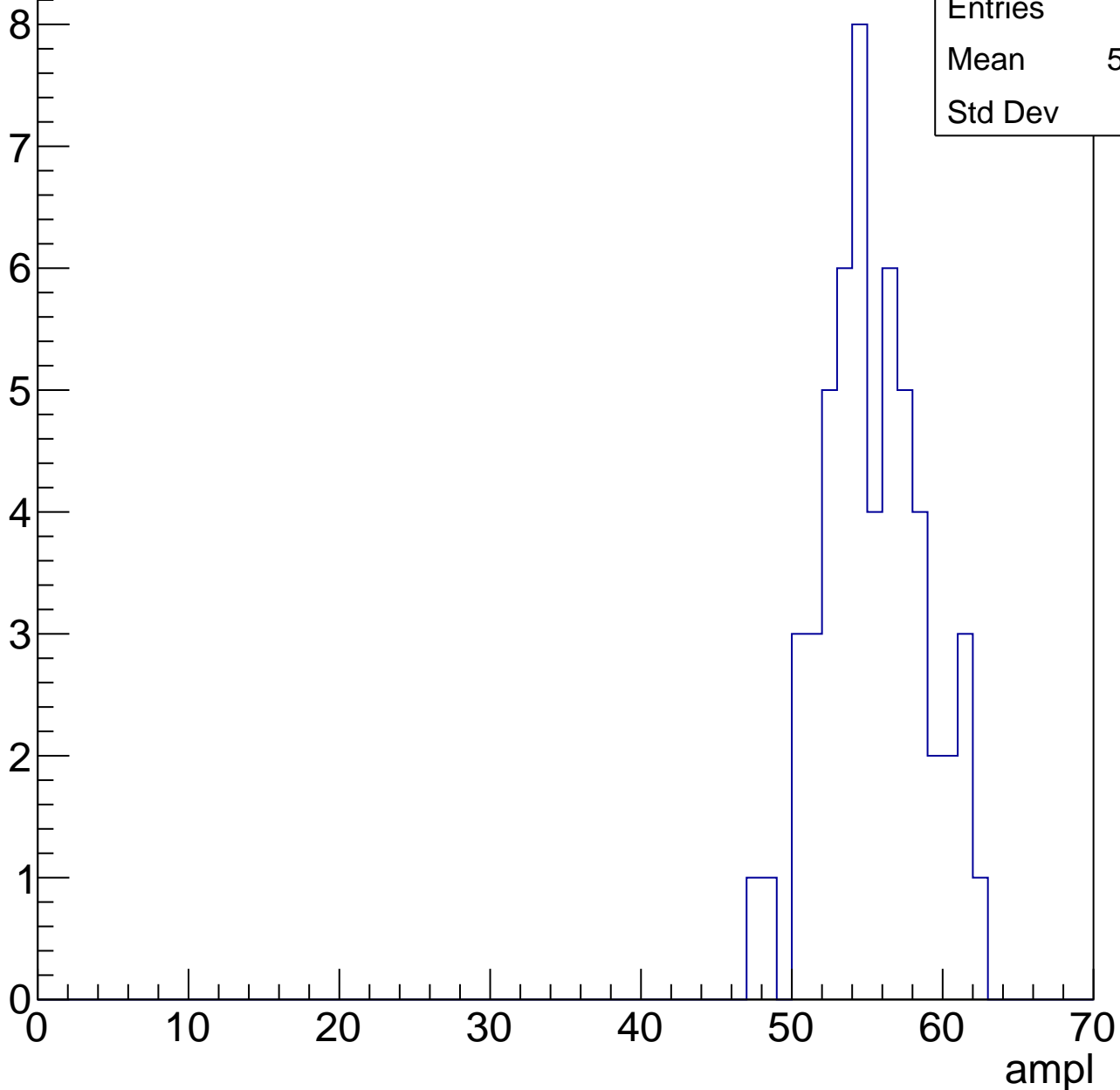
Entries	63
Mean	48.49
Std Dev	3.136

B1L101S, U17-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.89
Std Dev	3.37

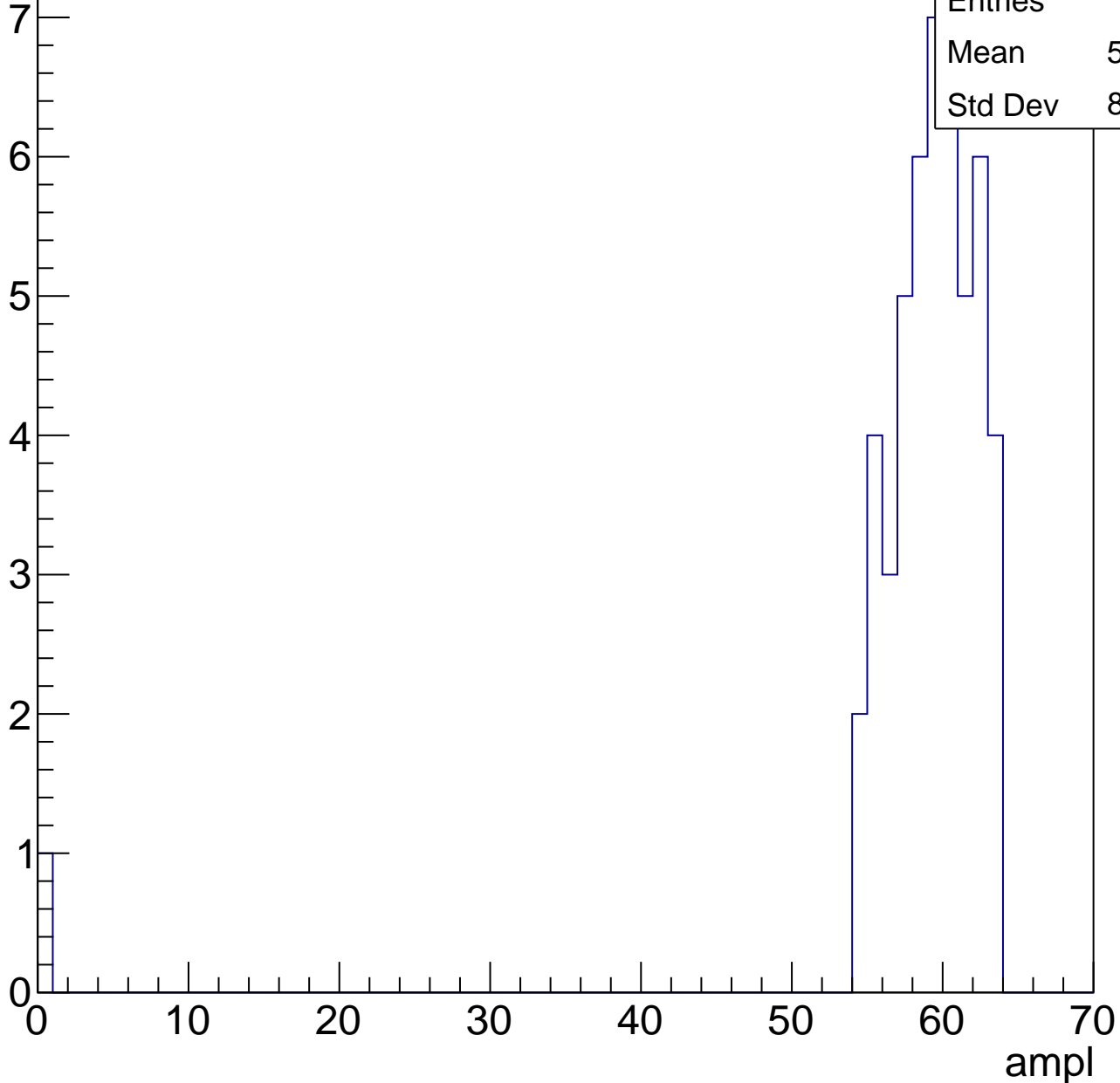


B1L101S, U17-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.82
Std Dev	8.629

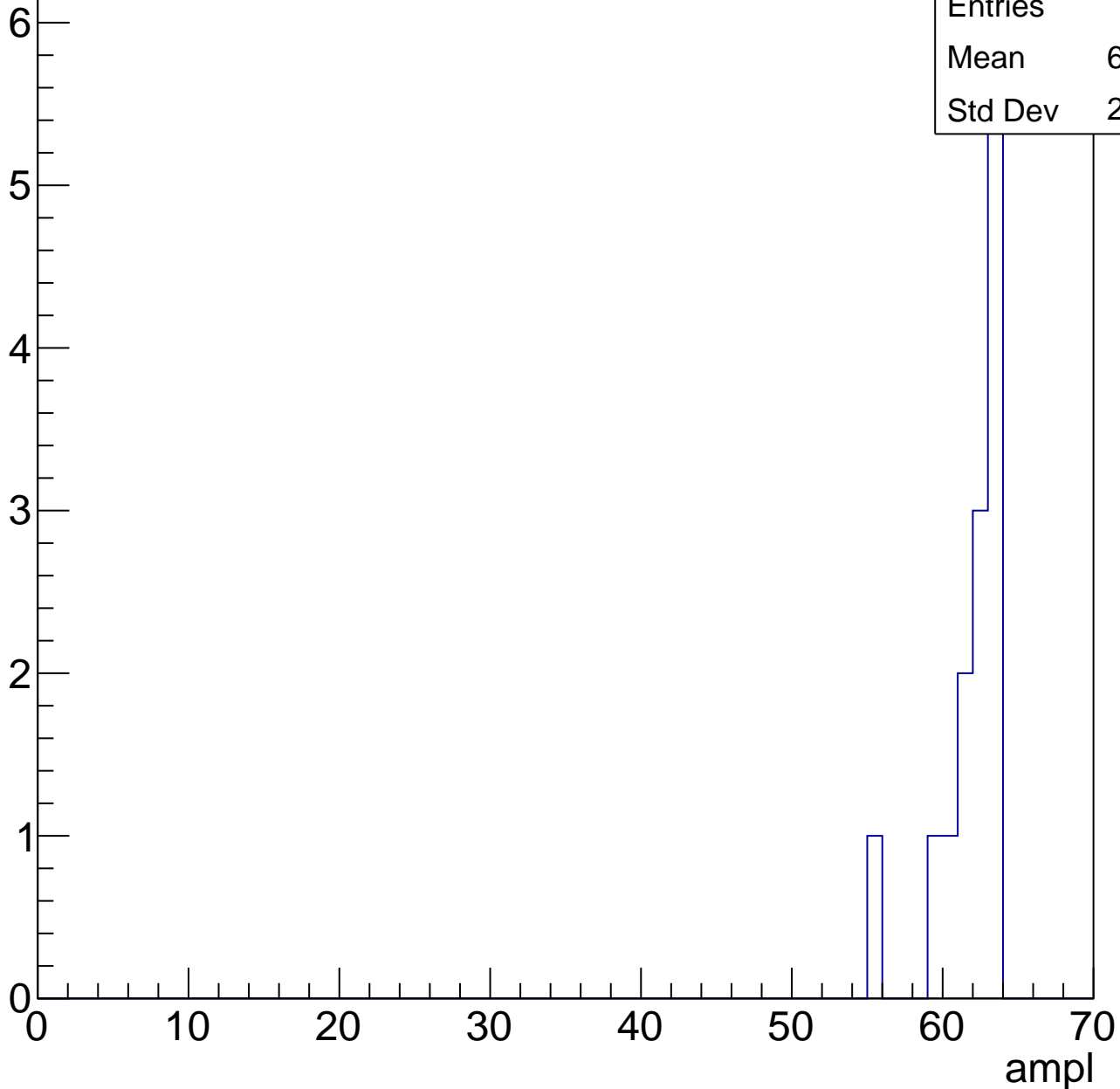


B1L101S, U17-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.43
Std Dev	2.162



B1L101S, U17-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch5, adc0

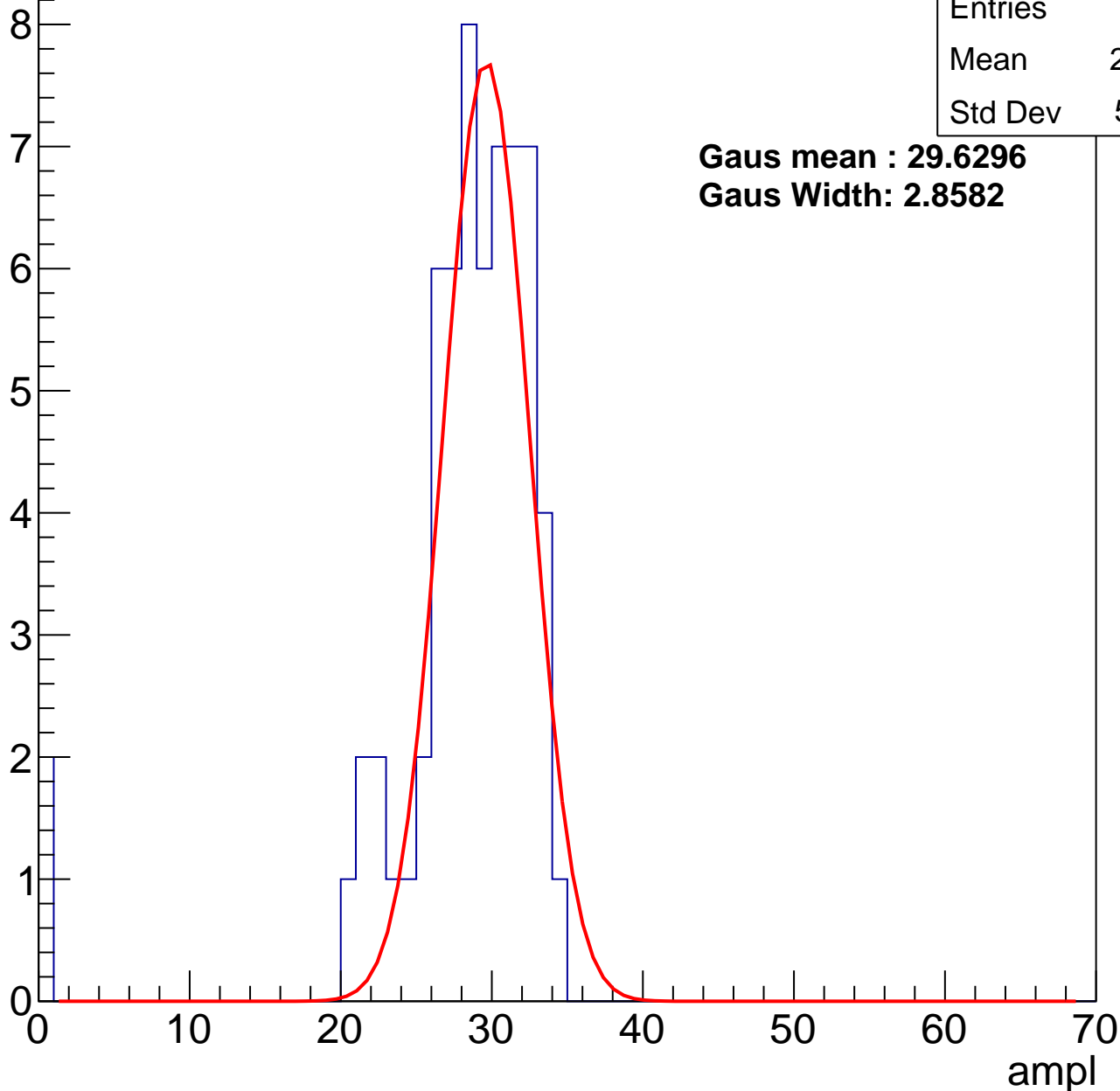
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.56
Std Dev	5.941

Gaus mean : 29.6296

Gaus Width: 2.8582



B1L101S, U17-ch5, adc1

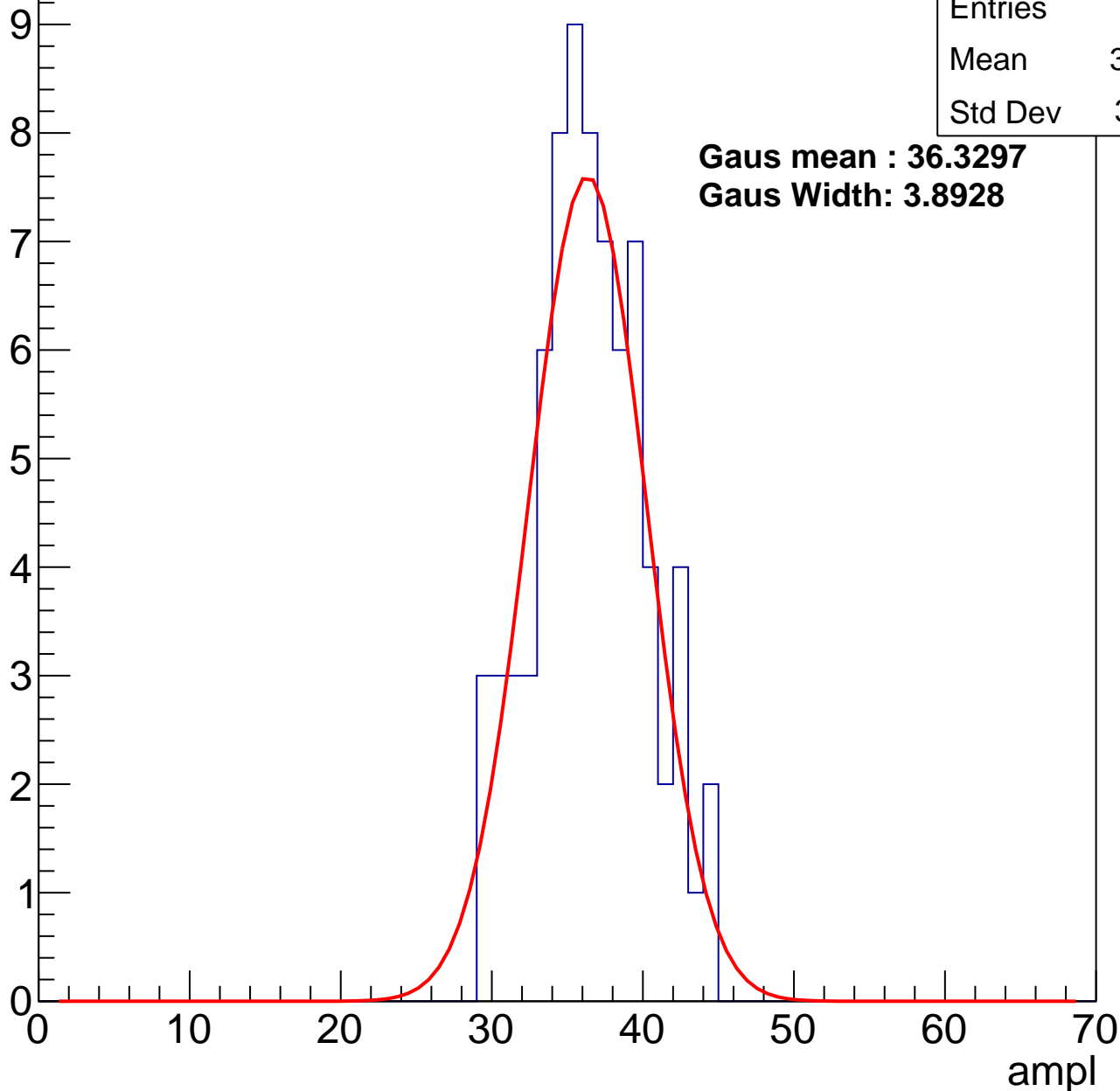
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.05
Std Dev	3.631

Gaus mean : 36.3297

Gaus Width: 3.8928



B1L101S, U17-ch5, adc2

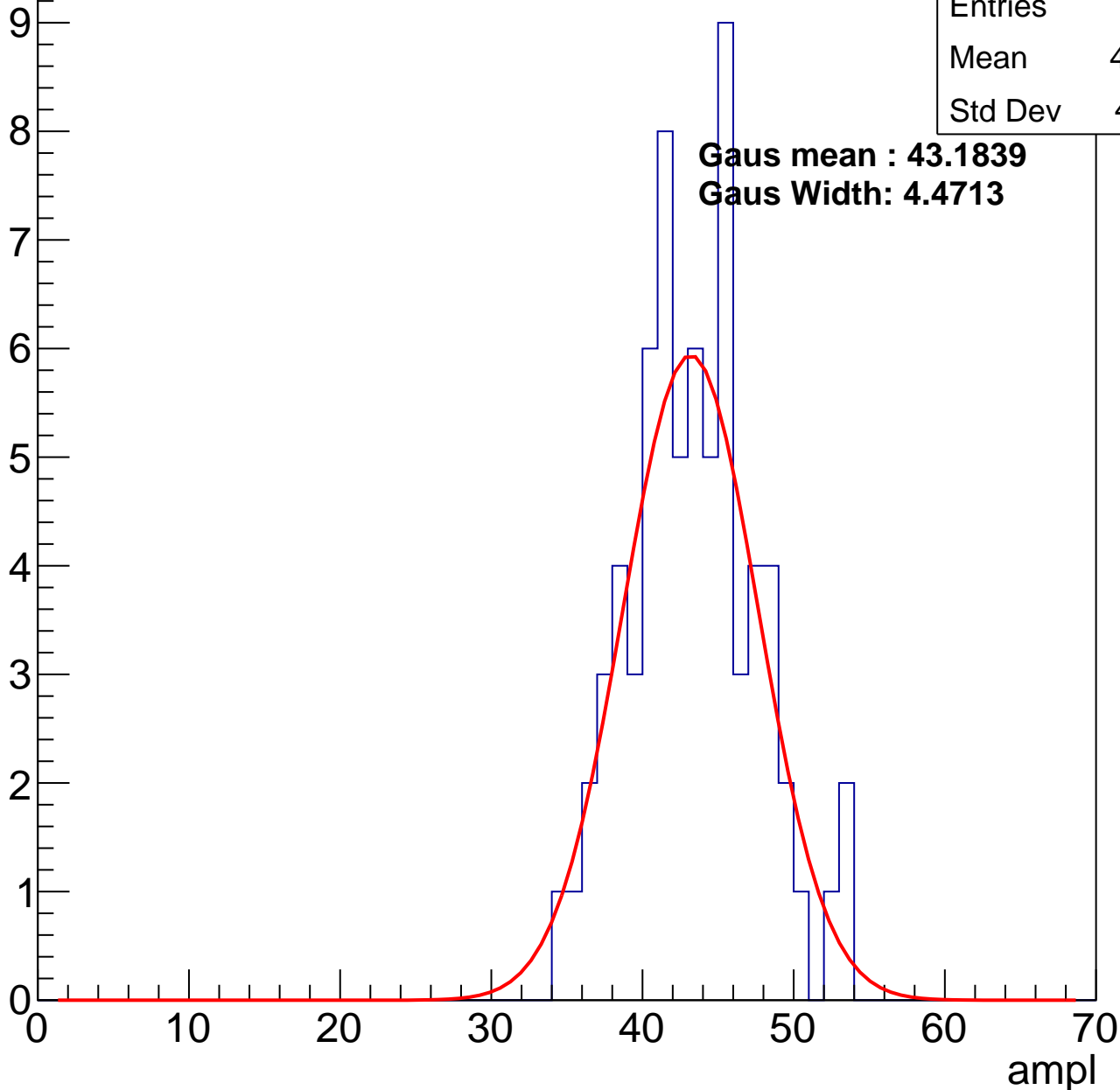
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.94
Std Dev	4.181

Gaus mean : 43.1839

Gaus Width: 4.4713

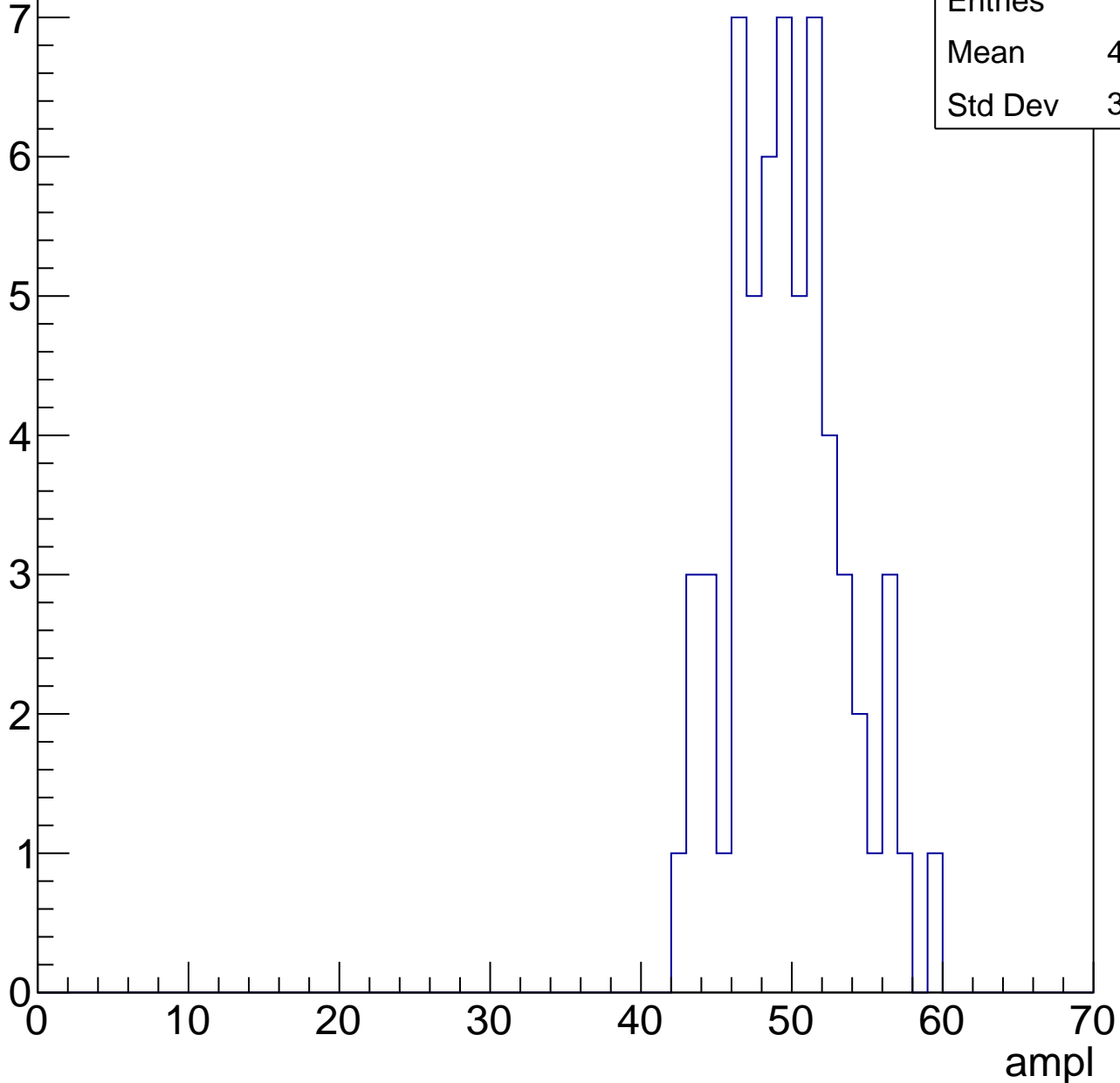


B1L101S, U17-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

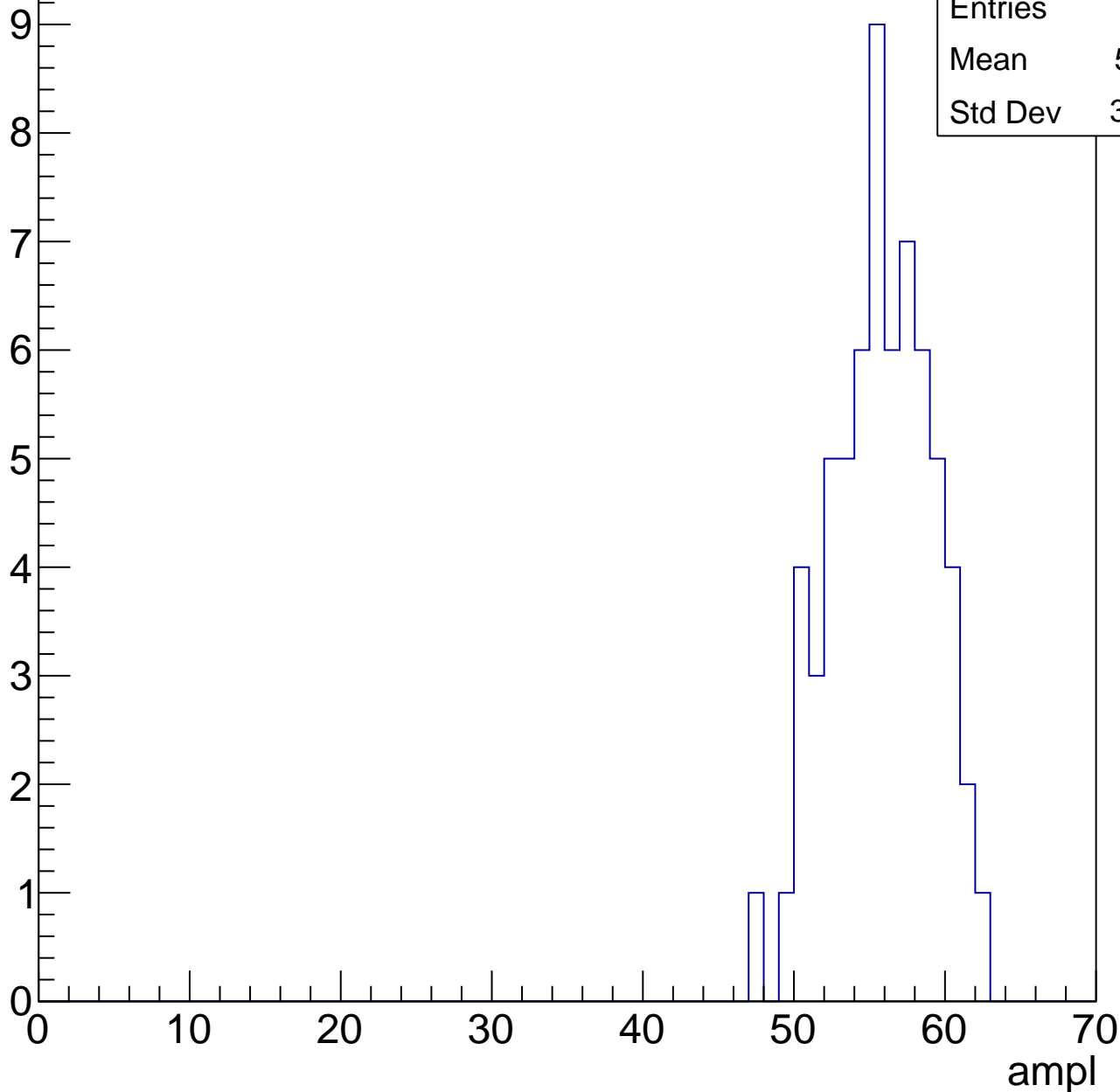
Entries	60
Mean	49.28
Std Dev	3.755



B1L101S, U17-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch5, adc5

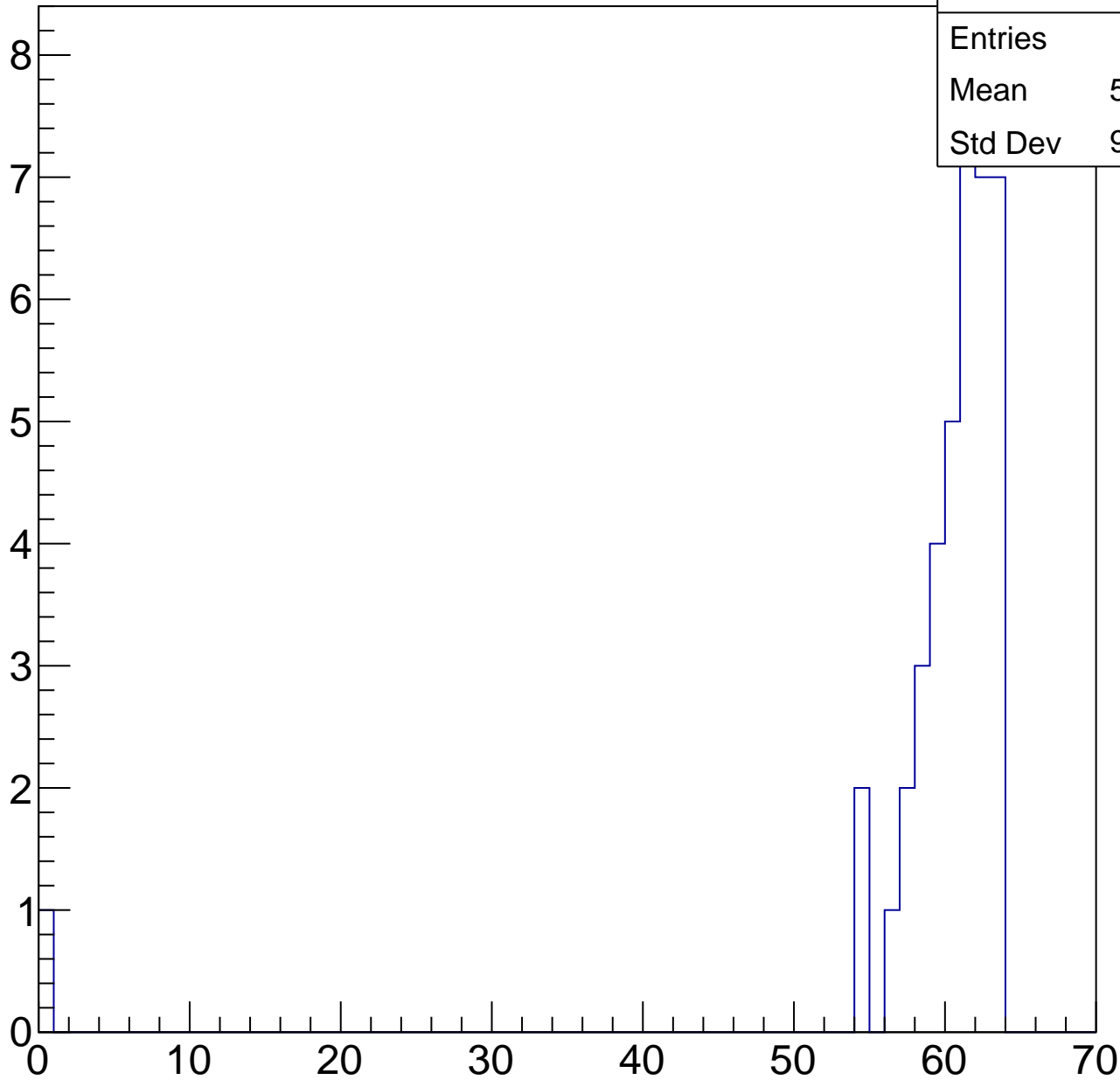
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.77
Std Dev	9.697

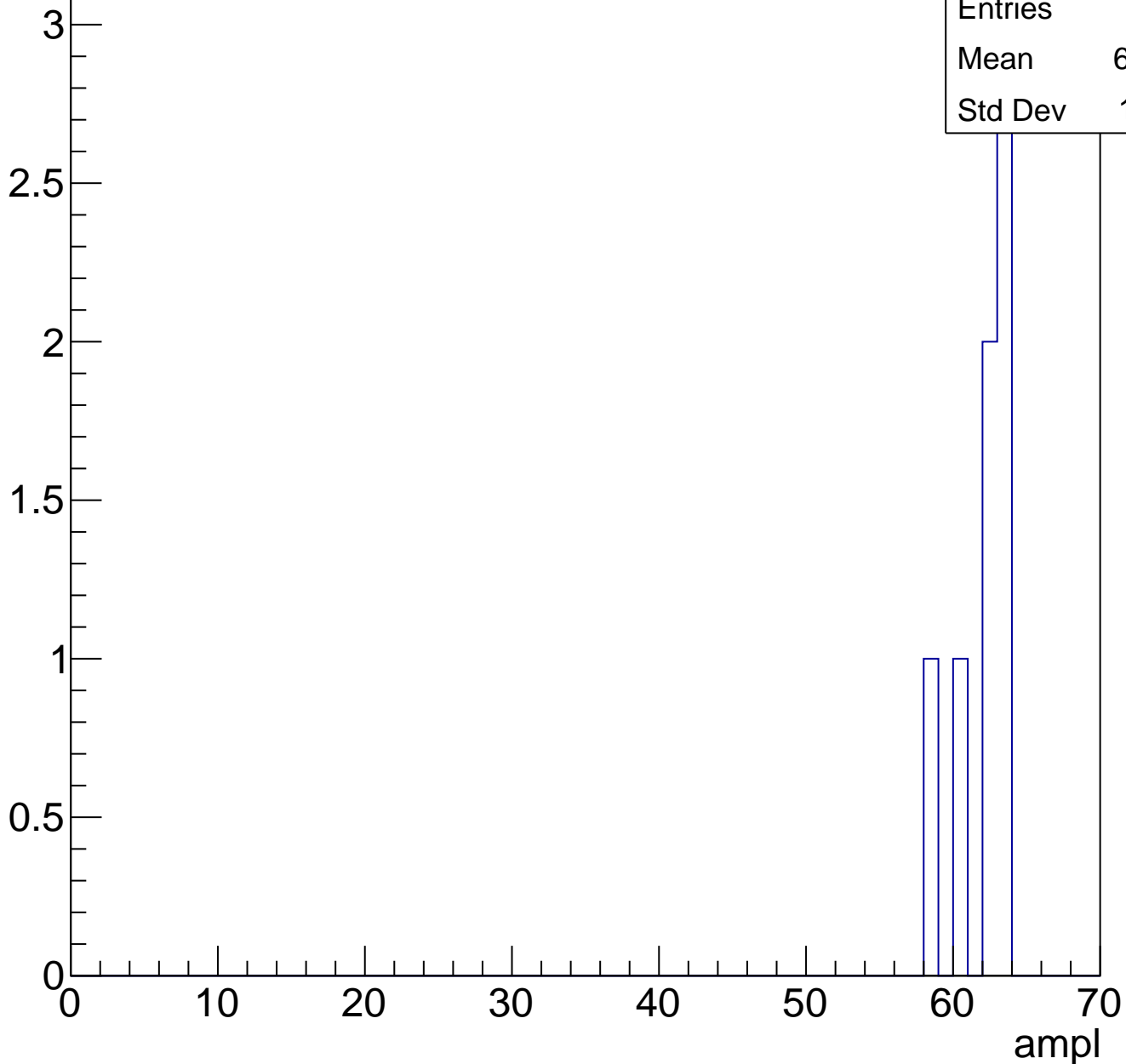
ampl



B1L101S, U17-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

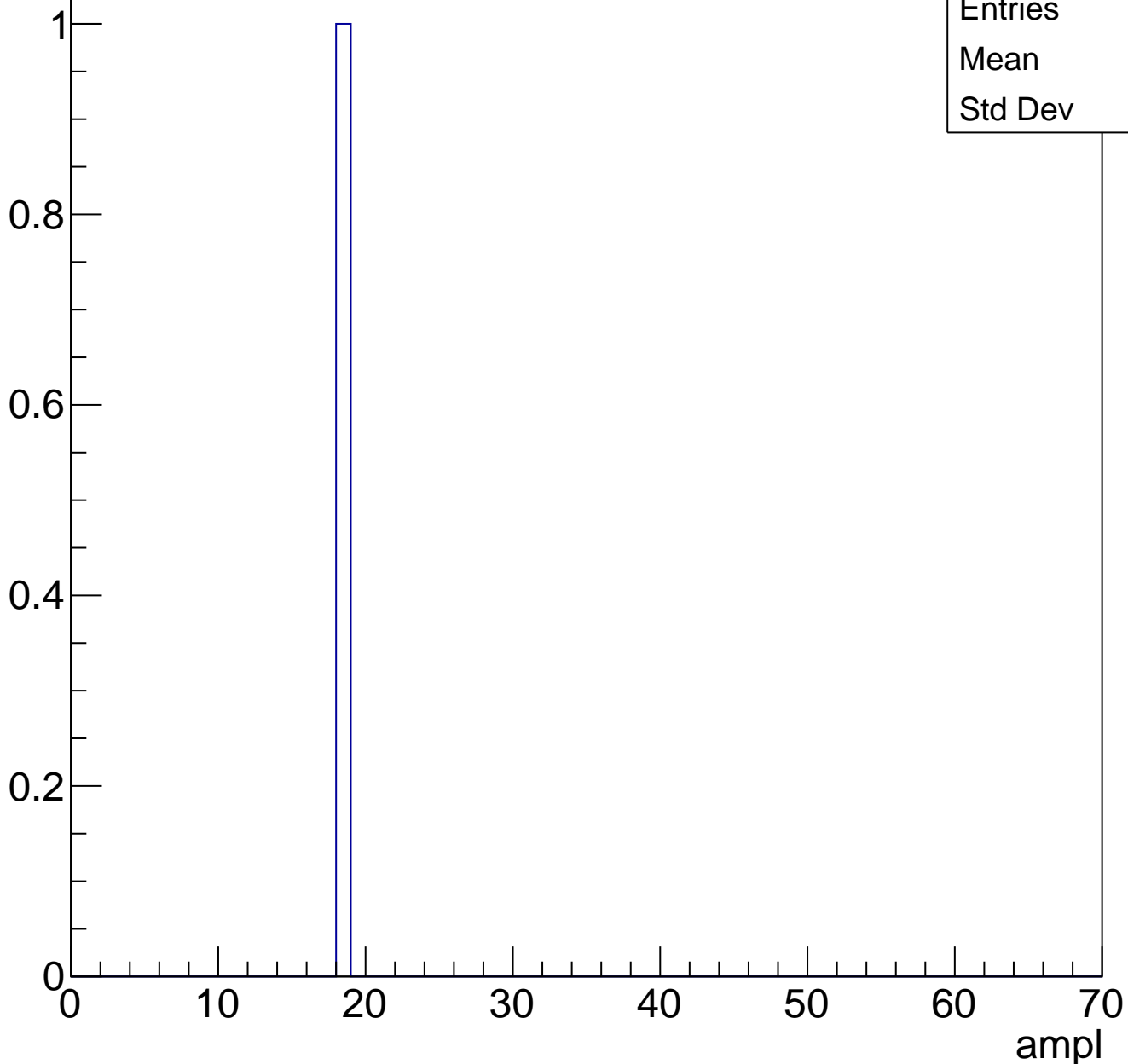
Entry



B1L101S, U17-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	18
Std Dev	0

B1L101S, U17-ch6, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	28.41
Std Dev	6.074

Gaus mean : 29.7320

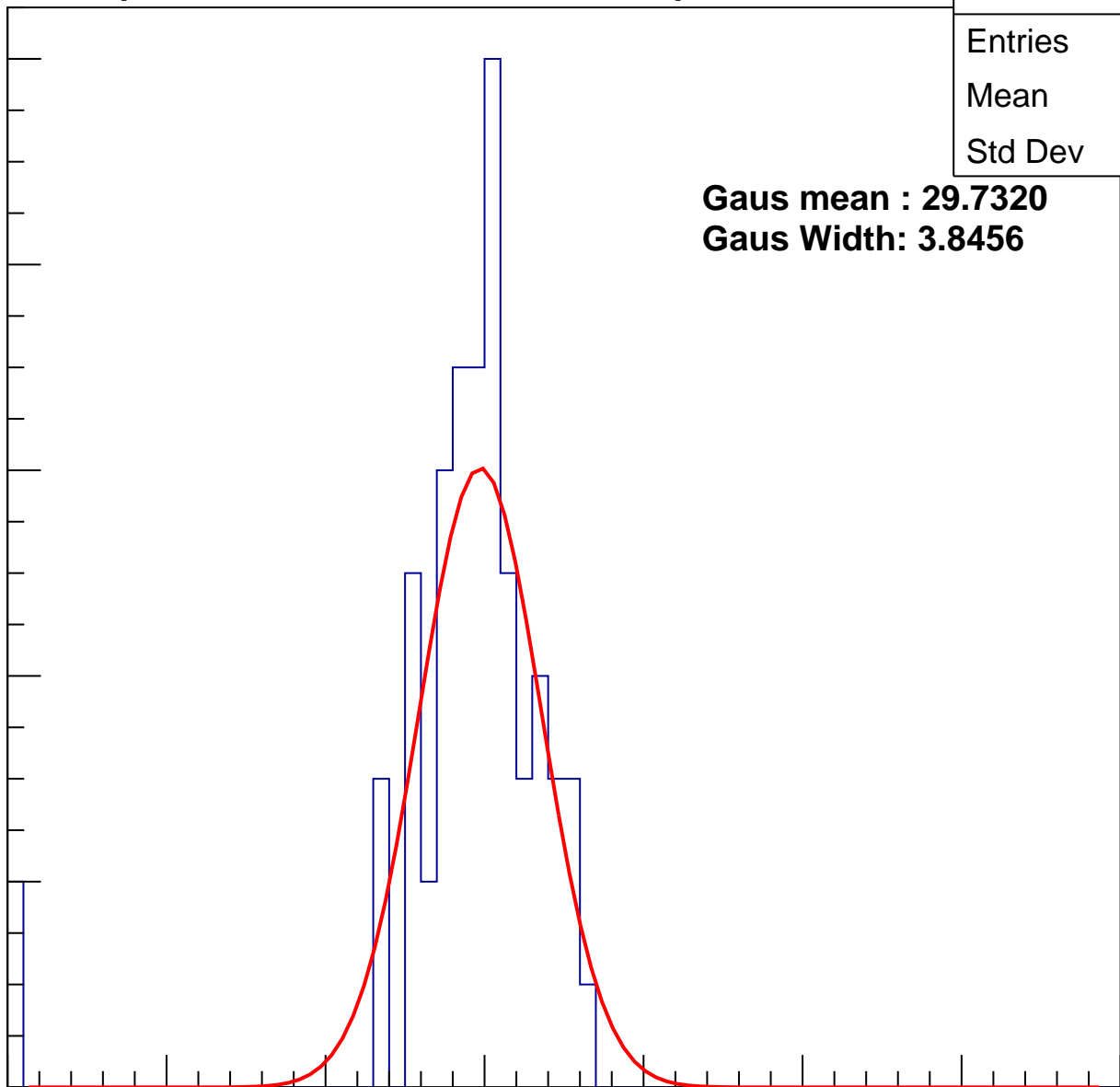
Gaus Width: 3.8456

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch6, adc1

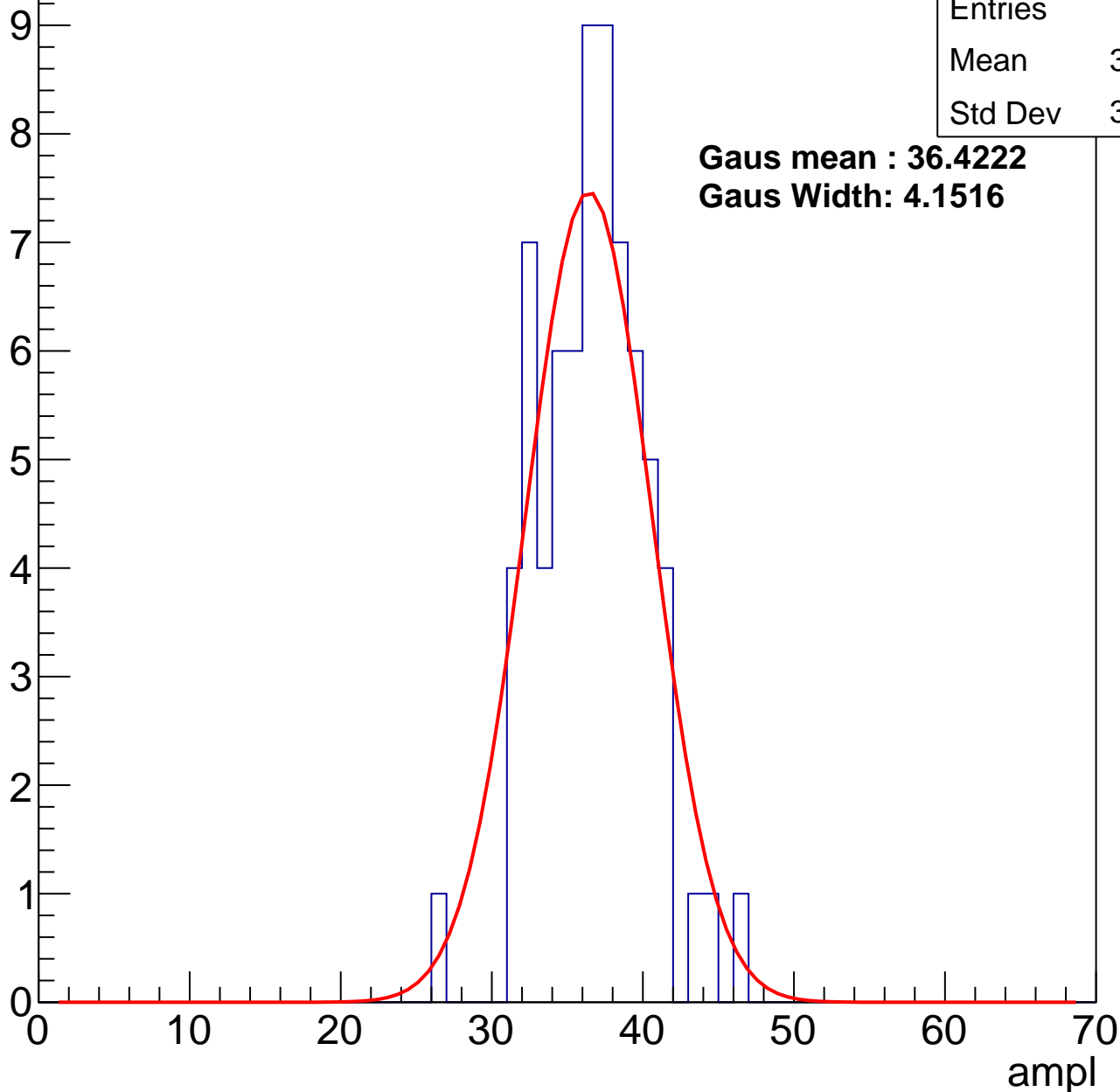
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.25
Std Dev	3.475

Gaus mean : 36.4222

Gaus Width: 4.1516



B1L101S, U17-ch6, adc2

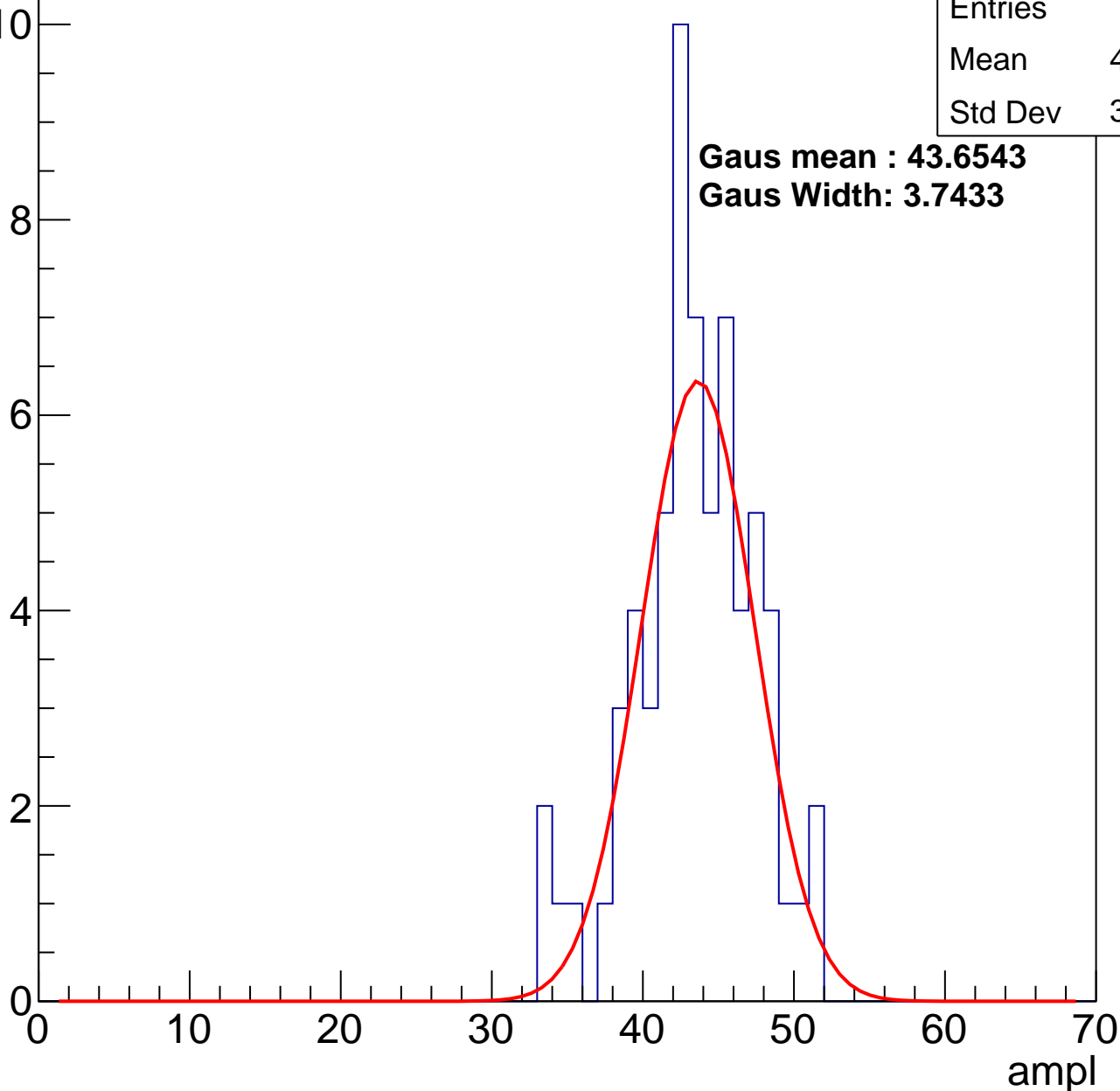
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.95
Std Dev	3.967

Gaus mean : 43.6543

Gaus Width: 3.7433

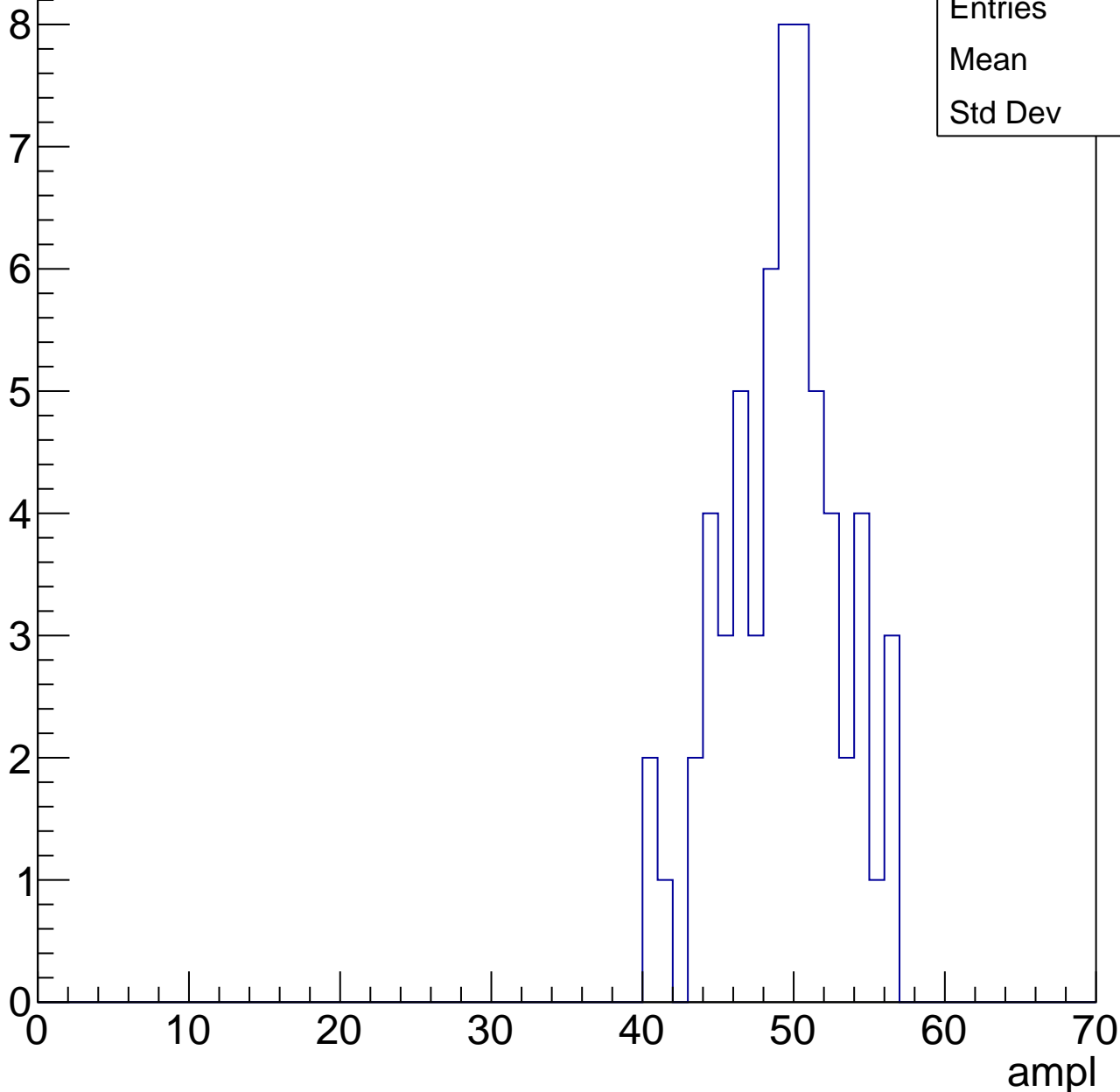


B1L101S, U17-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.8
Std Dev	3.81

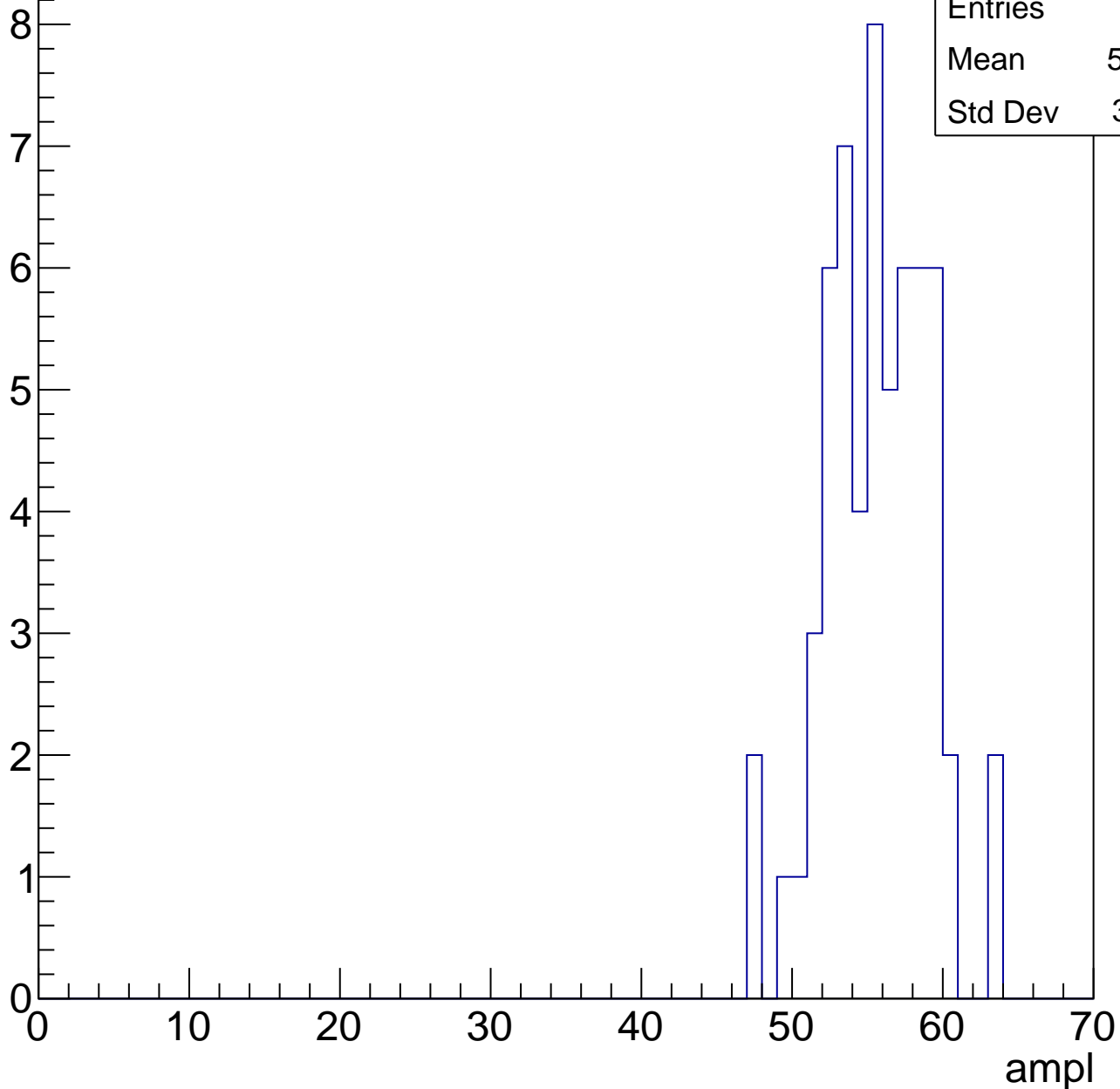


B1L101S, U17-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.17
Std Dev	3.391



B1L101S, U17-ch6, adc5

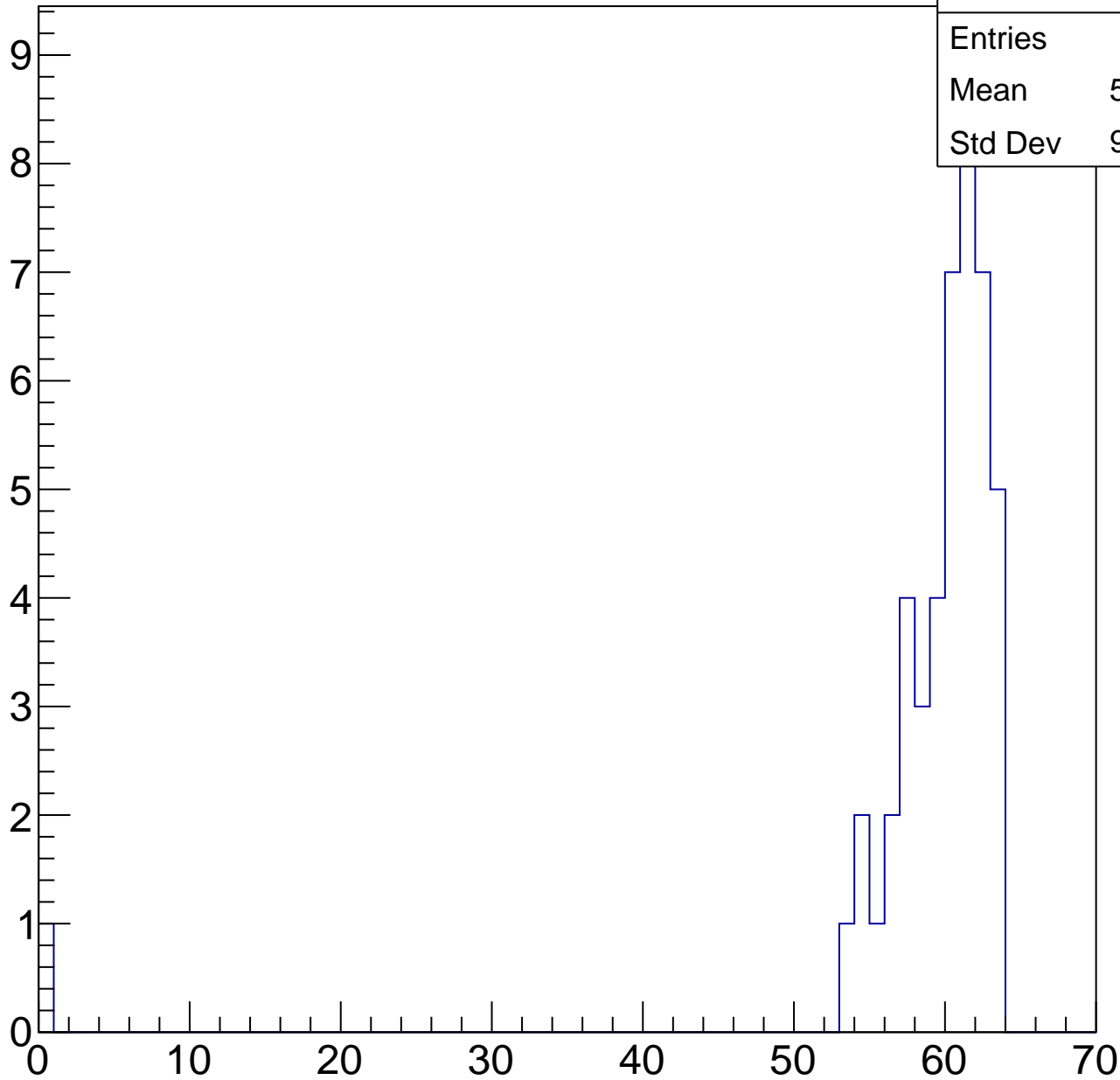
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.35
Std Dev	9.073

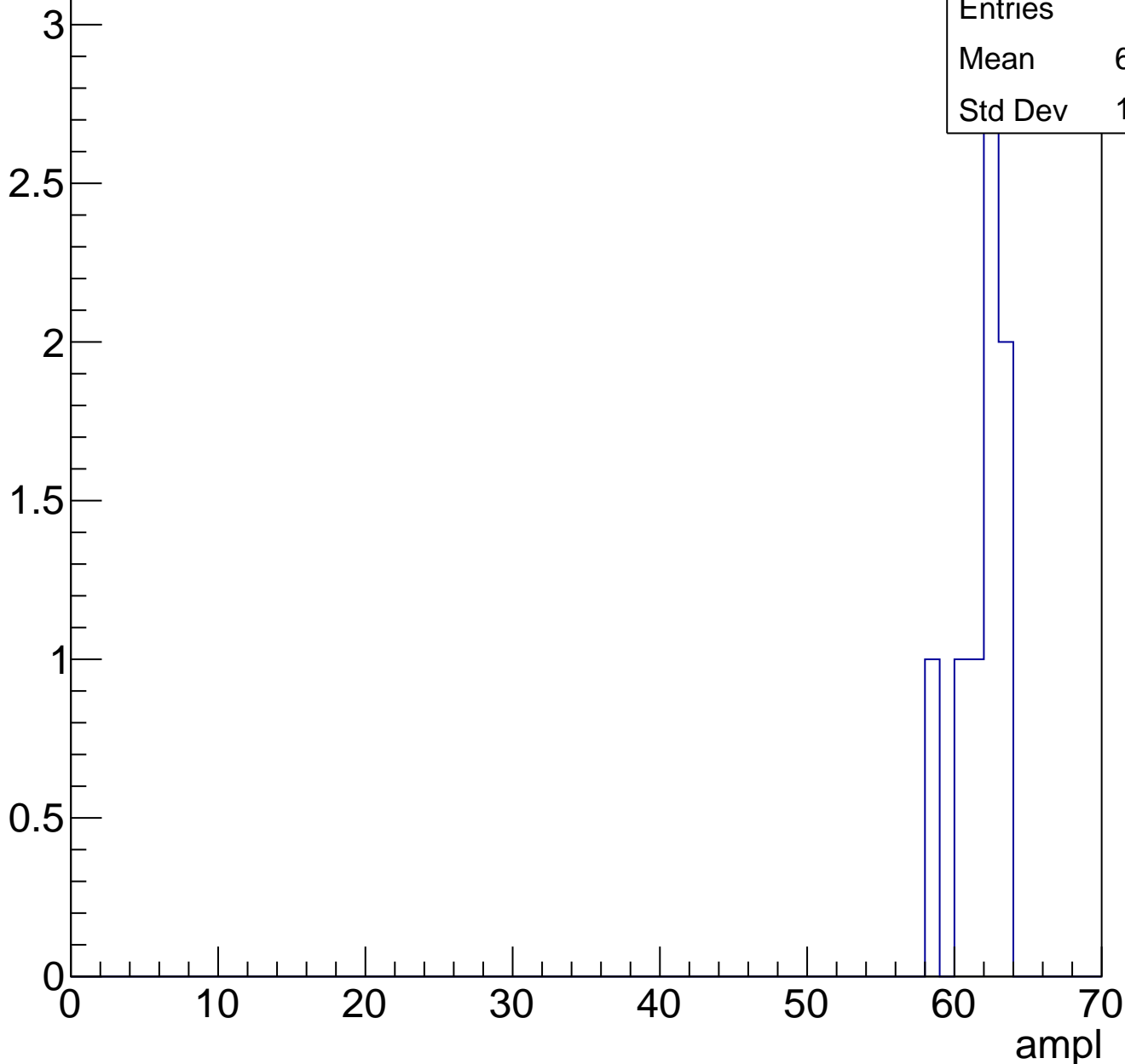
ampl



B1L101S, U17-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U17-ch7, adc0

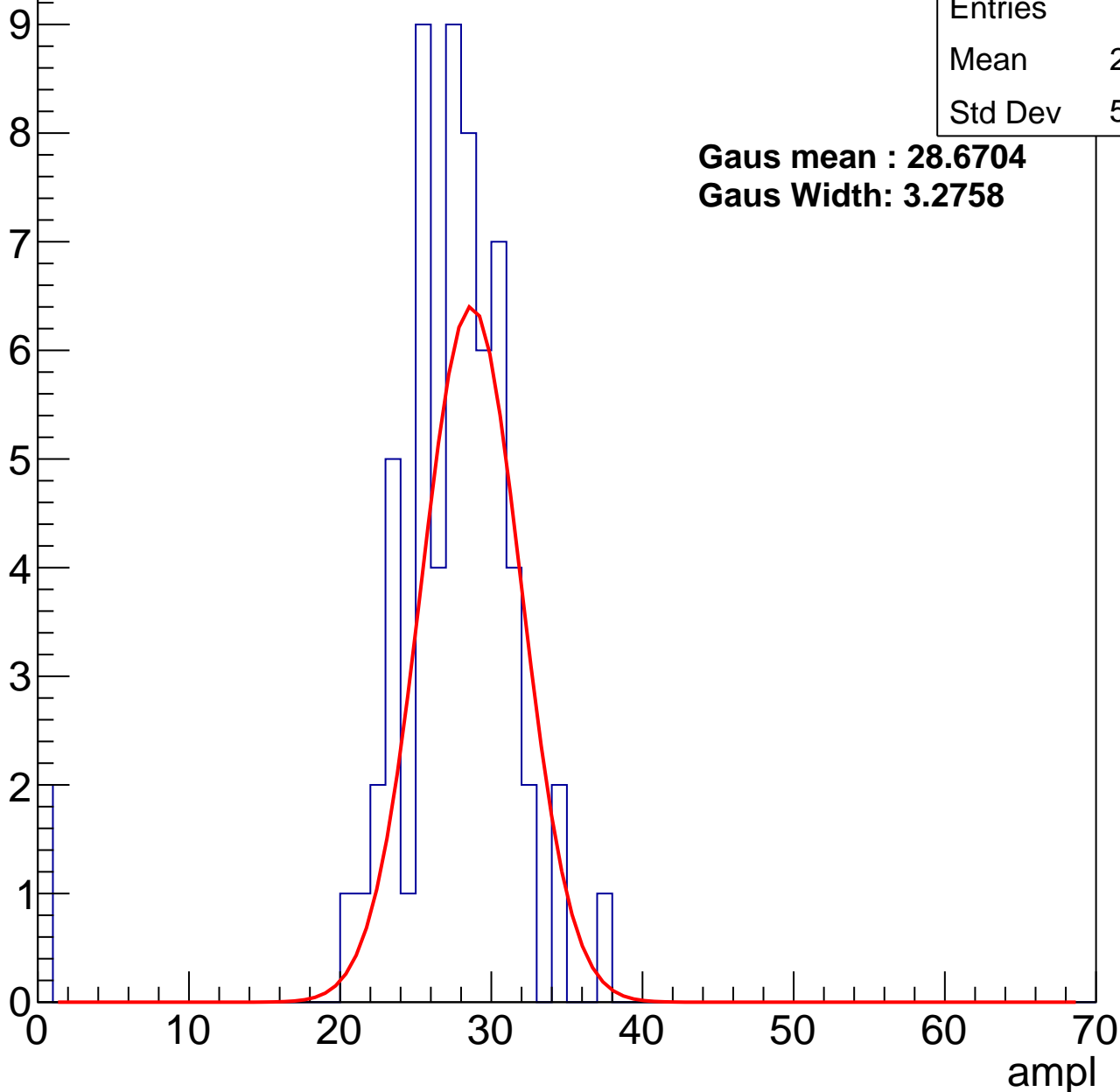
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	26.52
Std Dev	5.747

Gaus mean : 28.6704

Gaus Width: 3.2758



B1L101S, U17-ch7, adc1

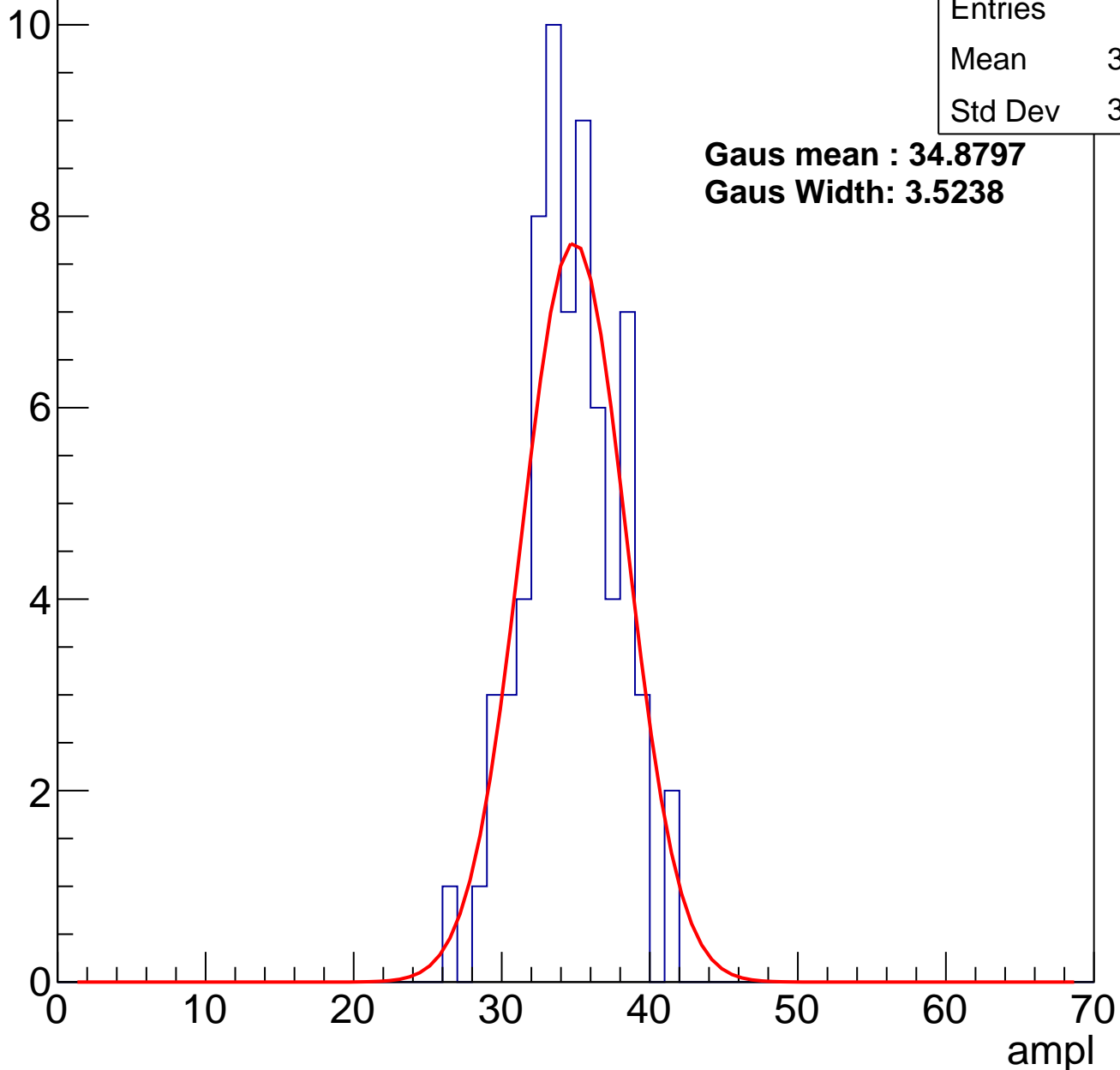
calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	34.16
Std Dev	3.099

Gaus mean : 34.8797

Gaus Width: 3.5238

Entry



B1L101S, U17-ch7, adc2

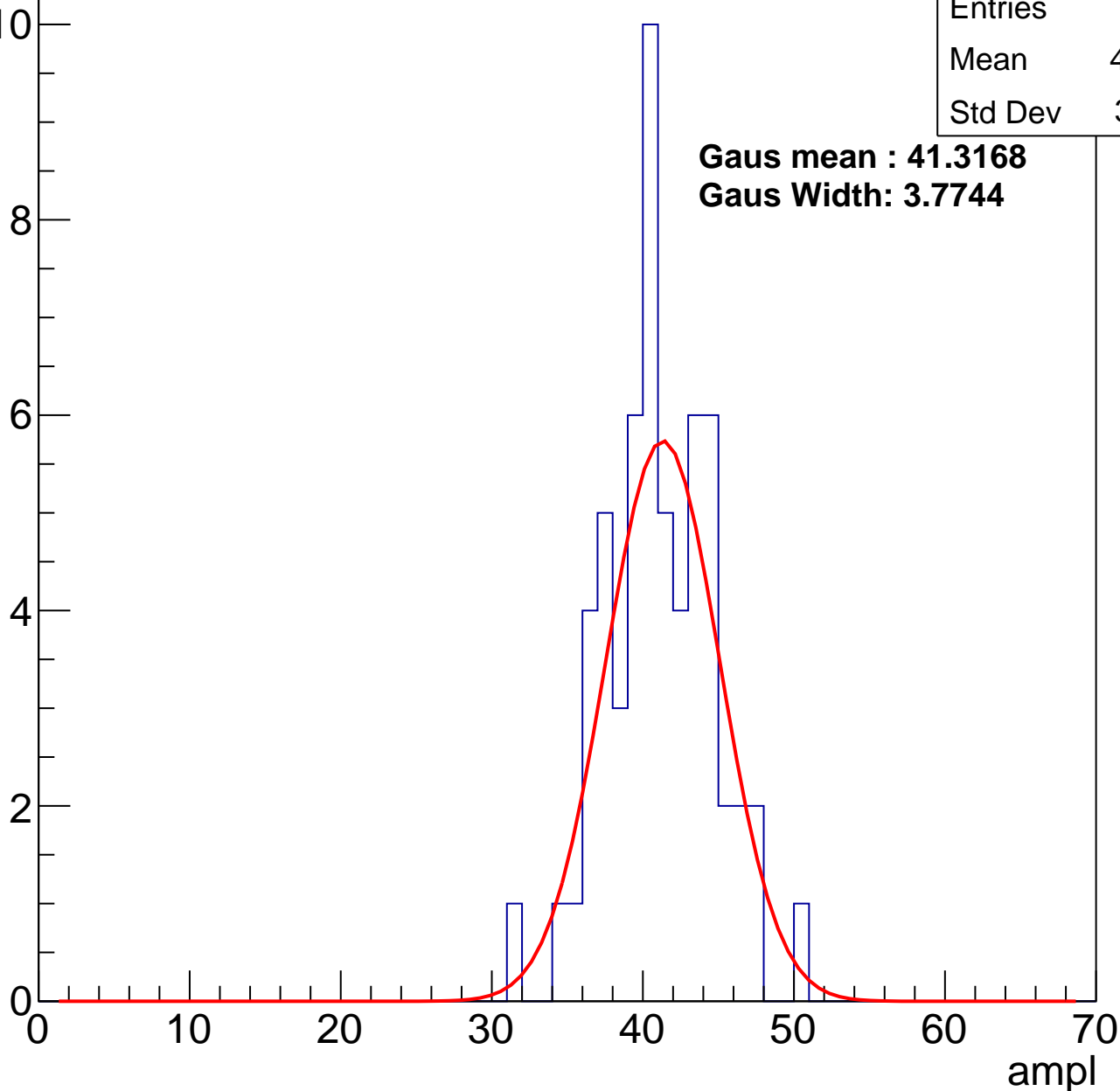
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	40.64
Std Dev	3.531

Gaus mean : 41.3168

Gaus Width: 3.7744

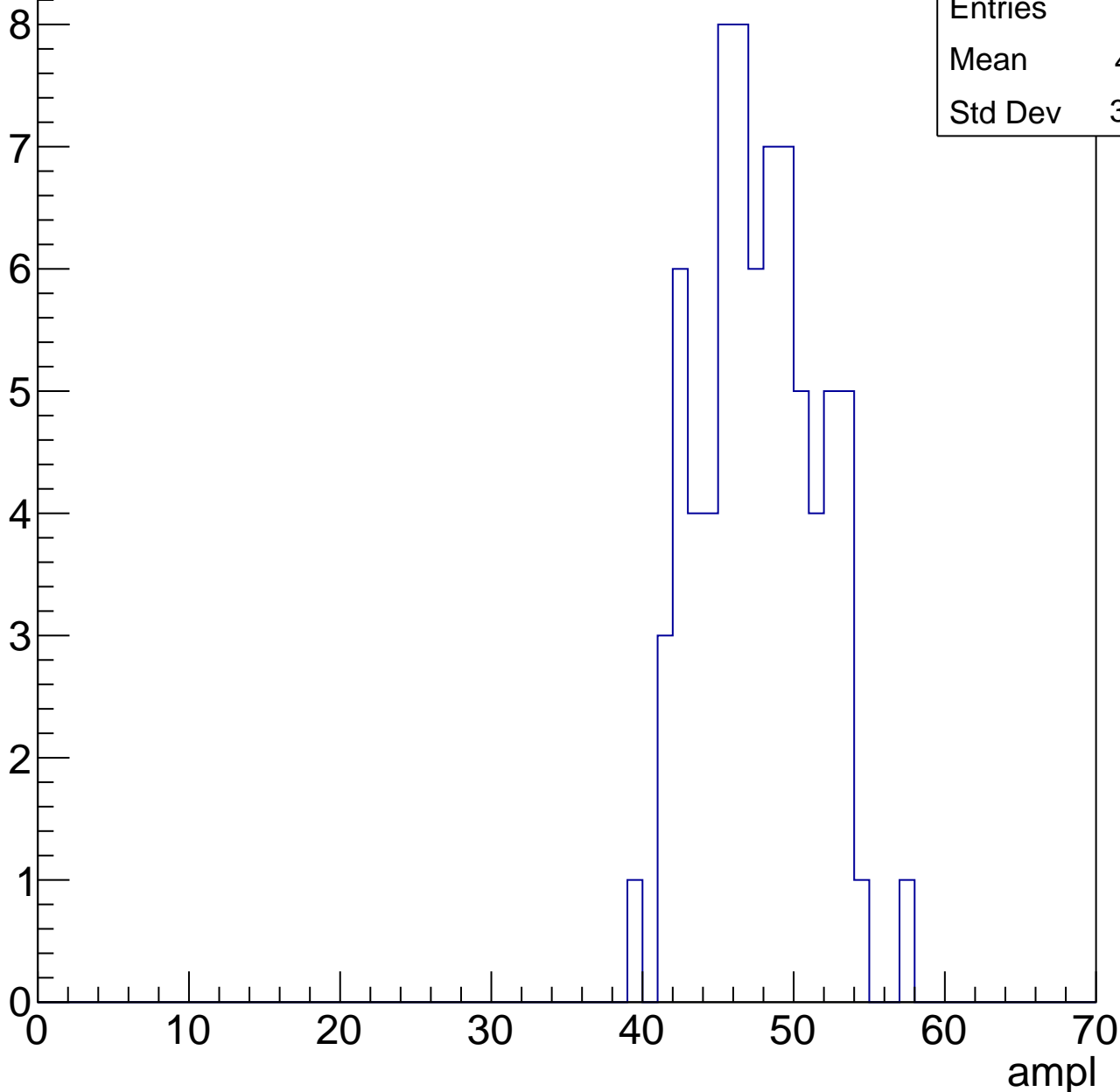


B1L101S, U17-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	47.21
Std Dev	3.753

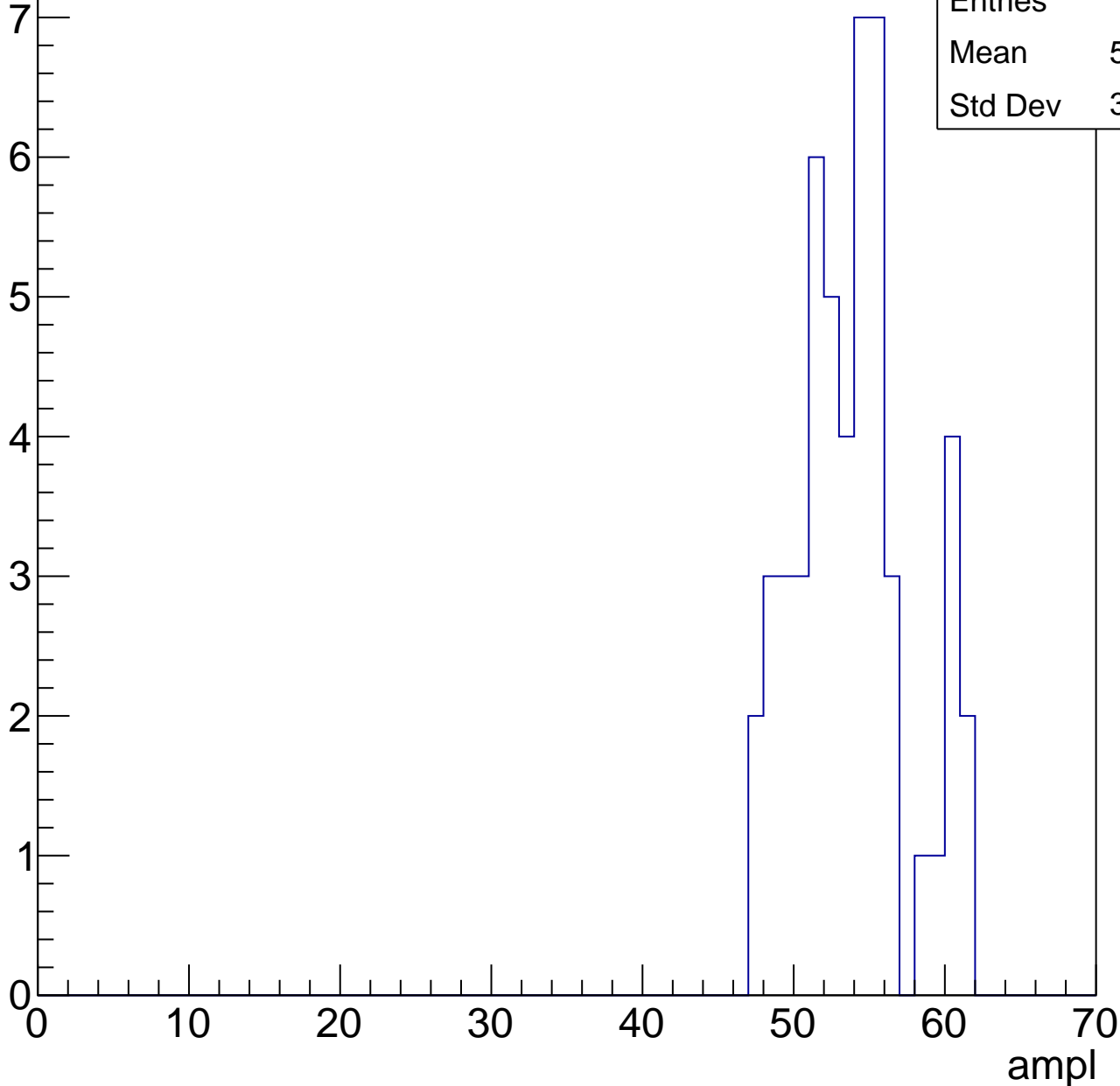


B1L101S, U17-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

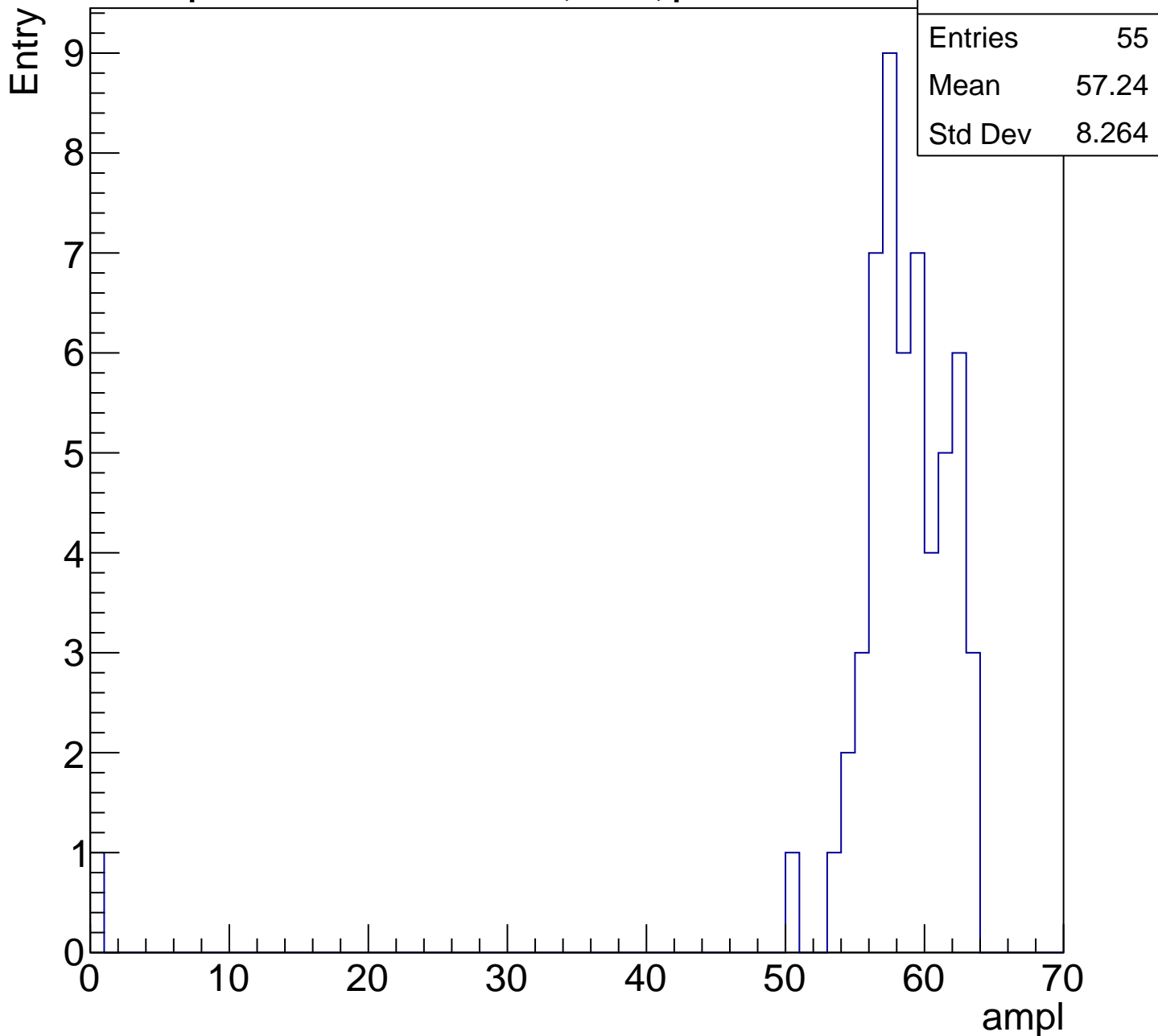
Entry

Entries	51
Mean	53.39
Std Dev	3.679



B1L101S, U17-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

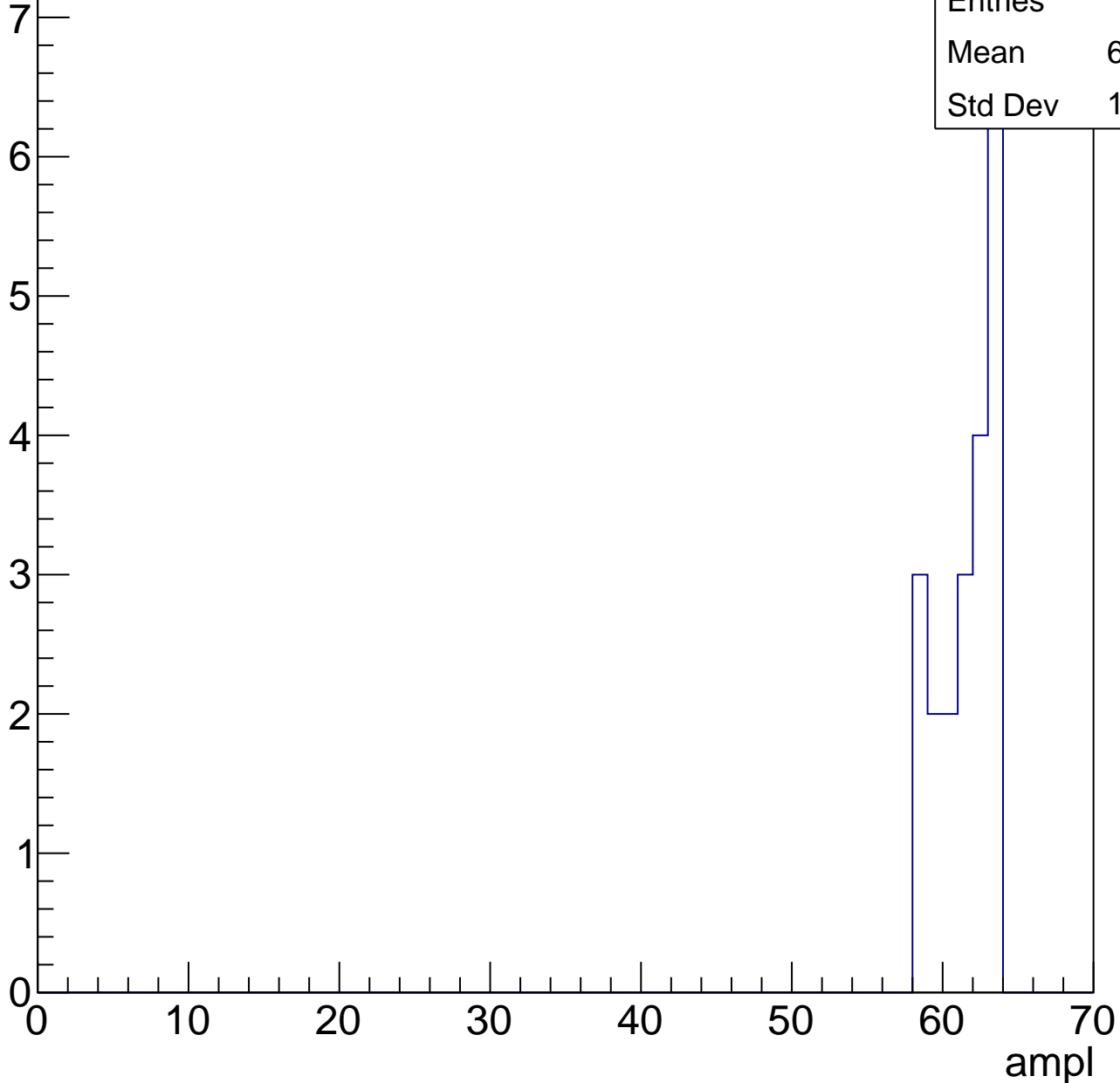


B1L101S, U17-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	61.14
Std Dev	1.807



B1L101S, U17-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch8, adc0

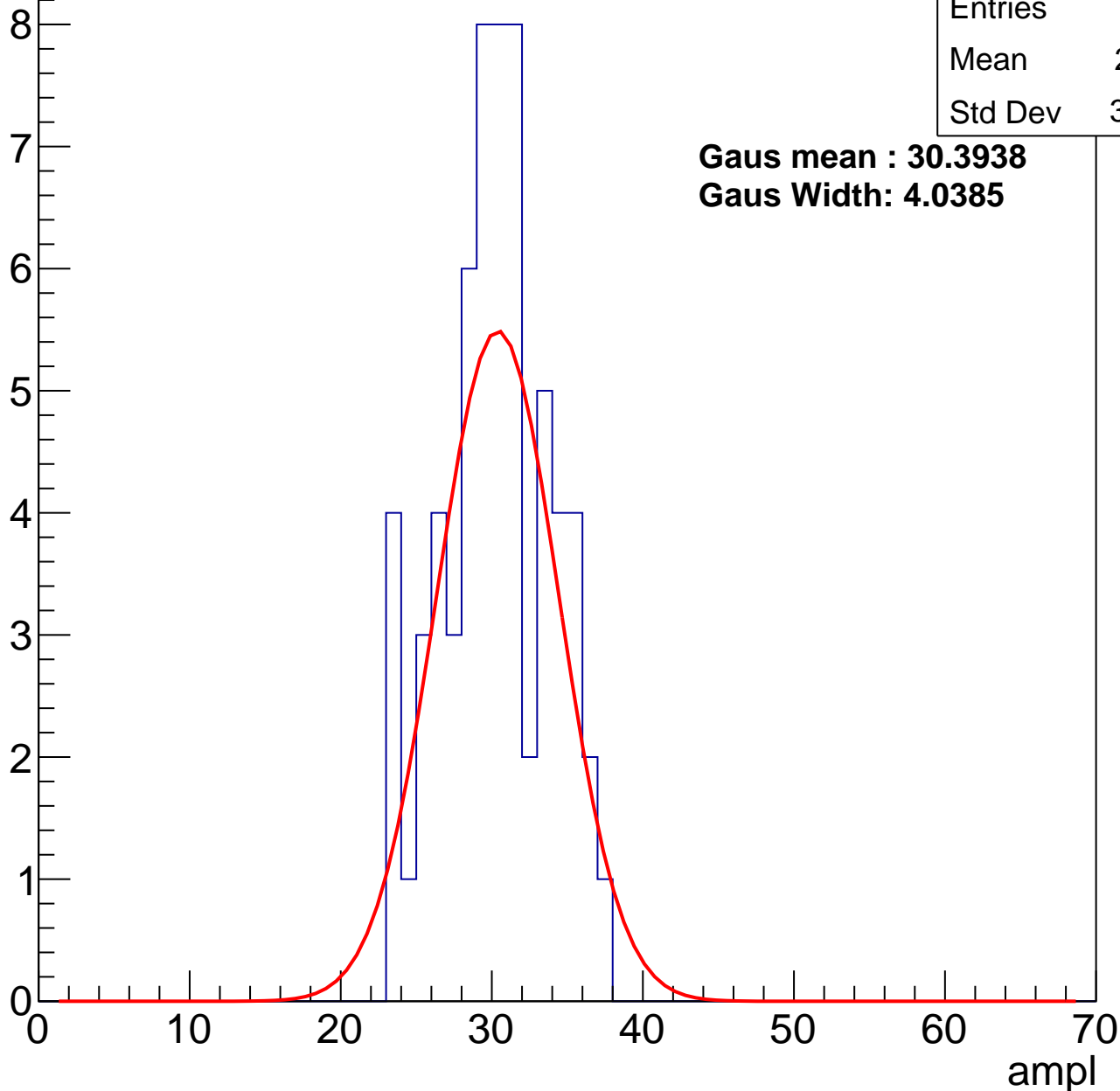
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.81
Std Dev	3.504

Gaus mean : 30.3938

Gaus Width: 4.0385



B1L101S, U17-ch8, adc1

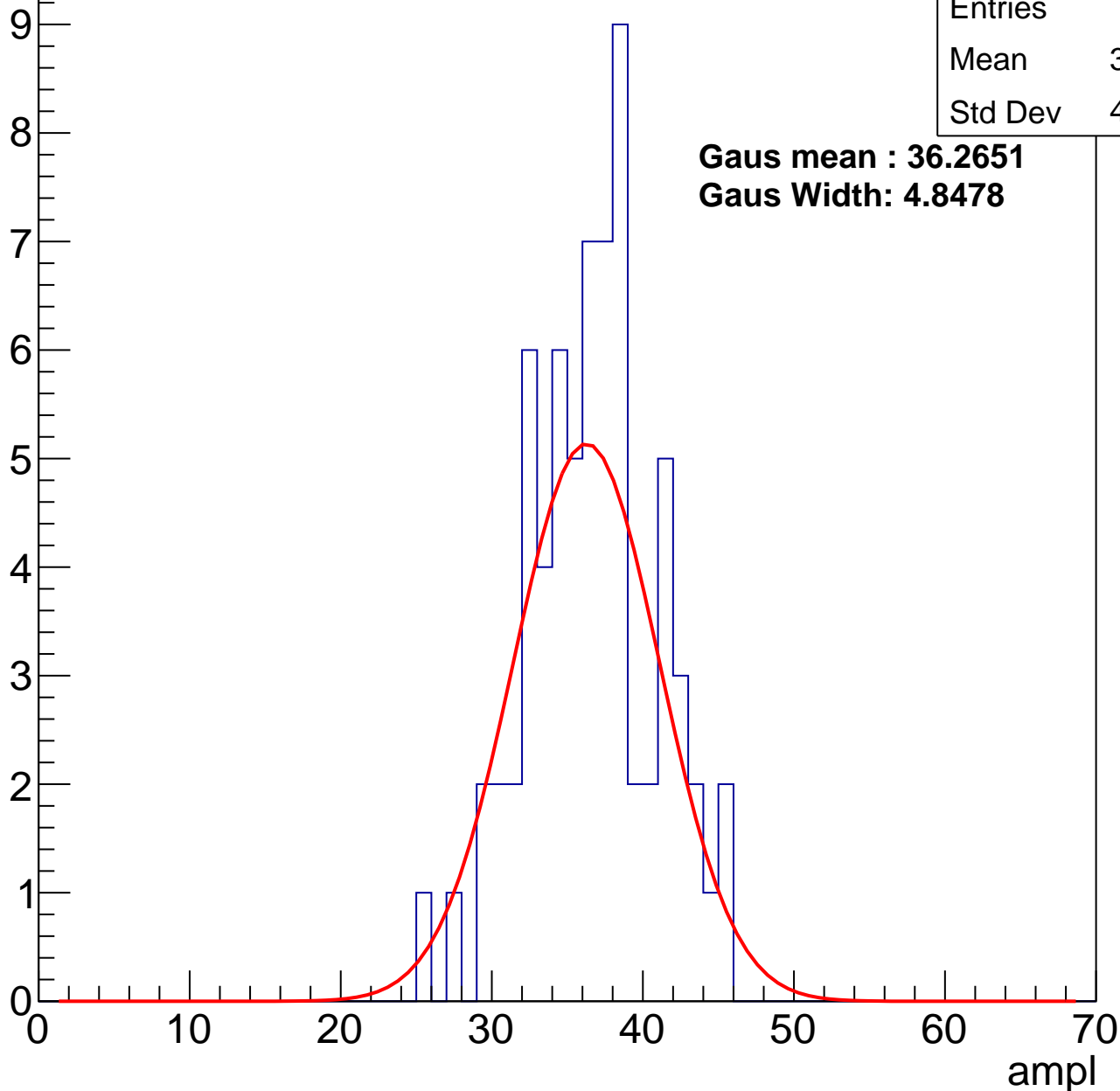
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.19
Std Dev	4.223

Gaus mean : 36.2651

Gaus Width: 4.8478



B1L101S, U17-ch8, adc2

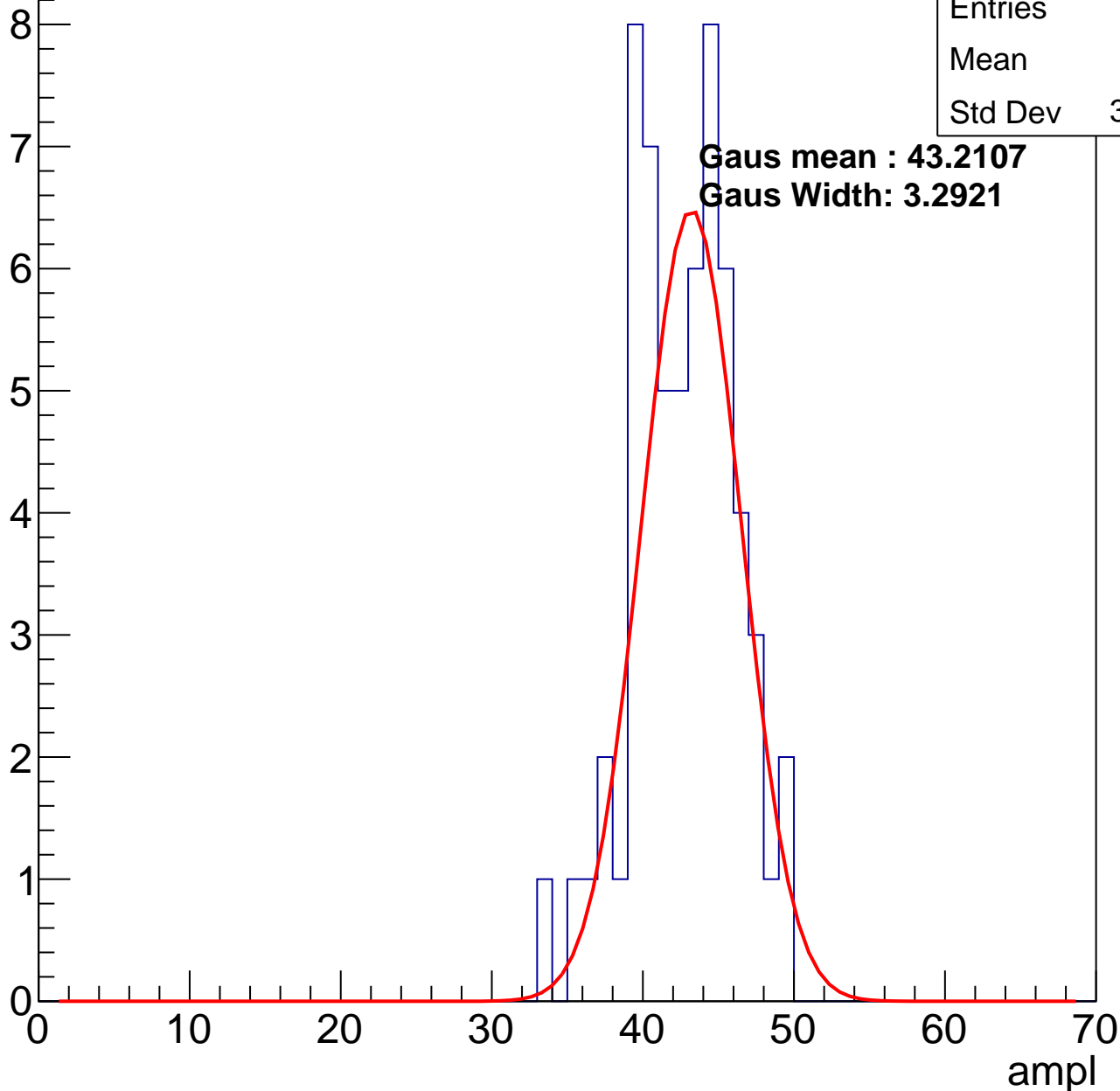
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.2
Std Dev	3.387

Gaus mean : 43.2107

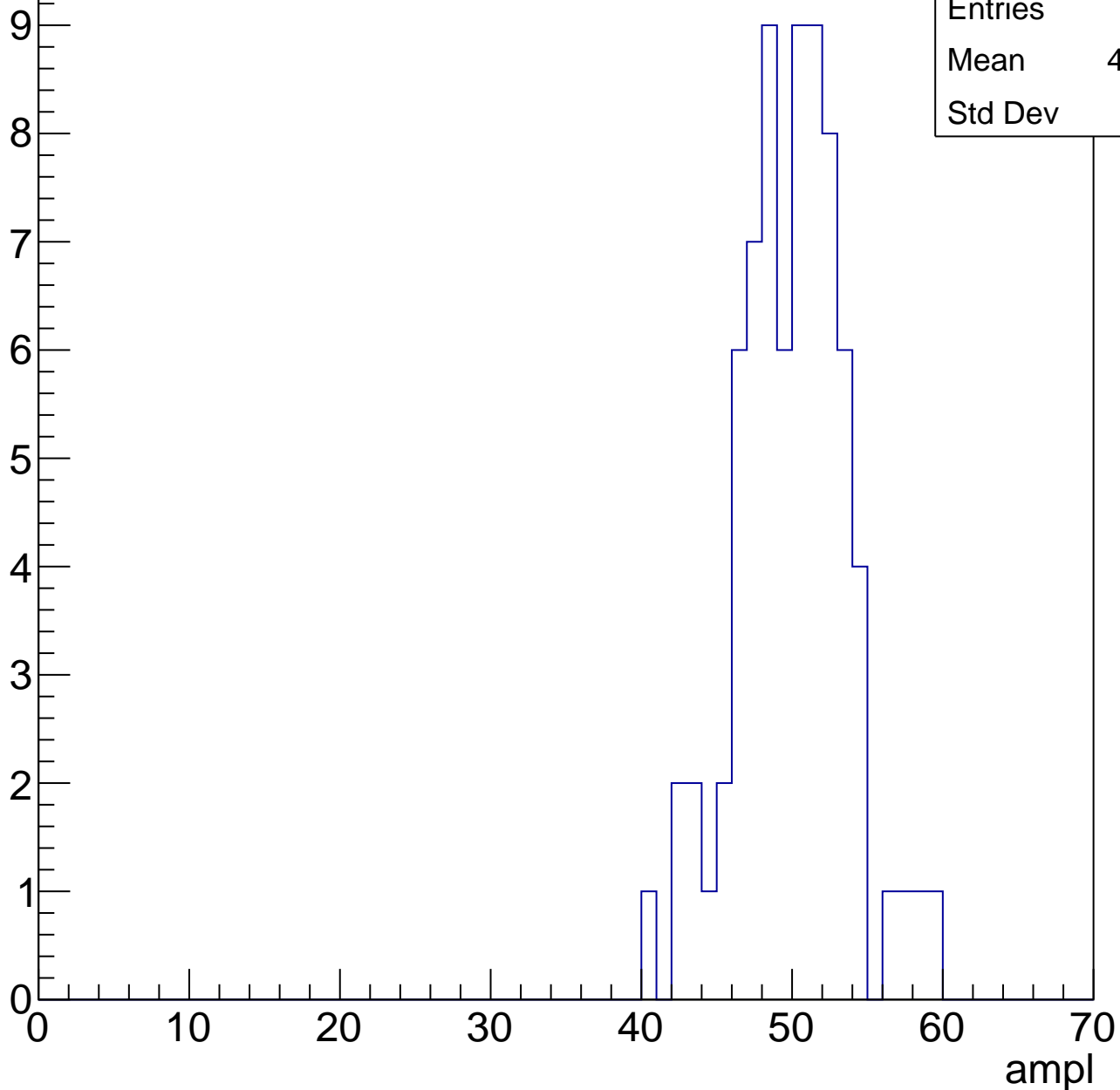
Gaus Width: 3.2921



B1L101S, U17-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

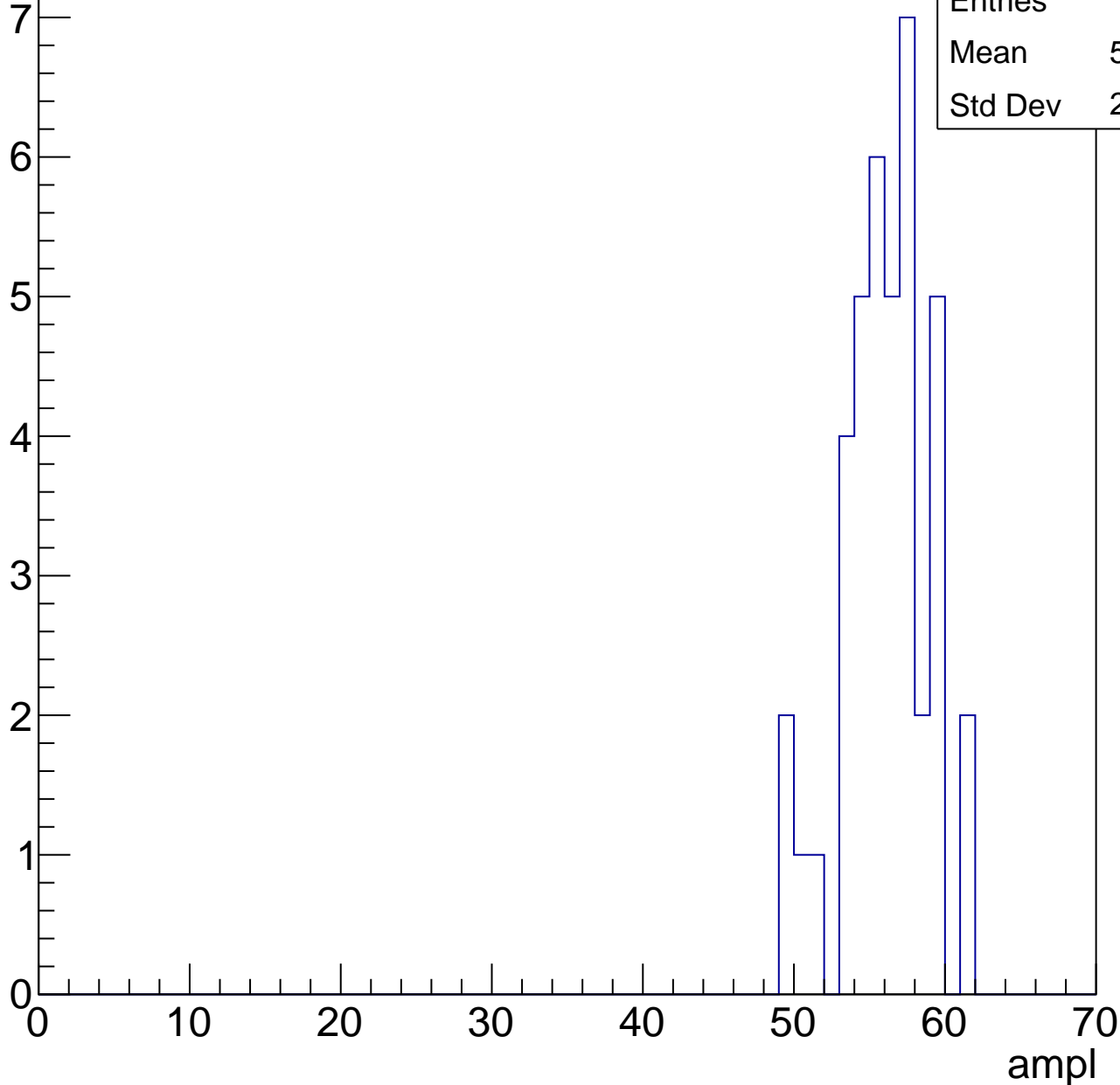
Entry



B1L101S, U17-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

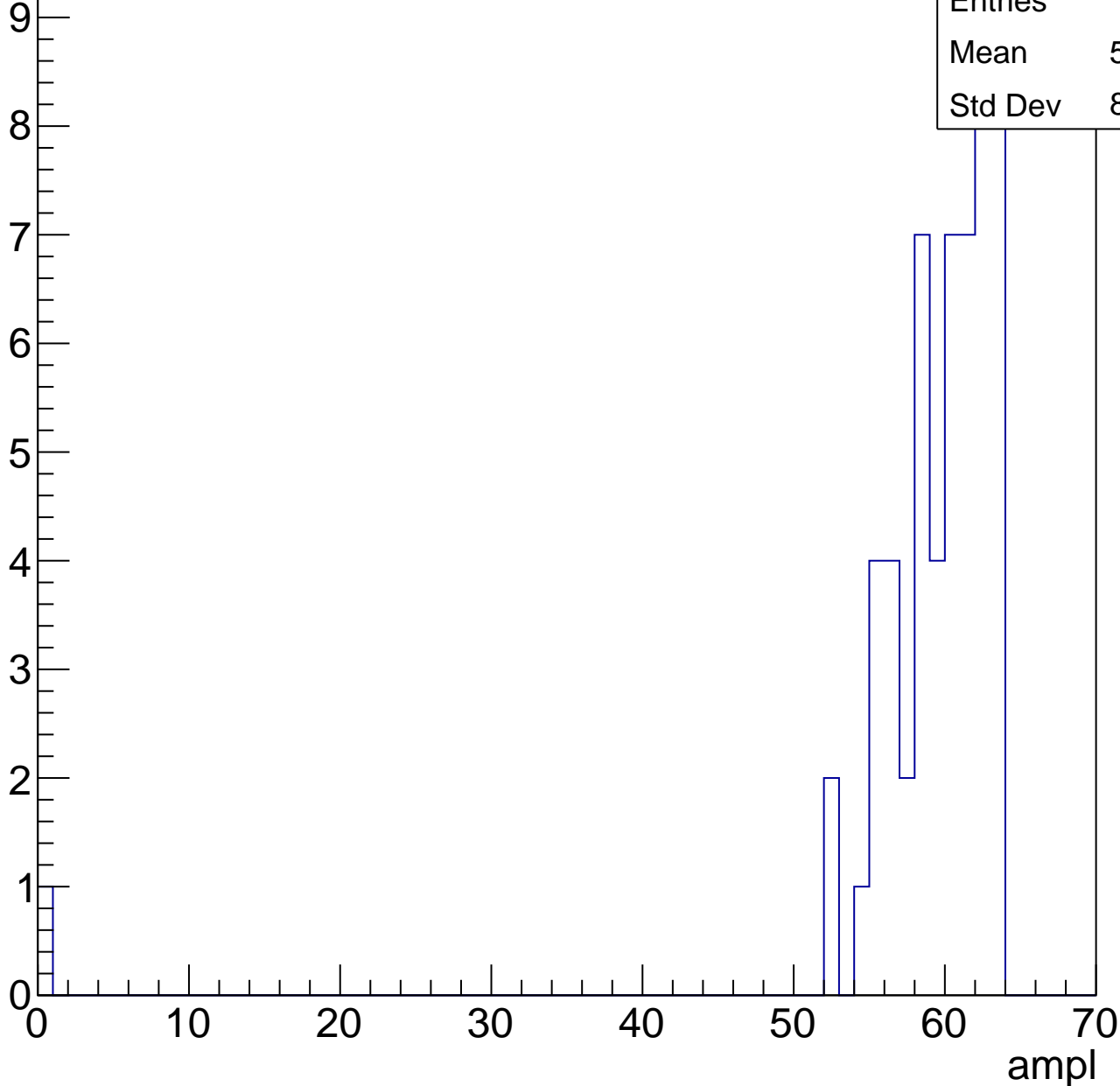


B1L101S, U17-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	58.34
Std Dev	8.386



B1L101S, U17-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

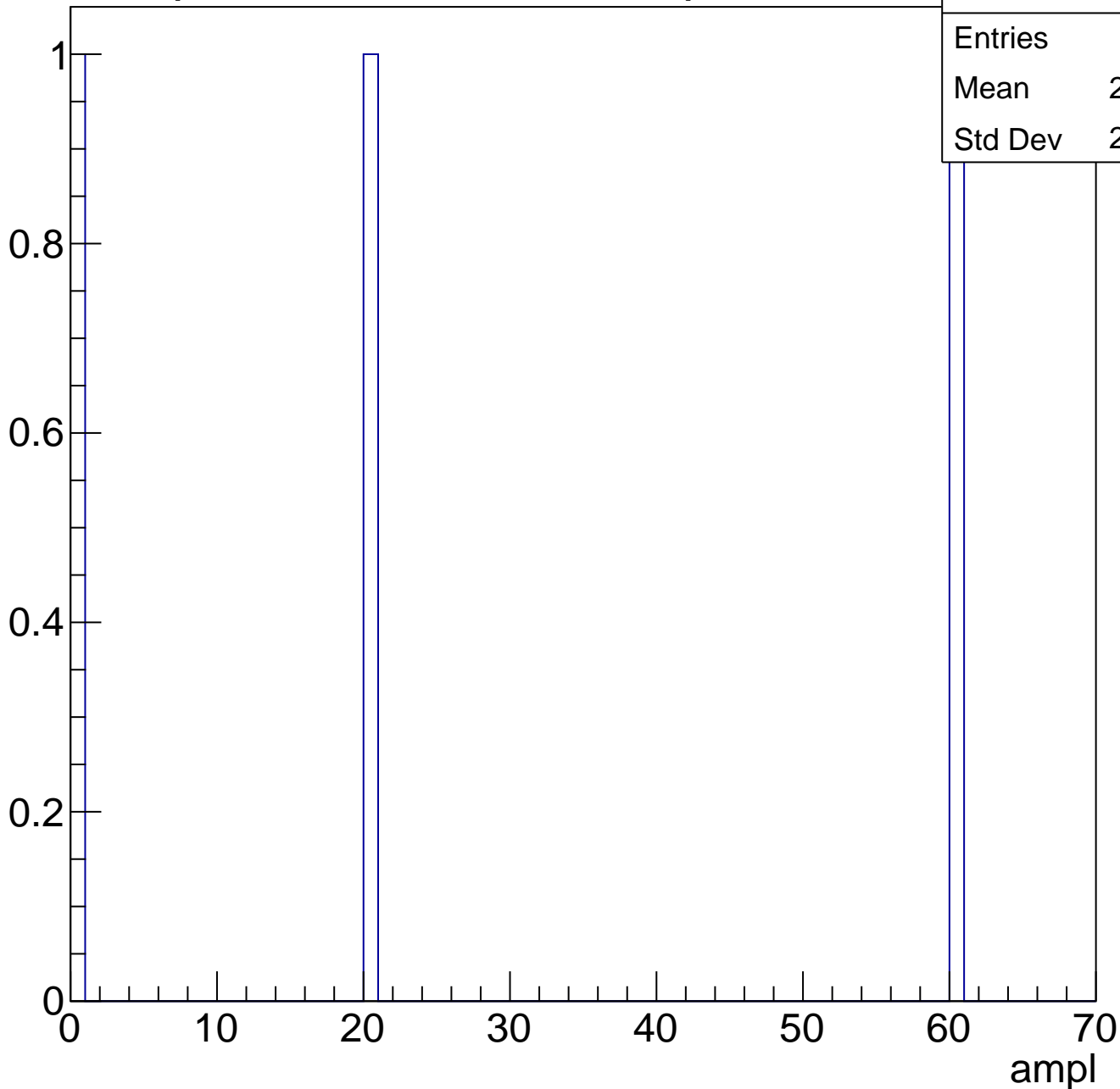
ampl

Entries	6
Mean	61.5
Std Dev	1.258

B1L101S, U17-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch9, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	29.72
Std Dev	3.851

Gaus mean : 30.4199

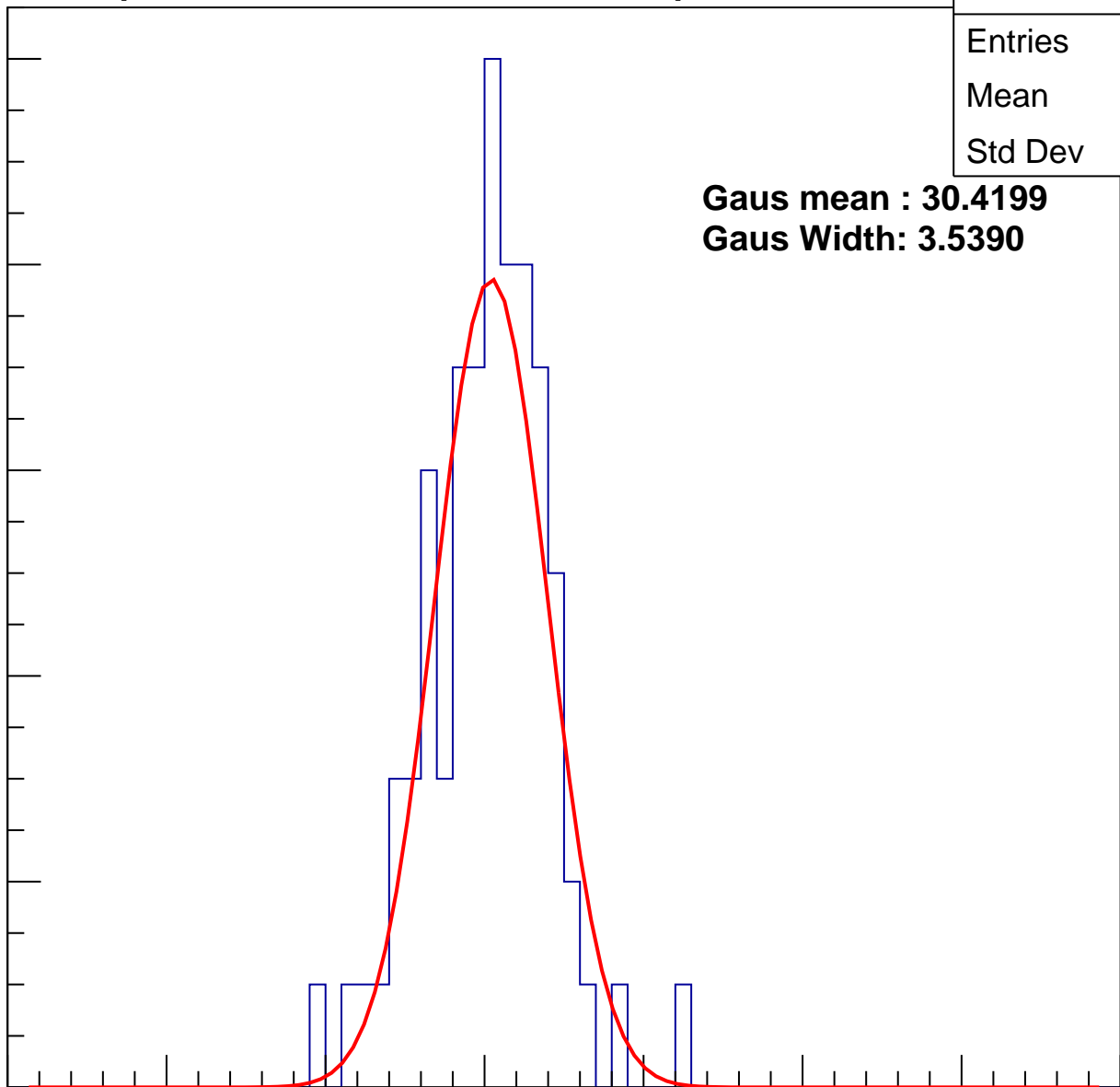
Gaus Width: 3.5390

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch9, adc1

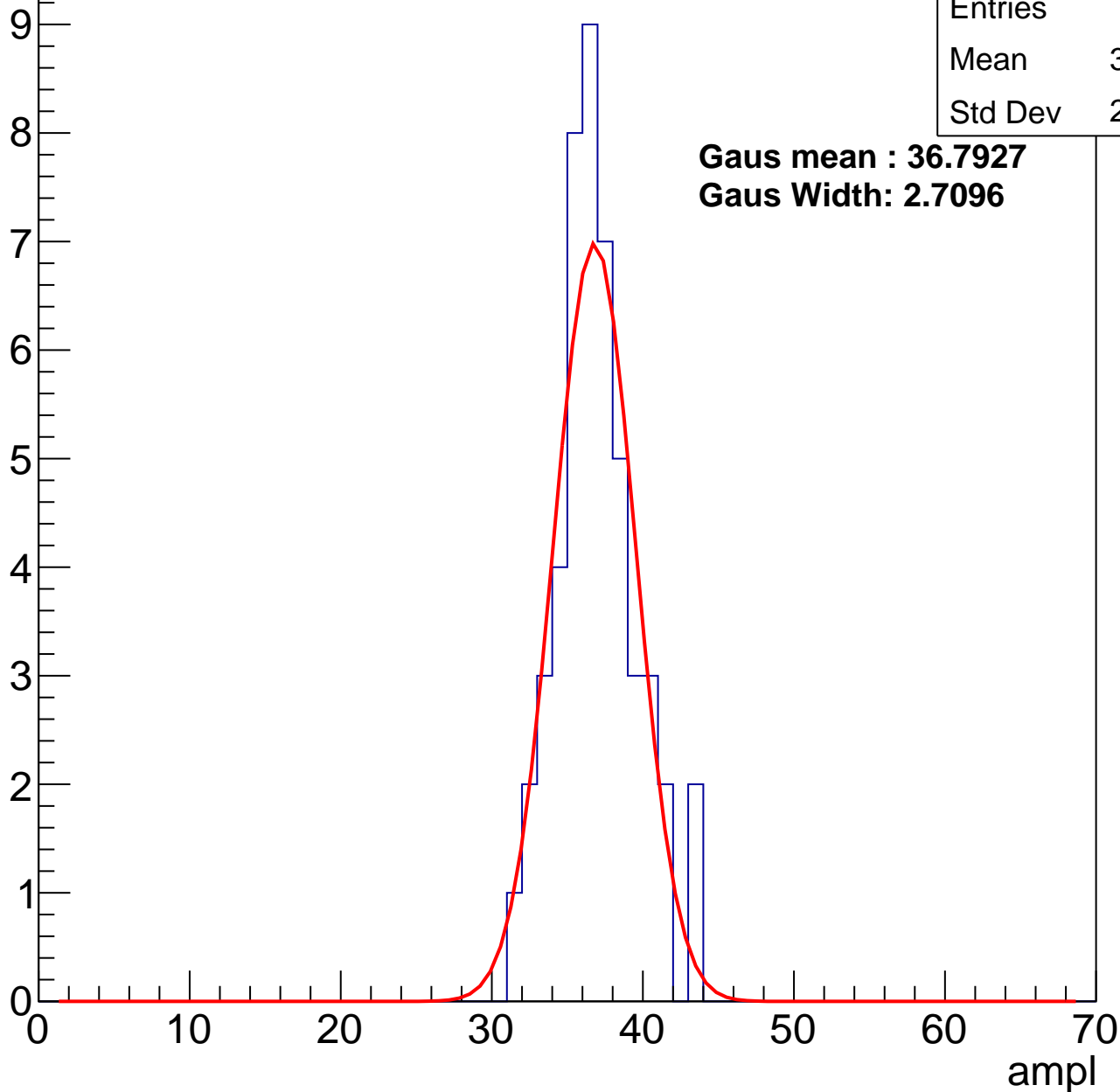
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	36.49
Std Dev	2.658

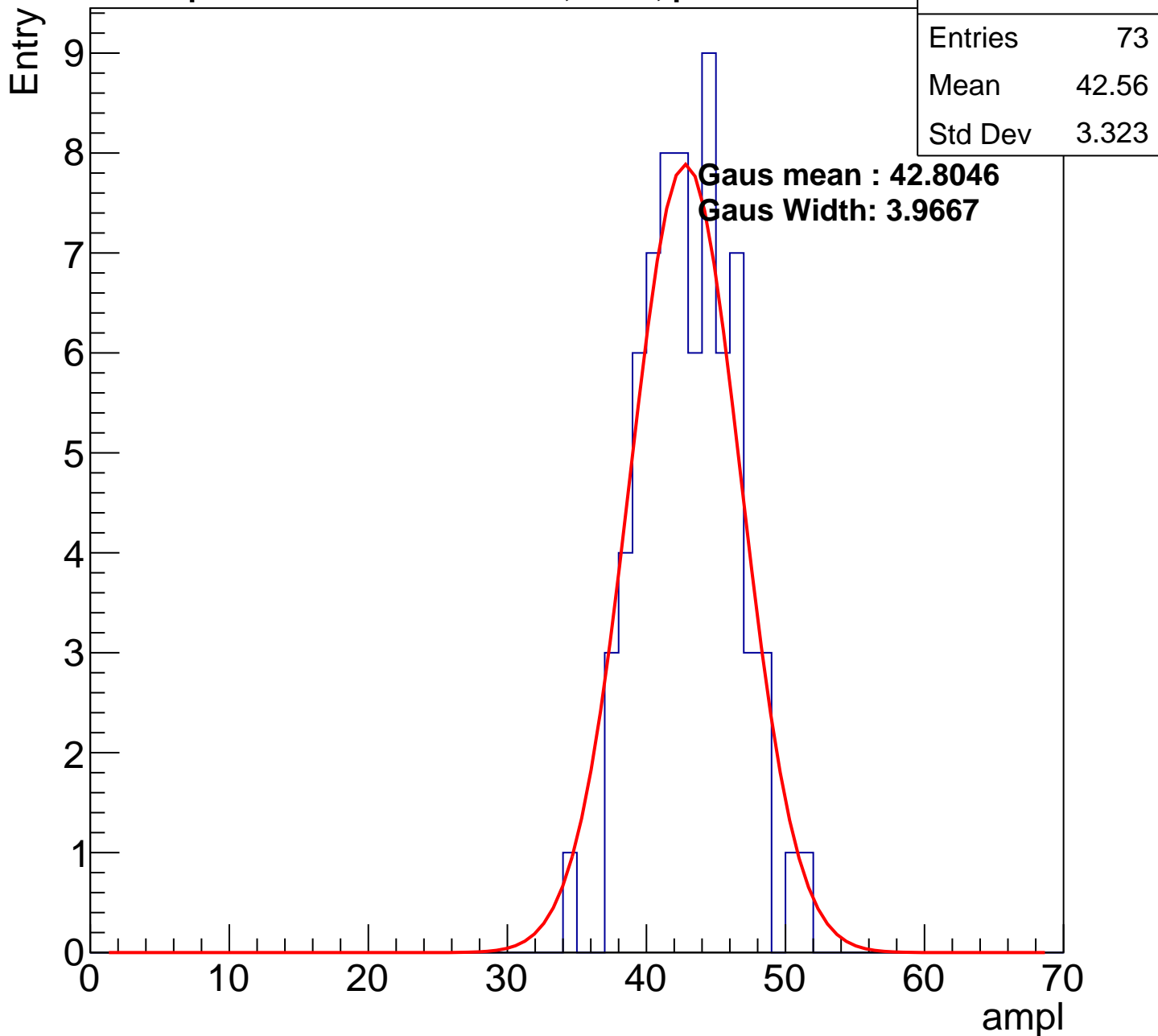
Gaus mean : 36.7927

Gaus Width: 2.7096



B1L101S, U17-ch9, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

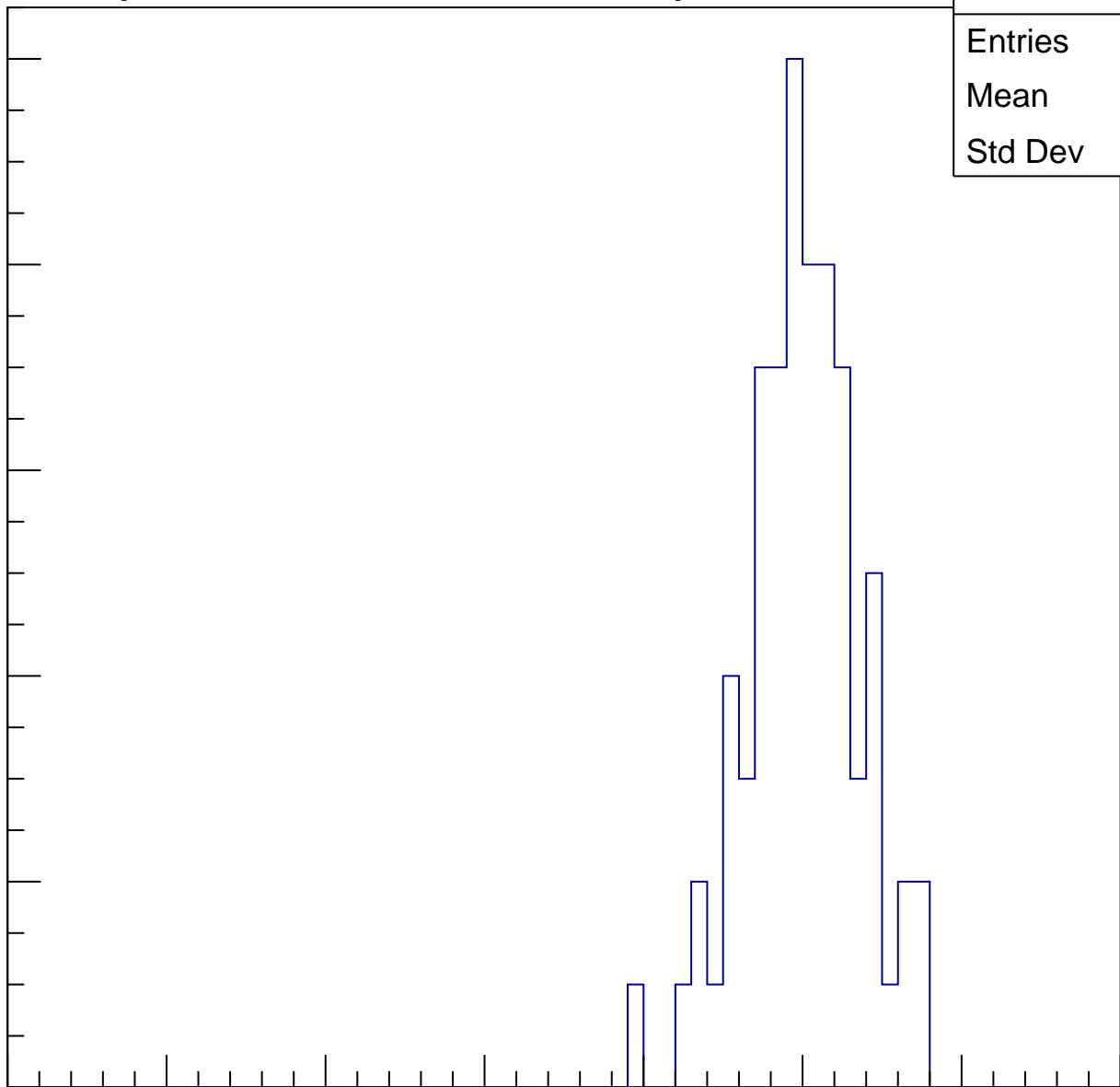
Entries	72
Mean	49.53
Std Dev	3.516

Entry

10
8
6
4
2
0

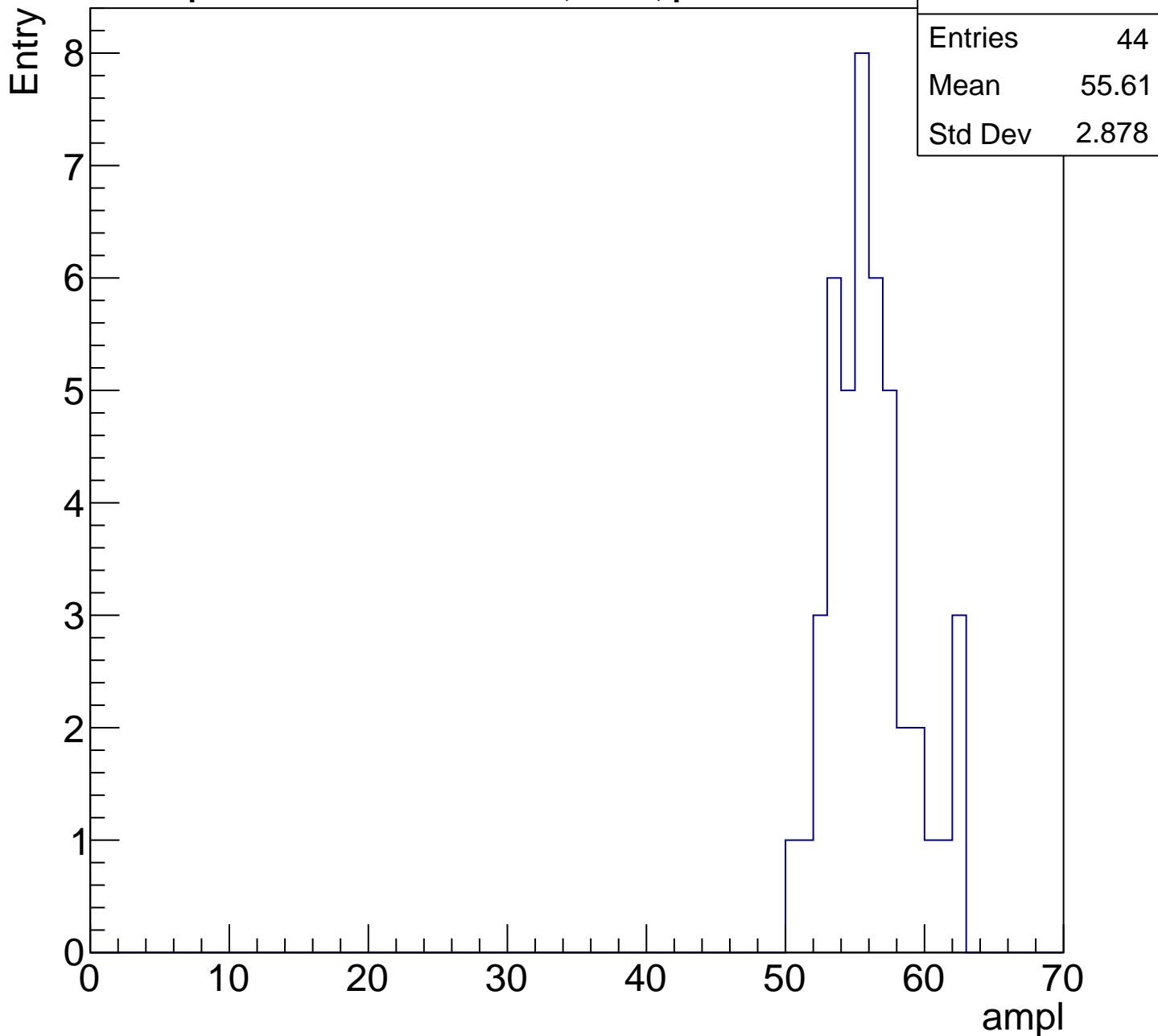
0 10 20 30 40 50 60 70

ampl



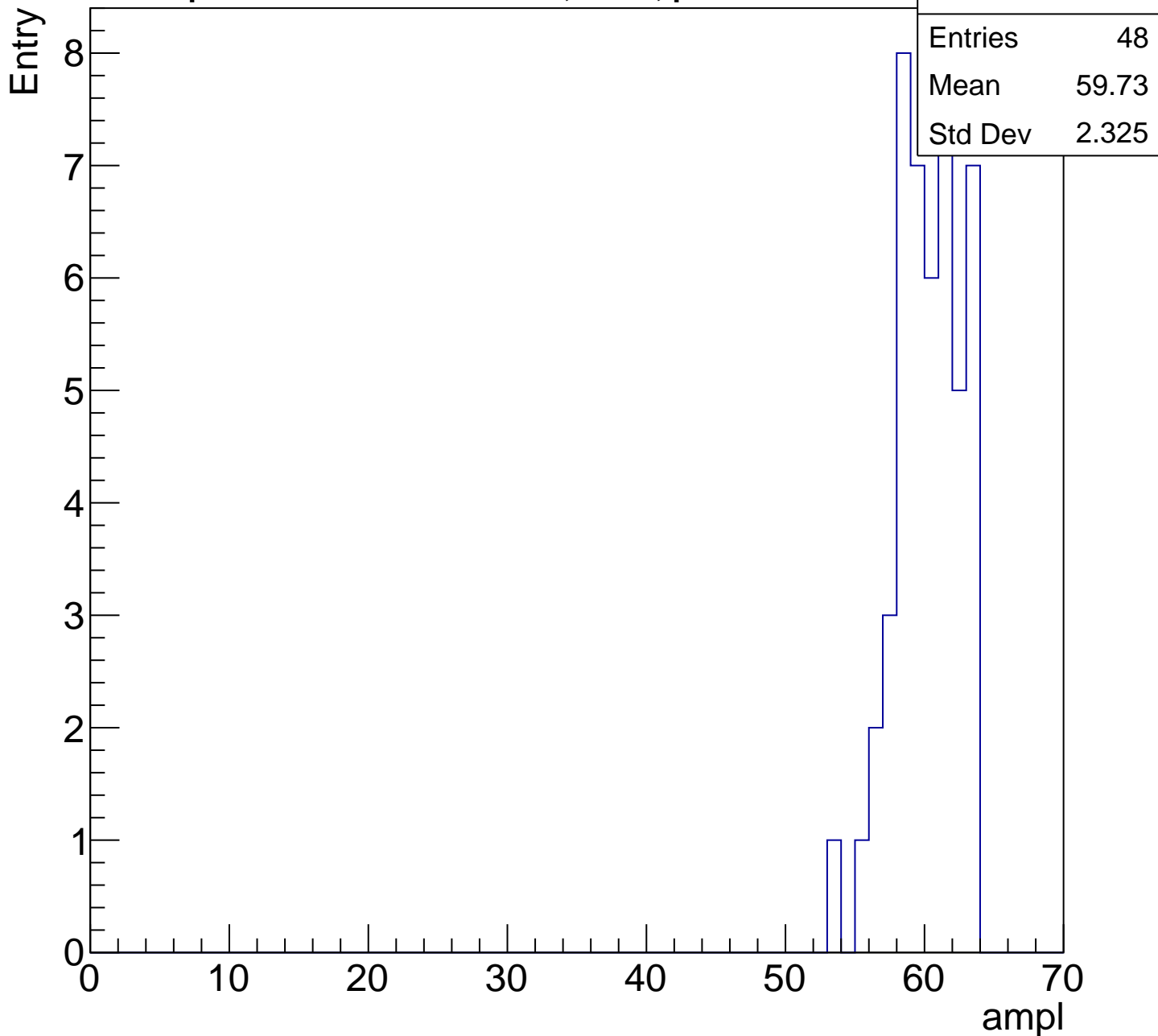
B1L101S, U17-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch9, adc5

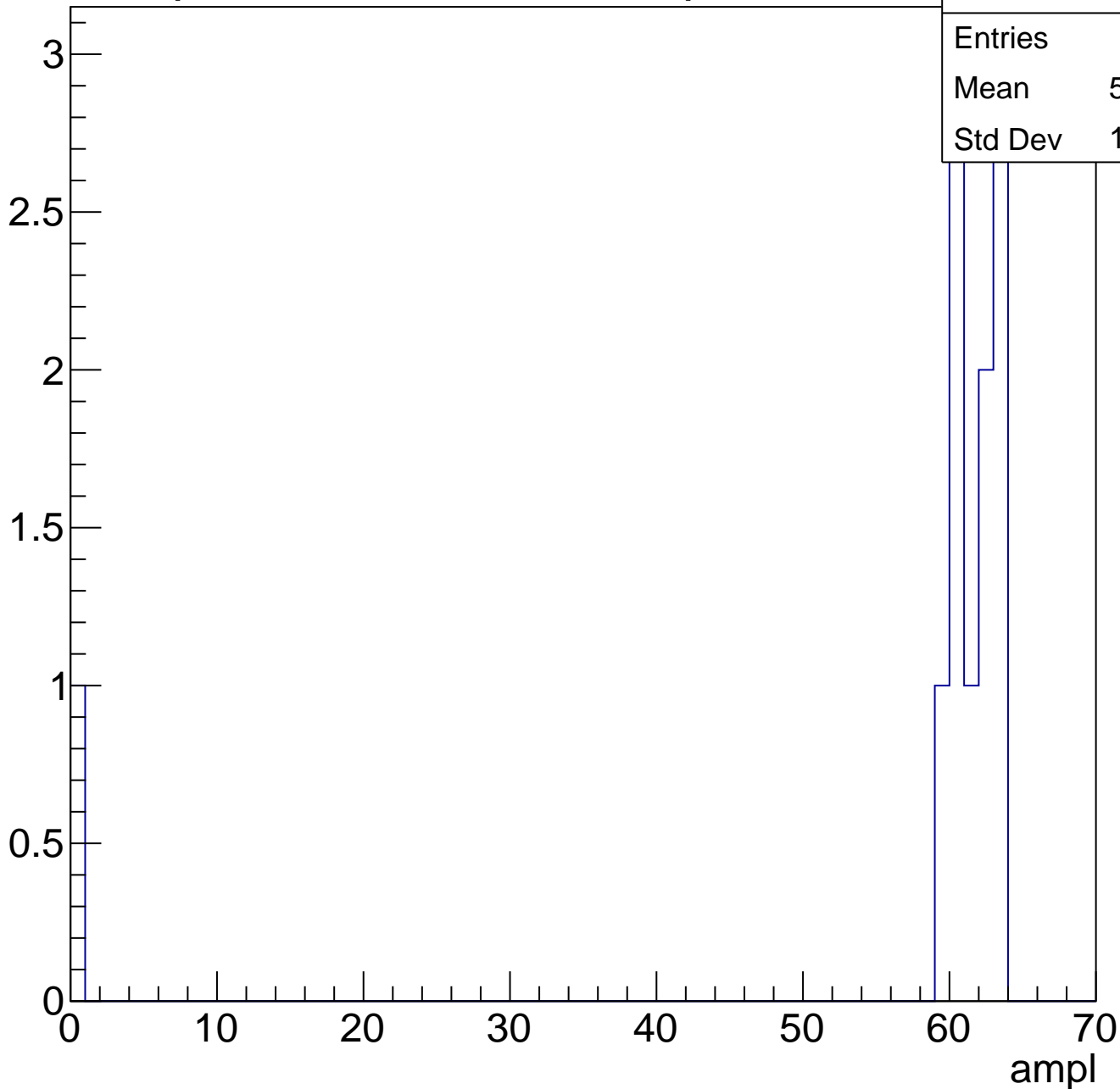
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch10, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	28.15
Std Dev	3.391

Gaus mean : 28.7709

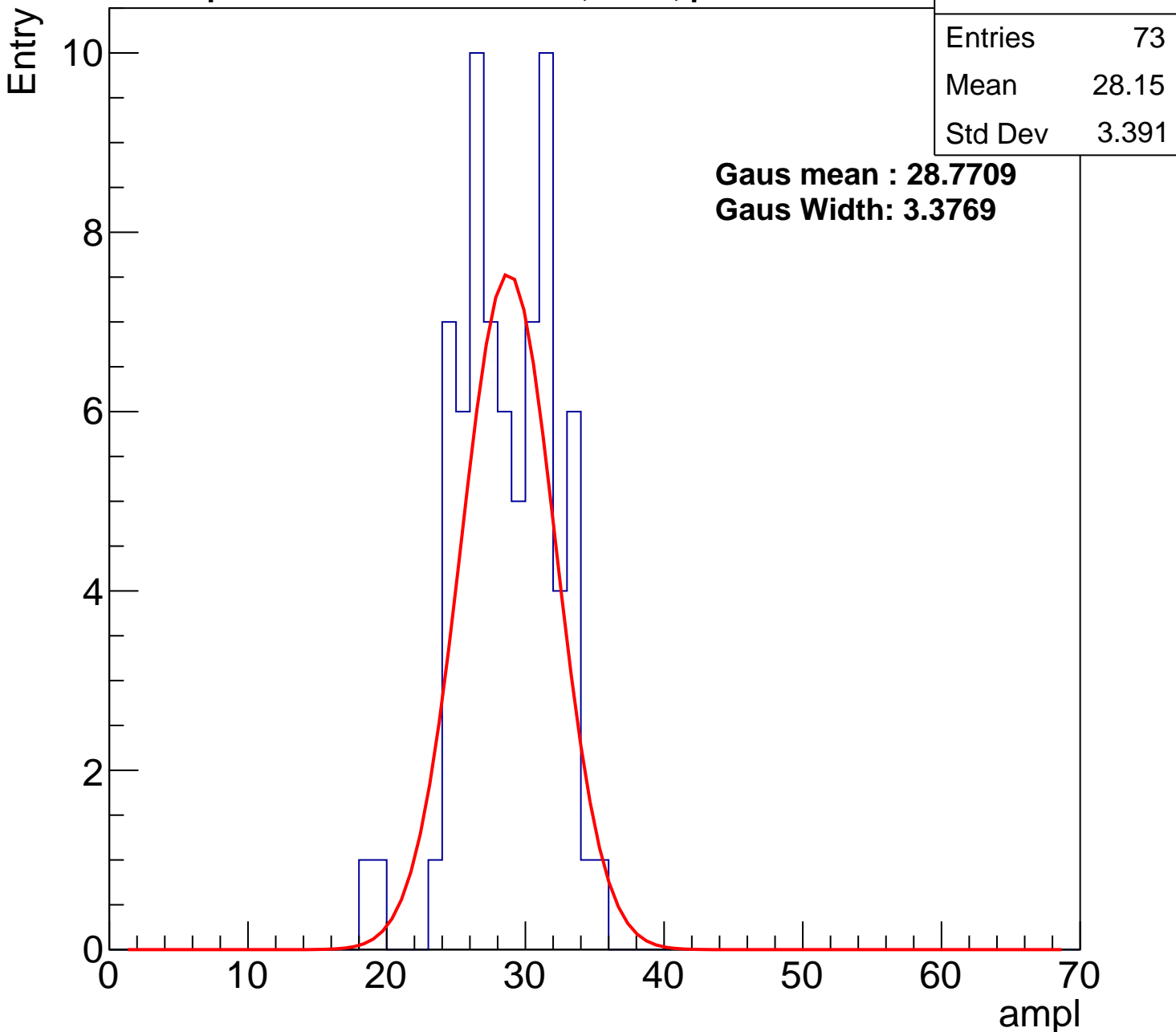
Gaus Width: 3.3769

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch10, adc1

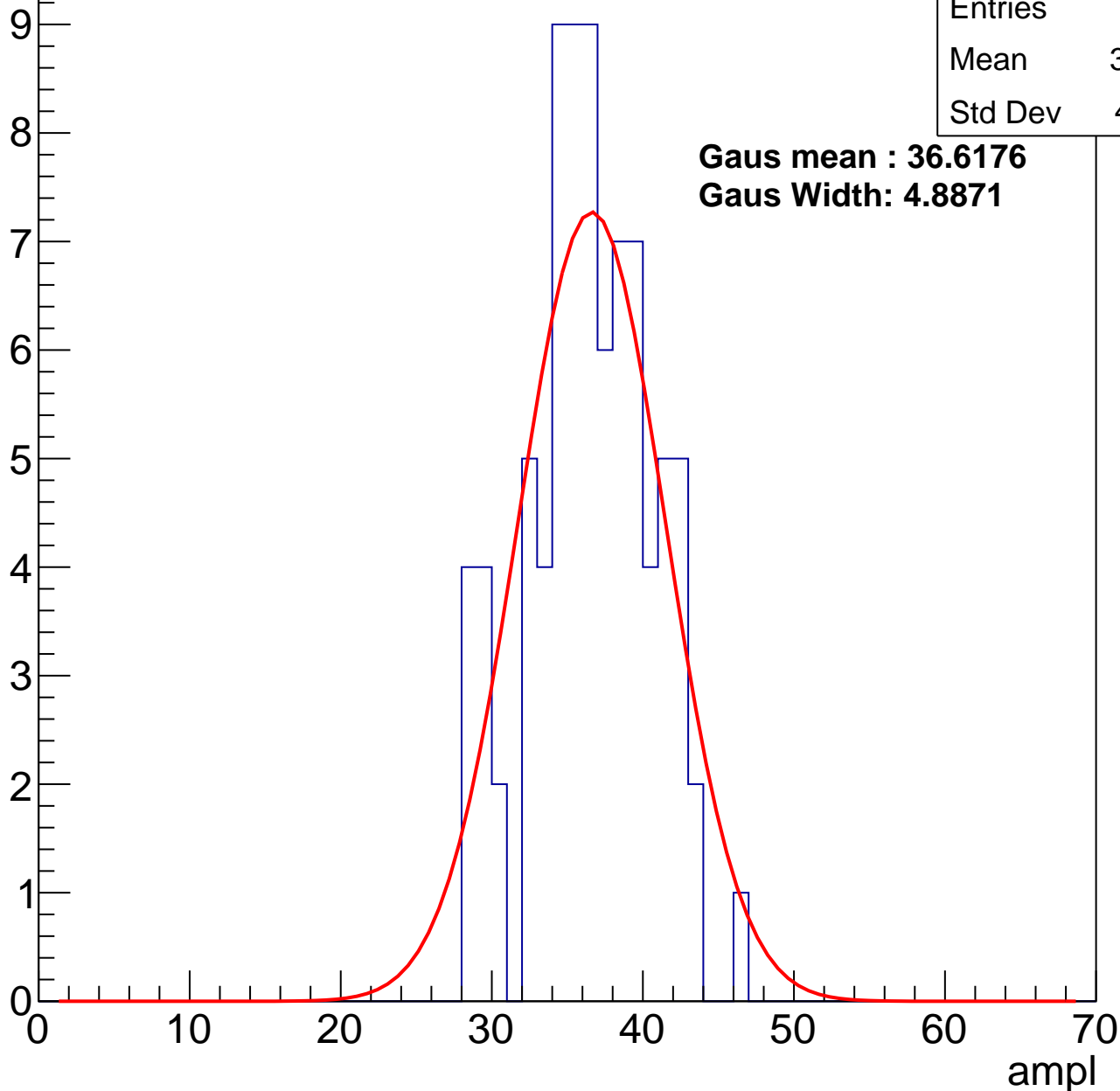
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.06
Std Dev	4.031

Gaus mean : 36.6176

Gaus Width: 4.8871



B1L101S, U17-ch10, adc2

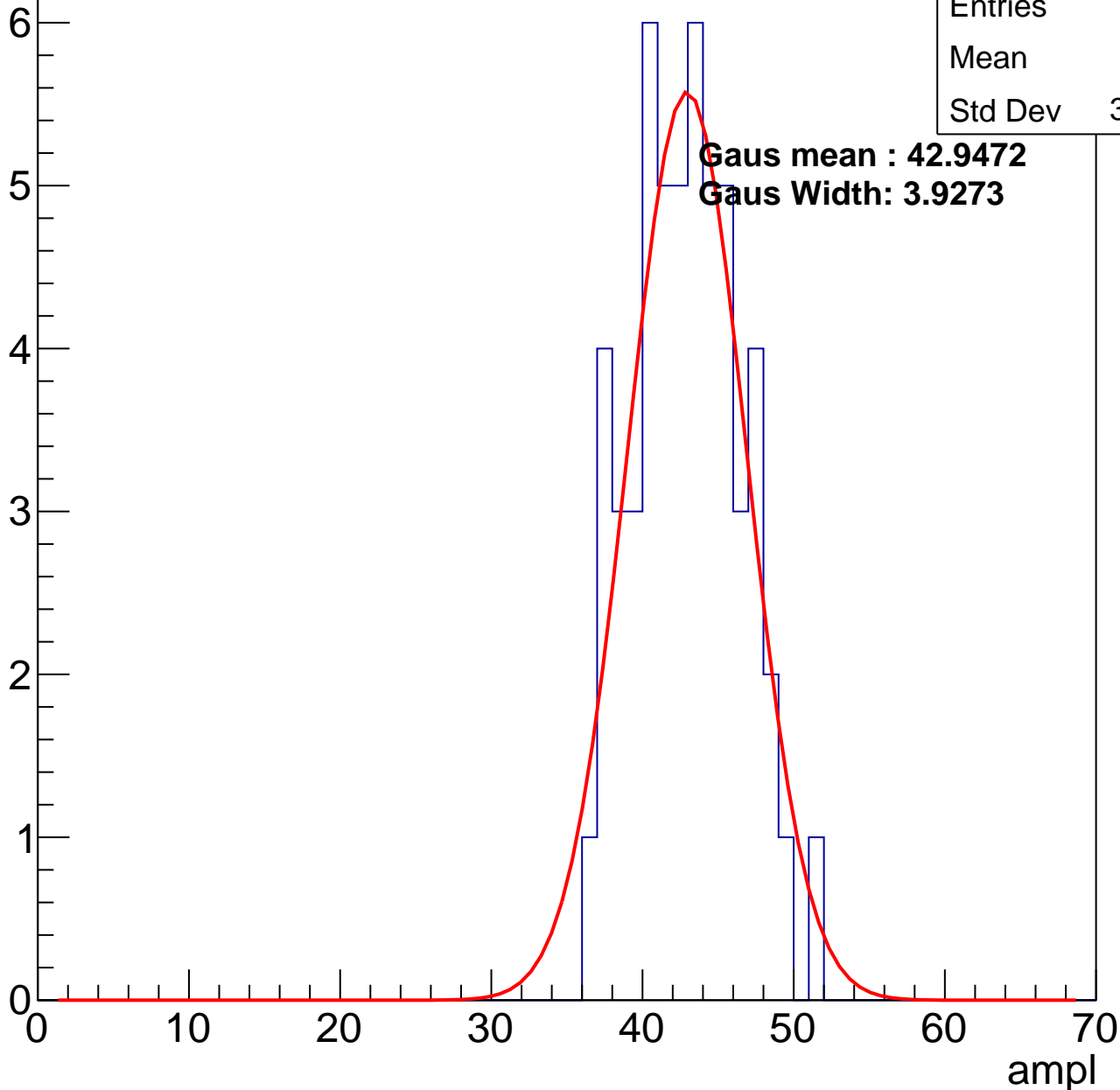
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	42.5
Std Dev	3.463

Gaus mean : 42.9472

Gaus Width: 3.9273

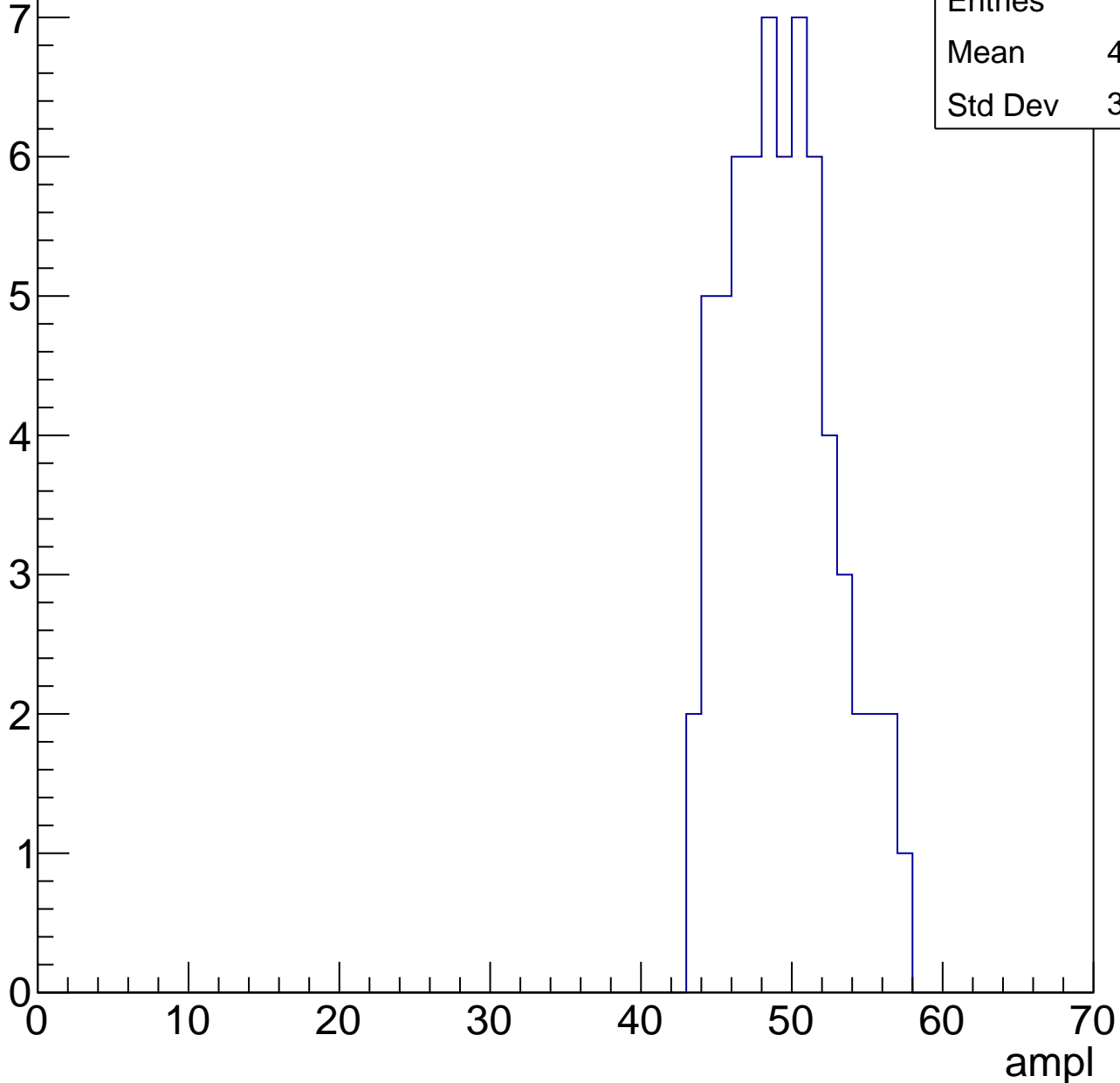


B1L101S, U17-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.89
Std Dev	3.447

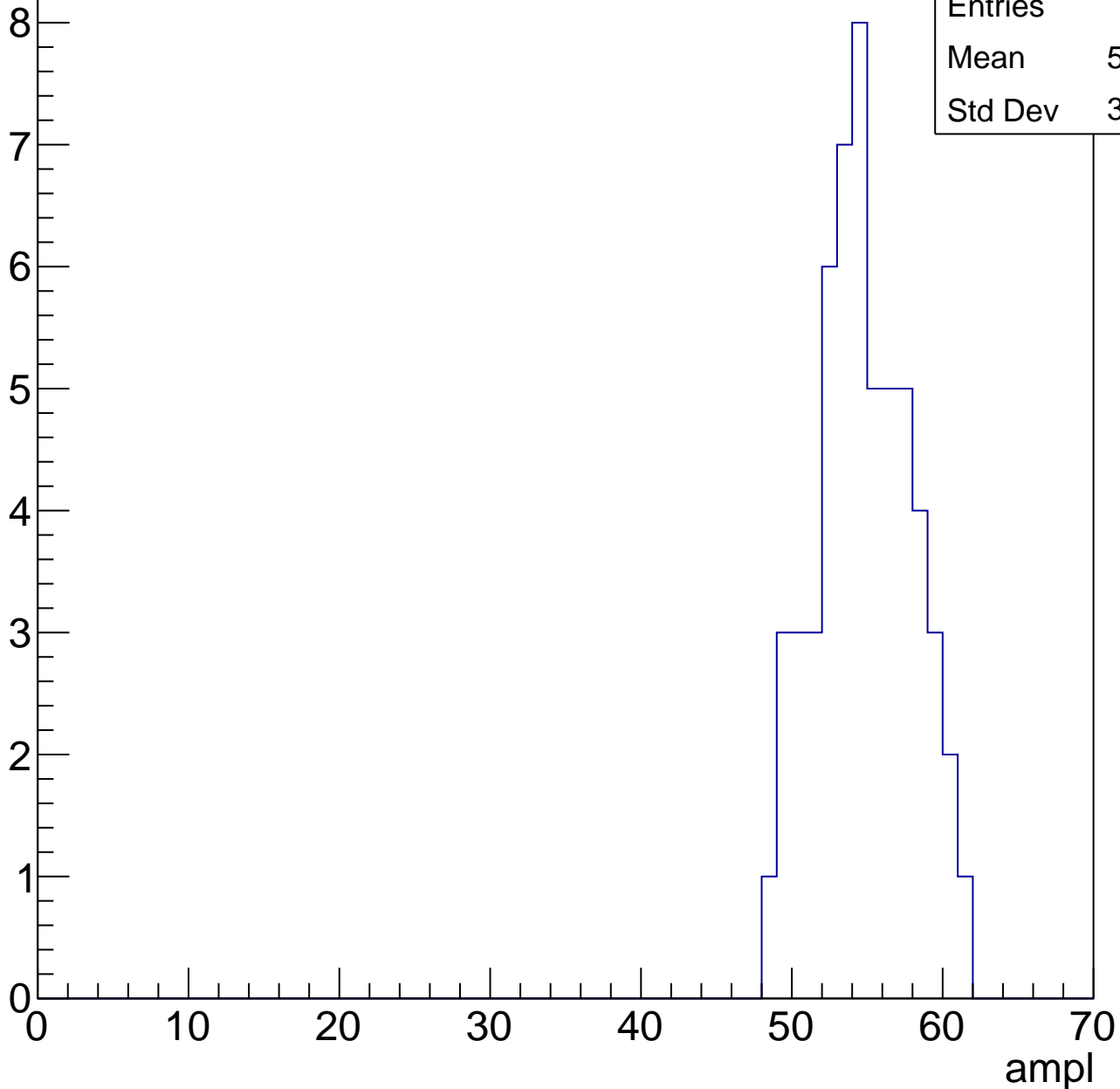


B1L101S, U17-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

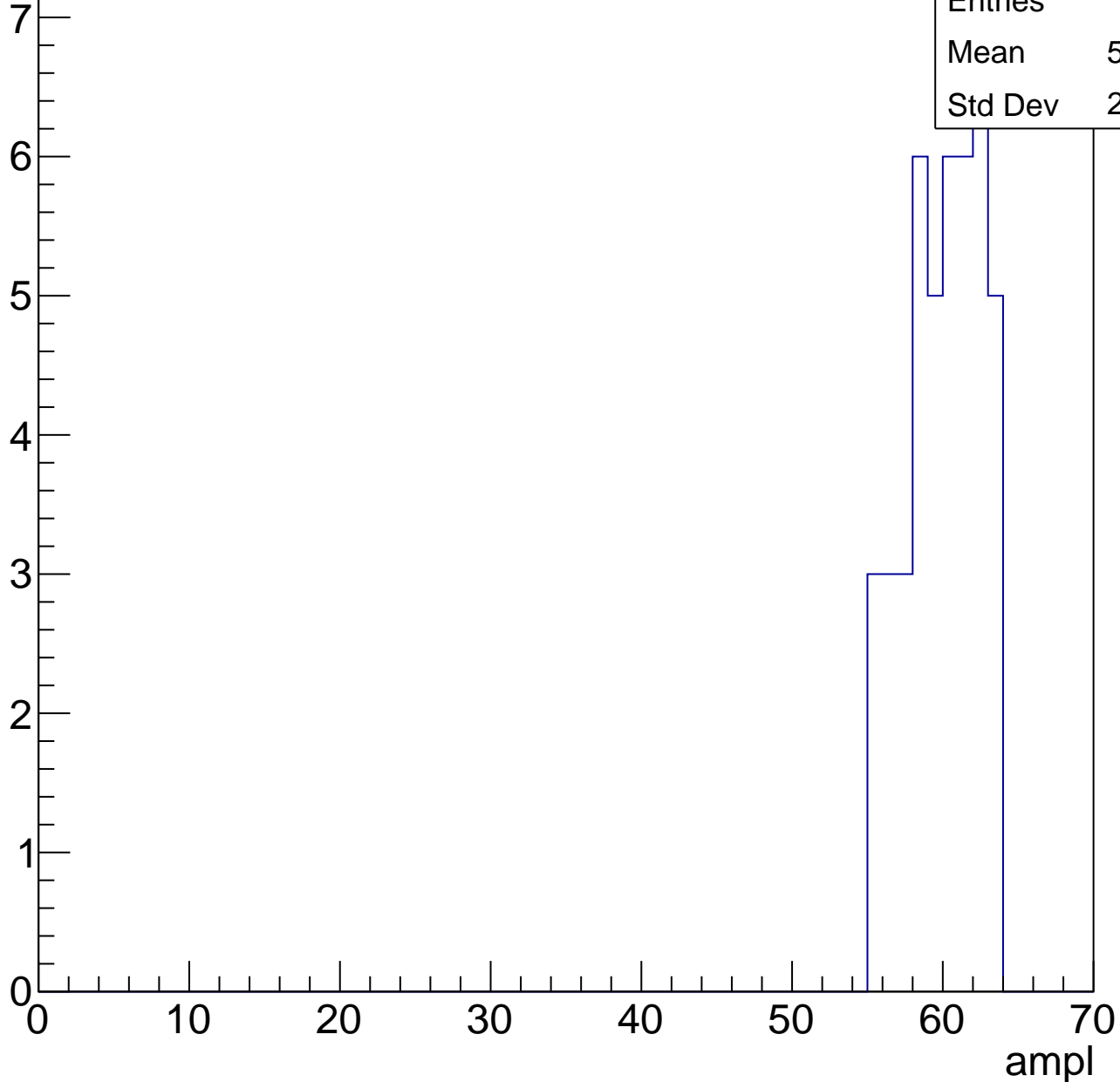
Entries	56
Mean	54.34
Std Dev	3.107



B1L101S, U17-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

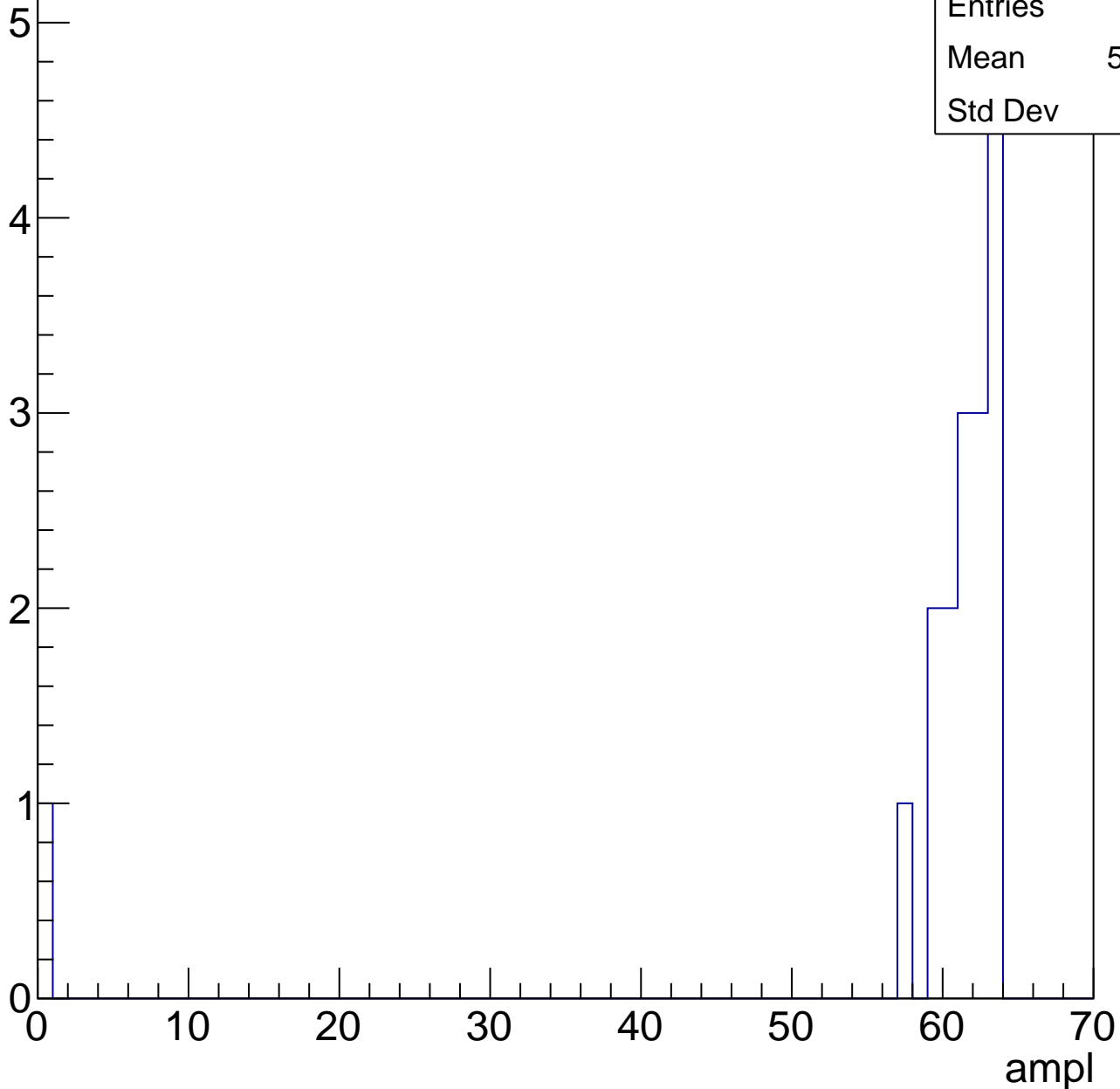


B1L101S, U17-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.59
Std Dev	14.5



B1L101S, U17-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U17-ch11, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	28.78
Std Dev	8.723

Gaus mean : 31.6645

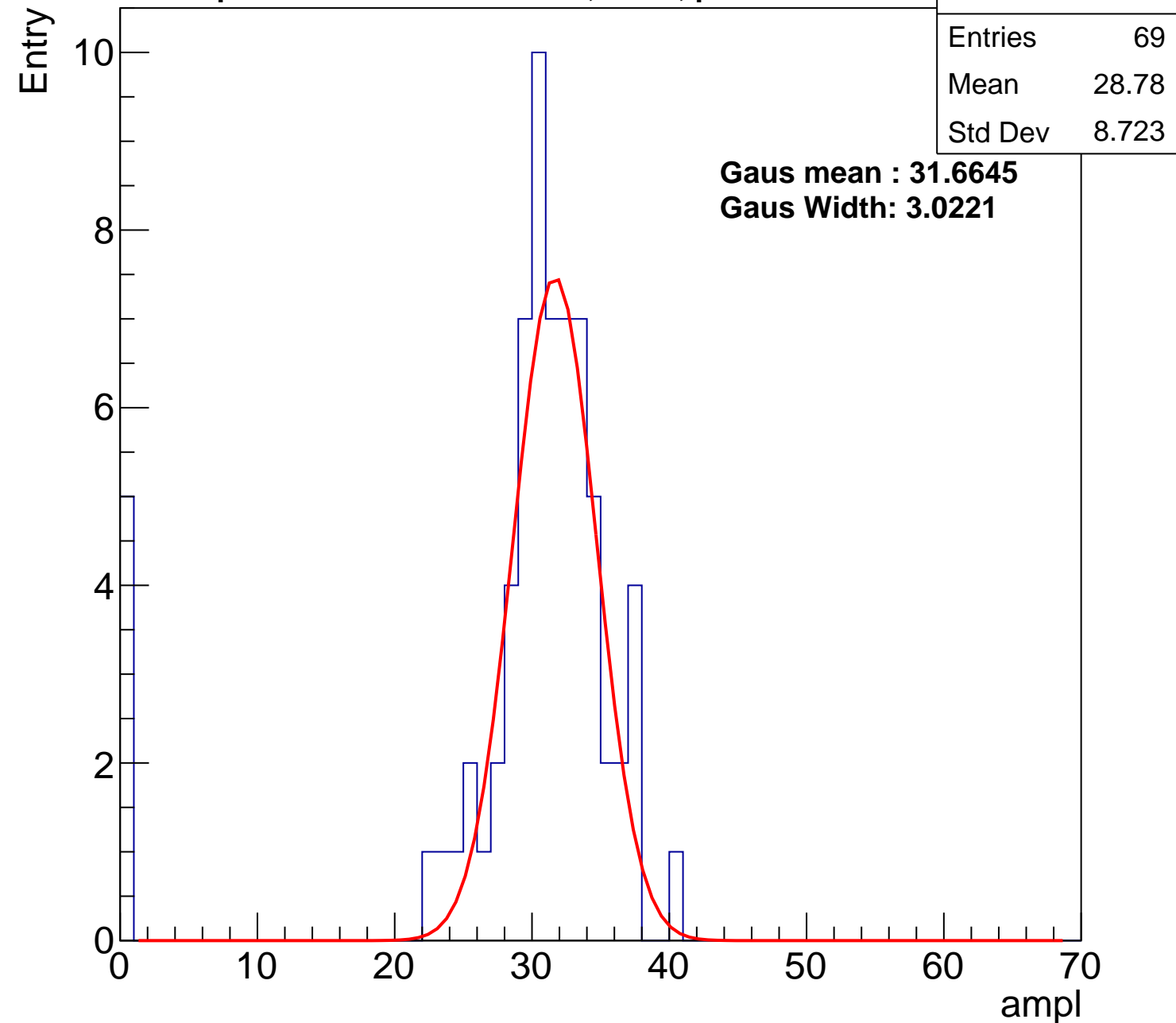
Gaus Width: 3.0221

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch11, adc1

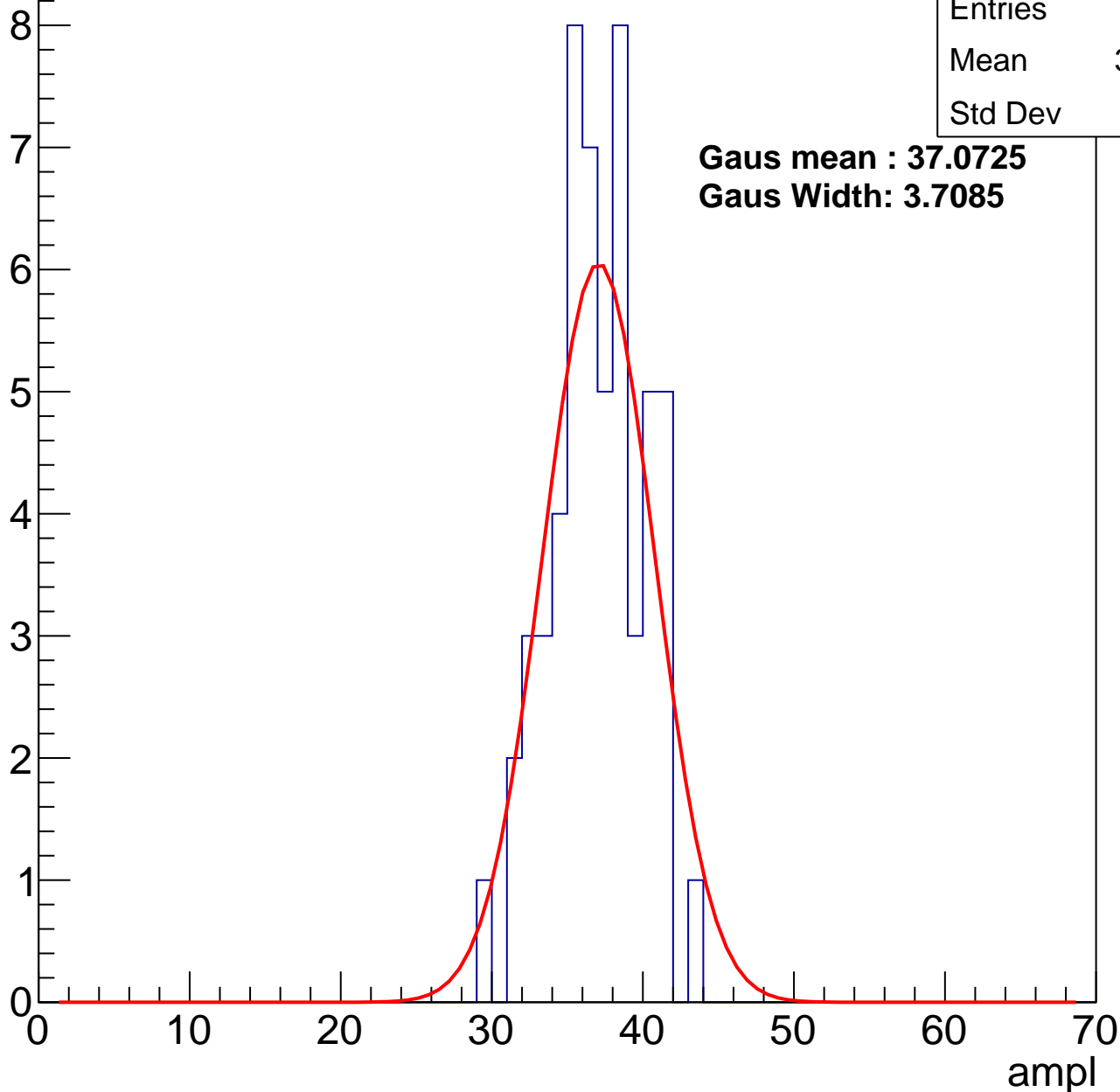
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.51
Std Dev	3.02

Gaus mean : 37.0725

Gaus Width: 3.7085



B1L101S, U17-ch11, adc2

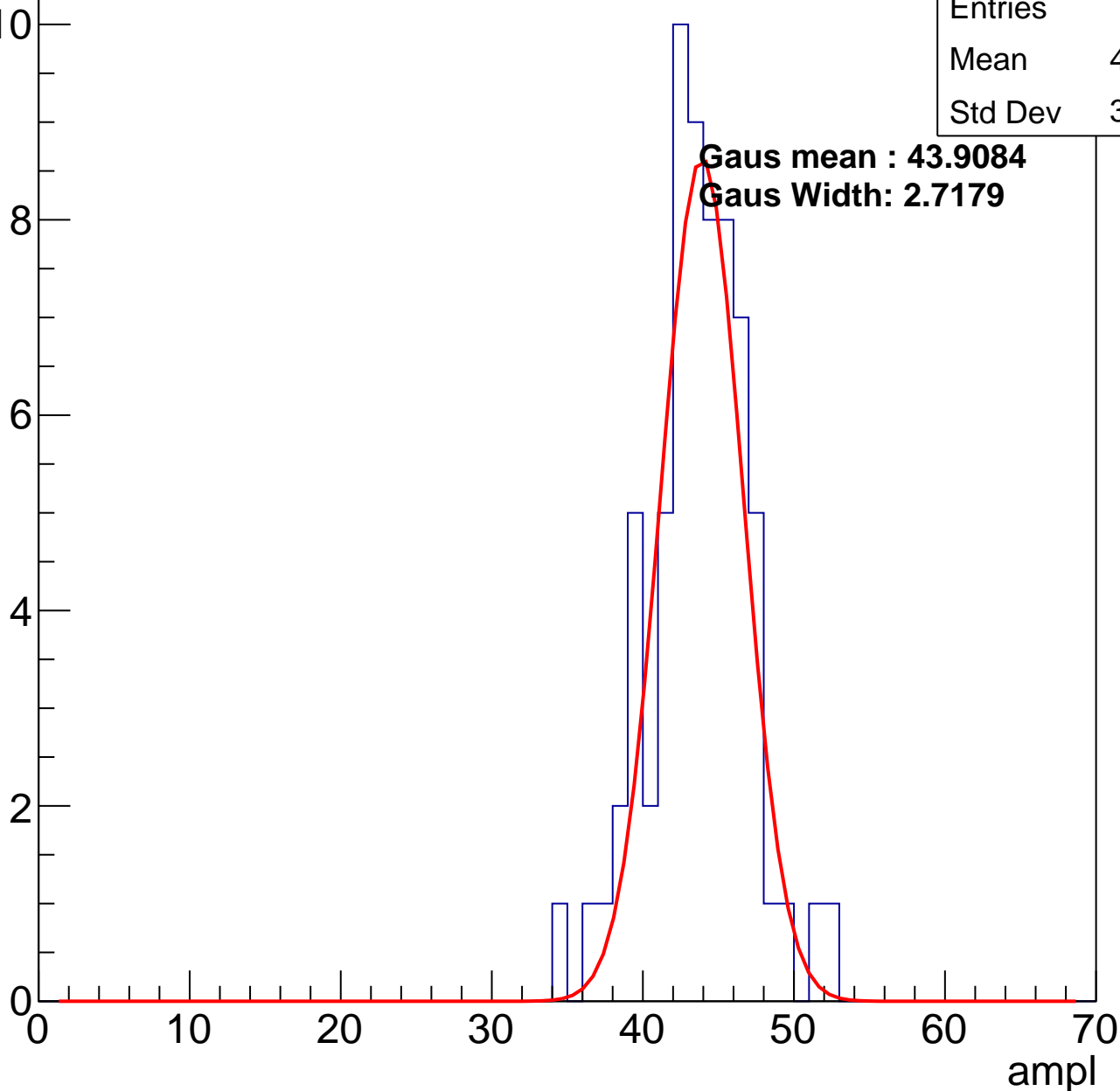
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.22
Std Dev	3.276

Gaus mean : 43.9084

Gaus Width: 2.7179



B1L101S, U17-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	49.62
Std Dev	3.823

Entry

10

8

6

4

2

0

0

10

20

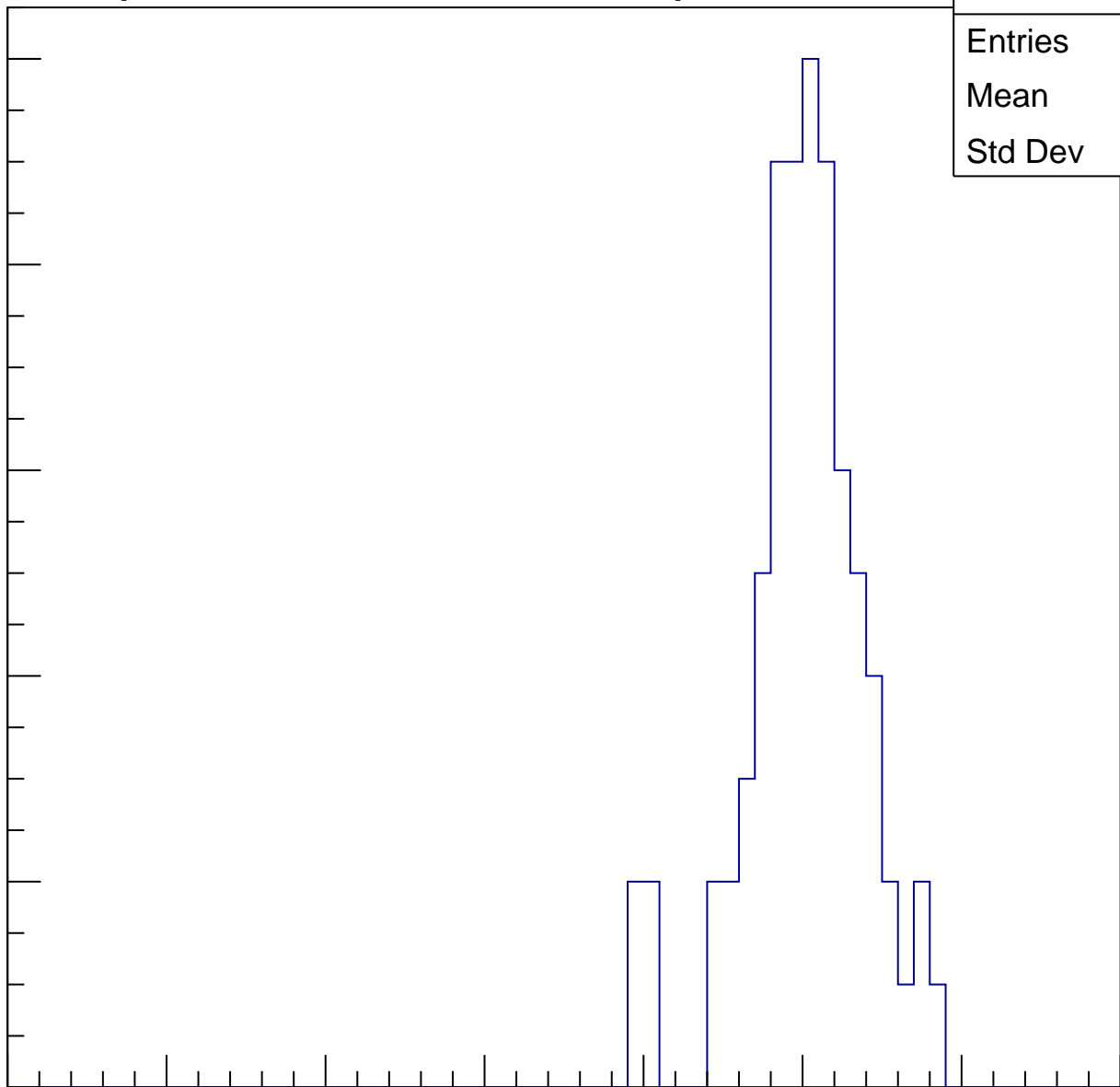
30

40

50

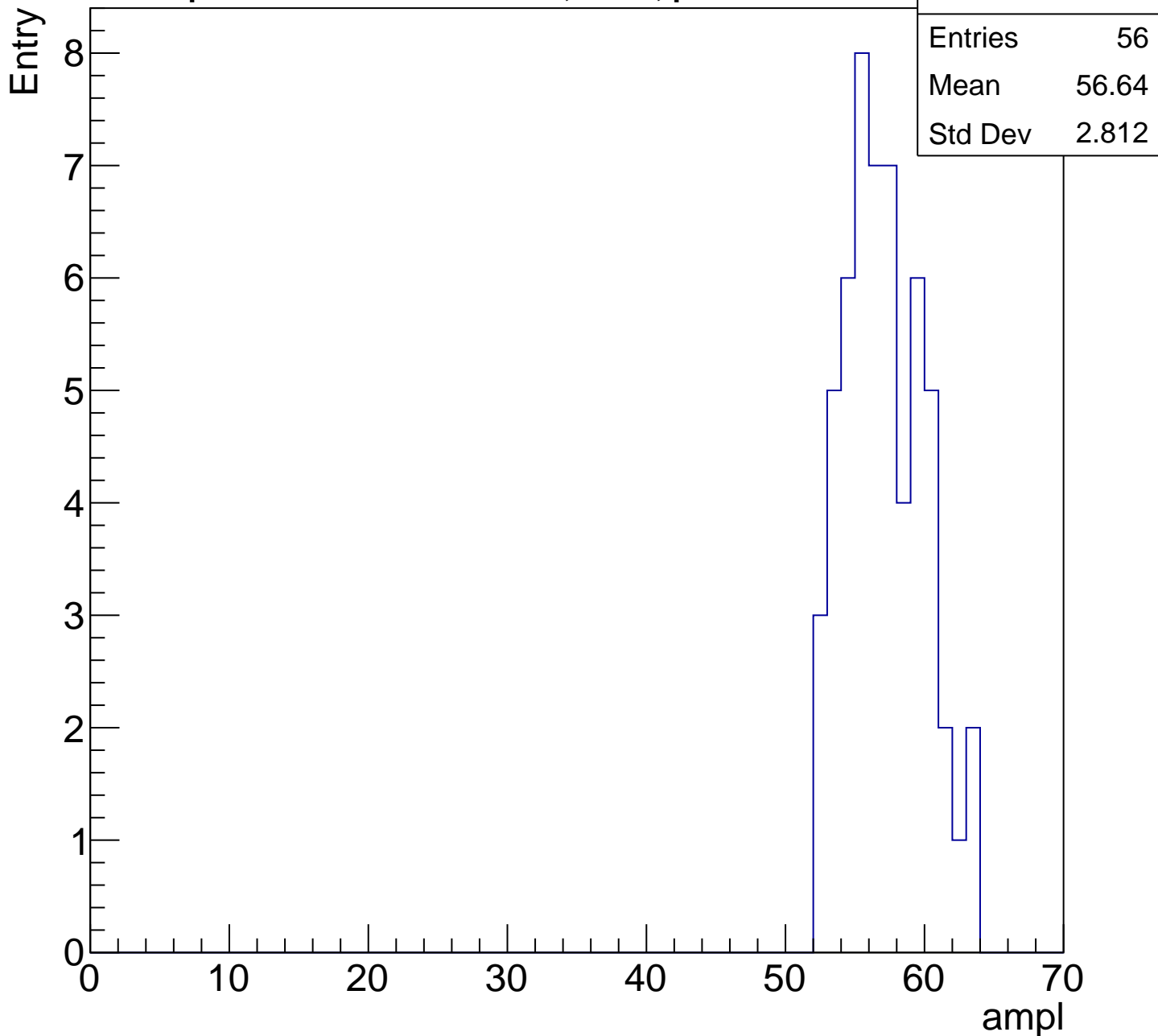
60

ampl



B1L101S, U17-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

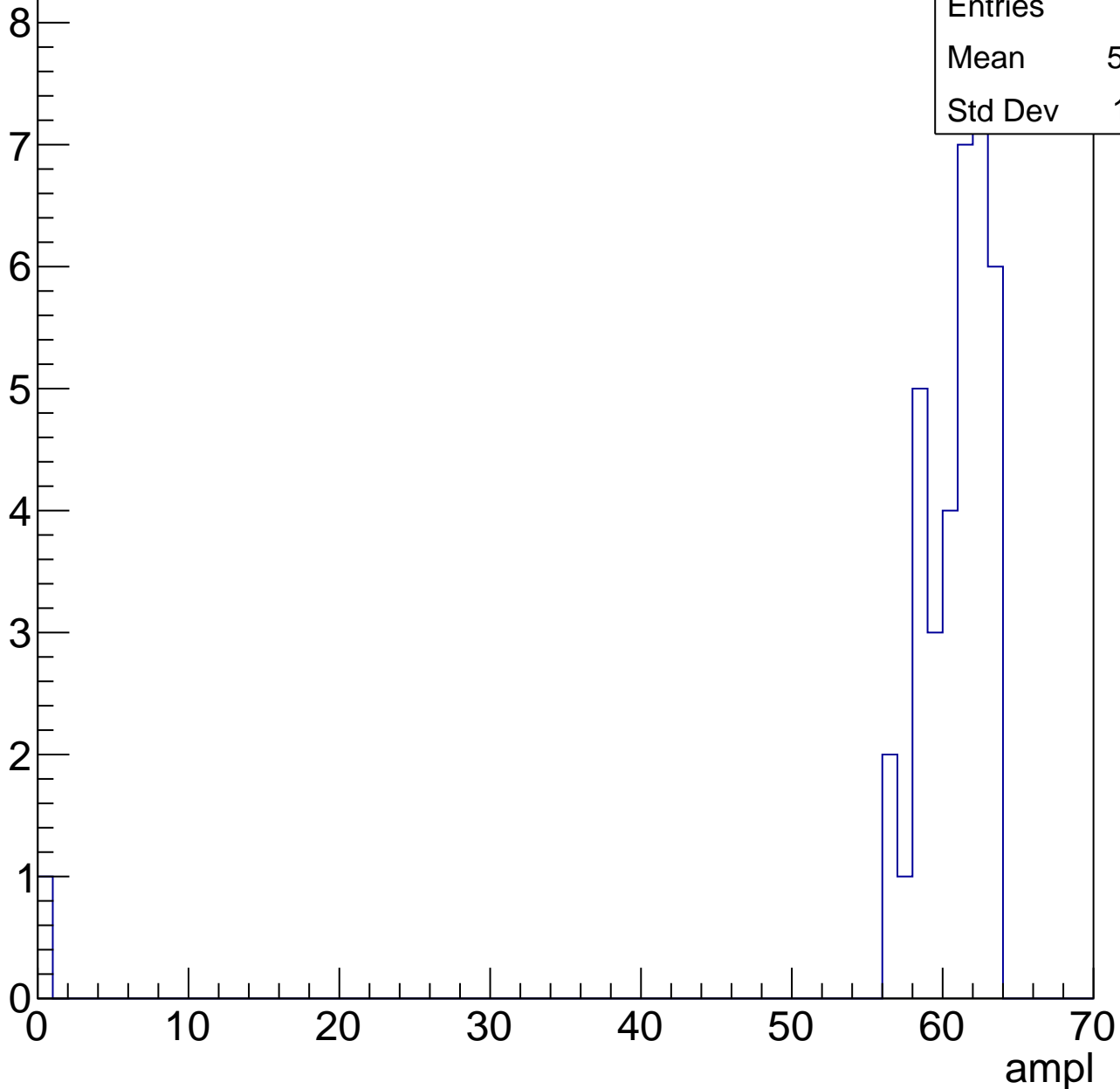


B1L101S, U17-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.84
Std Dev	10.01



B1L101S, U17-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

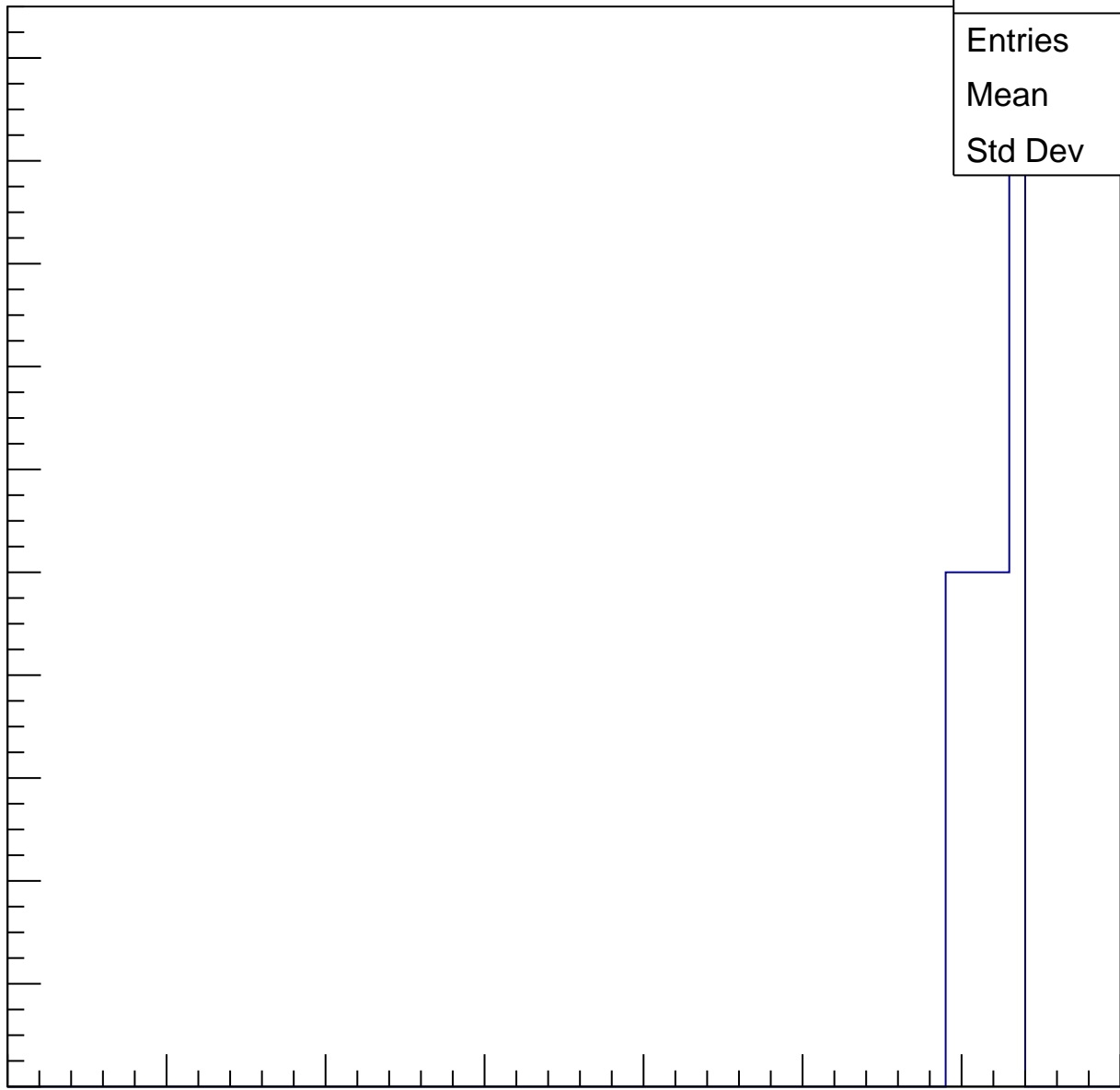
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch12, adc0

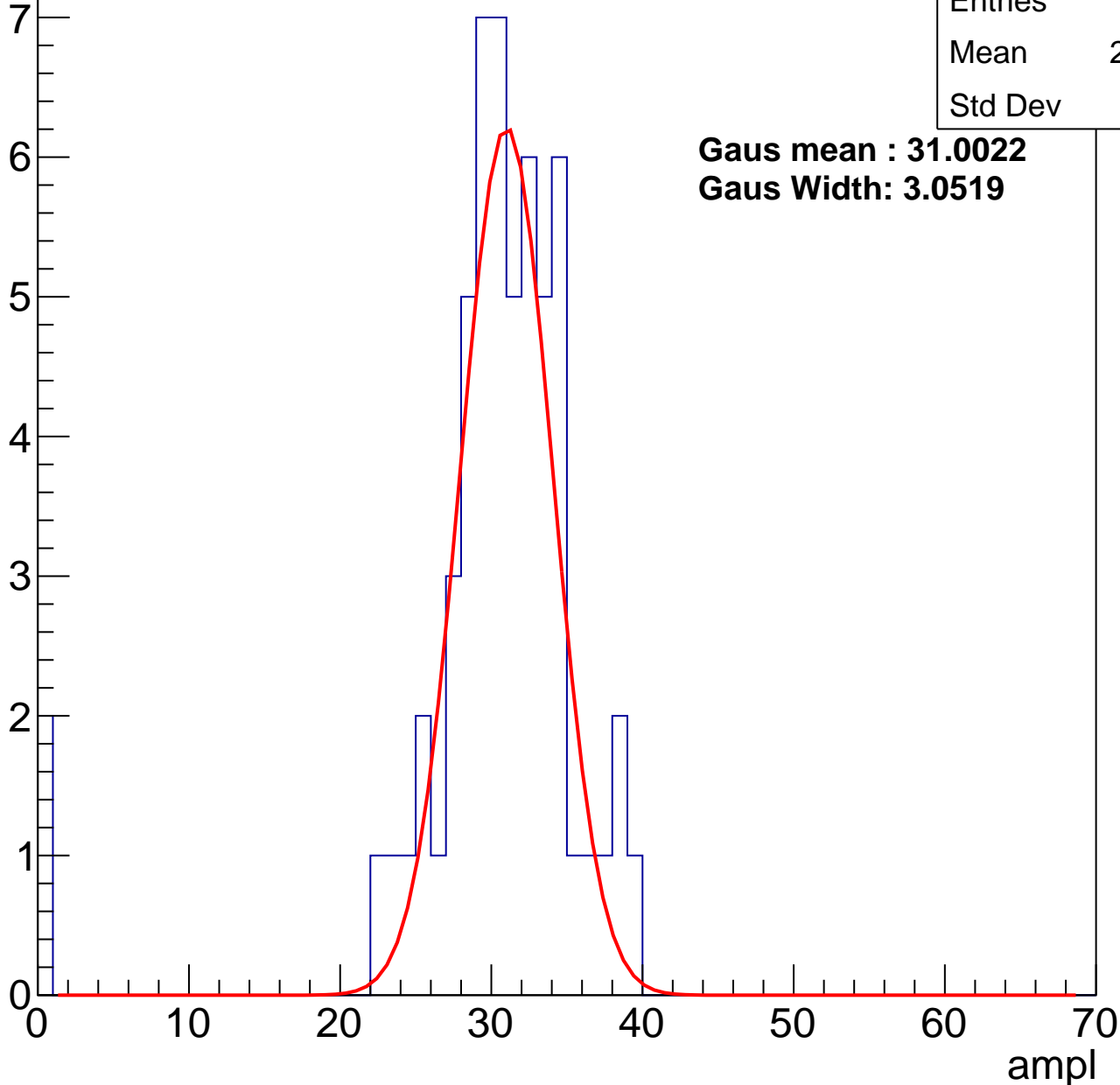
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.62
Std Dev	6.63

Gaus mean : 31.0022

Gaus Width: 3.0519



B1L101S, U17-ch12, adc1

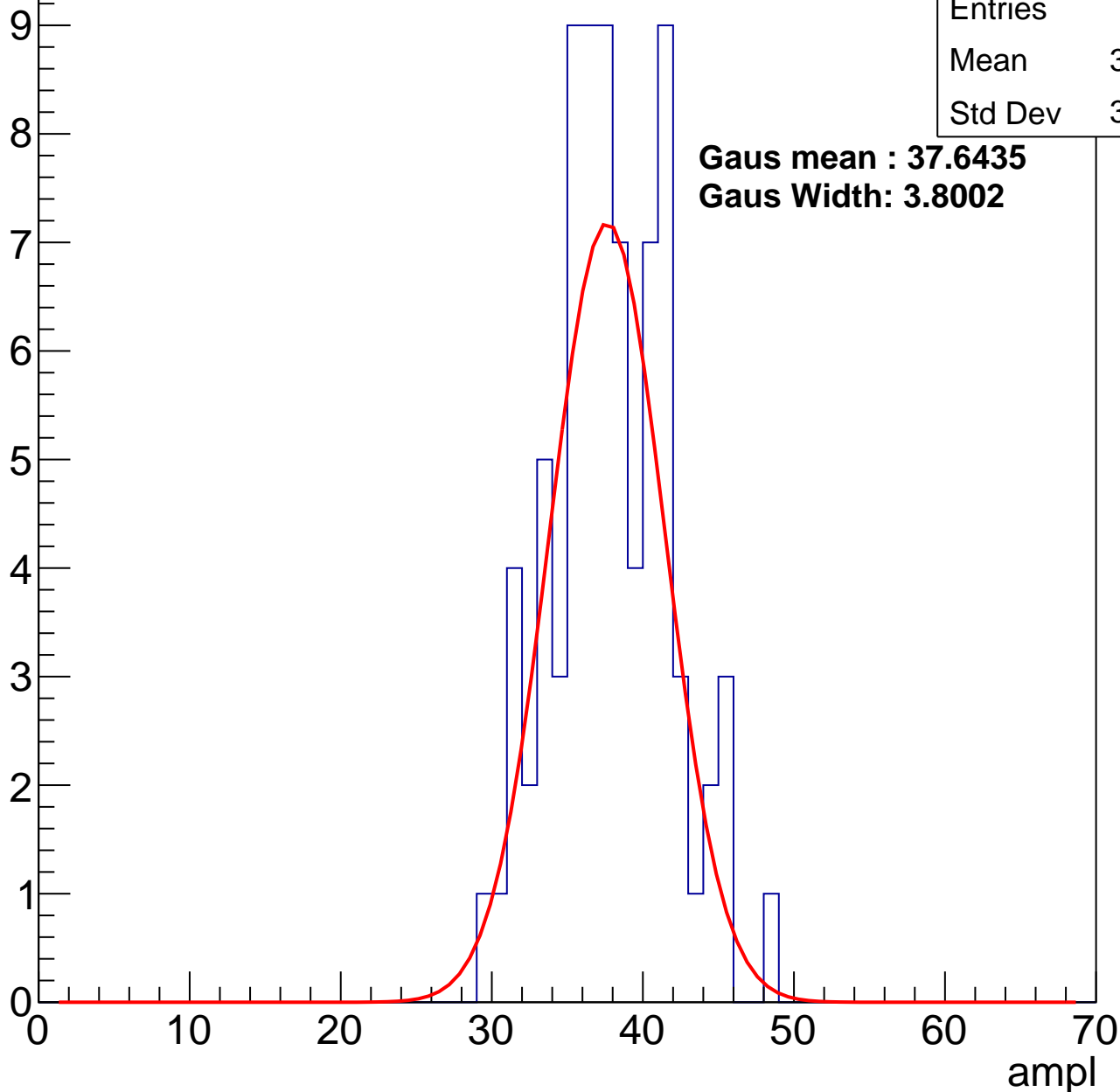
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	37.46
Std Dev	3.863

Gaus mean : 37.6435

Gaus Width: 3.8002



B1L101S, U17-ch12, adc2

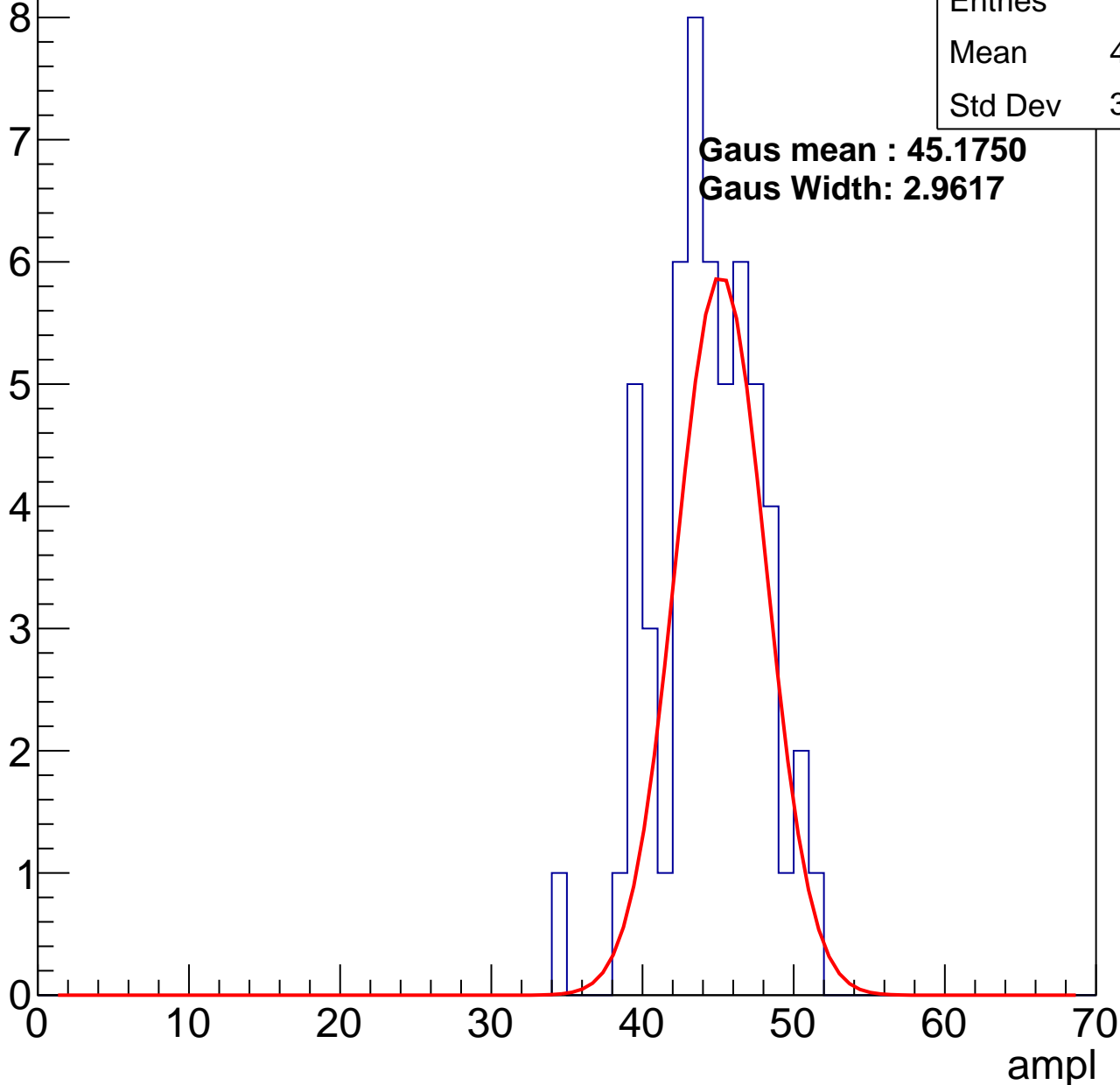
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.93
Std Dev	3.394

Gaus mean : 45.1750

Gaus Width: 2.9617

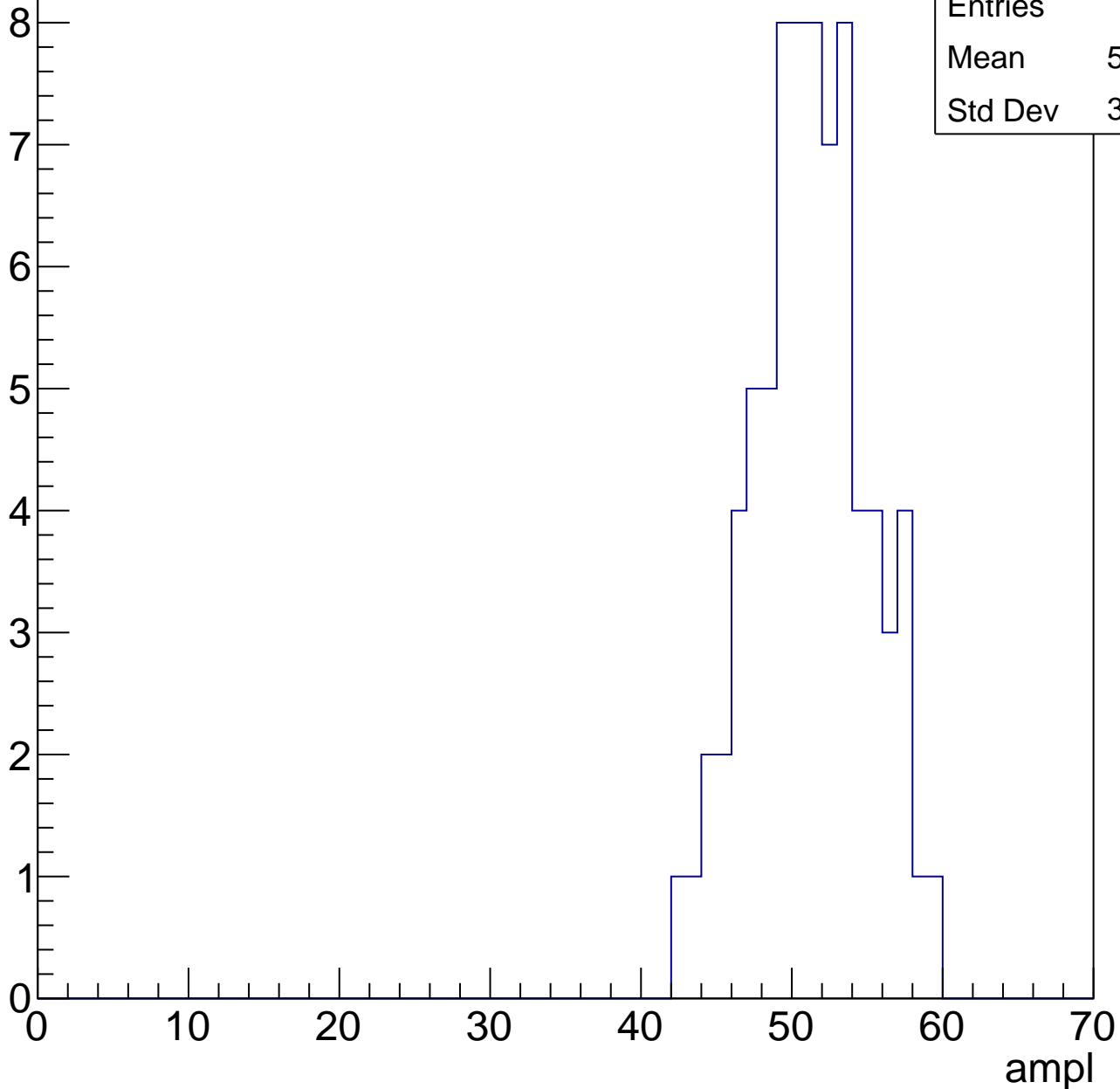


B1L101S, U17-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	50.78
Std Dev	3.712

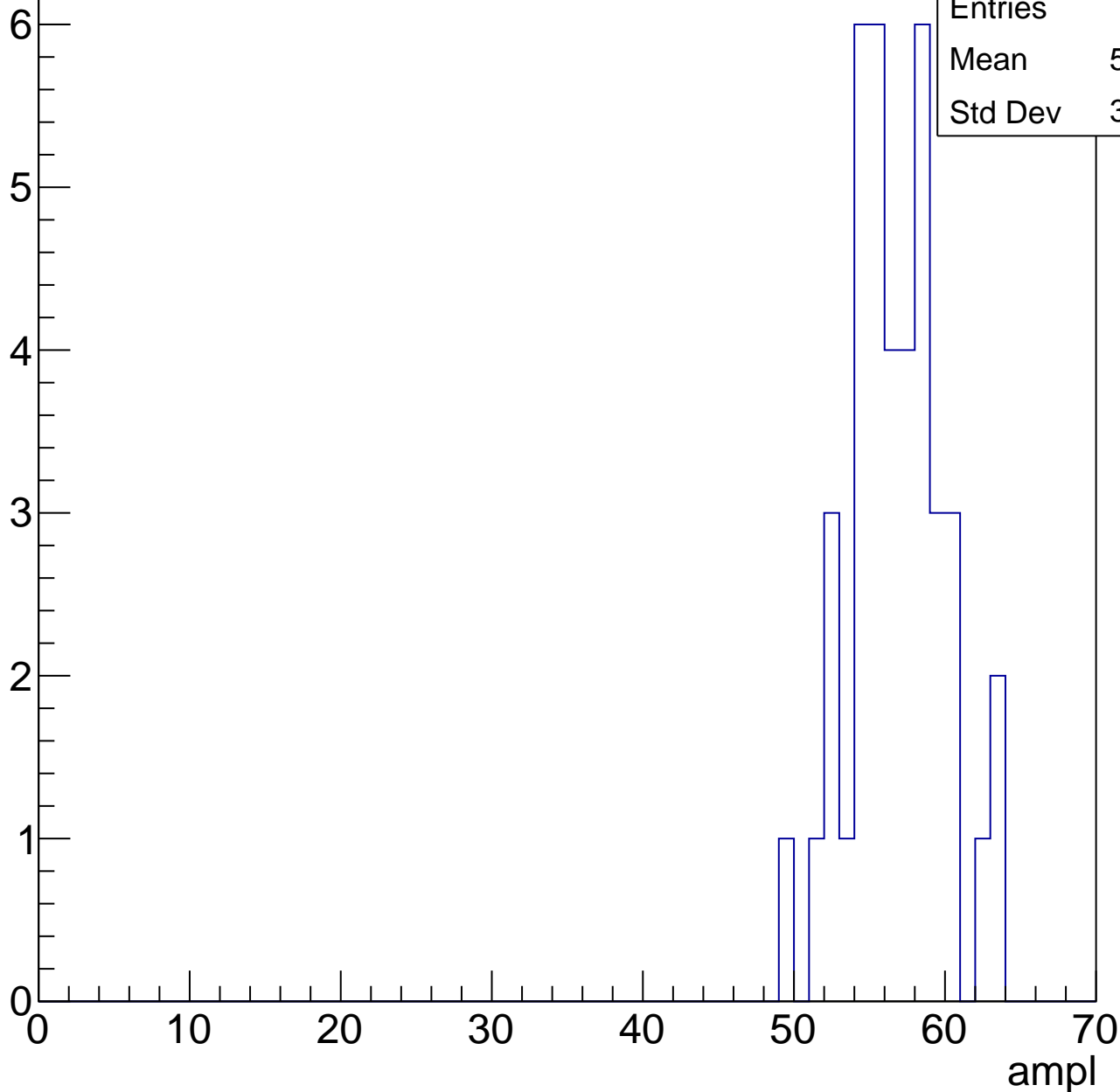


B1L101S, U17-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	56.29
Std Dev	3.102

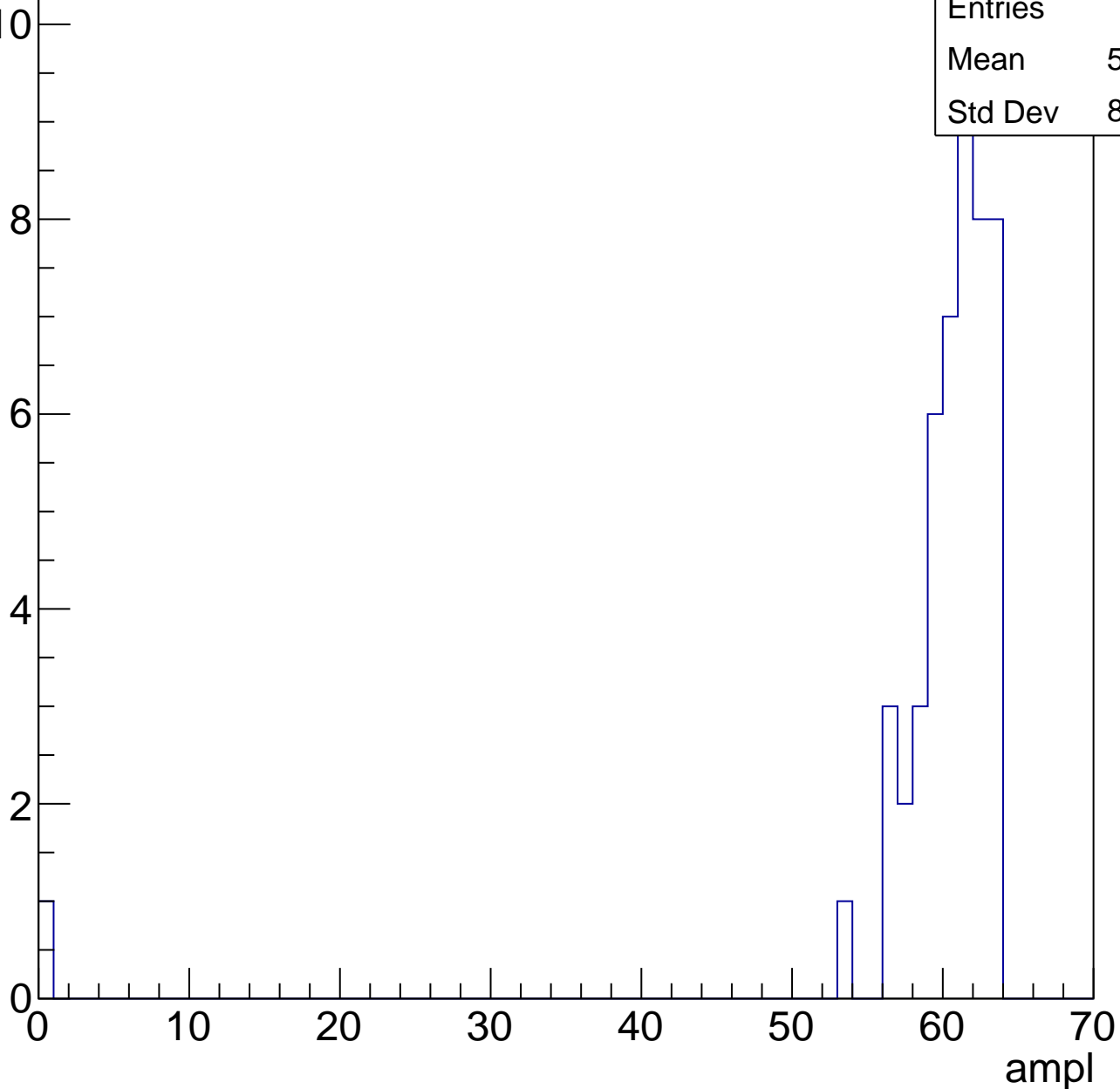


B1L101S, U17-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.04
Std Dev	8.809



B1L101S, U17-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch13, adc0

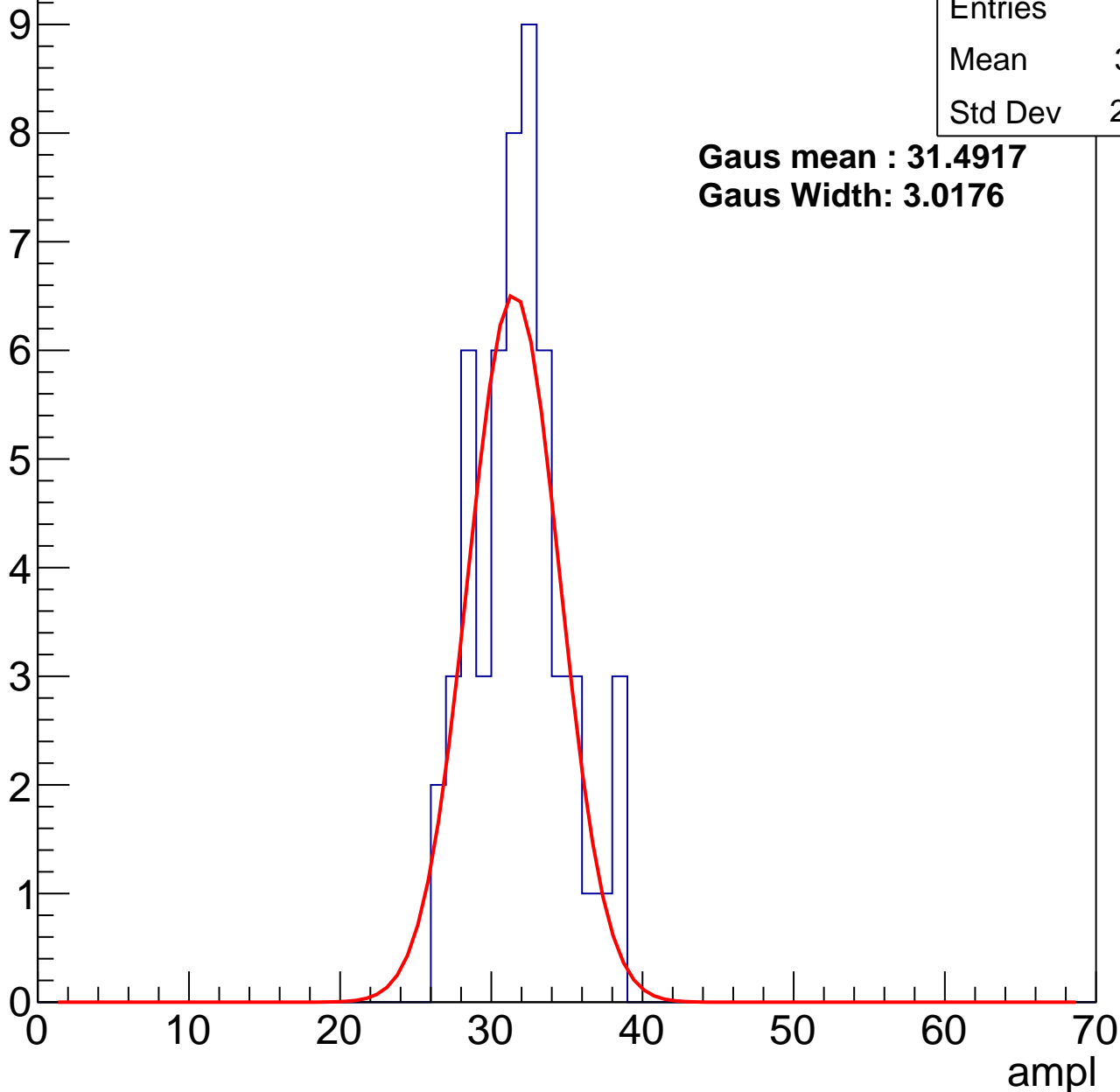
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	31.41
Std Dev	2.972

Gaus mean : 31.4917

Gaus Width: 3.0176



B1L101S, U17-ch13, adc1

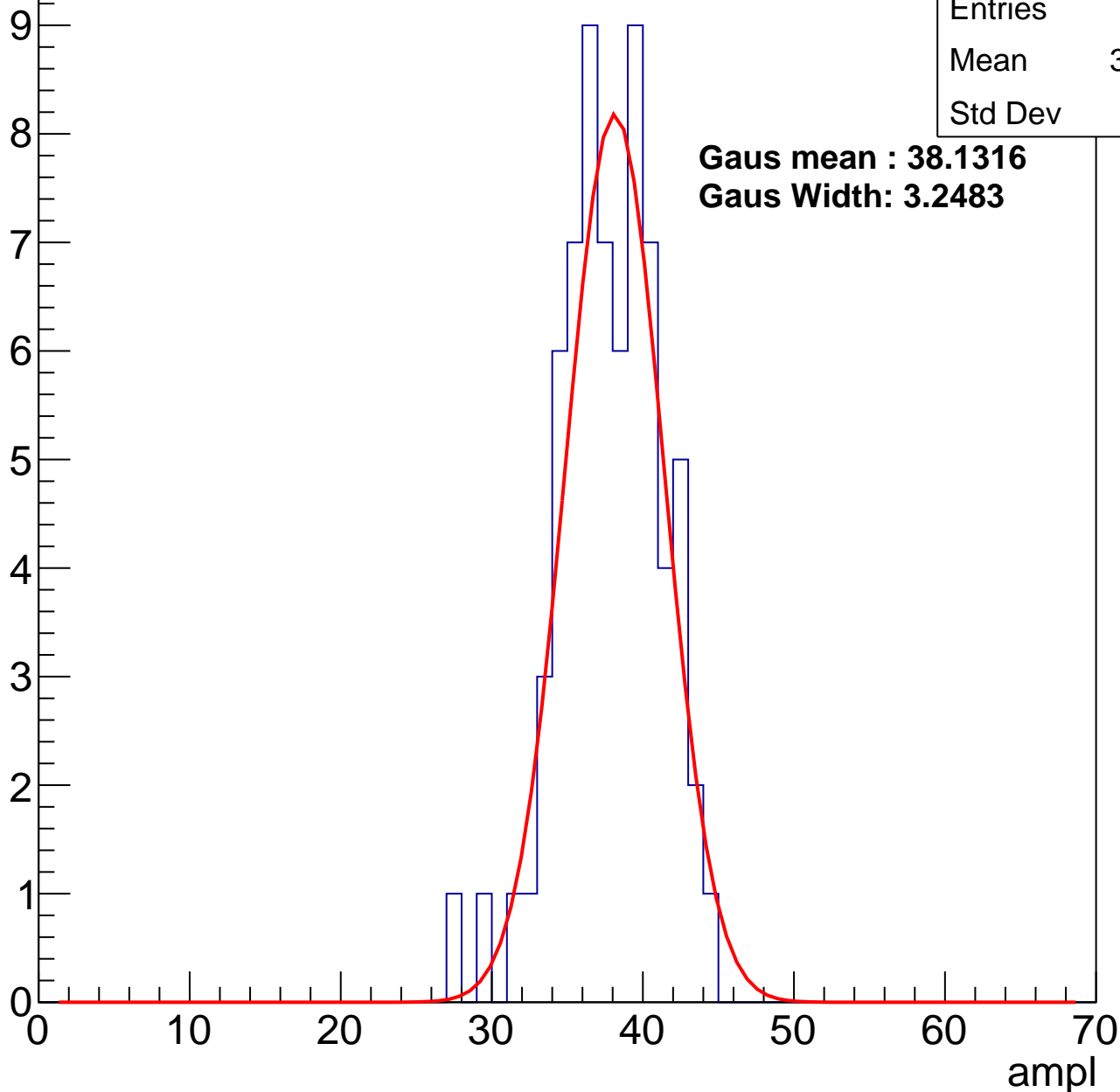
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.33
Std Dev	3.32

Gaus mean : 38.1316

Gaus Width: 3.2483



B1L101S, U17-ch13, adc2

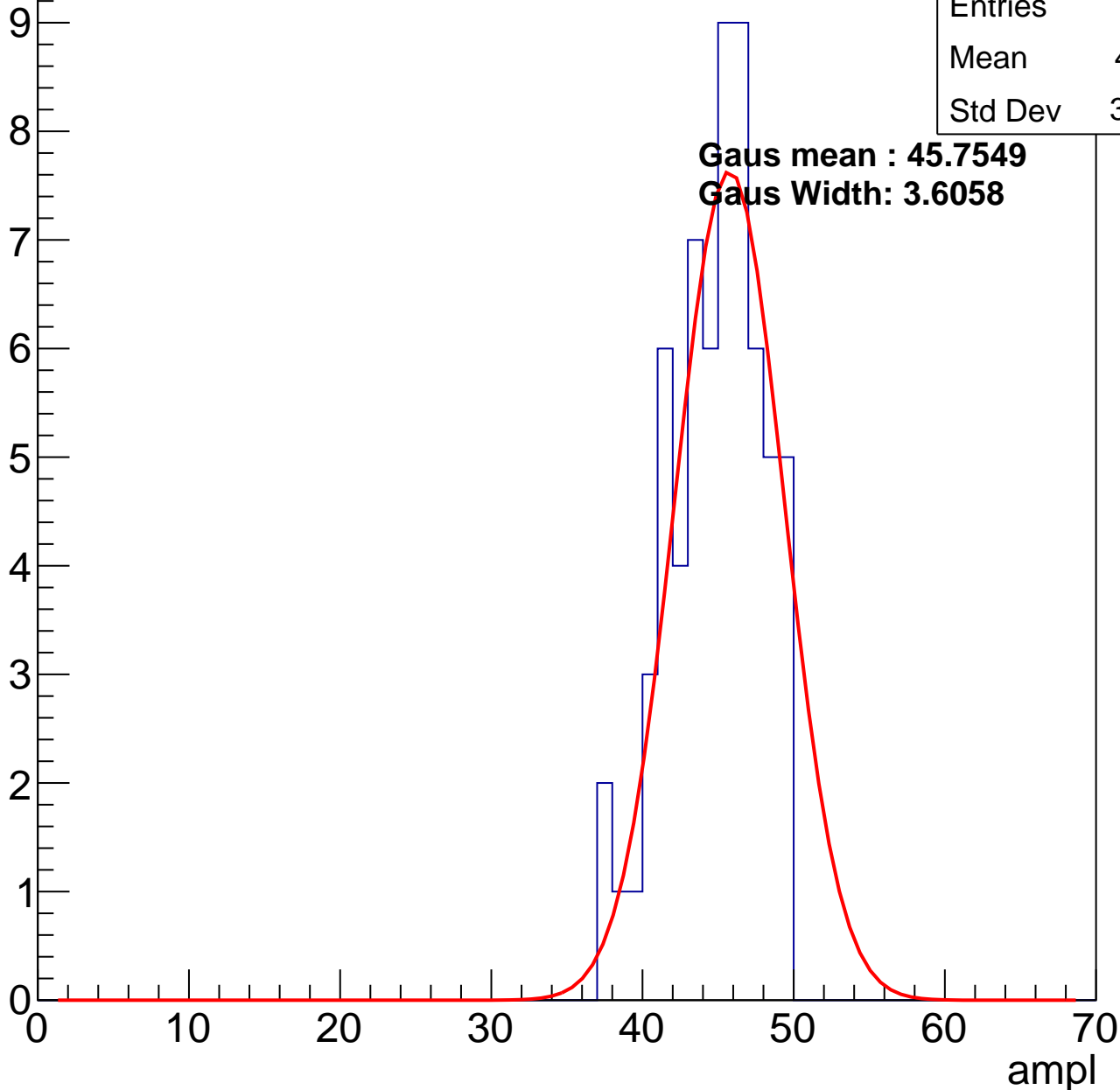
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.31
Std Dev	3.015

Gaus mean : 45.7549

Gaus Width: 3.6058

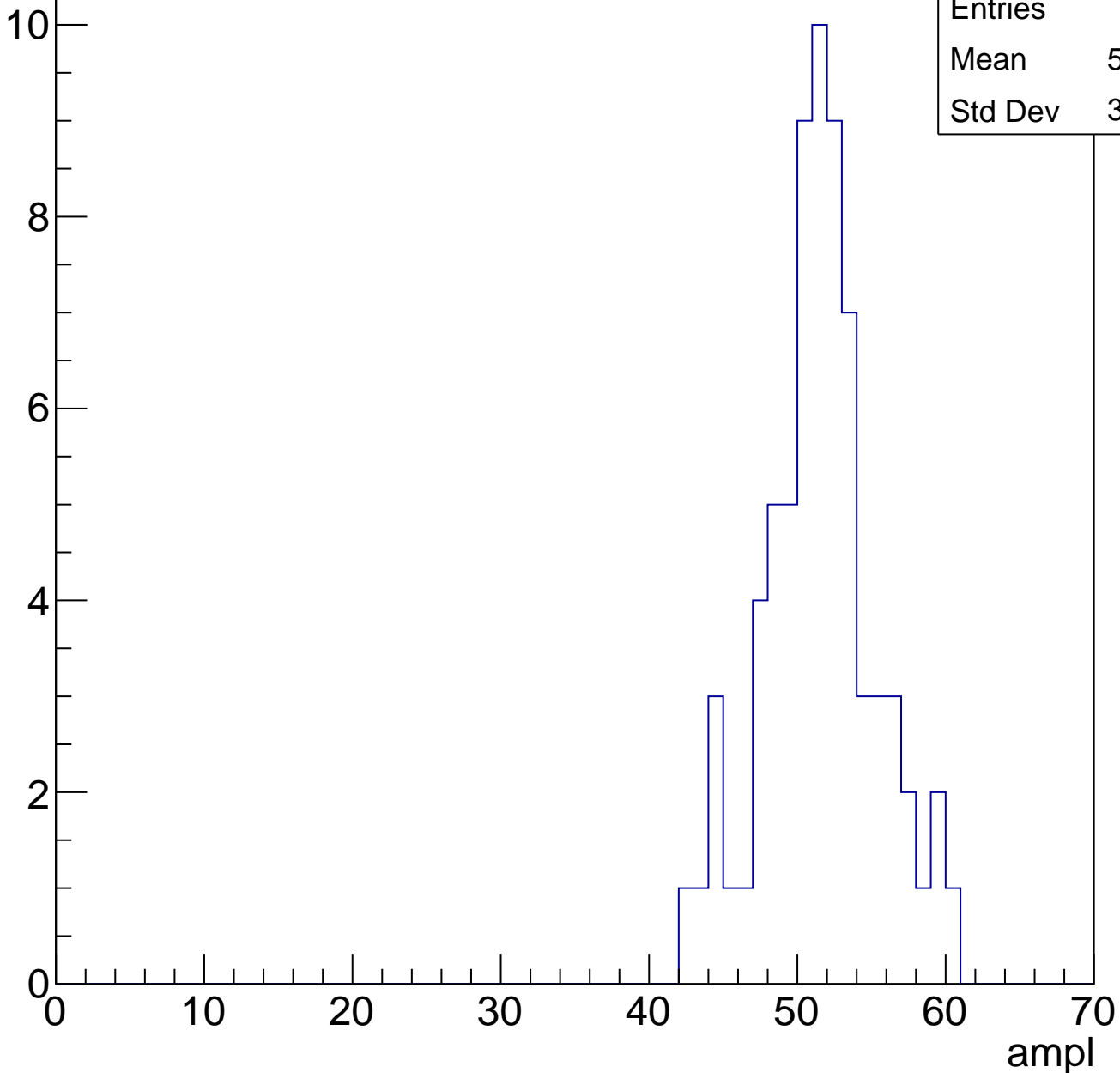


B1L101S, U17-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.06
Std Dev	3.767

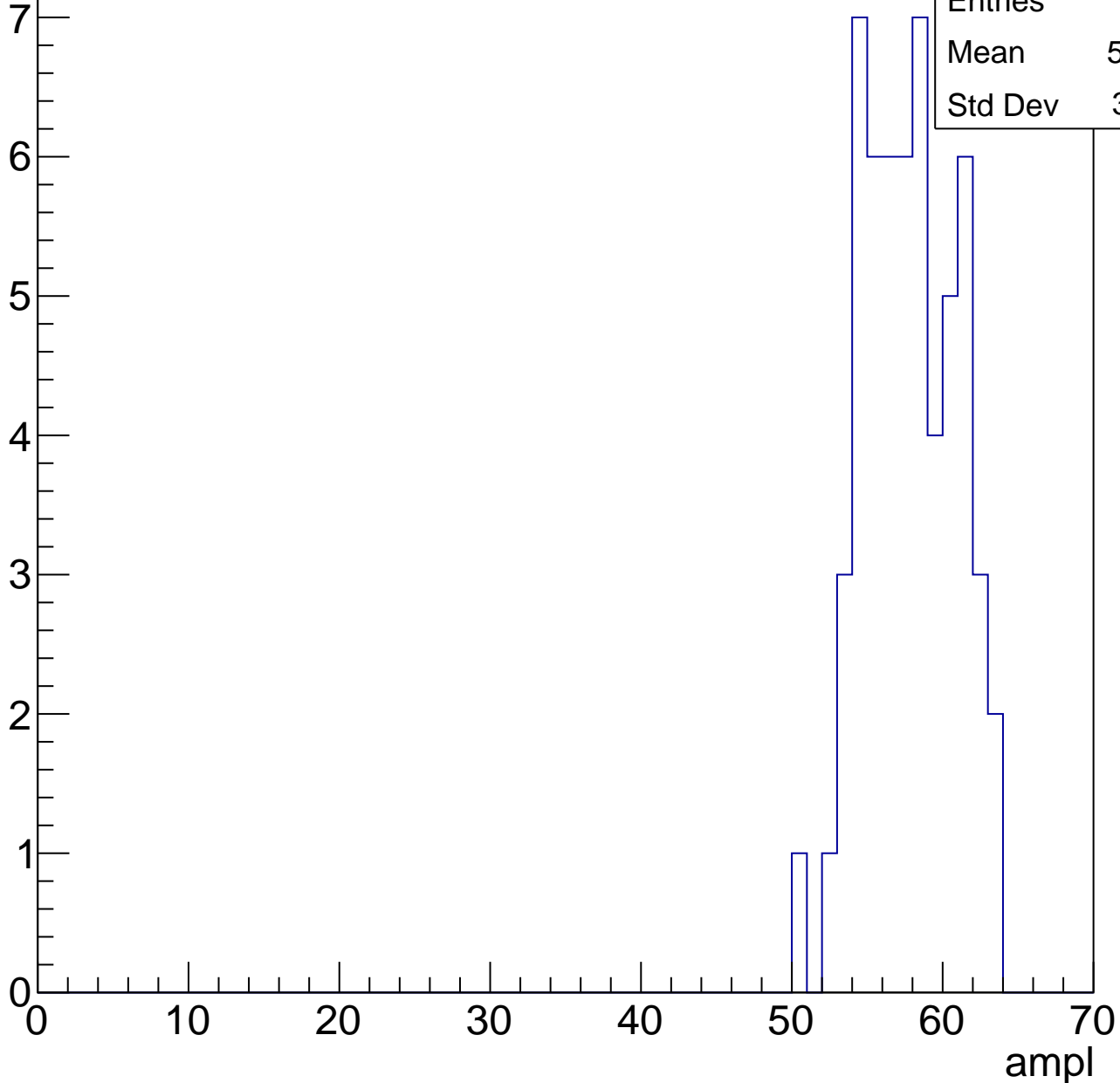


B1L101S, U17-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	57.32
Std Dev	3.021

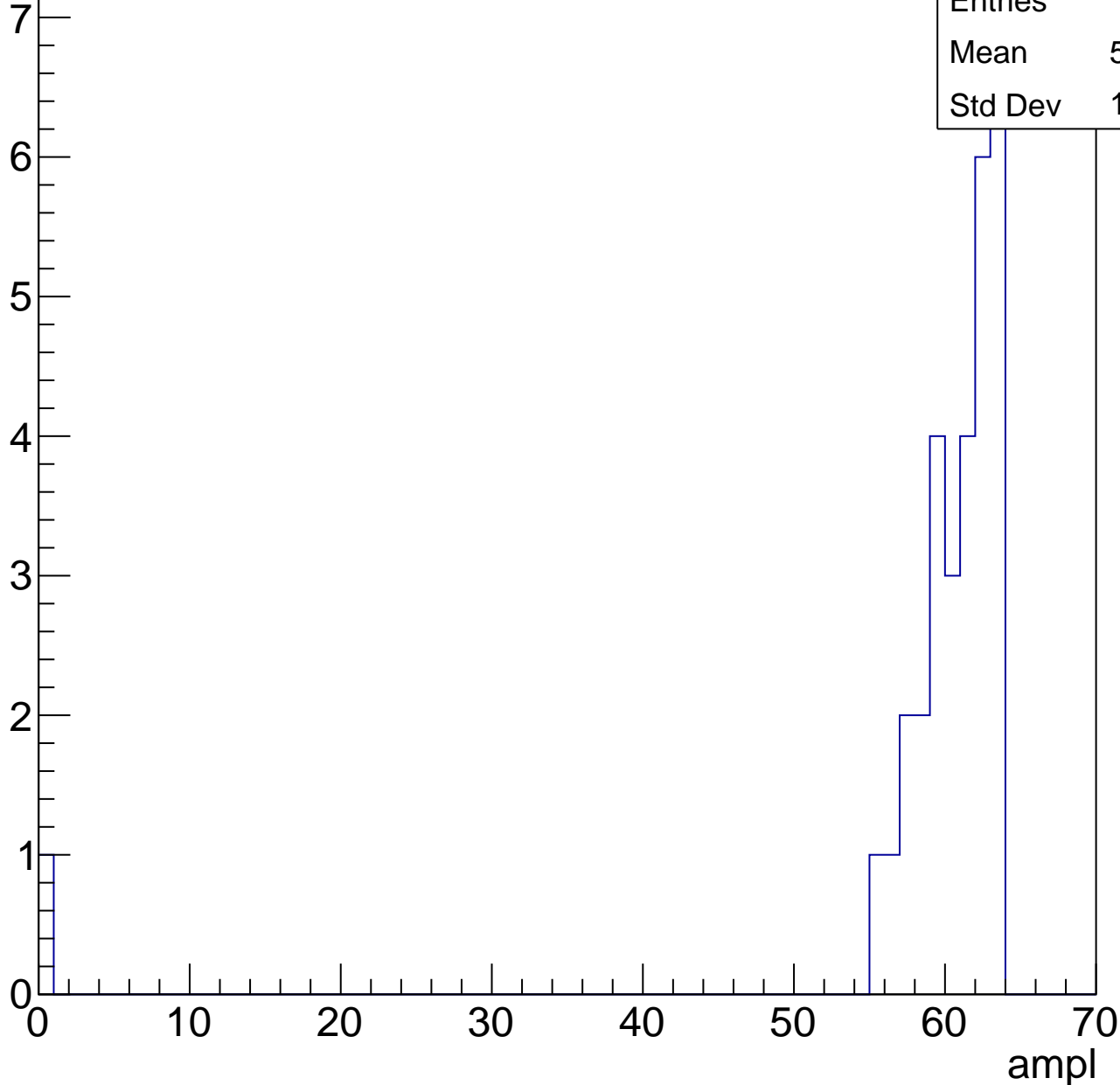


B1L101S, U17-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

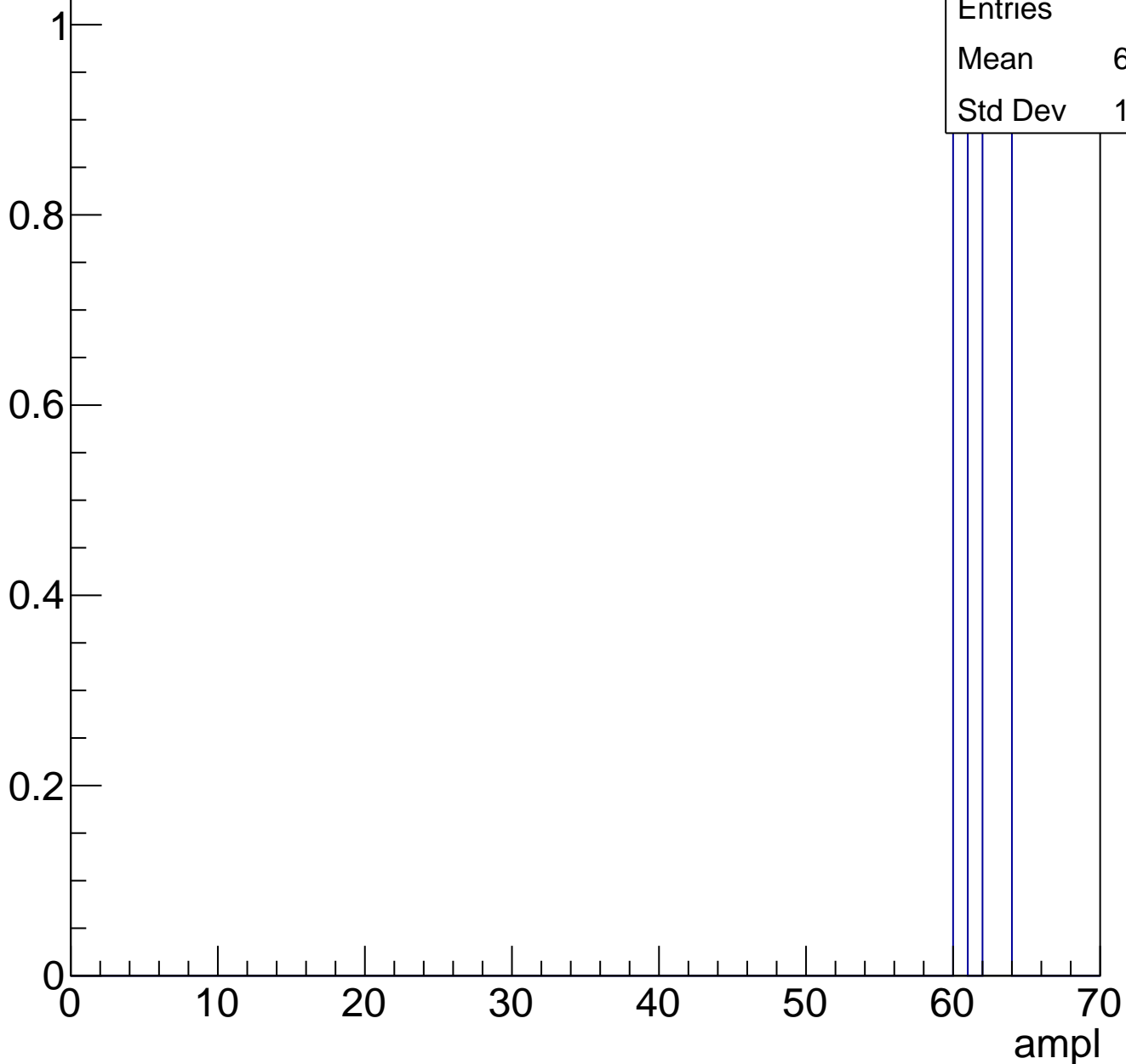
Entries	31
Mean	58.52
Std Dev	10.92



B1L101S, U17-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch14, adc0

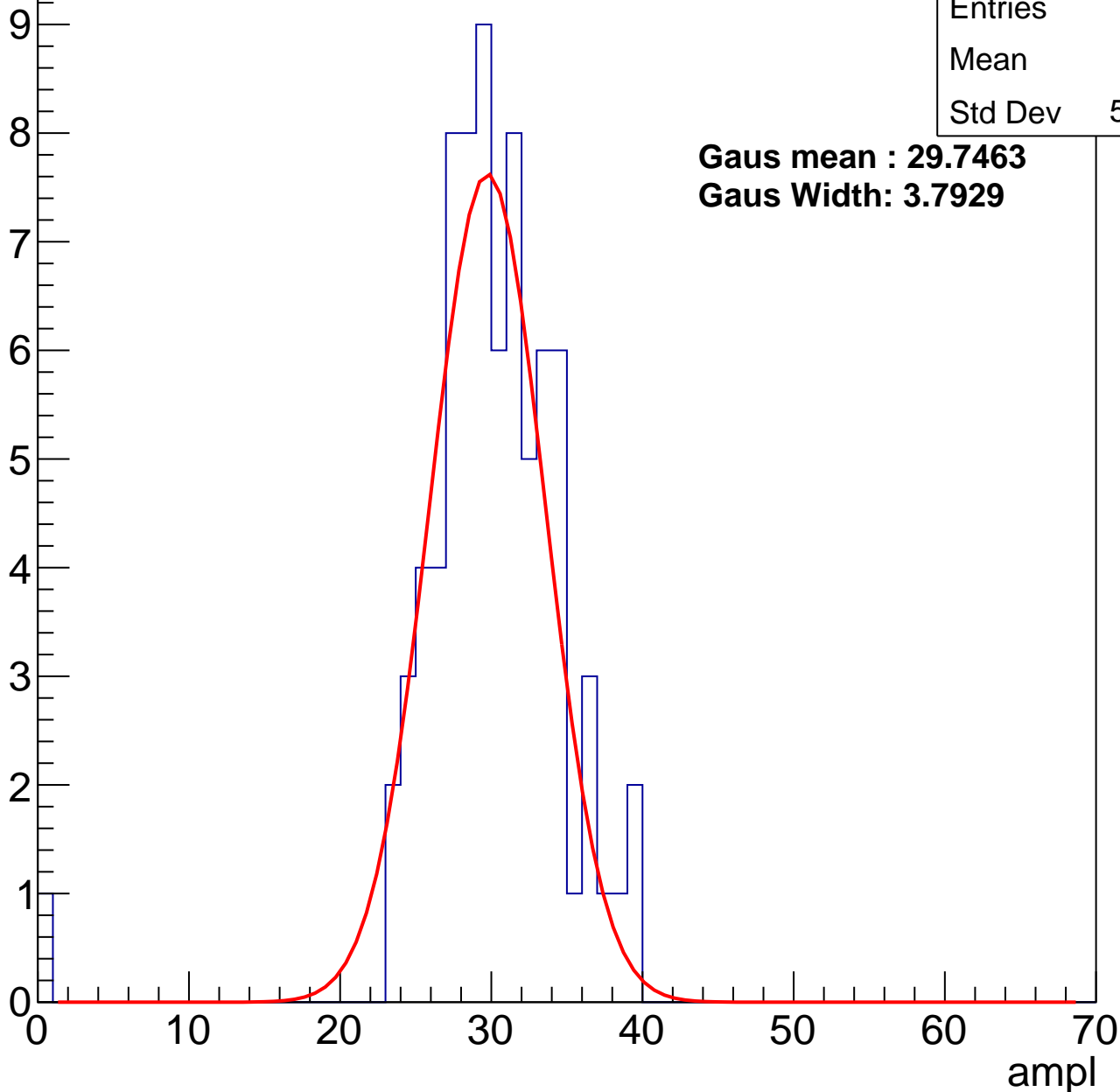
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.6
Std Dev	5.009

Gaus mean : 29.7463

Gaus Width: 3.7929



B1L101S, U17-ch14, adc1

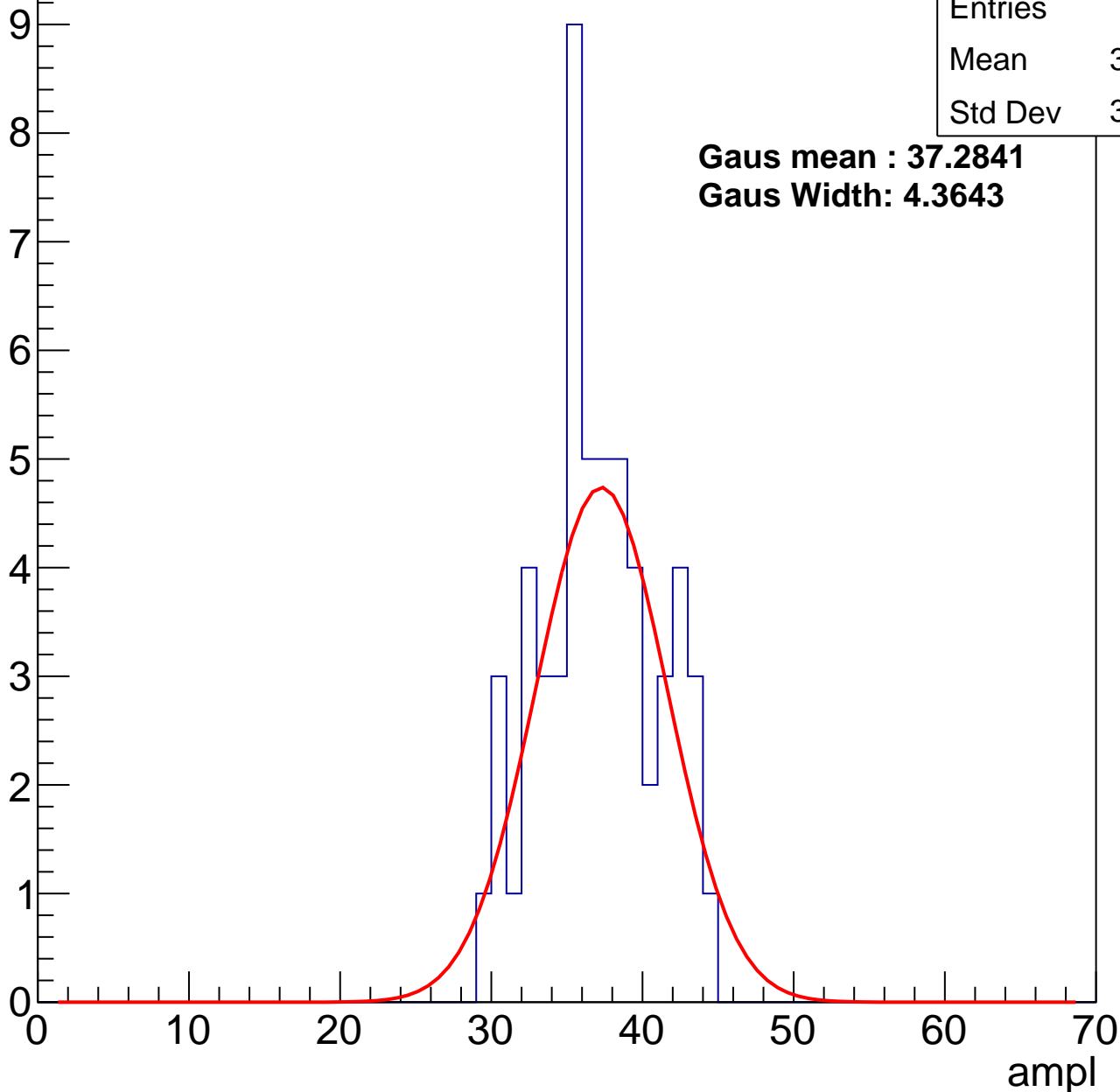
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	36.59
Std Dev	3.774

Gaus mean : 37.2841

Gaus Width: 4.3643



B1L101S, U17-ch14, adc2

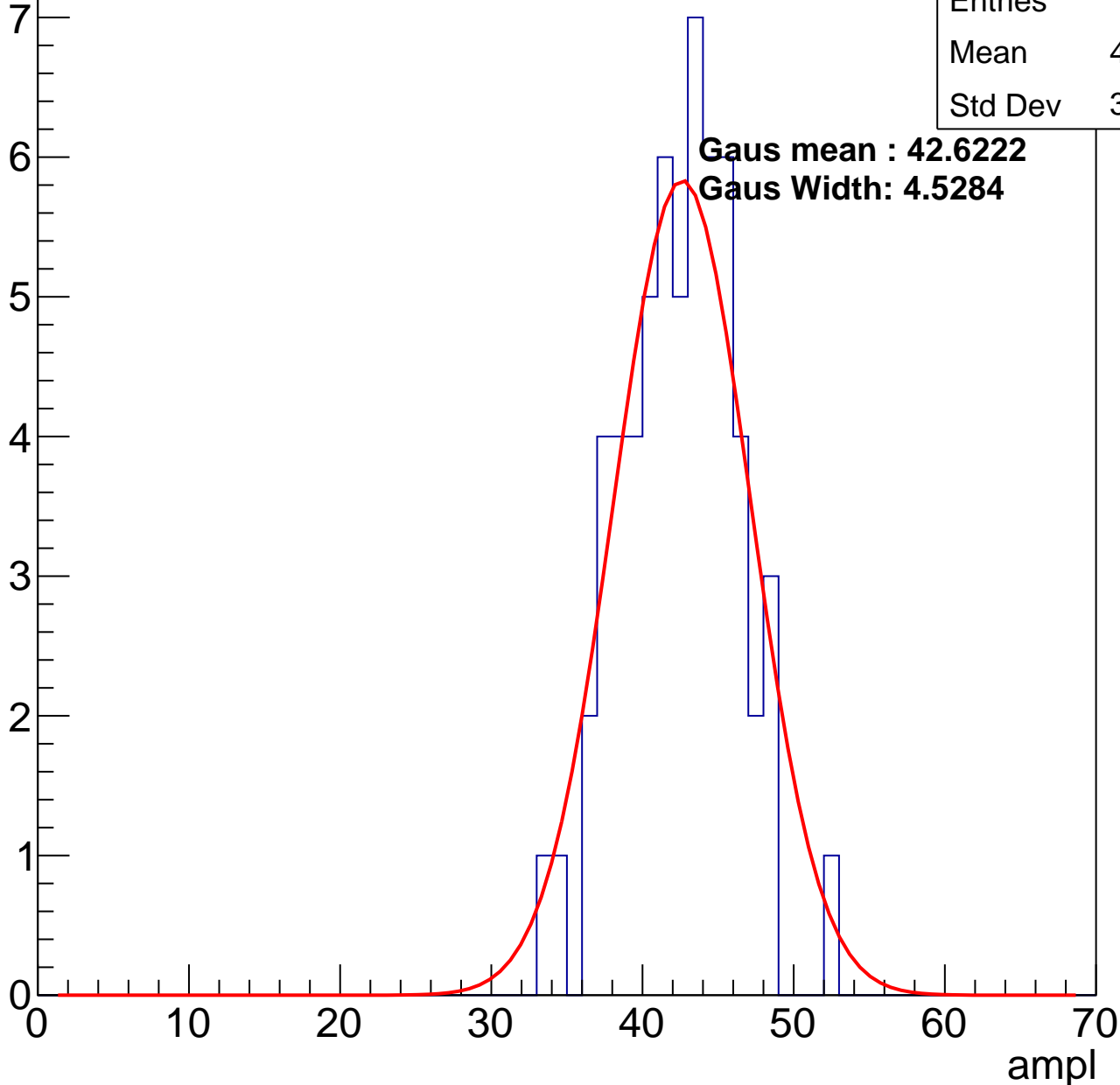
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.97
Std Dev	3.733

Gaus mean : 42.6222

Gaus Width: 4.5284

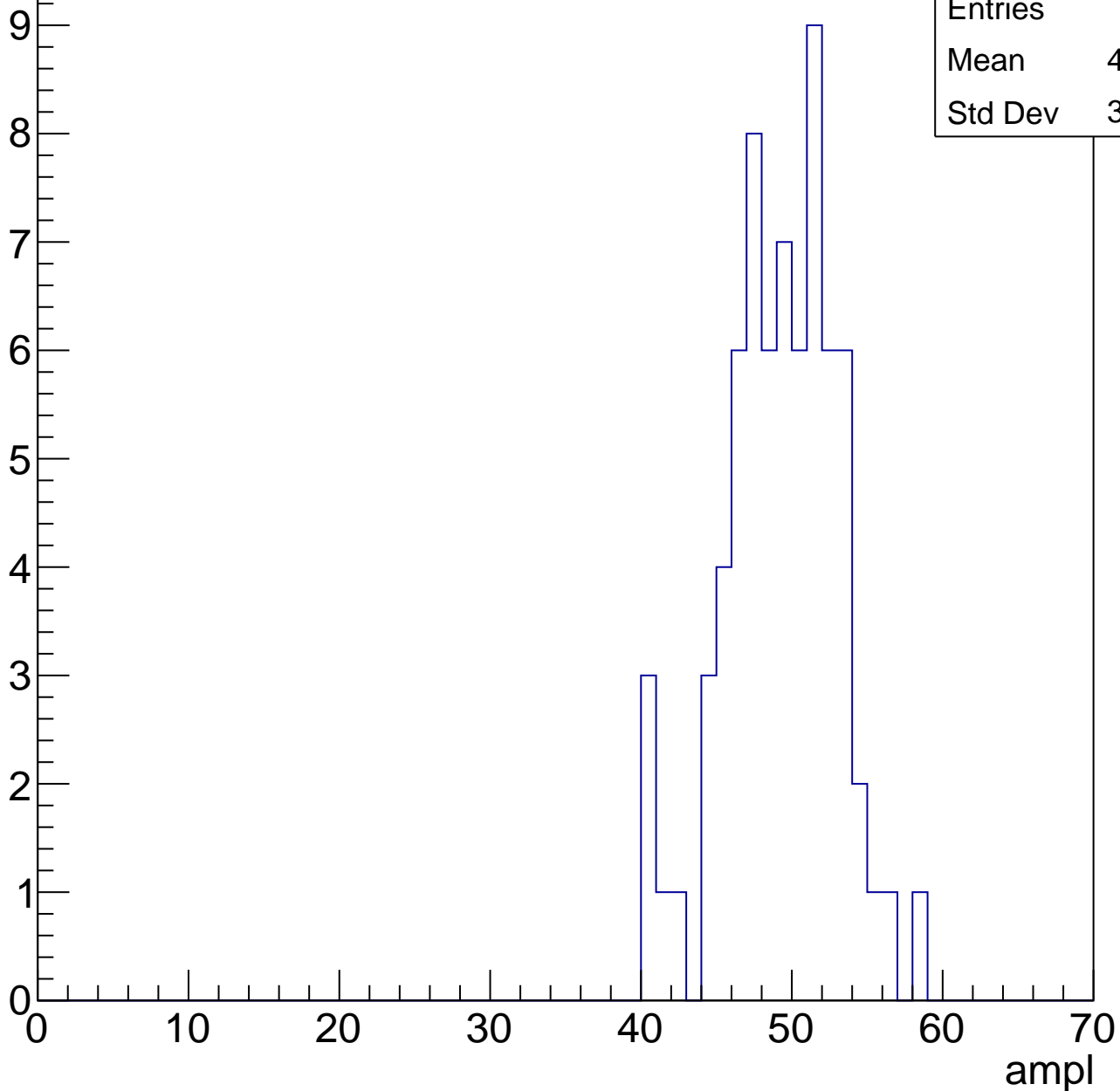


B1L101S, U17-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	48.79
Std Dev	3.738

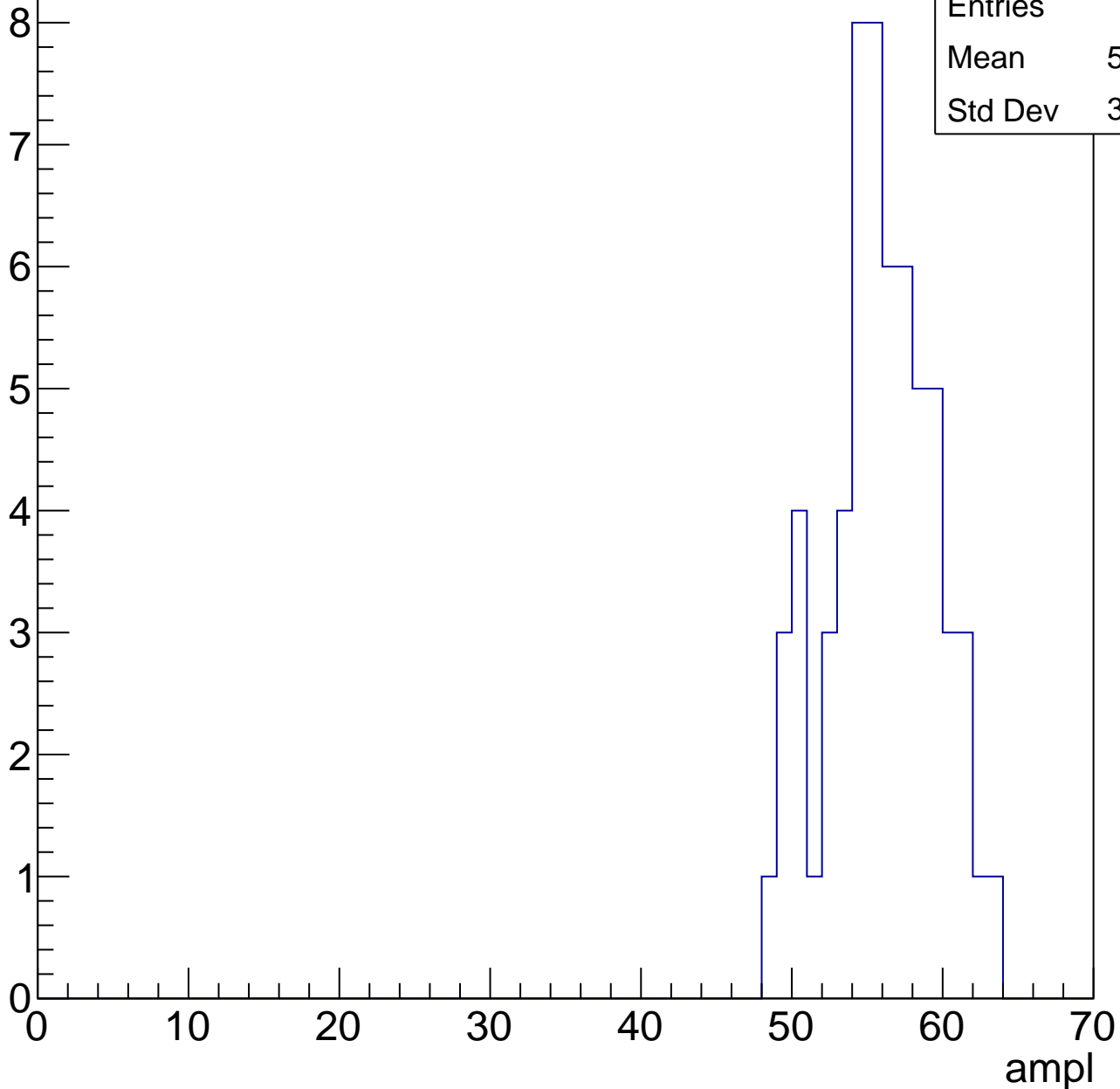


B1L101S, U17-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

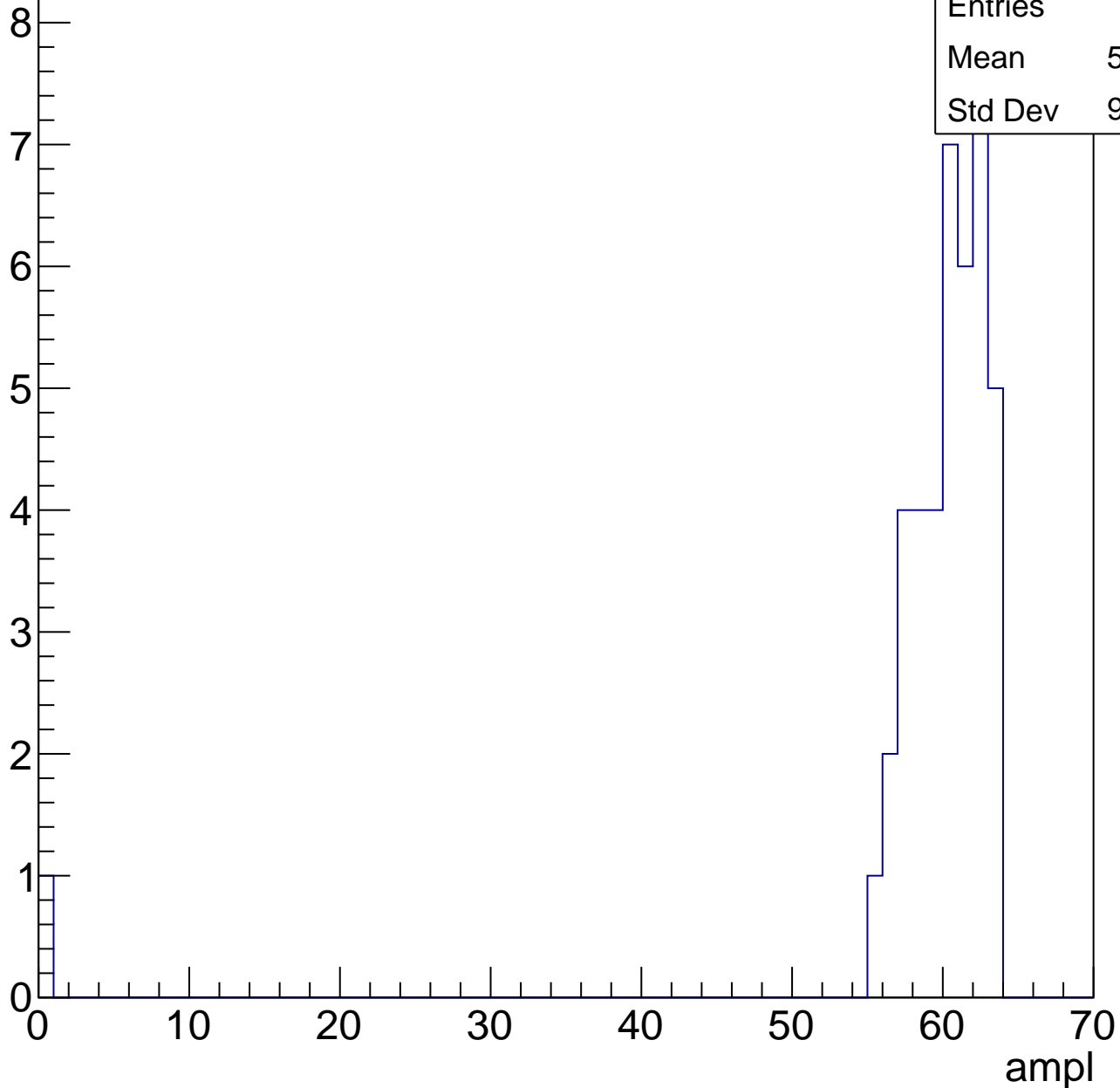
Entries	62
Mean	55.44
Std Dev	3.509



B1L101S, U17-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

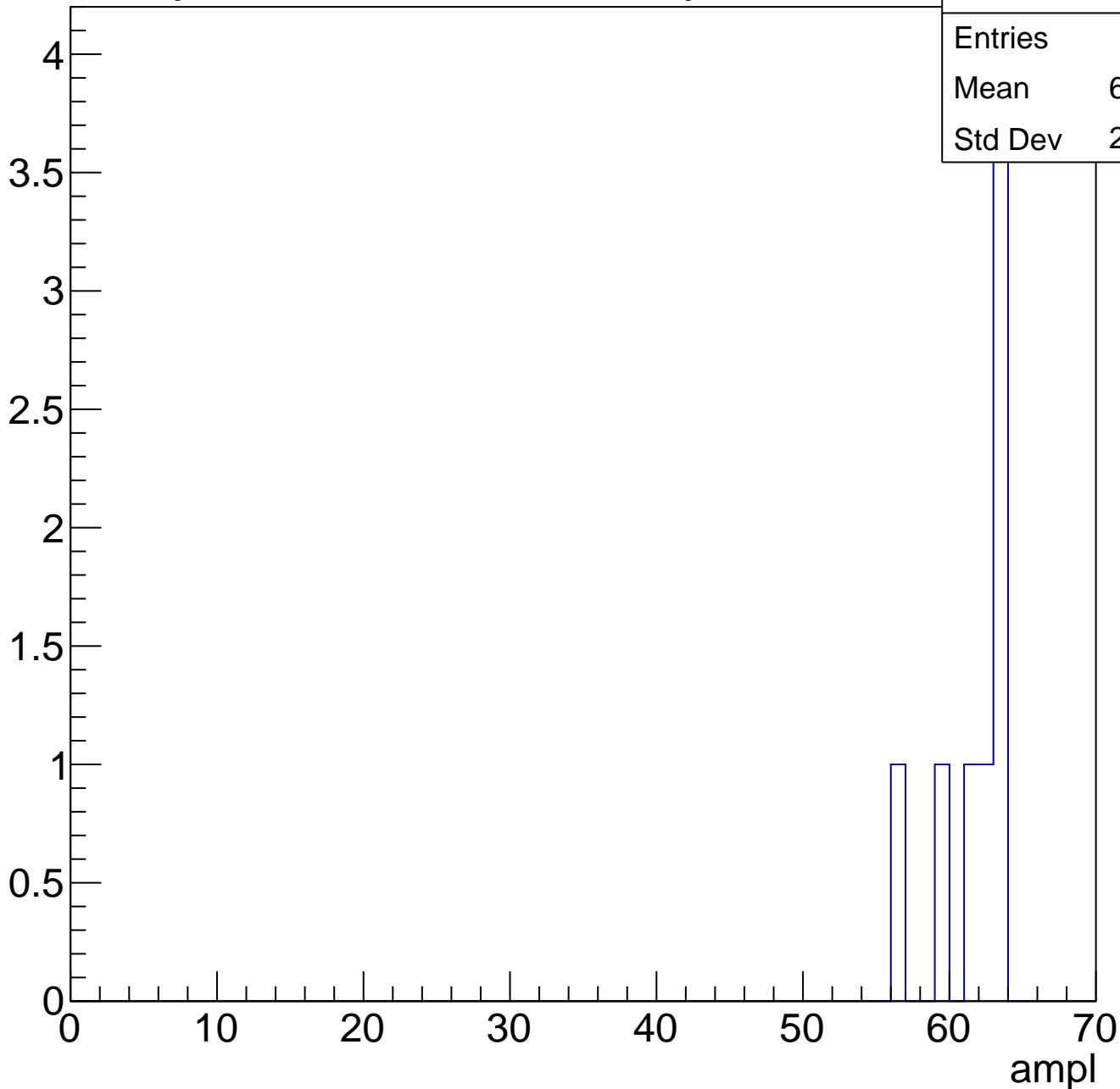
Entry



B1L101S, U17-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	61.25
Std Dev	2.385

B1L101S, U17-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch15, adc0

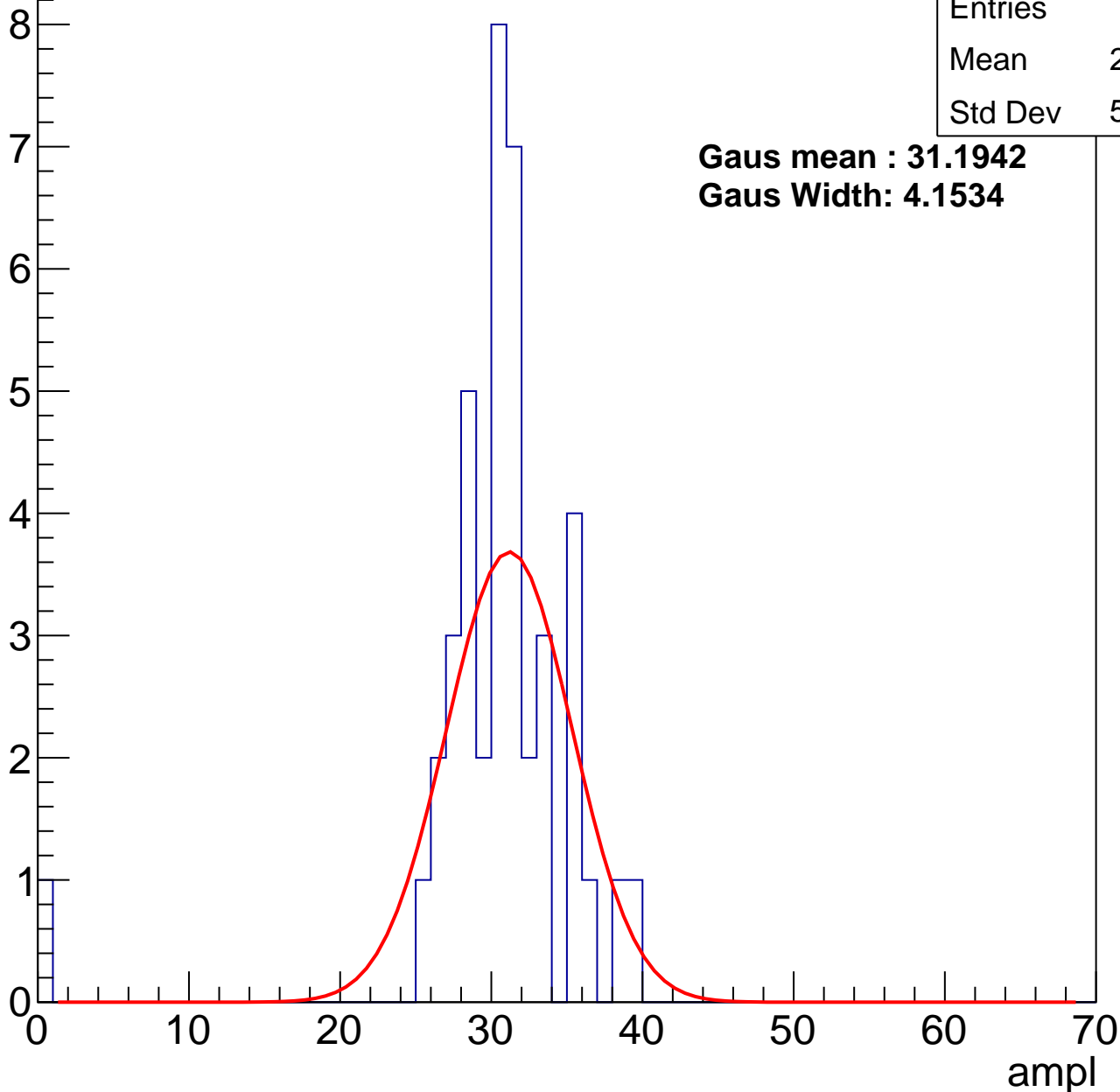
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	29.98
Std Dev	5.693

Gaus mean : 31.1942

Gaus Width: 4.1534



B1L101S, U17-ch15, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	36.76
Std Dev	3.628

Gaus mean : 37.0712

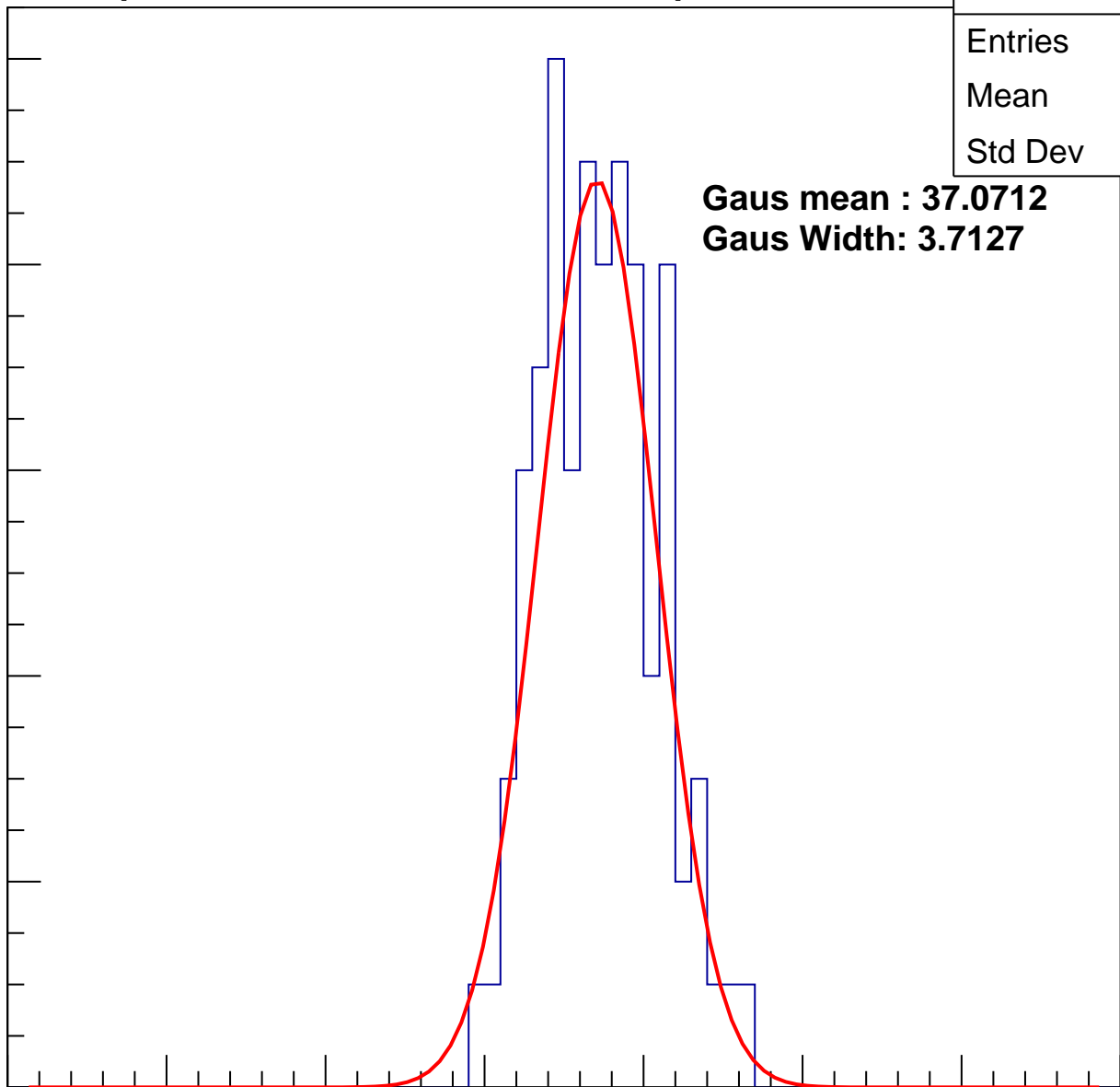
Gaus Width: 3.7127

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch15, adc2

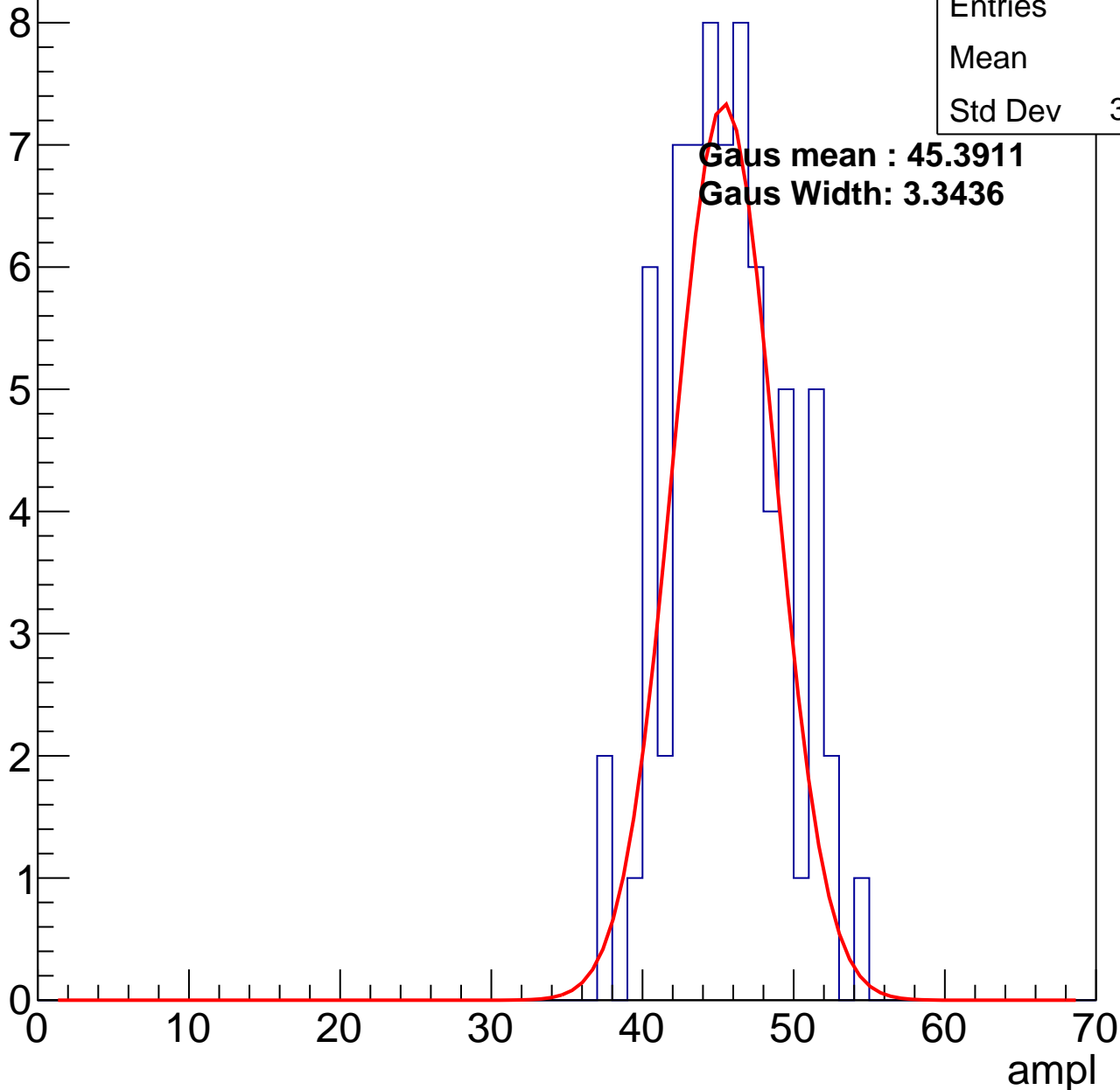
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	45.1
Std Dev	3.682

Gaus mean : 45.3911

Gaus Width: 3.3436

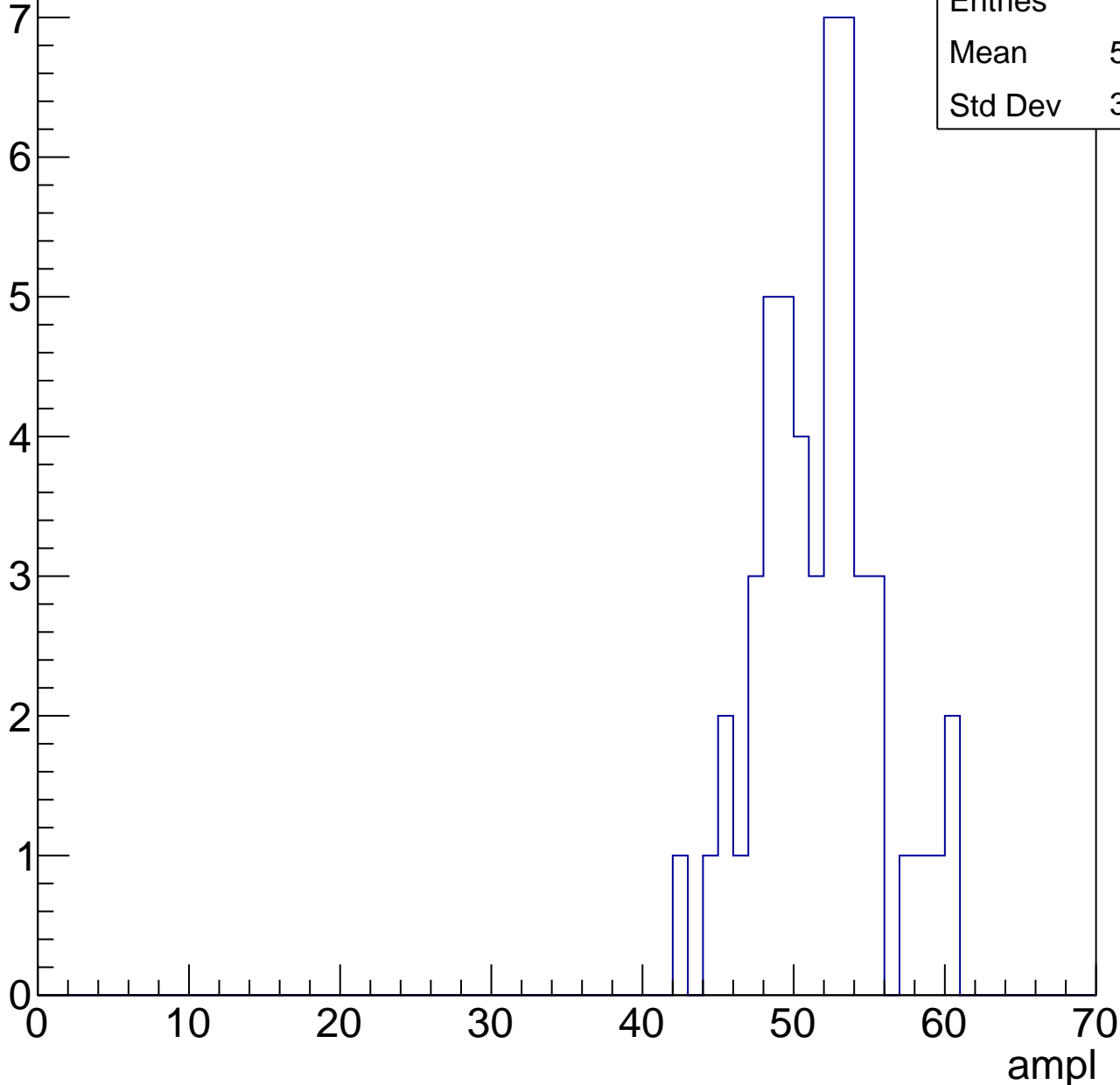


B1L101S, U17-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	51.14
Std Dev	3.909

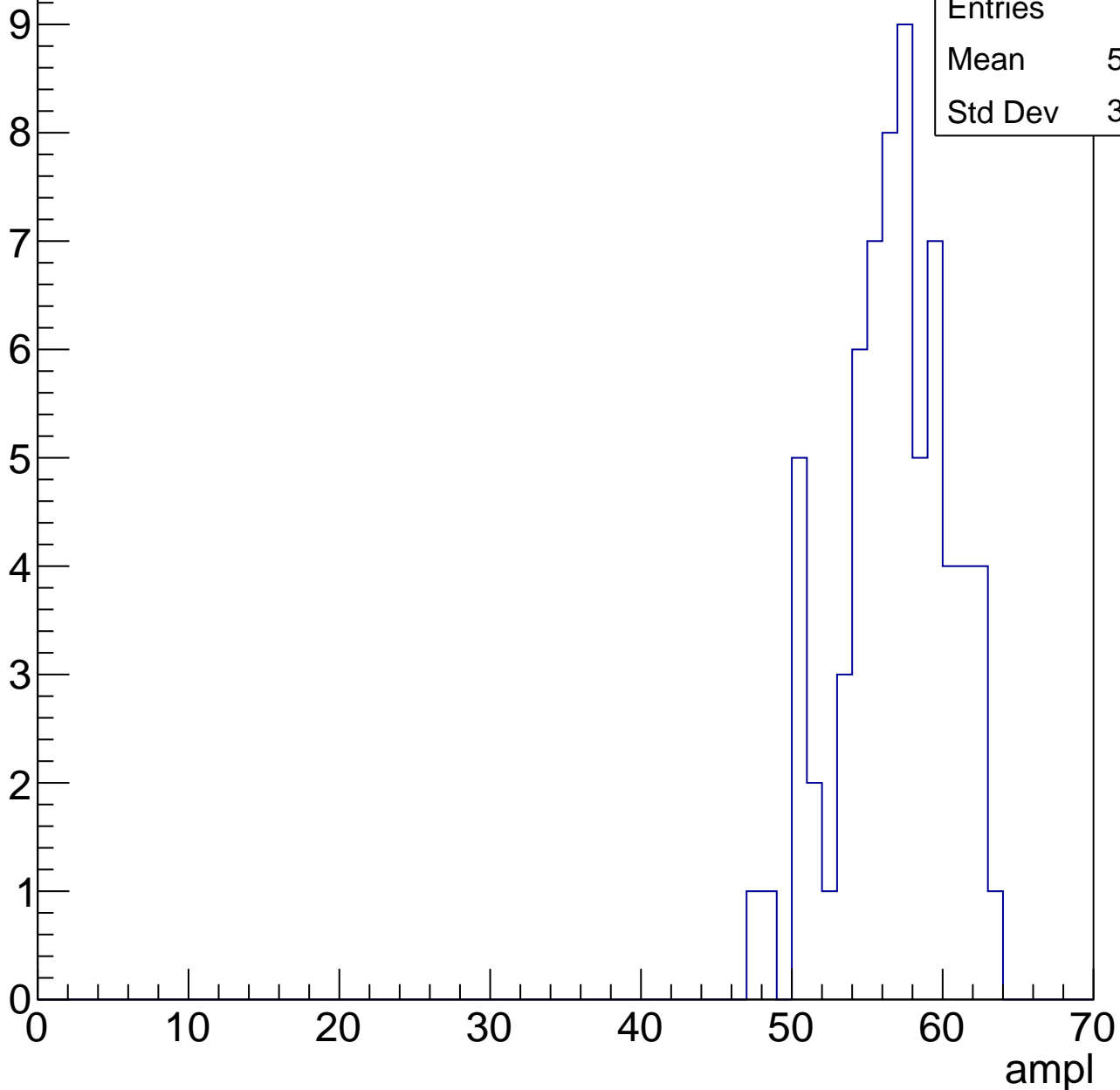


B1L101S, U17-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	56.26
Std Dev	3.624

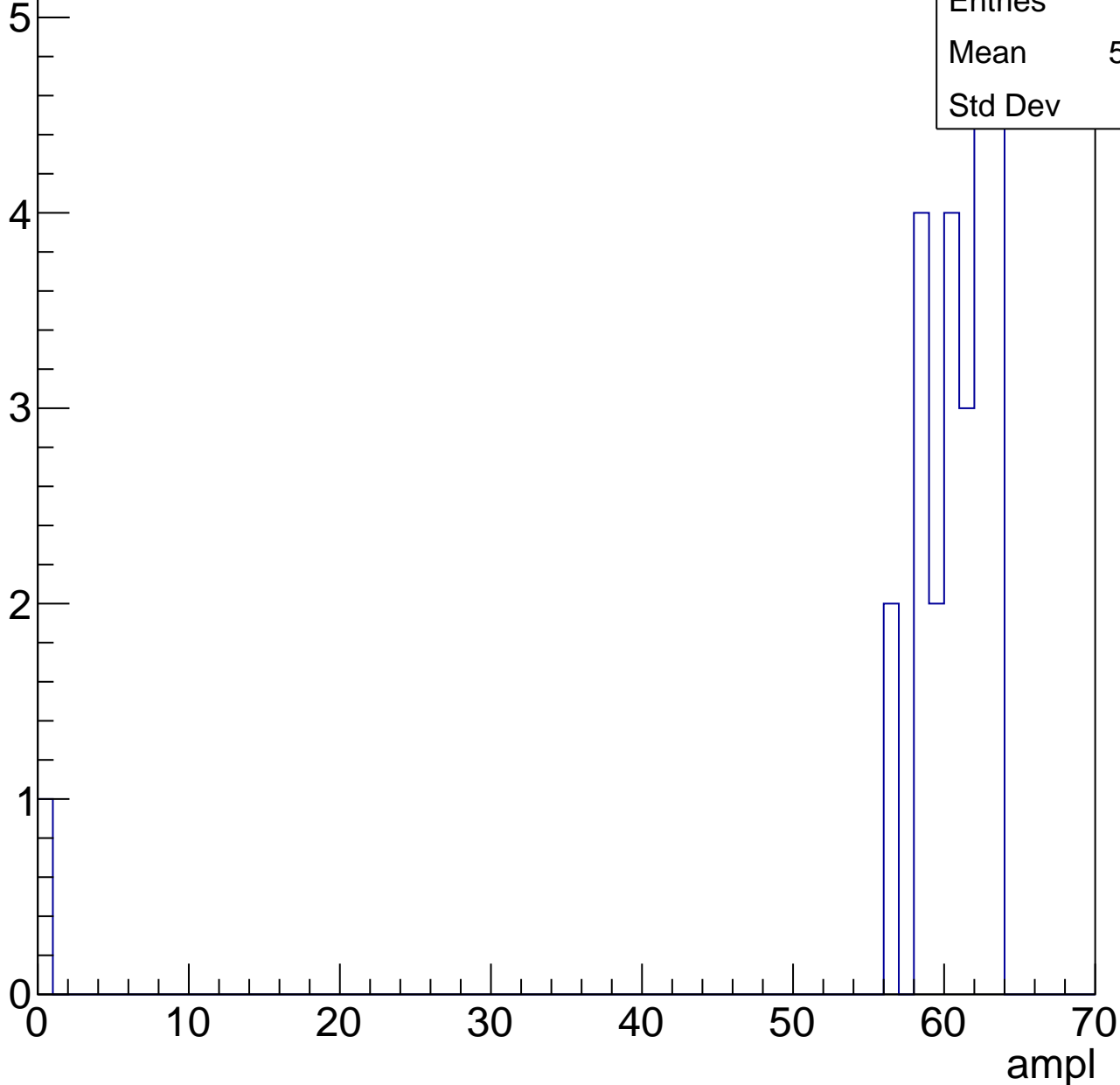


B1L101S, U17-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

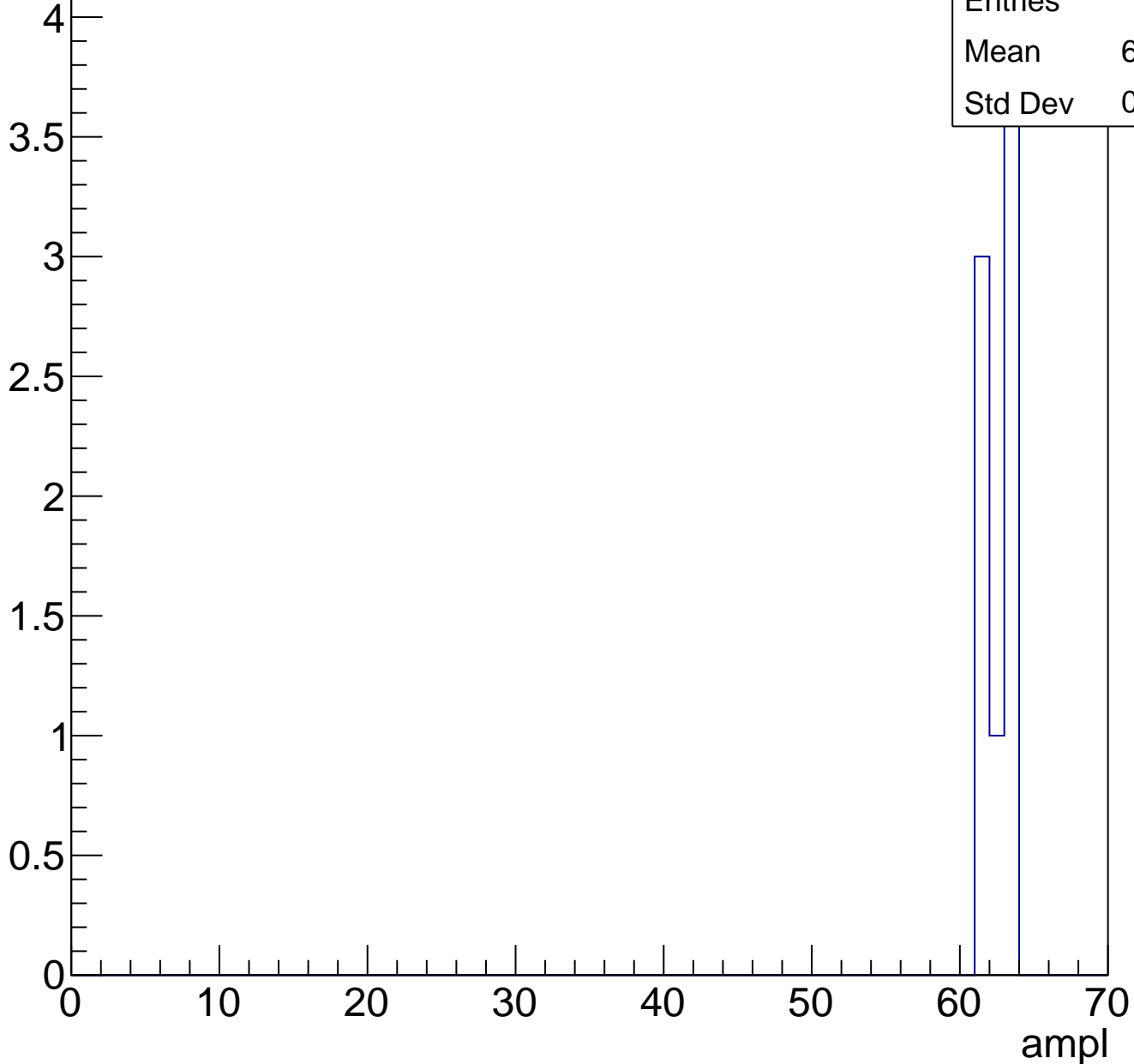
Entries	26
Mean	58.08
Std Dev	11.8



B1L101S, U17-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch16, adc0

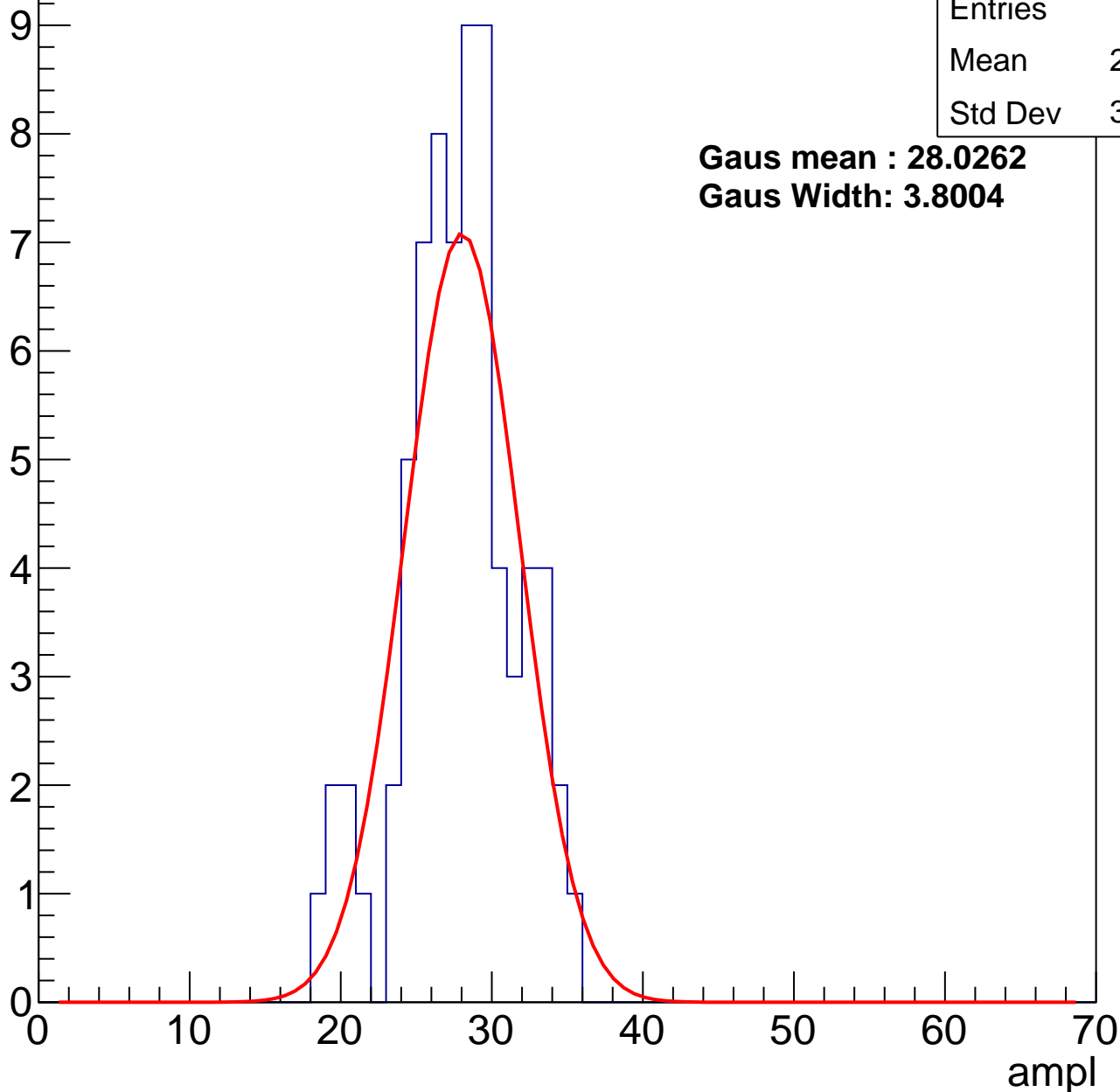
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	27.38
Std Dev	3.717

Gaus mean : 28.0262

Gaus Width: 3.8004



B1L101S, U17-ch16, adc1

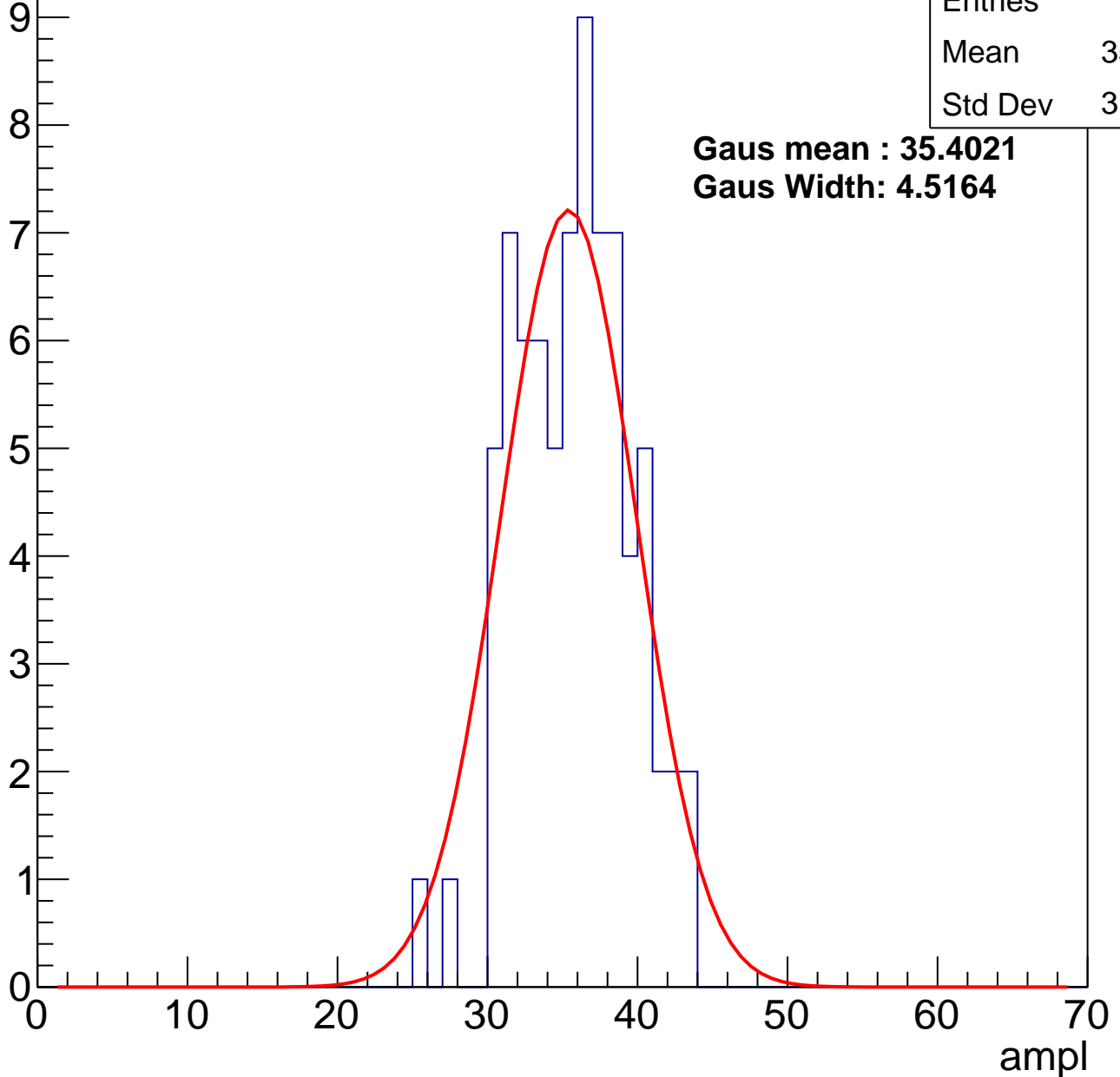
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.28
Std Dev	3.744

Gaus mean : 35.4021

Gaus Width: 4.5164



B1L101S, U17-ch16, adc2

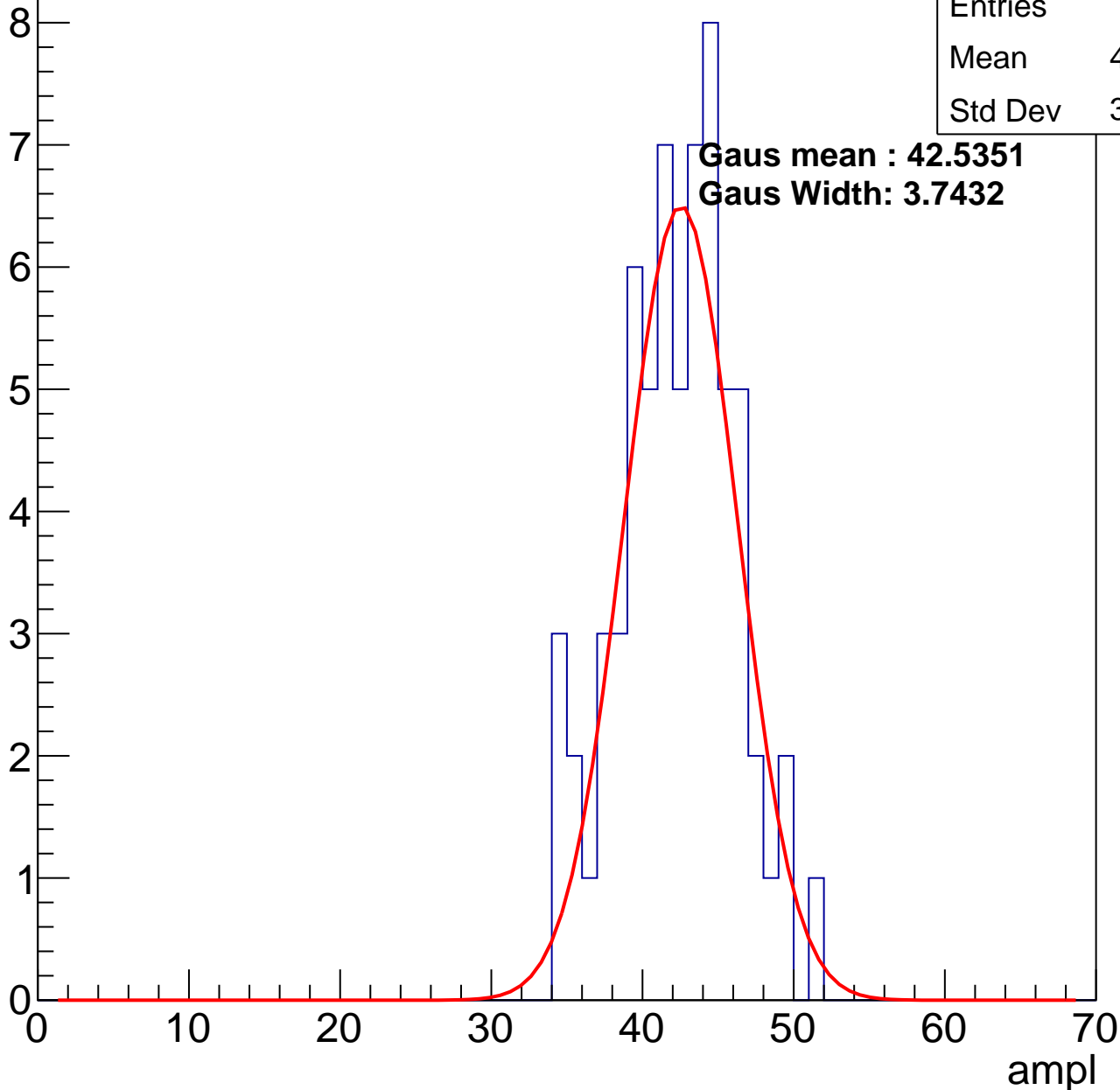
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.86
Std Dev	3.809

Gaus mean : 42.5351

Gaus Width: 3.7432

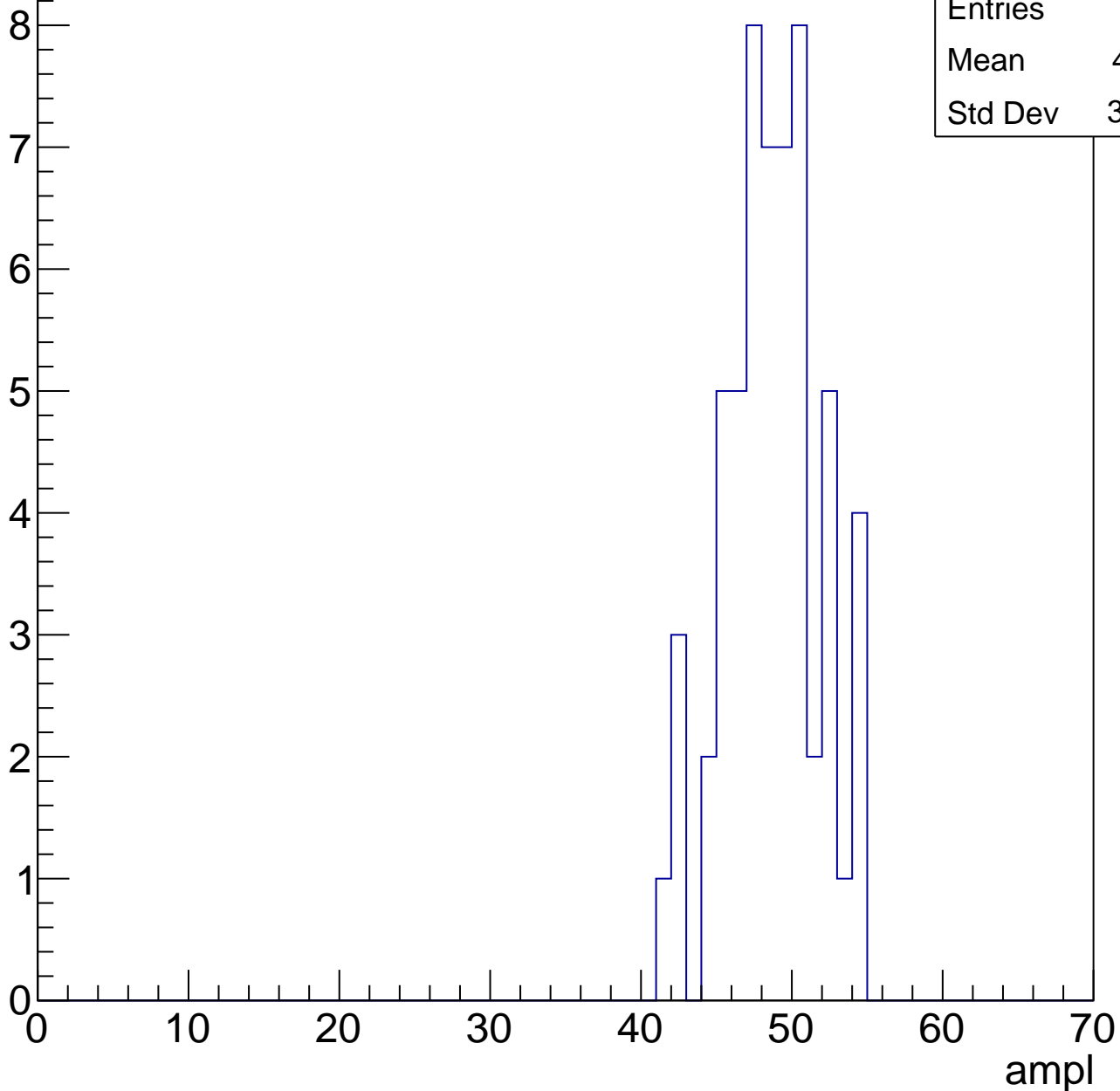


B1L101S, U17-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.21
Std Dev	3.123



B1L101S, U17-ch16, adc4

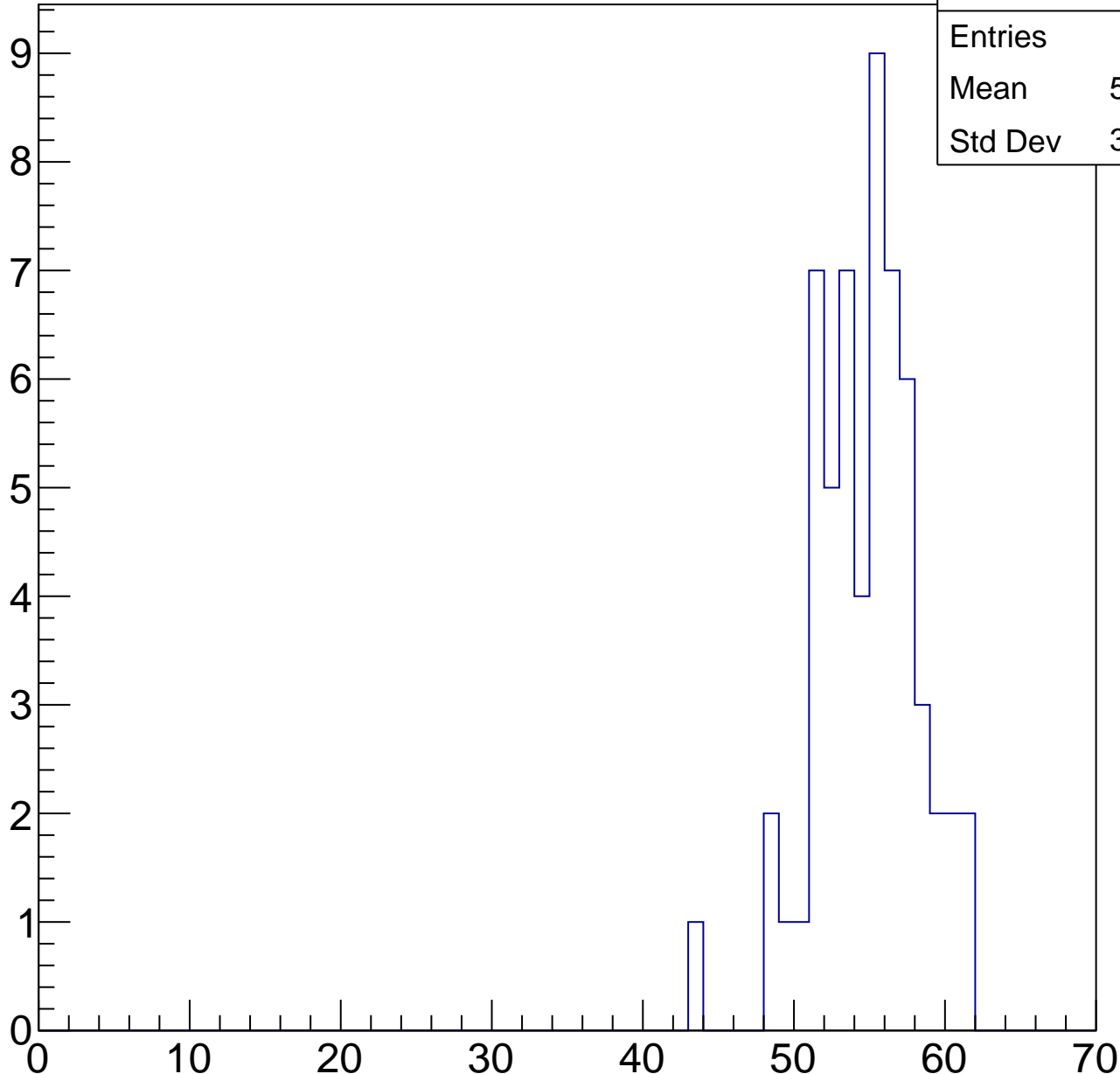
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	54.32
Std Dev	3.387

ampl

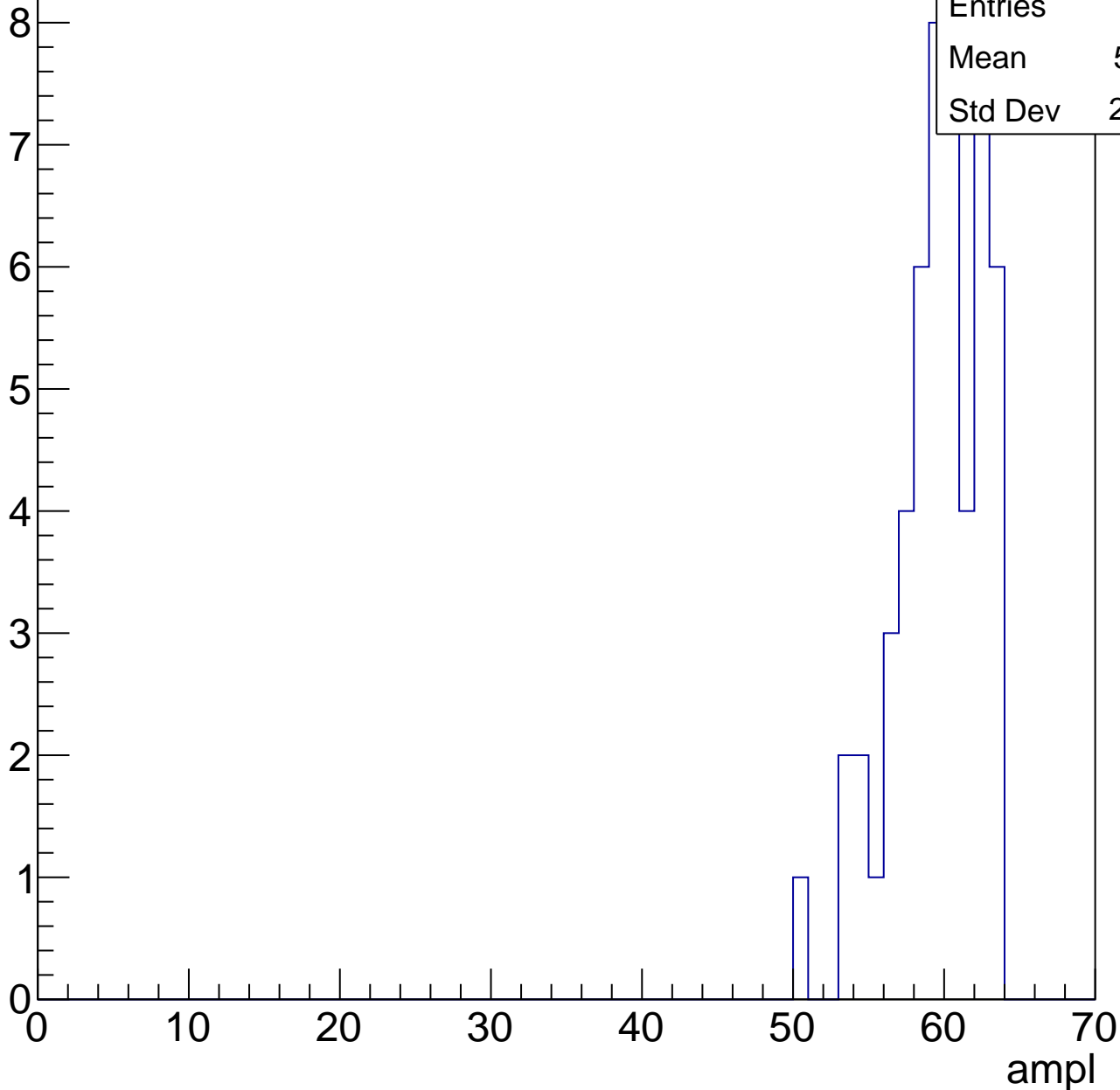


B1L101S, U17-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

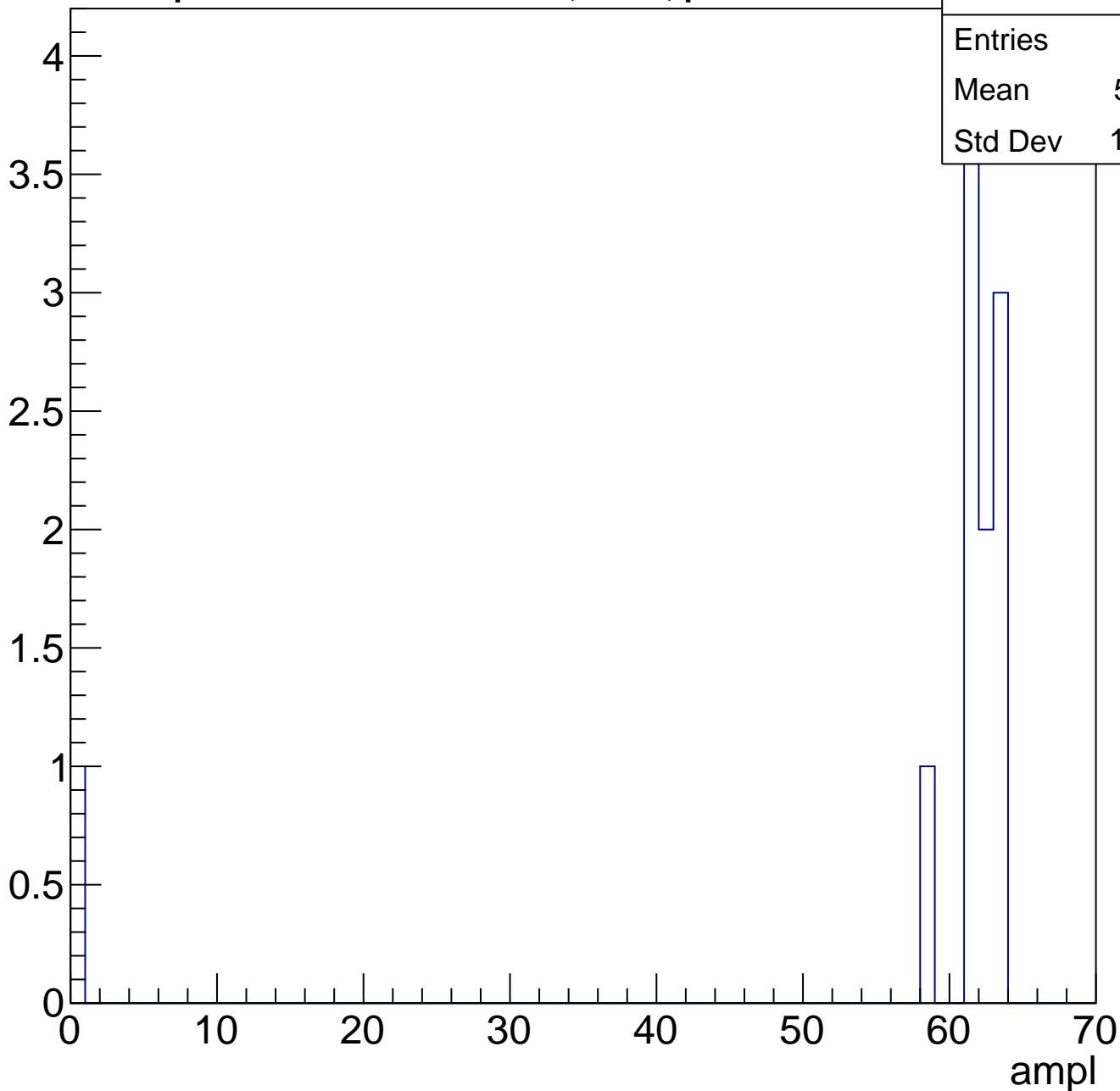
Entries	53
Mean	59.11
Std Dev	2.944



B1L101S, U17-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	11
Mean	55.91
Std Dev	17.73

B1L101S, U17-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U17-ch17, adc0

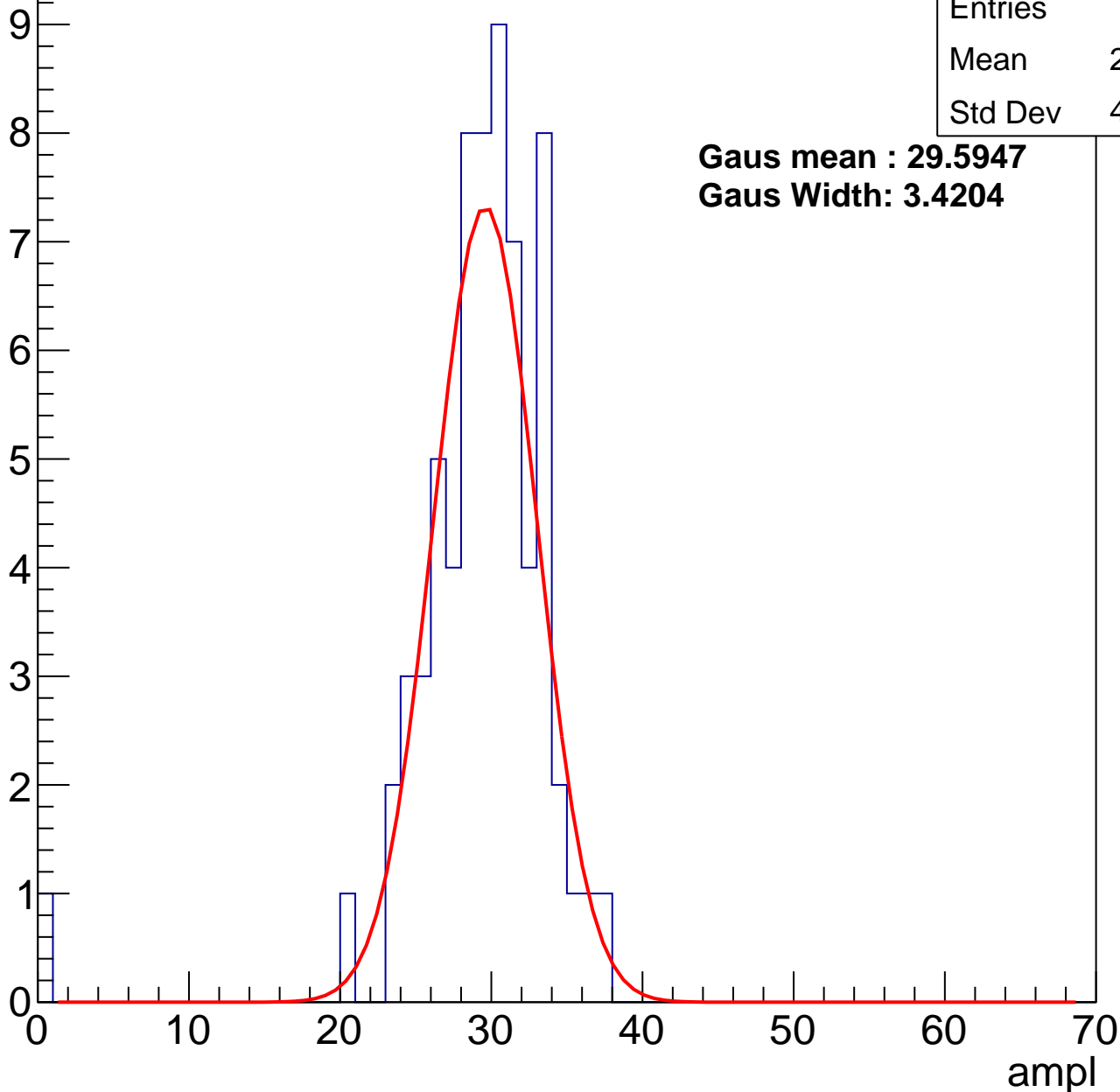
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.85
Std Dev	4.827

Gaus mean : 29.5947

Gaus Width: 3.4204



B1L101S, U17-ch17, adc1

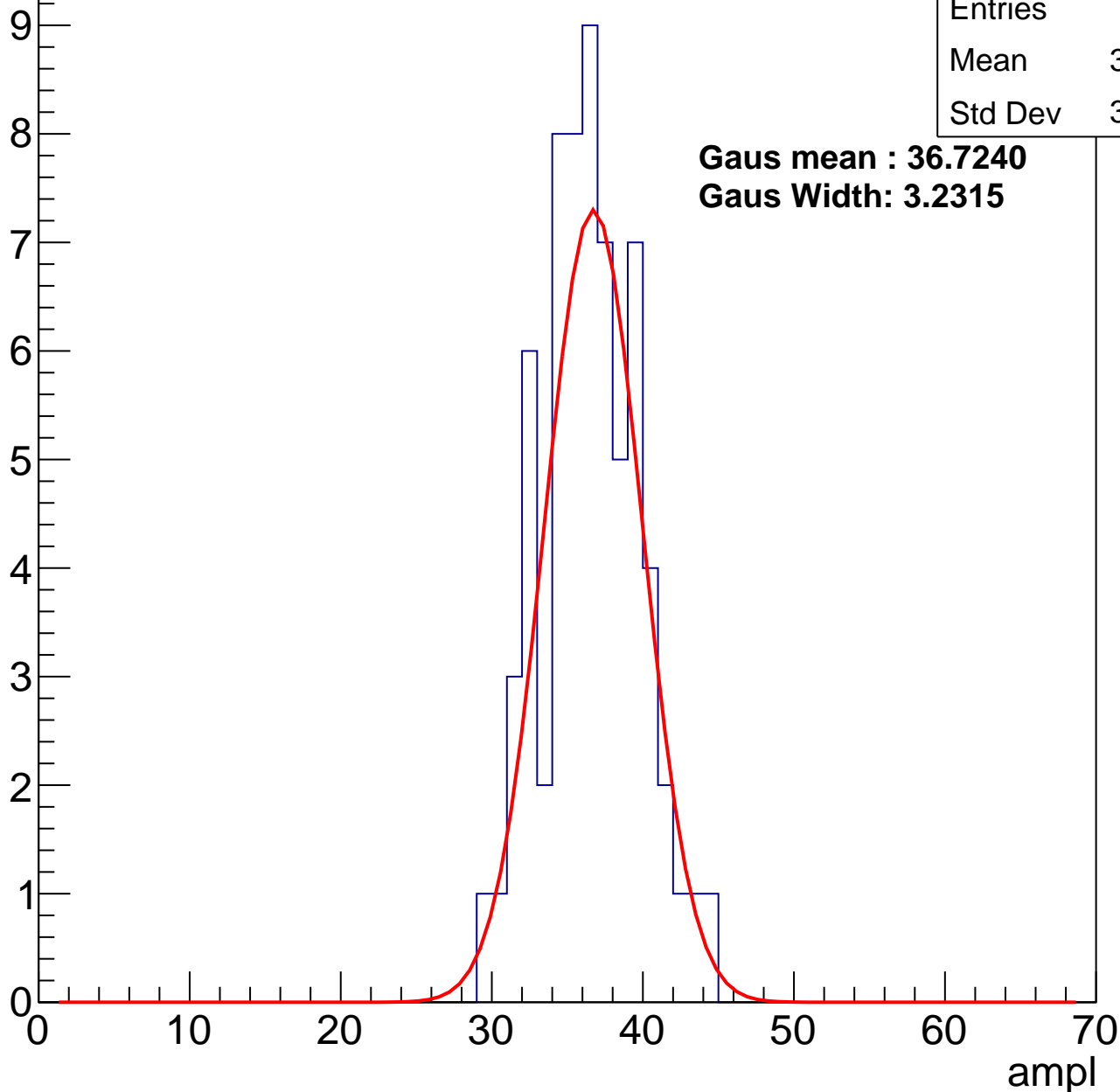
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.05
Std Dev	3.179

Gaus mean : 36.7240

Gaus Width: 3.2315



B1L101S, U17-ch17, adc2

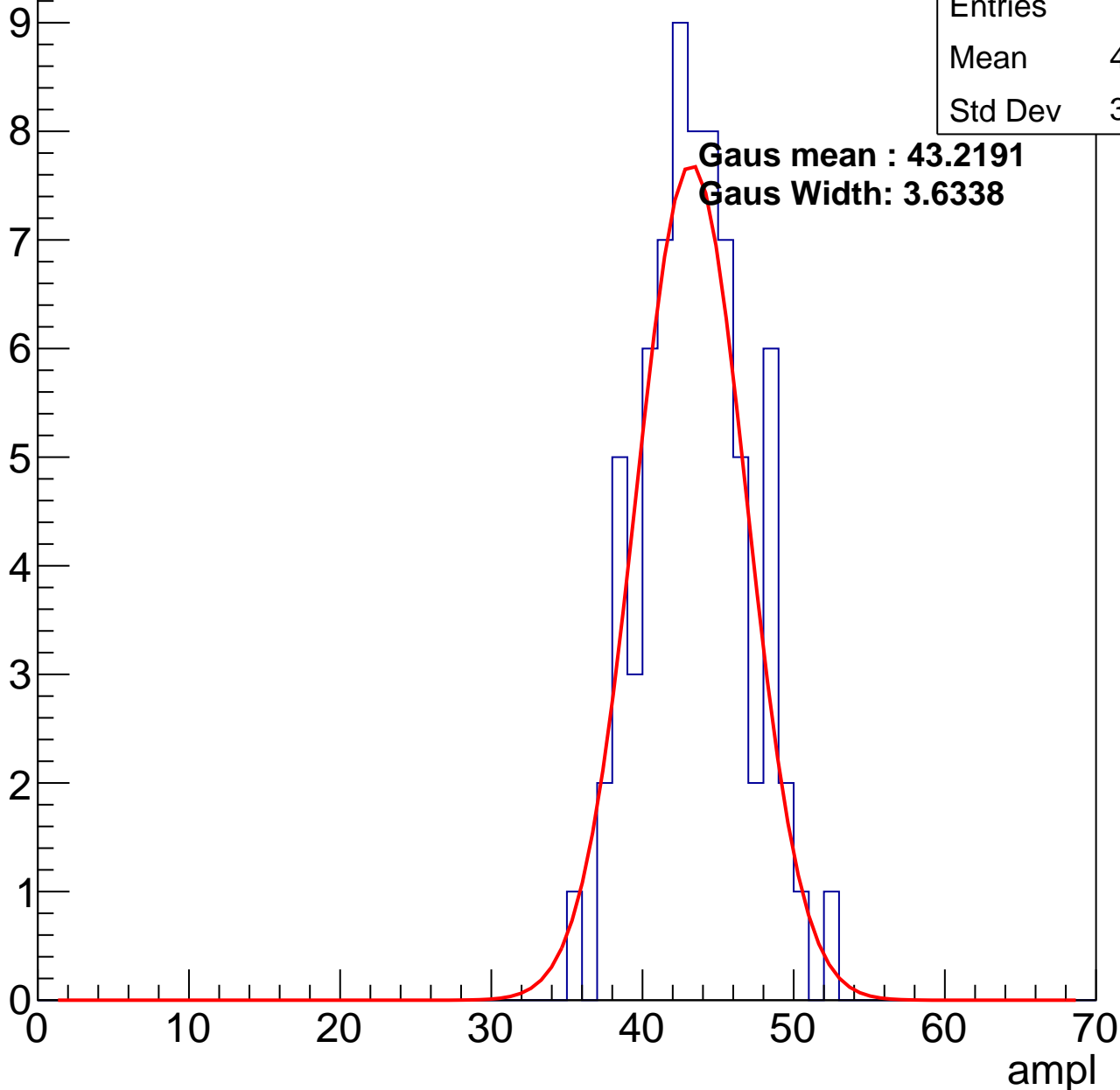
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.07
Std Dev	3.442

Gaus mean : 43.2191

Gaus Width: 3.6338

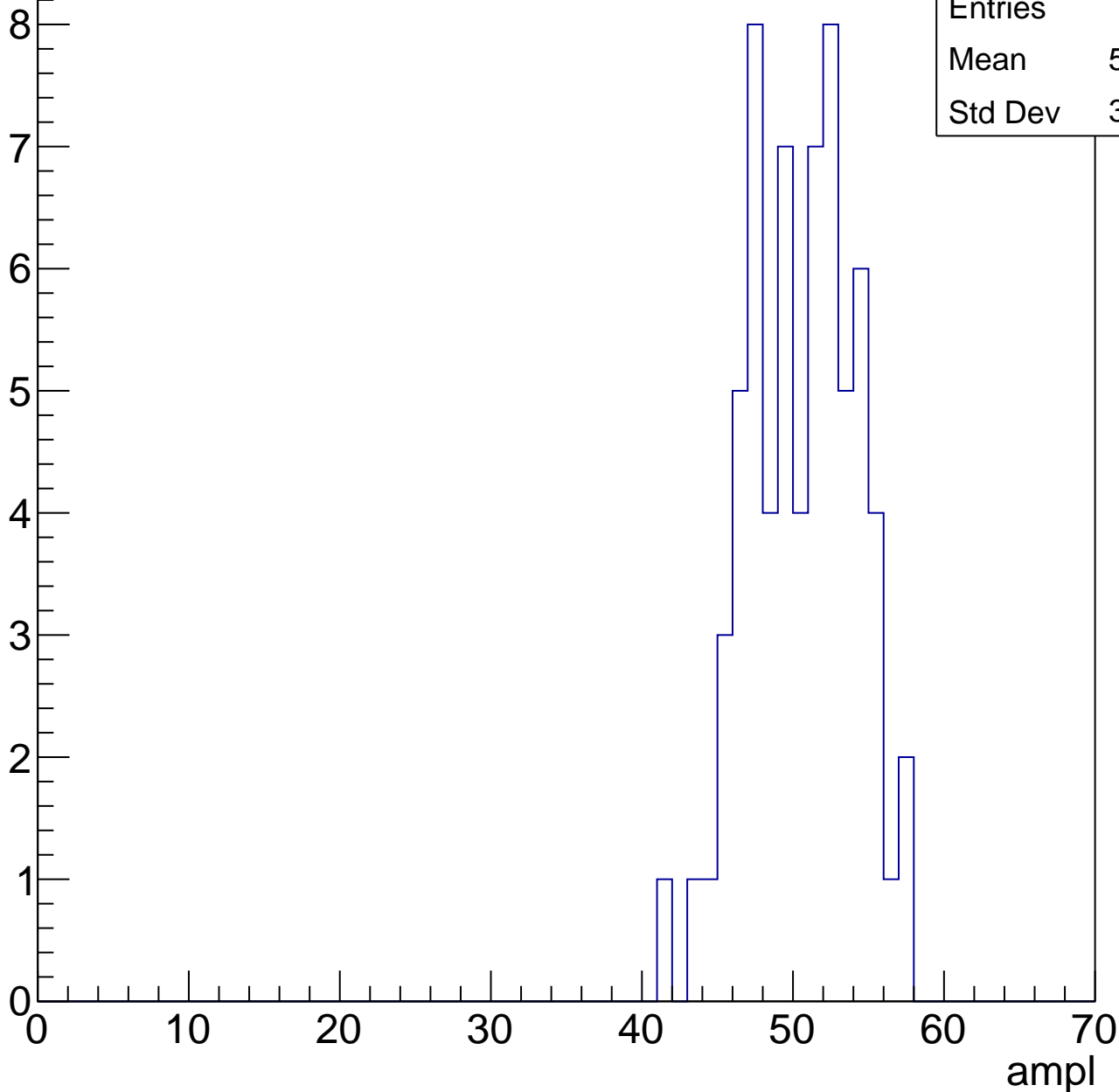


B1L101S, U17-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.09
Std Dev	3.519

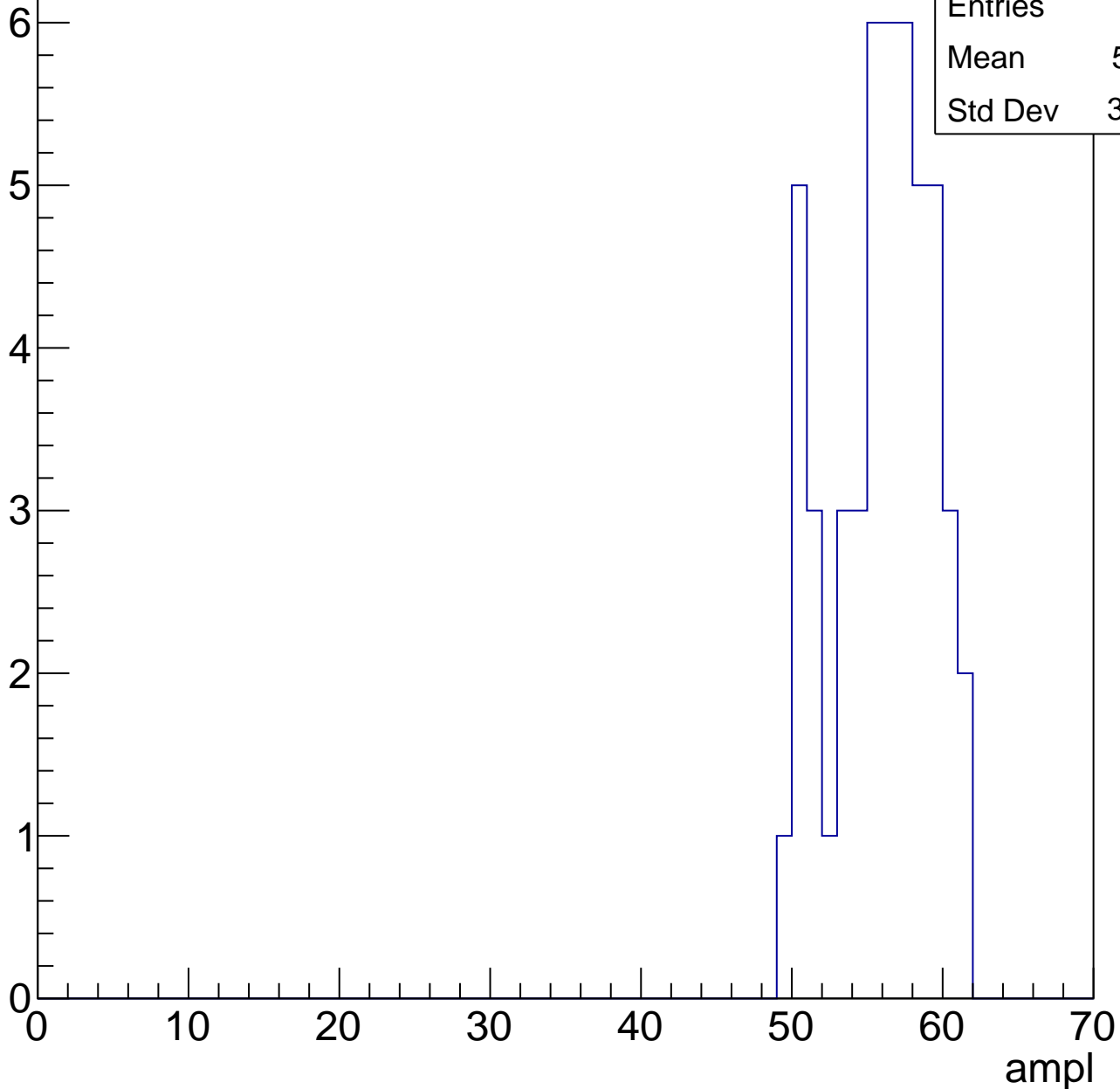


B1L101S, U17-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.51
Std Dev	3.265

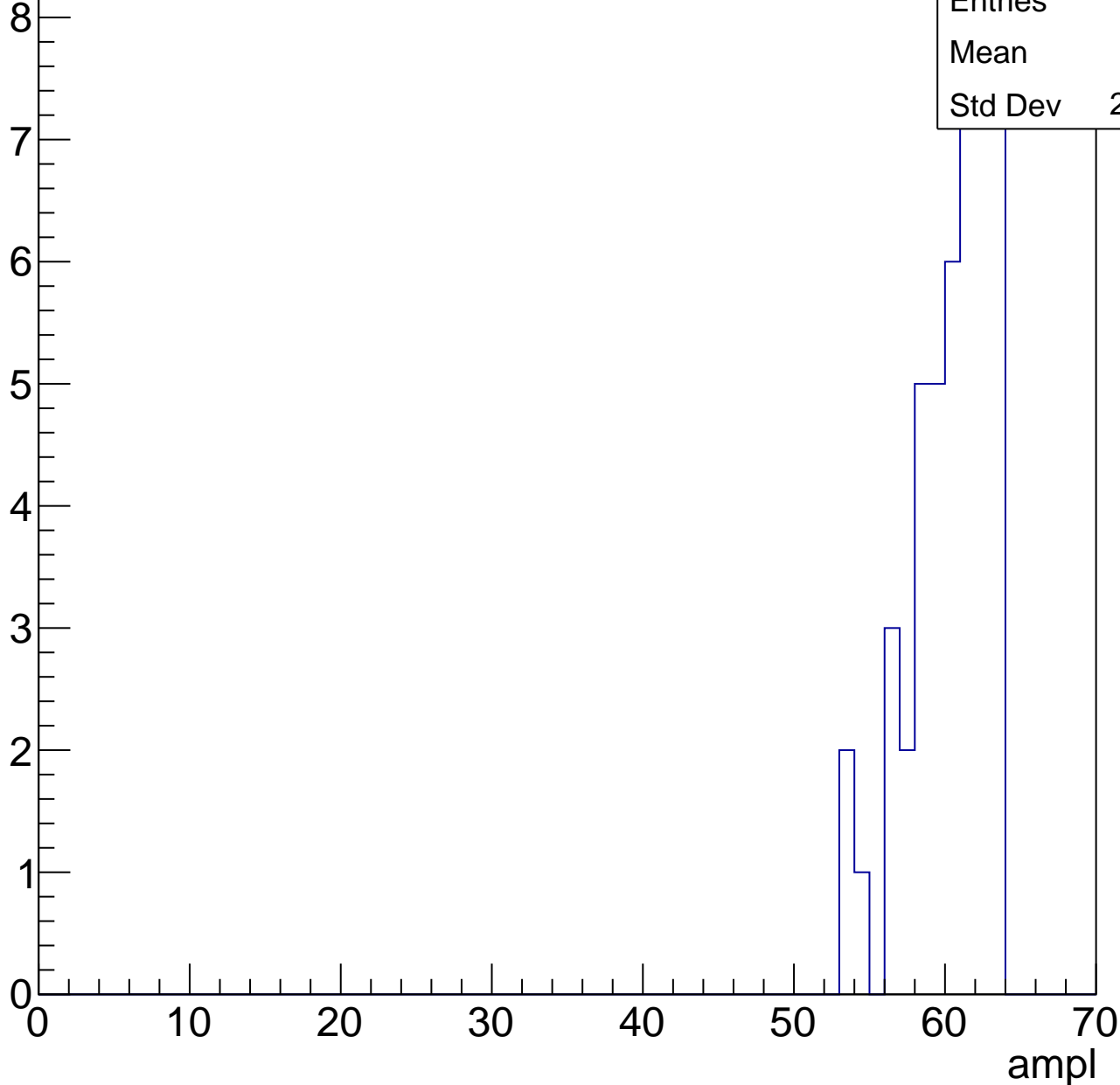


B1L101S, U17-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

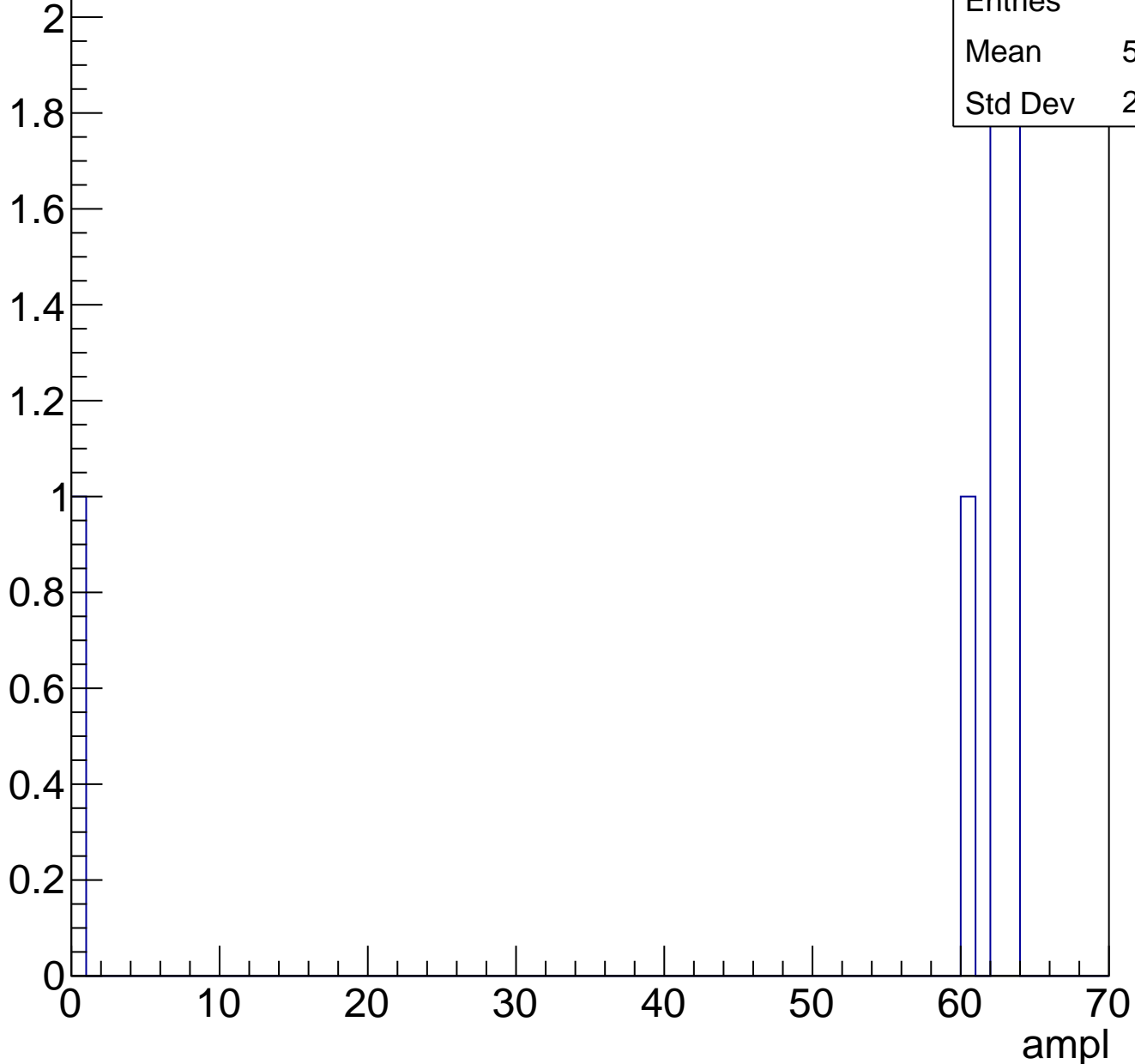
Entries	48
Mean	59.9
Std Dev	2.648



B1L101S, U17-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch18, adc0

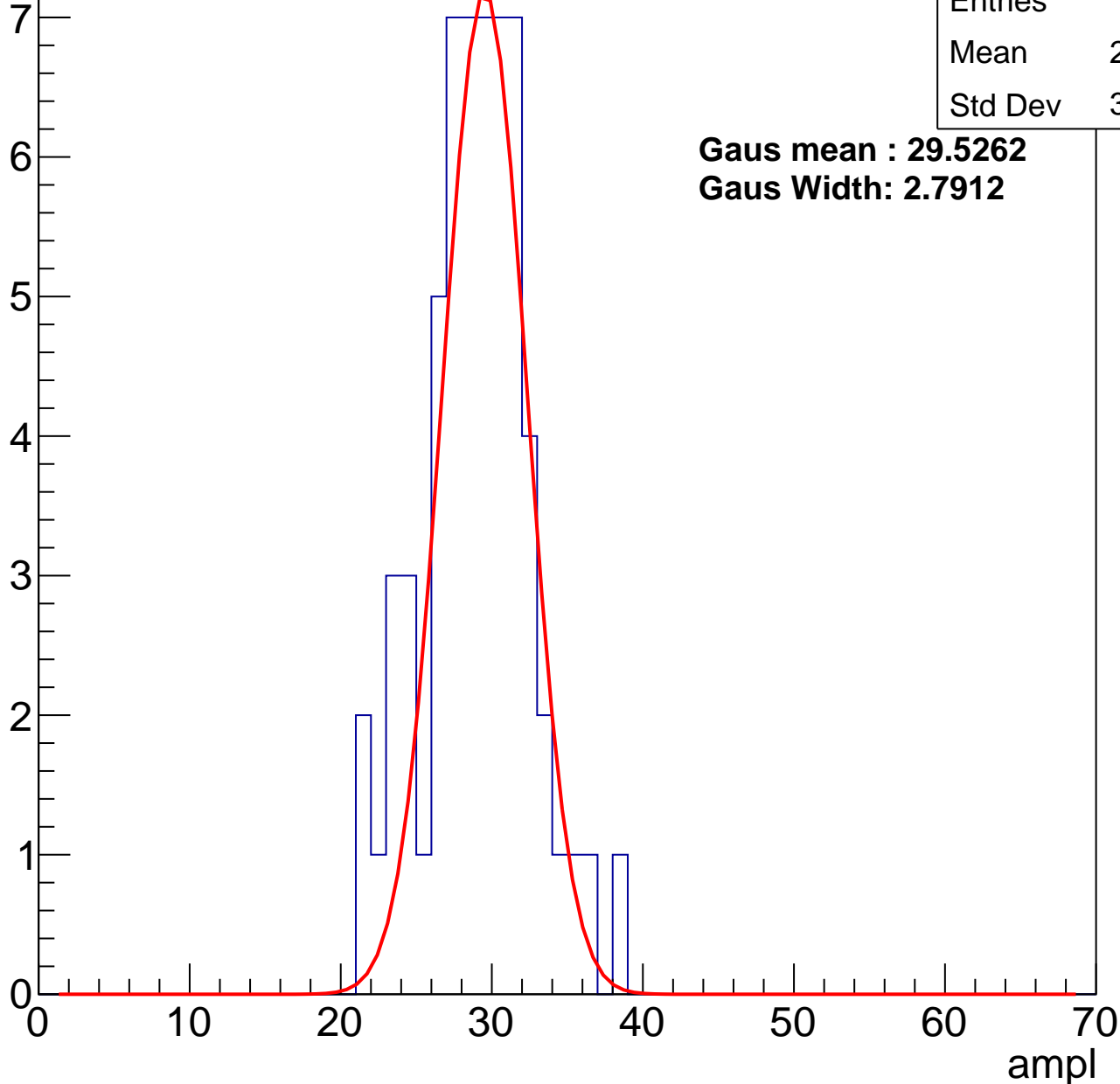
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.53
Std Dev	3.505

Gaus mean : 29.5262

Gaus Width: 2.7912



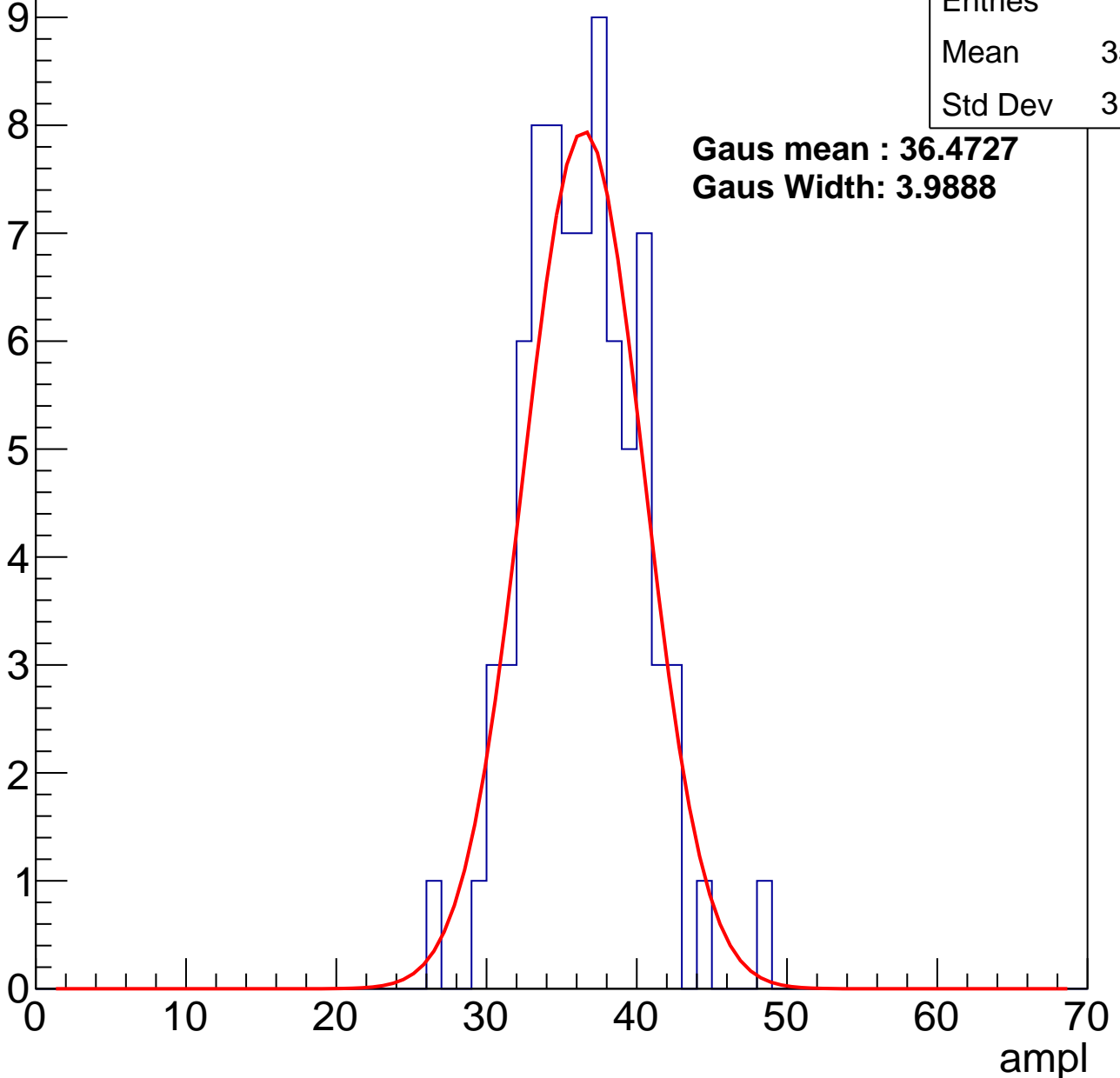
B1L101S, U17-ch18, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	35.95
Std Dev	3.765

Gaus mean : 36.4727
Gaus Width: 3.9888



B1L101S, U17-ch18, adc2

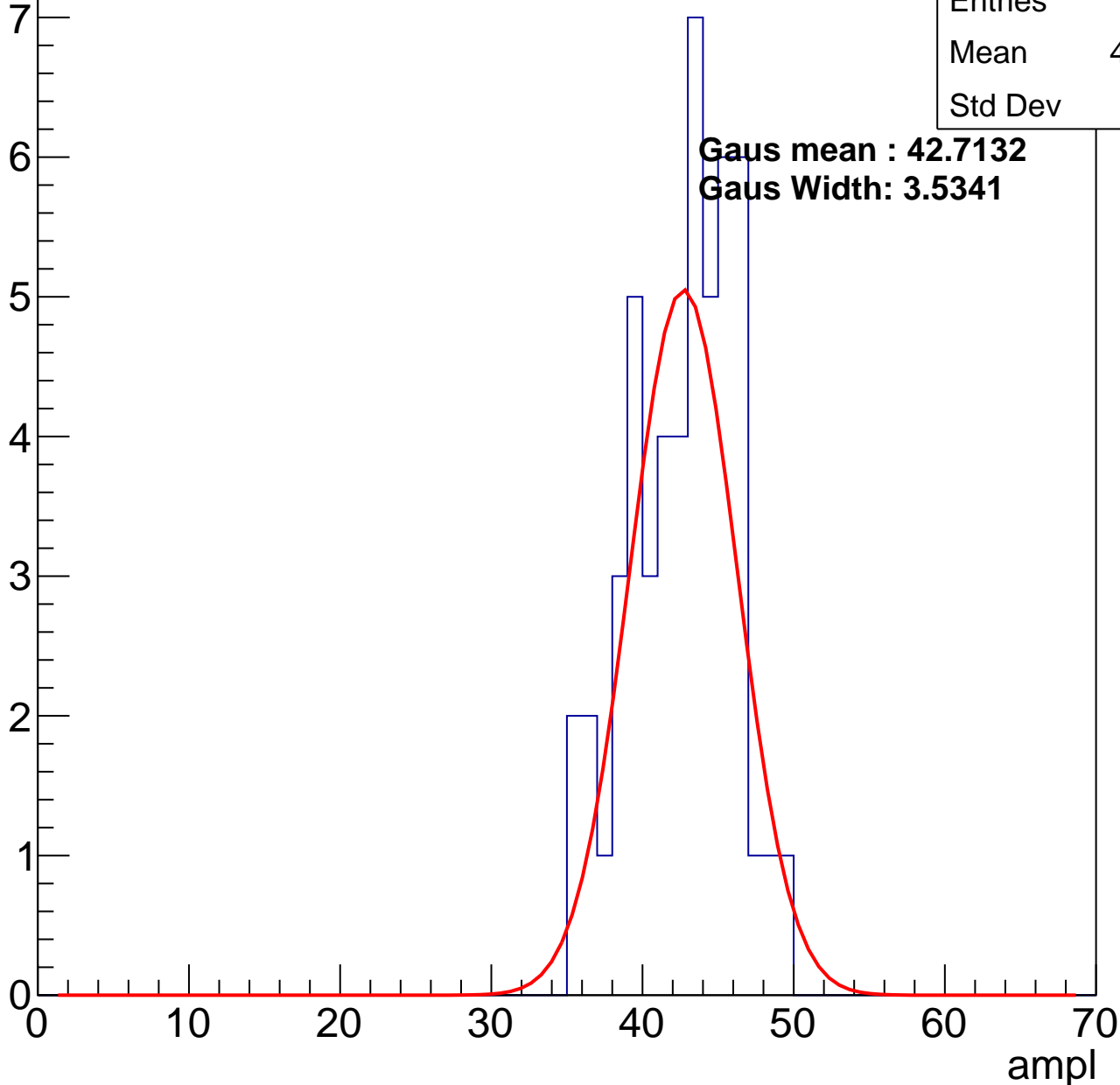
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.18
Std Dev	3.4

Gaus mean : 42.7132

Gaus Width: 3.5341



B1L101S, U17-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	49
Std Dev	3.883

Entry

10

8

6

4

2

0

0

10

20

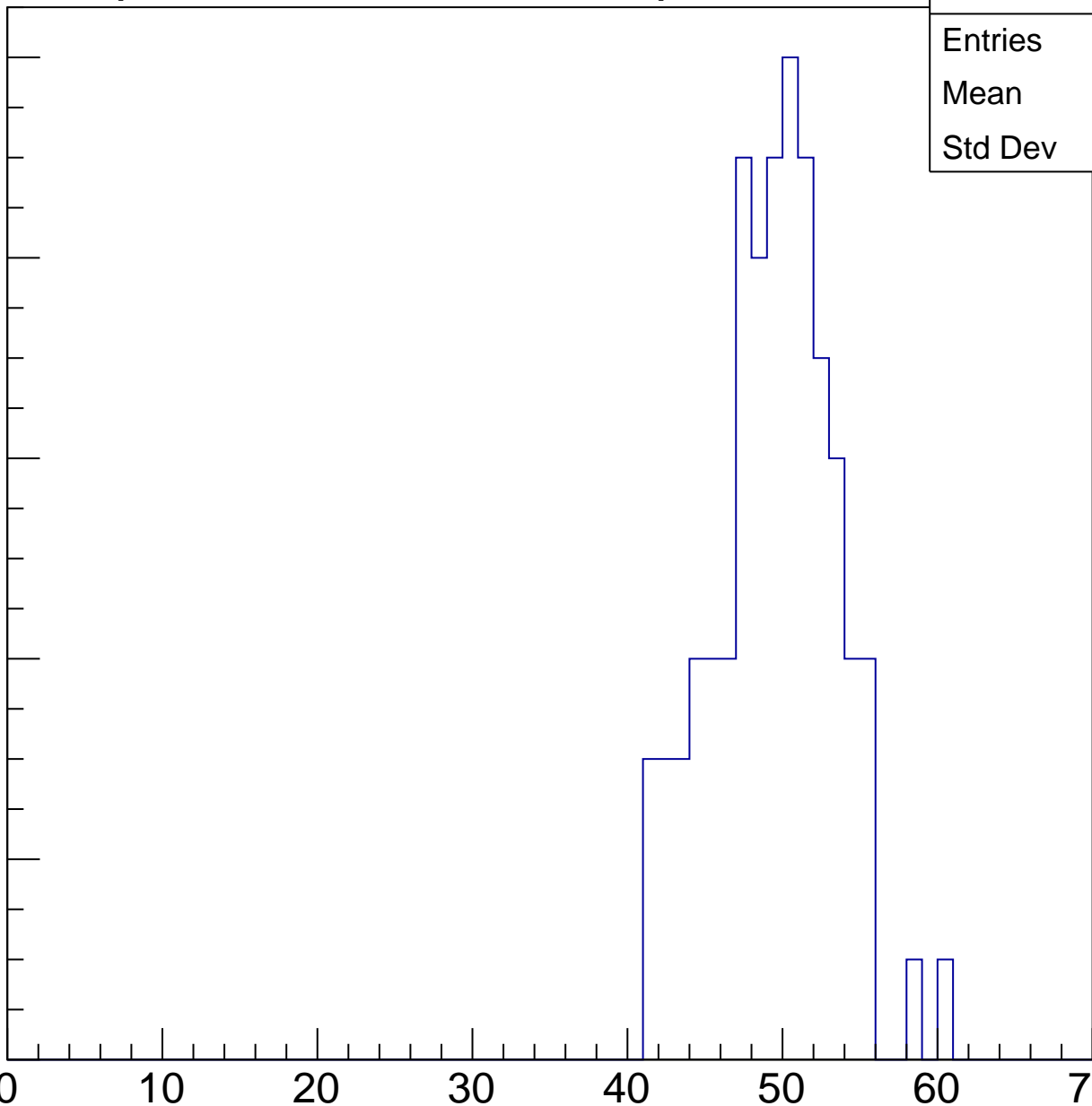
30

40

50

60

ampl

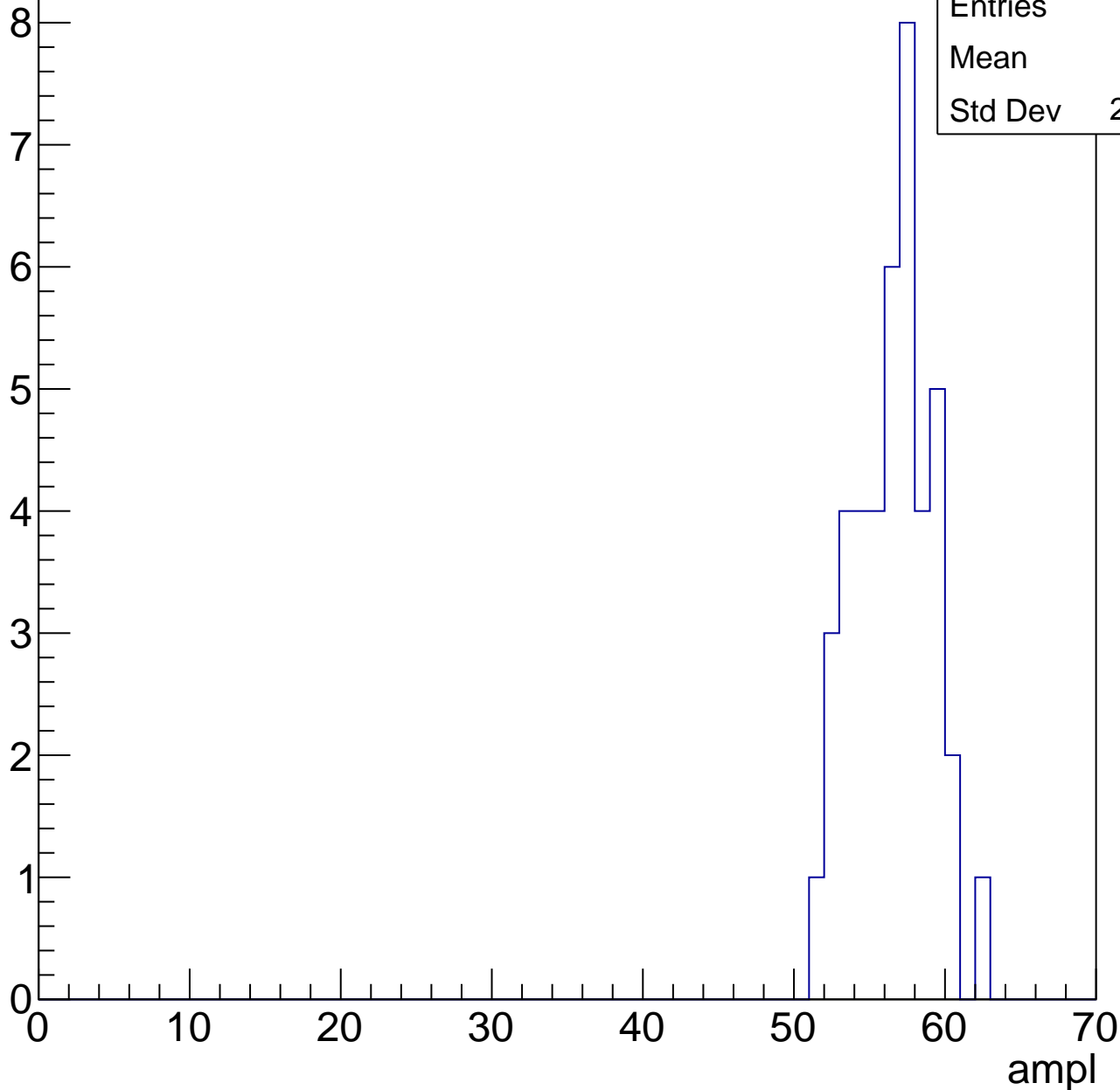


B1L101S, U17-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

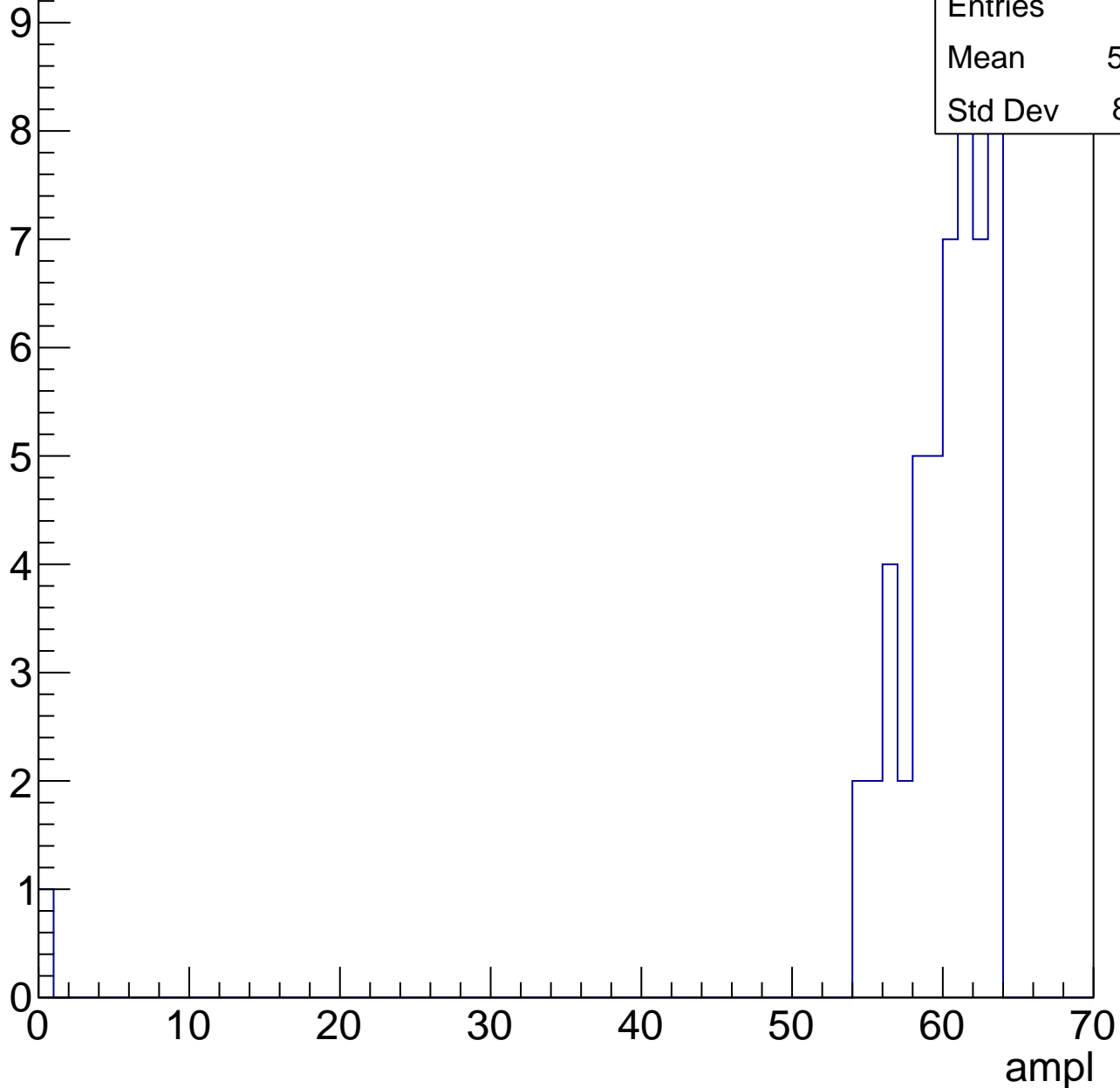
Entries	42
Mean	56.1
Std Dev	2.515



B1L101S, U17-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L101S, U17-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch19, adc0

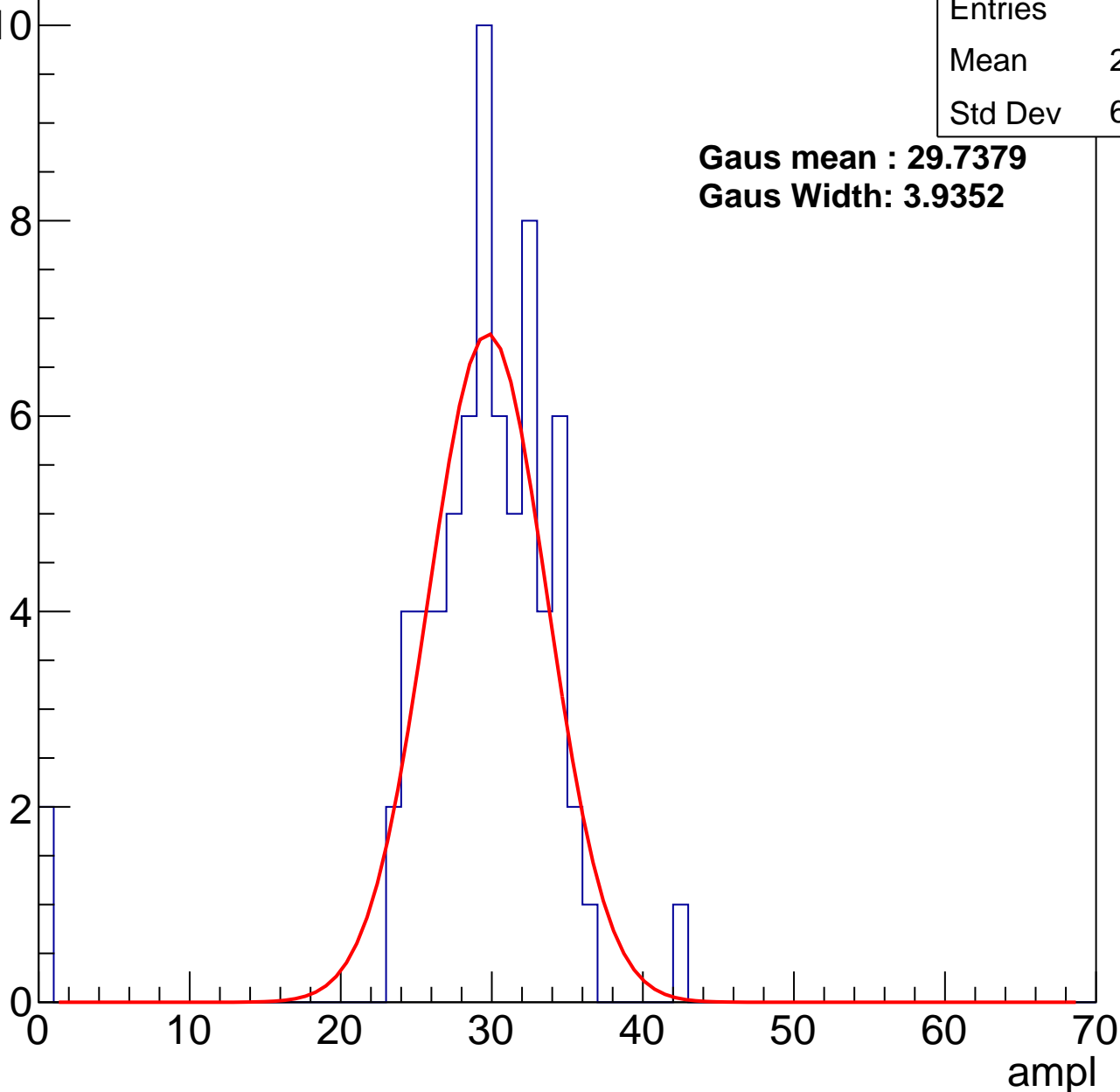
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.77
Std Dev	6.069

Gaus mean : 29.7379

Gaus Width: 3.9352



B1L101S, U17-ch19, adc1

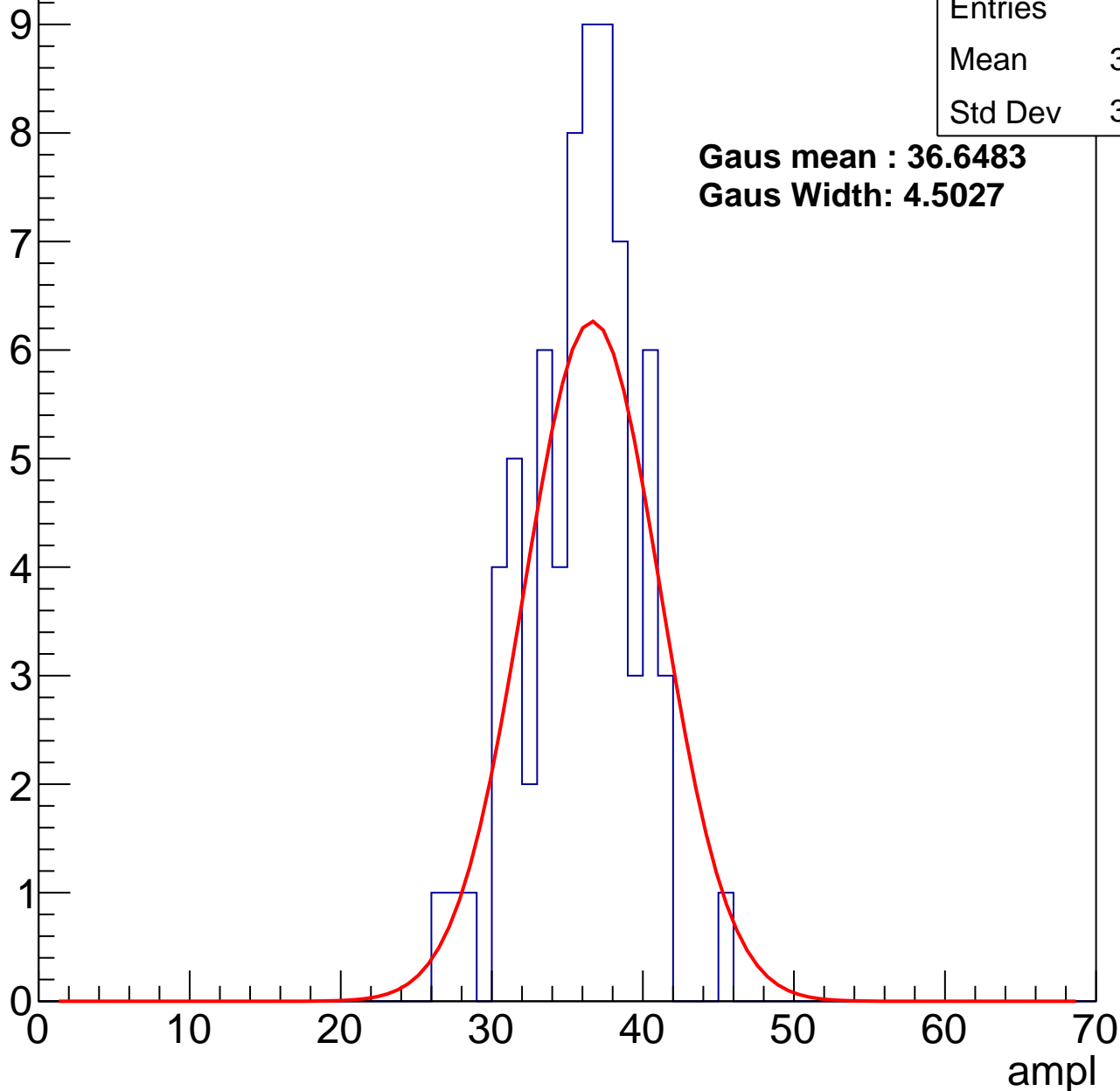
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.46
Std Dev	3.624

Gaus mean : 36.6483

Gaus Width: 4.5027



B1L101S, U17-ch19, adc2

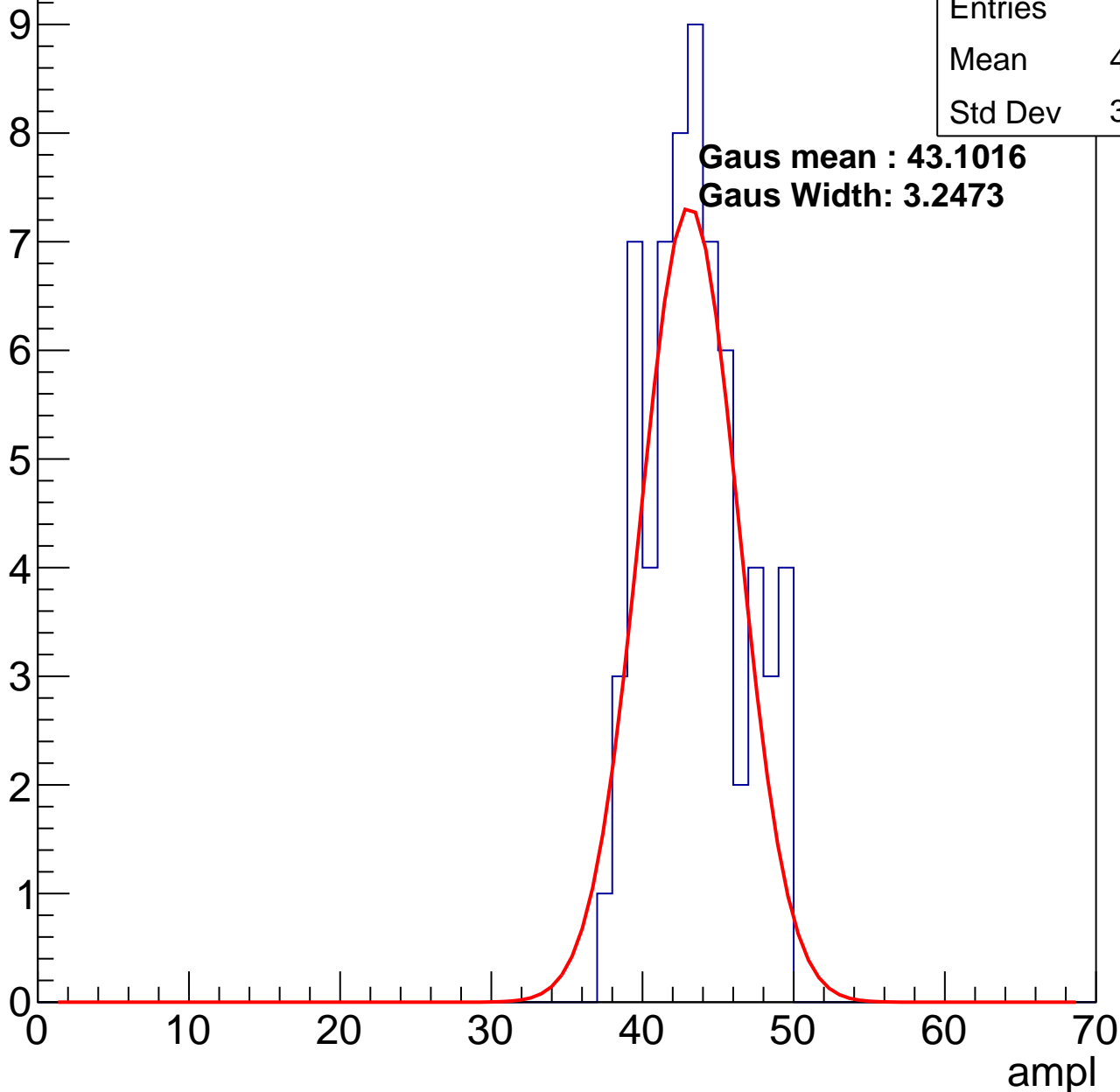
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.95
Std Dev	3.105

Gaus mean : 43.1016

Gaus Width: 3.2473

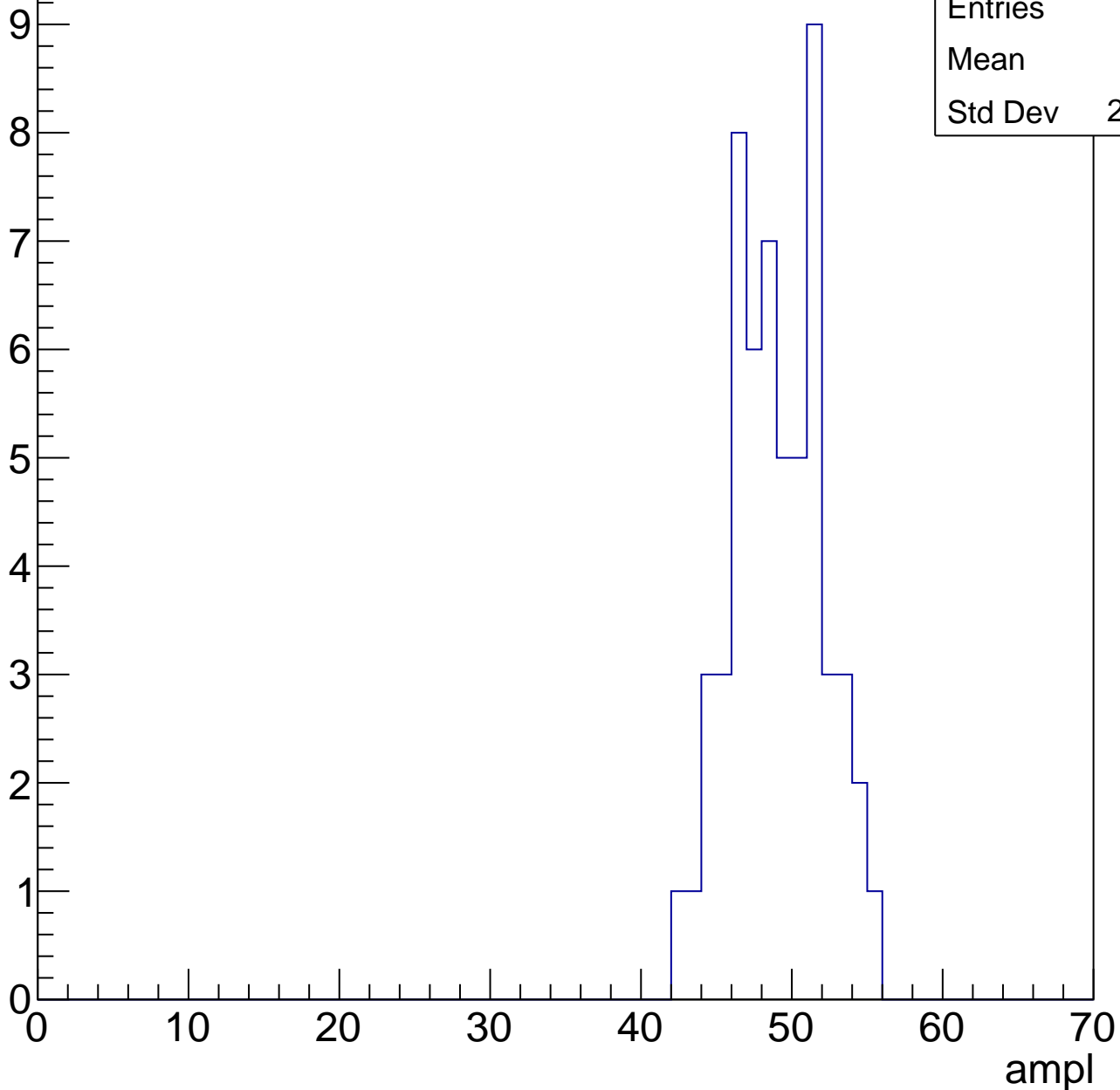


B1L101S, U17-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.6
Std Dev	2.973

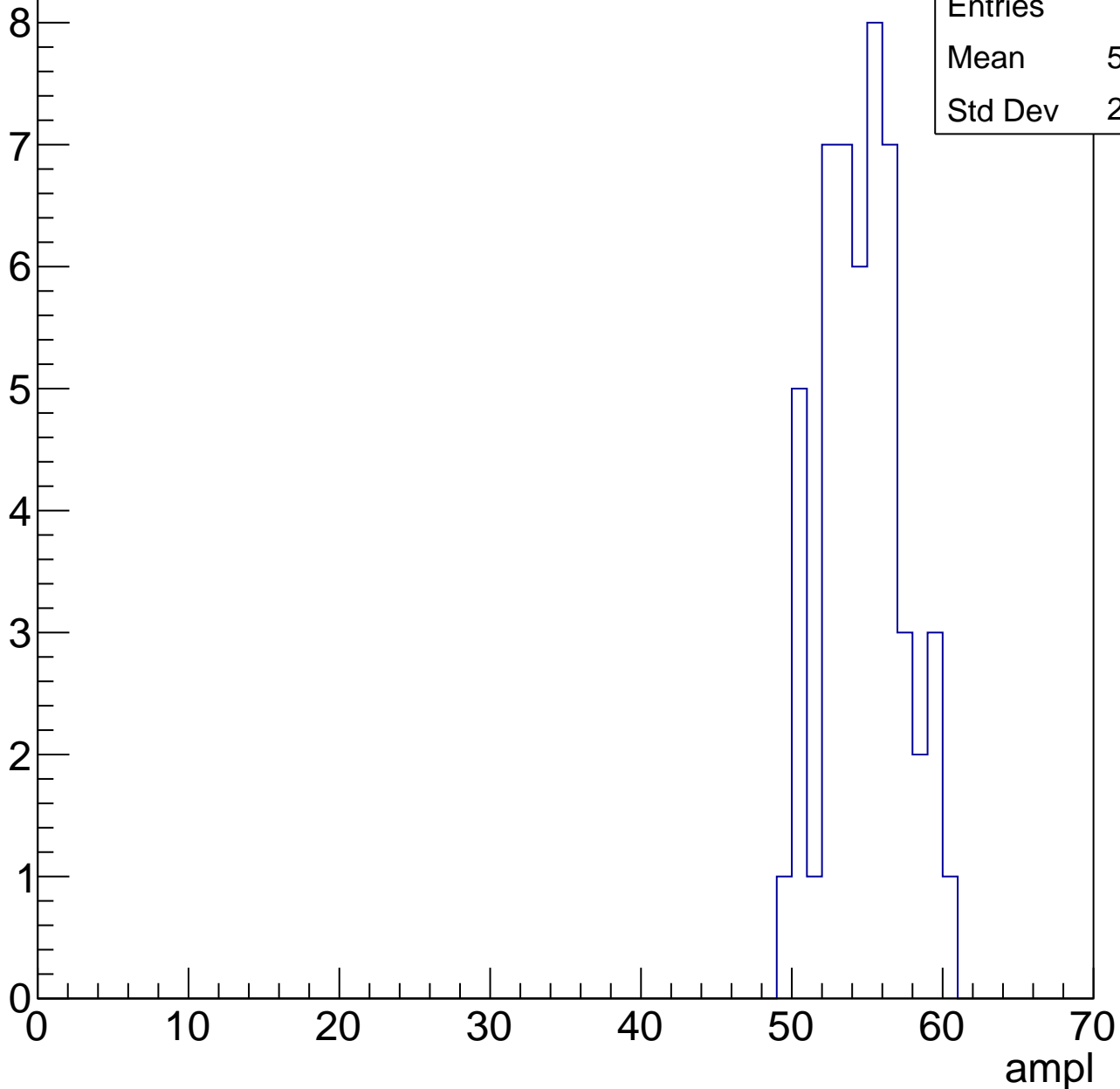


B1L101S, U17-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	54.22
Std Dev	2.629

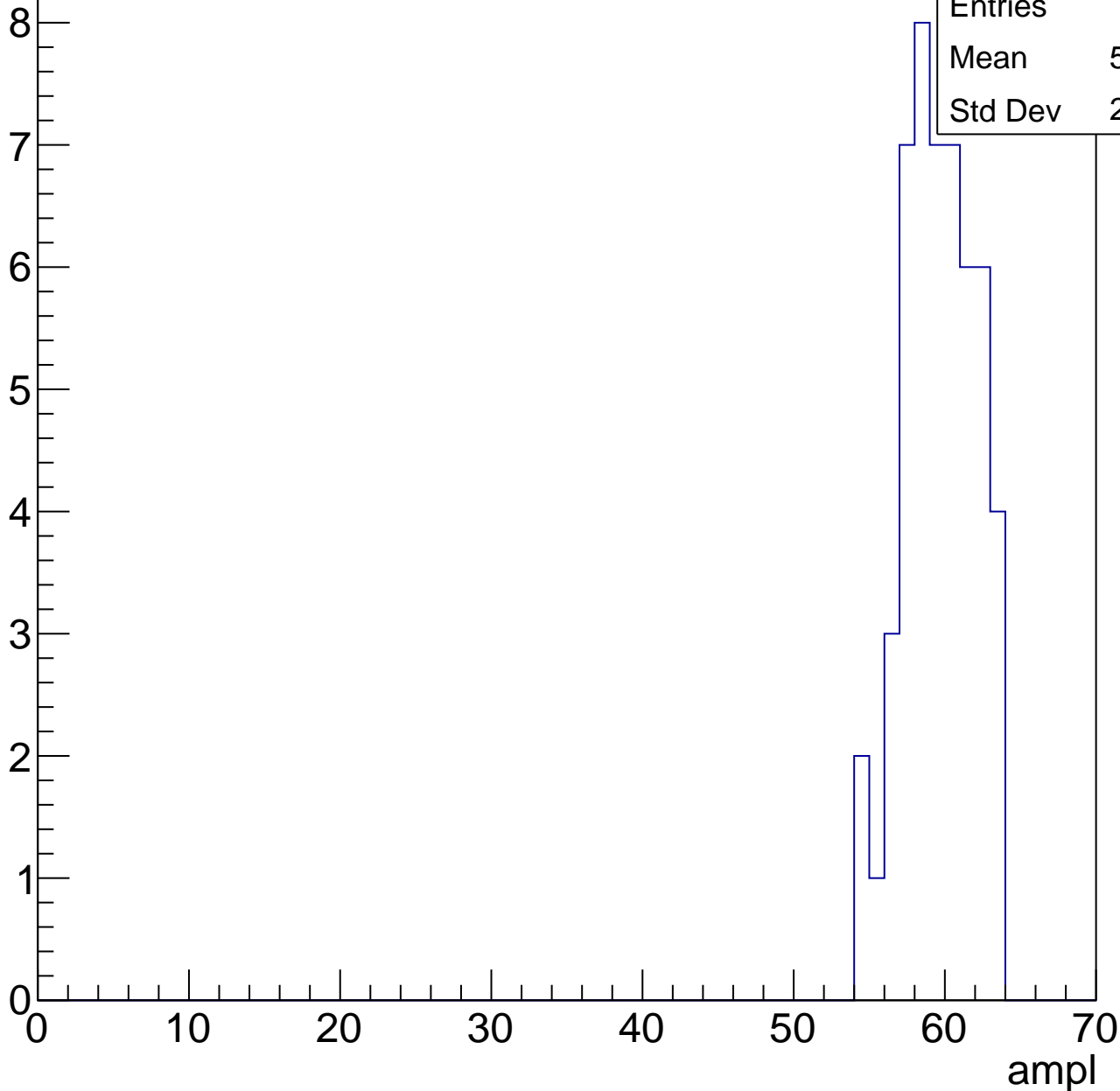


B1L101S, U17-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	59.16
Std Dev	2.329

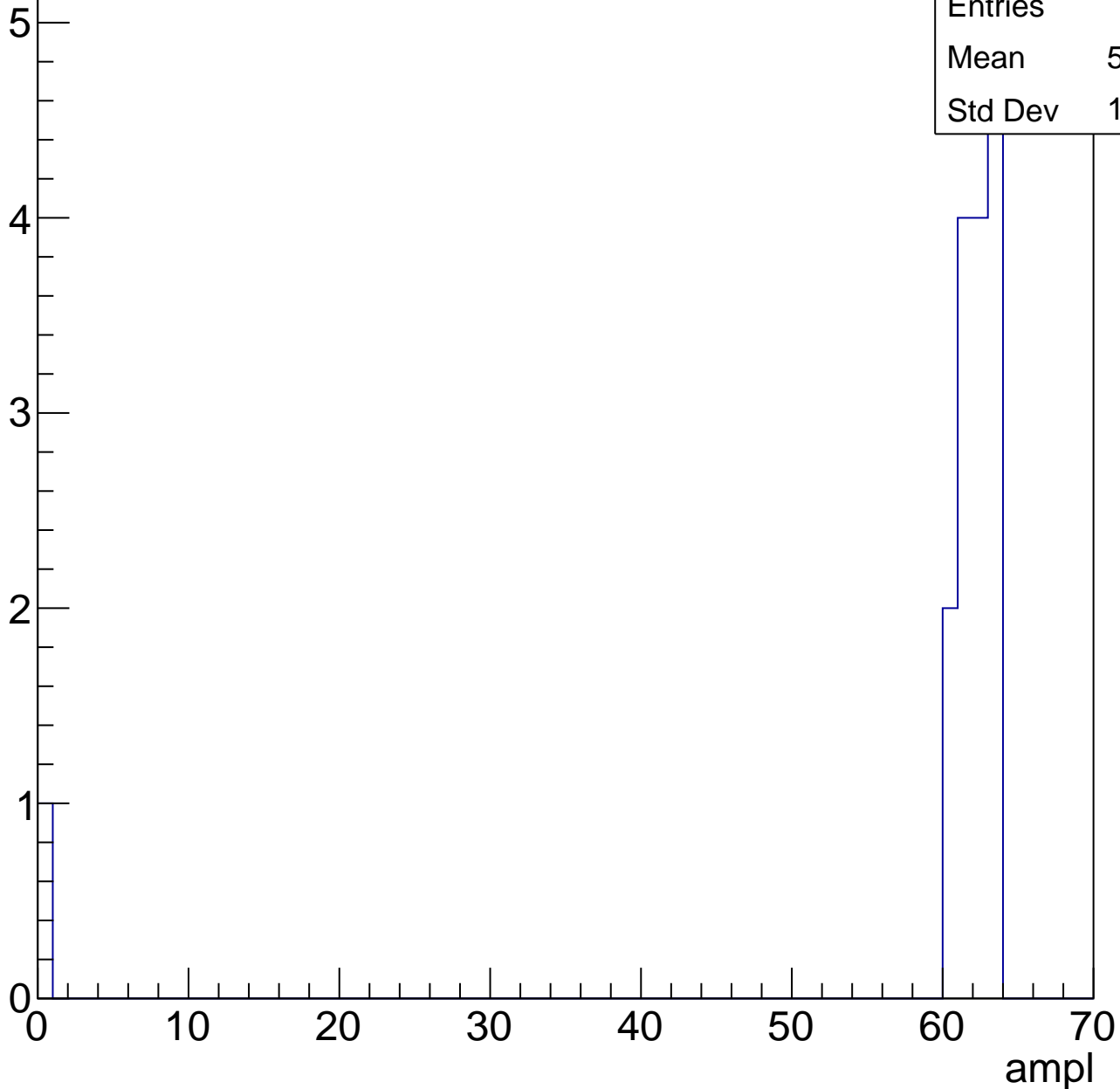


B1L101S, U17-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

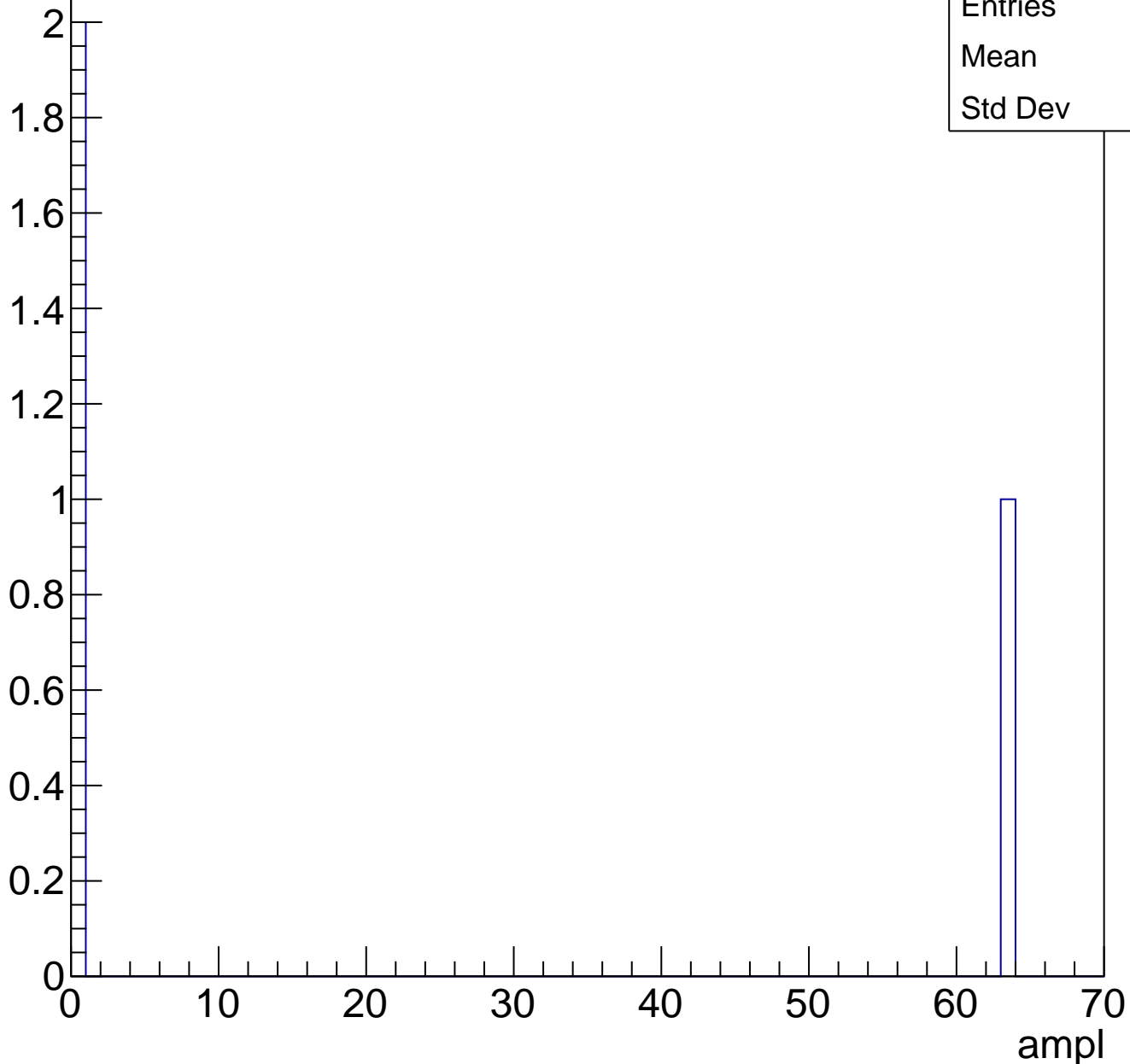
Entries	16
Mean	57.94
Std Dev	14.99



B1L101S, U17-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U17-ch20, adc0

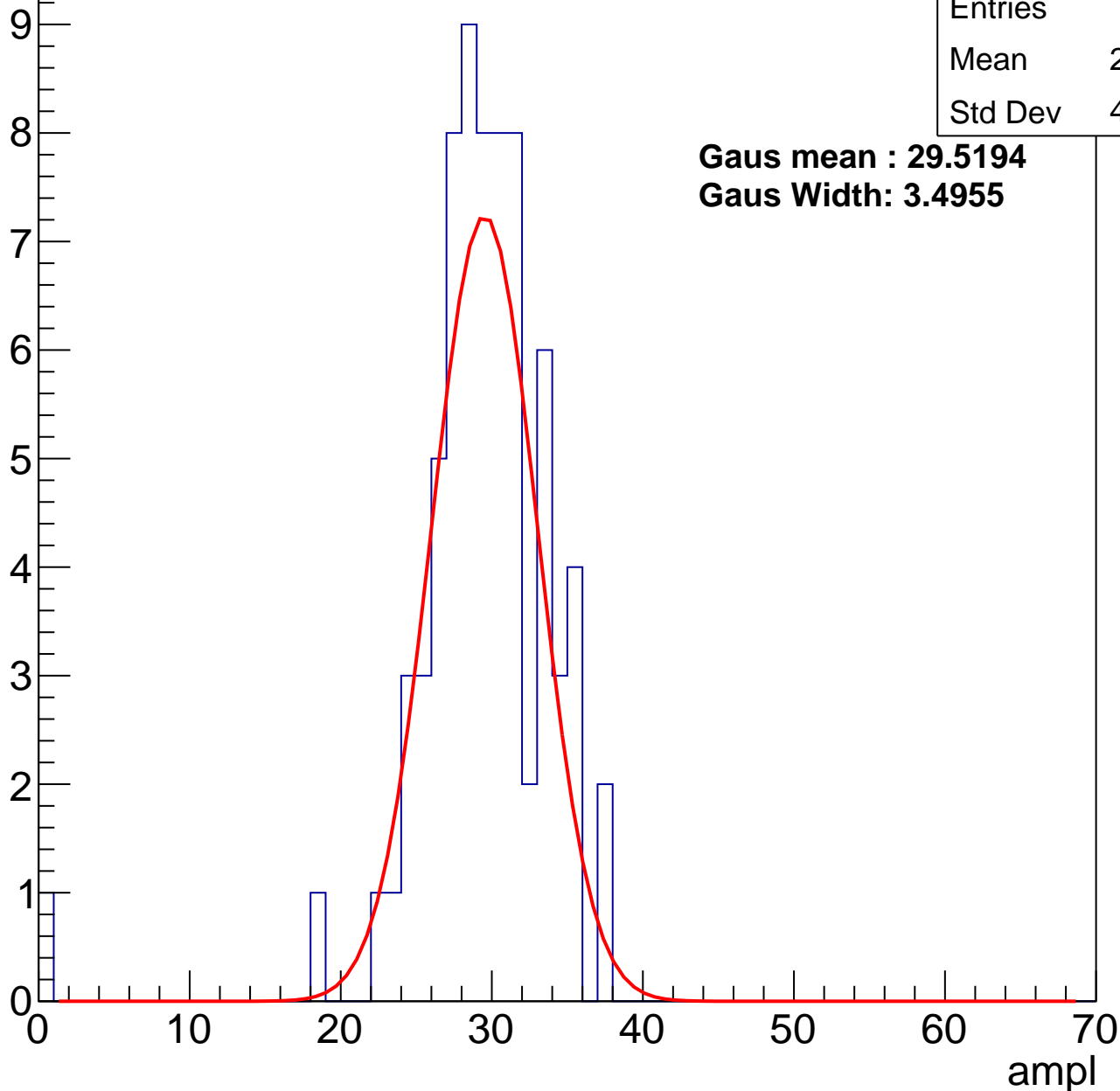
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.85
Std Dev	4.904

Gaus mean : 29.5194

Gaus Width: 3.4955

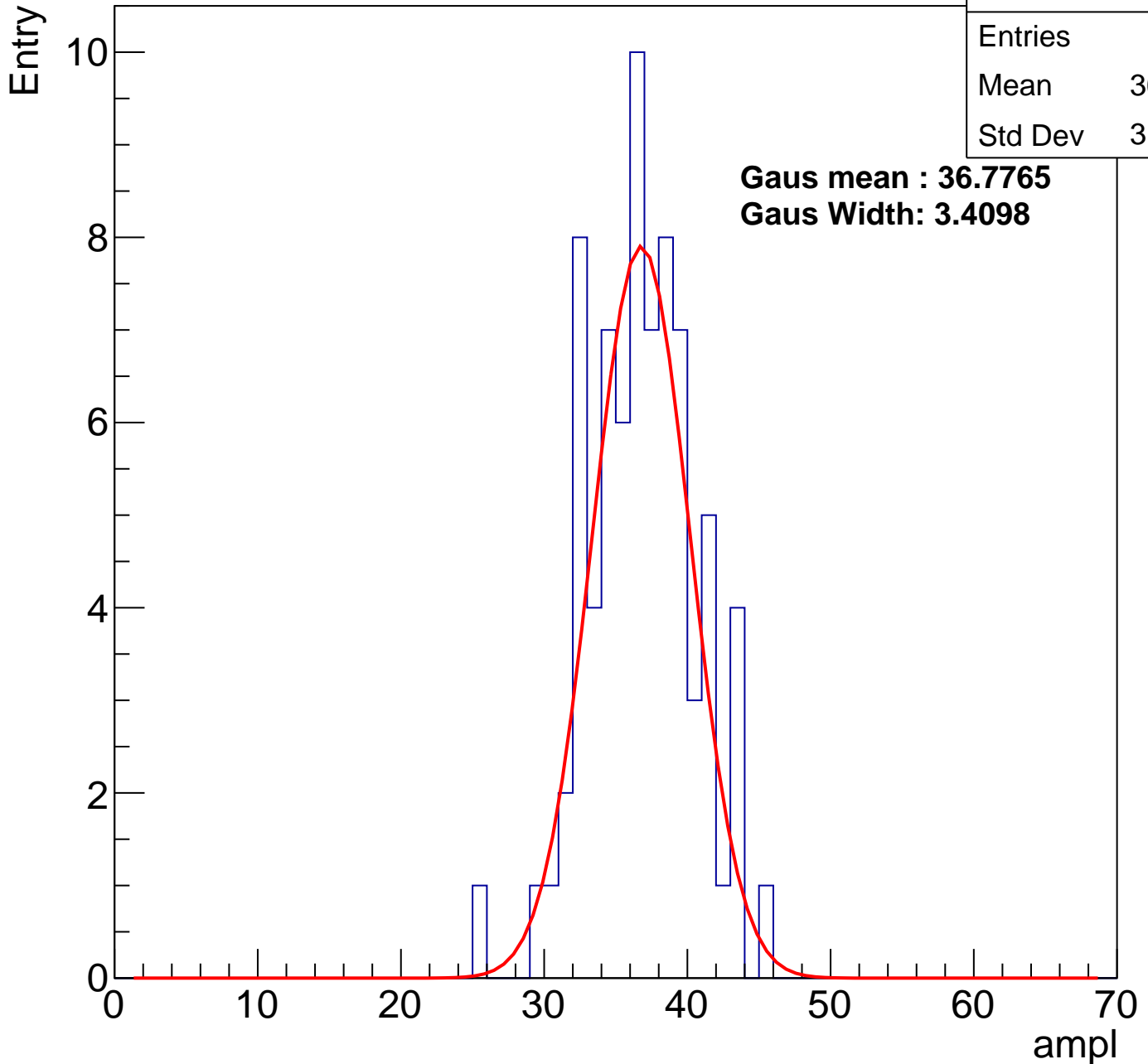


B1L101S, U17-ch20, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.34
Std Dev	3.687

Gaus mean : 36.7765
Gaus Width: 3.4098



B1L101S, U17-ch20, adc2

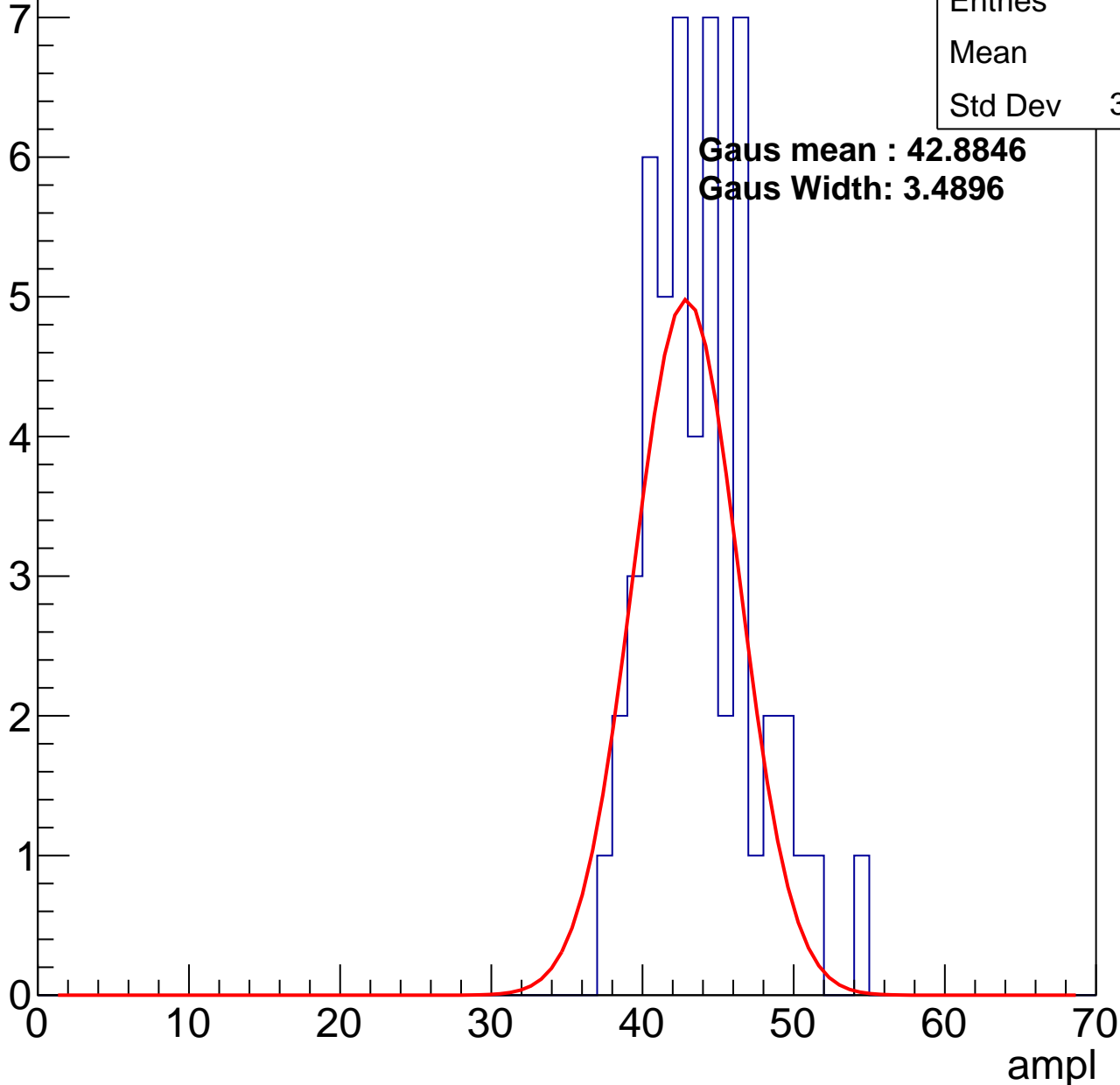
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.4
Std Dev	3.553

Gaus mean : 42.8846

Gaus Width: 3.4896

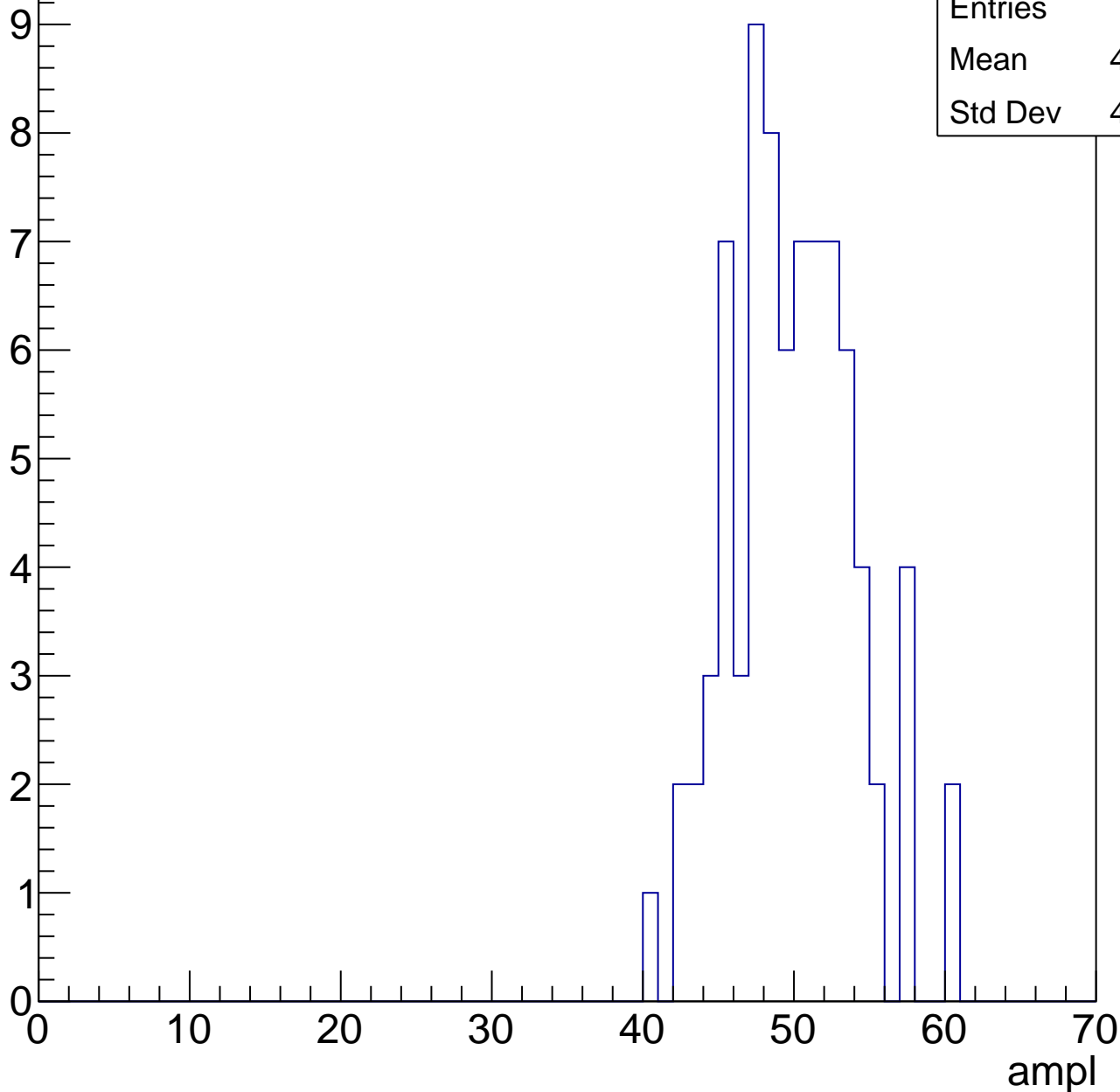


B1L101S, U17-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	49.49
Std Dev	4.114

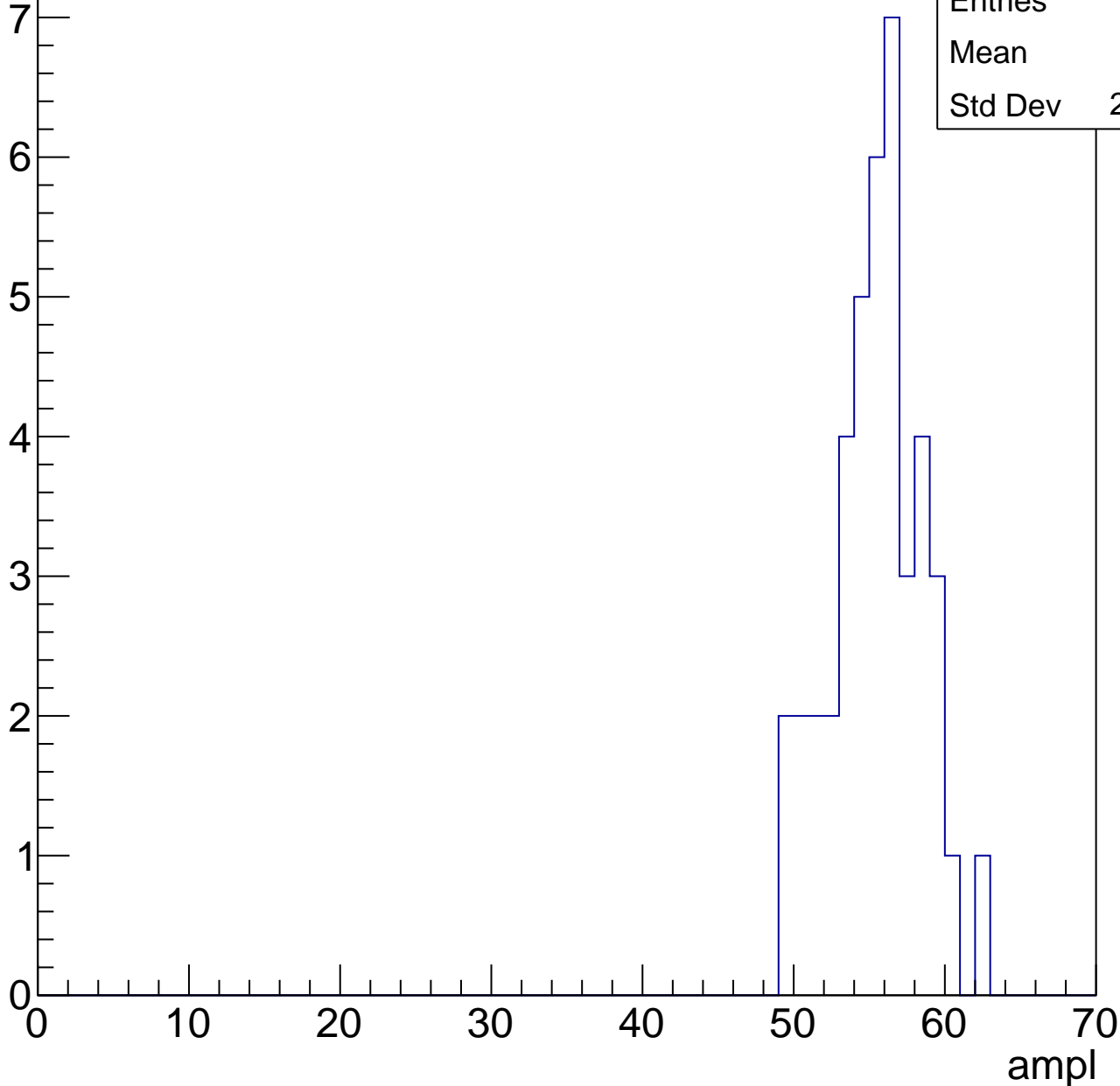


B1L101S, U17-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	55
Std Dev	2.968



B1L101S, U17-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	58.32
Std Dev	8.964

ampl

B1L101S, U17-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.55
Std Dev	1.233

B1L101S, U17-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch21, adc0

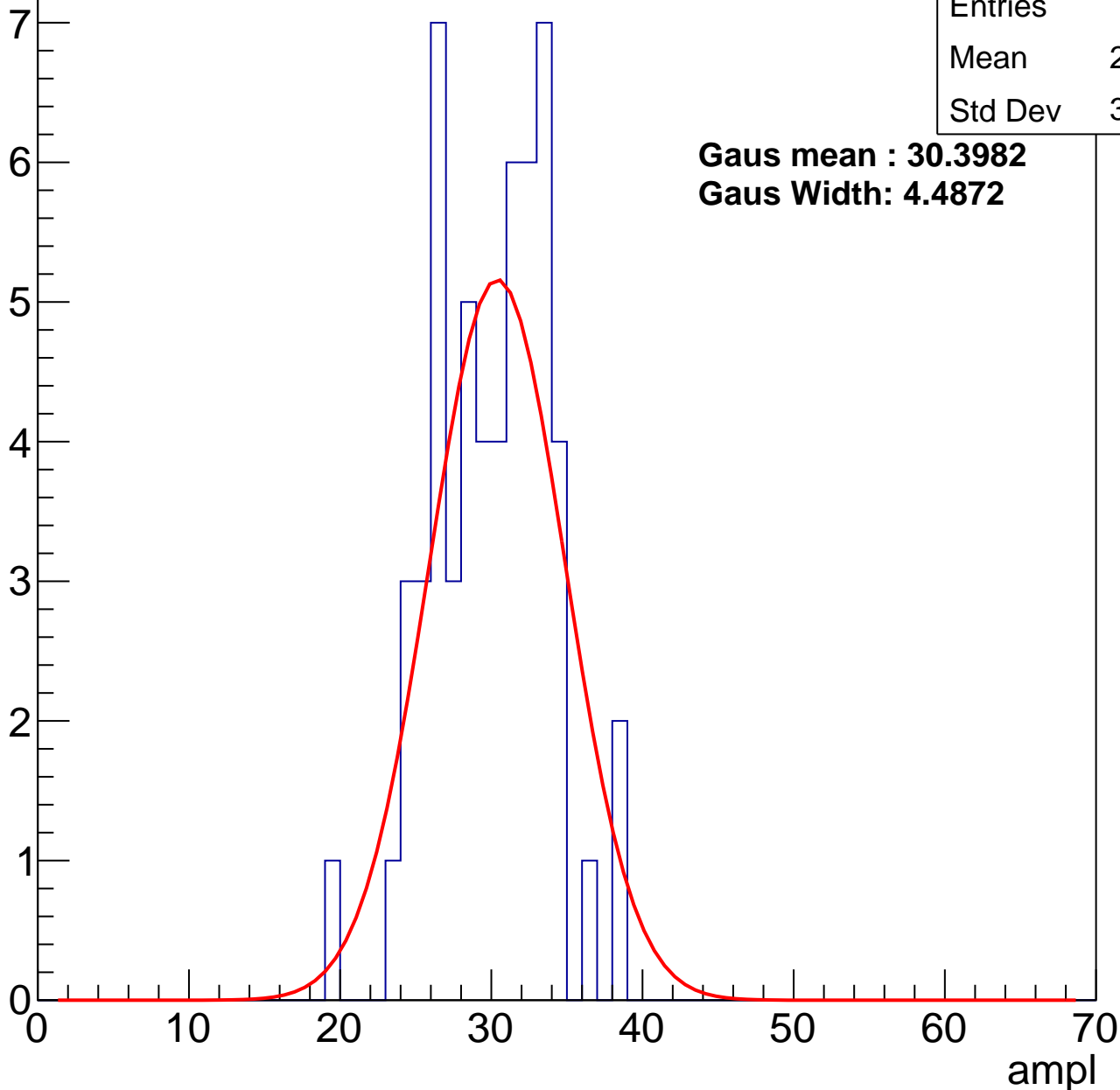
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	29.56
Std Dev	3.807

Gaus mean : 30.3982

Gaus Width: 4.4872



B1L101S, U17-ch21, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	36.79
Std Dev	4.242

Gaus mean : 37.7101

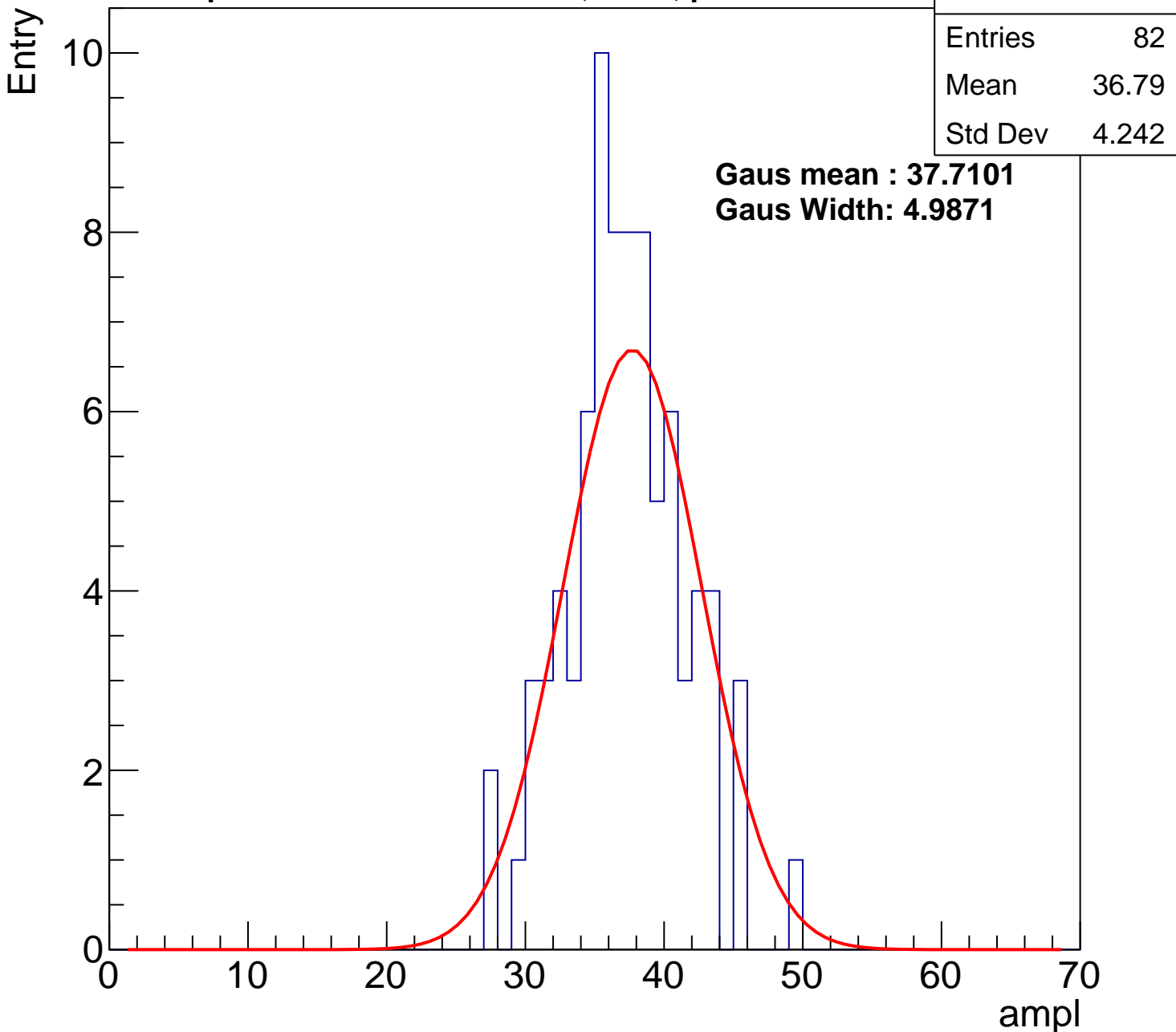
Gaus Width: 4.9871

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch21, adc2

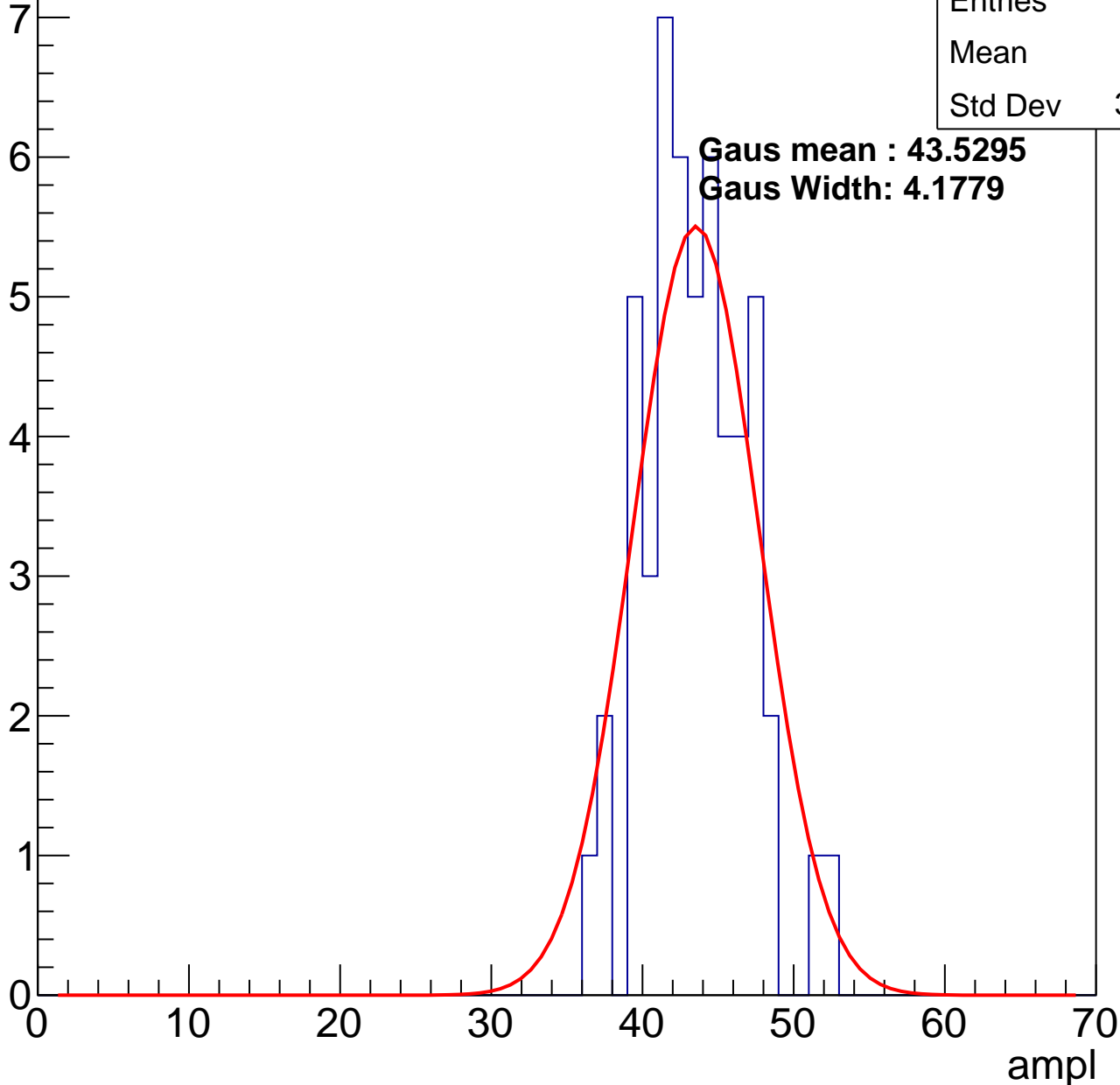
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.1
Std Dev	3.381

Gaus mean : 43.5295

Gaus Width: 4.1779

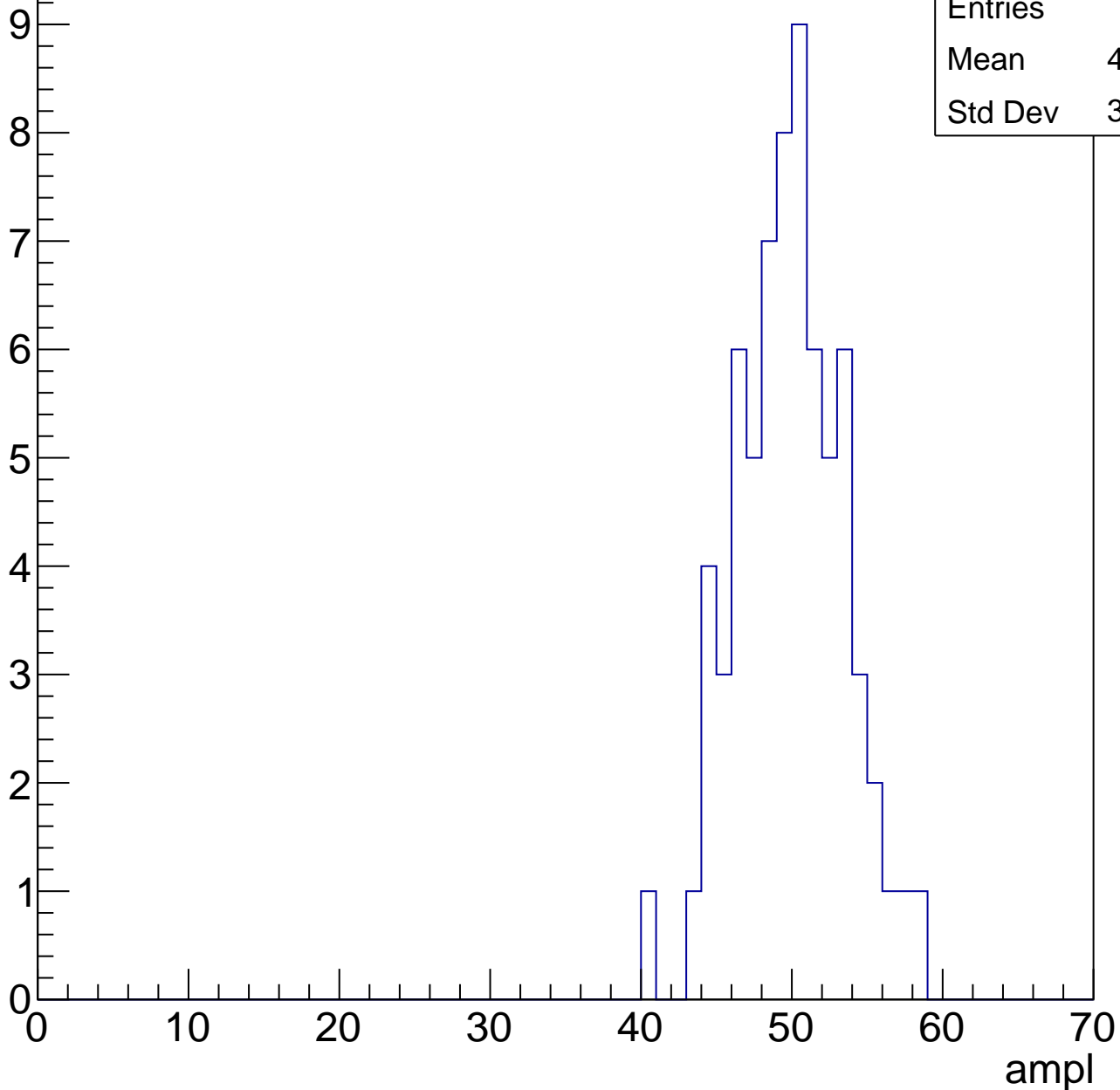


B1L101S, U17-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

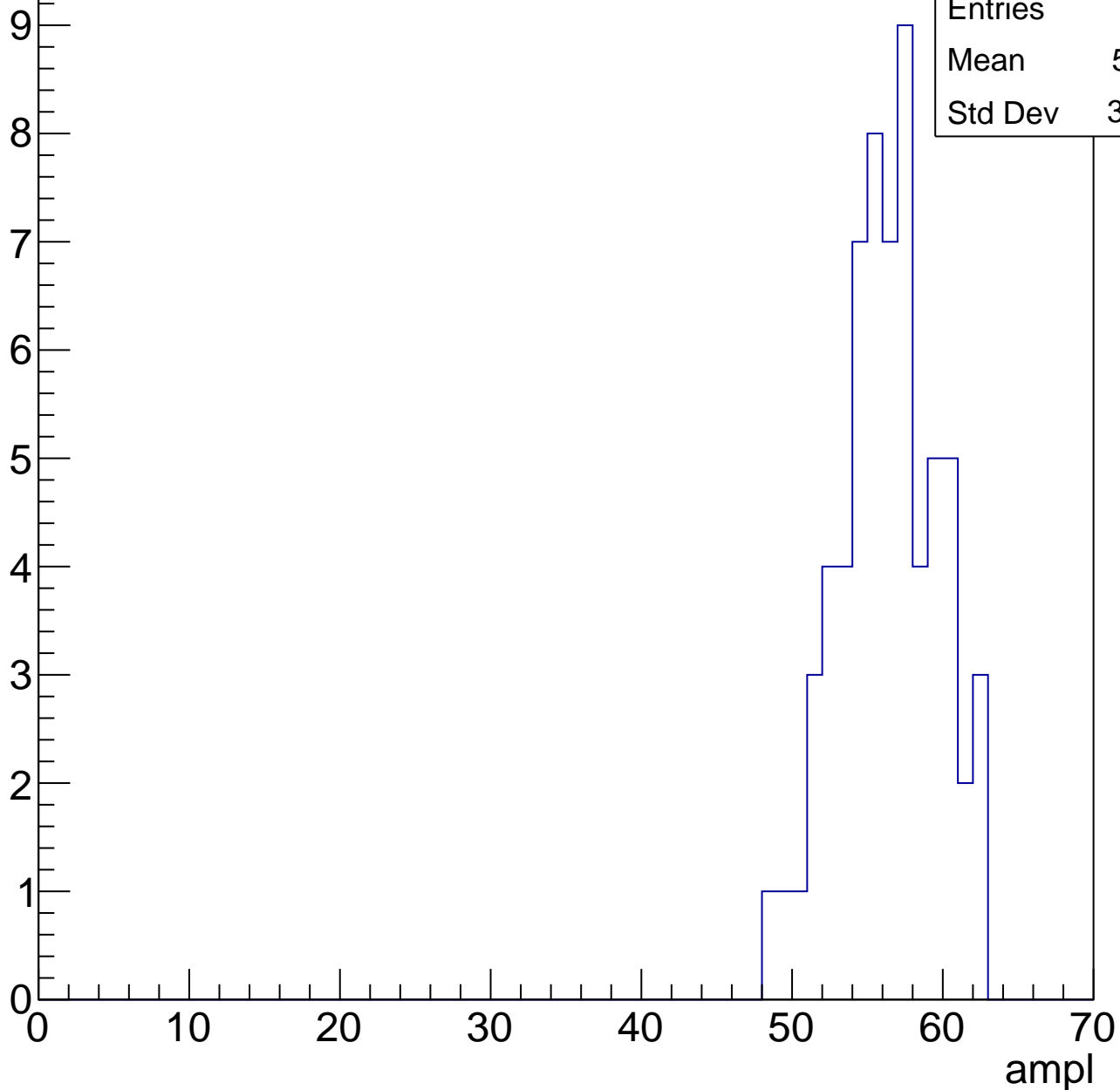
Entries	69
Mean	49.42
Std Dev	3.507



B1L101S, U17-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



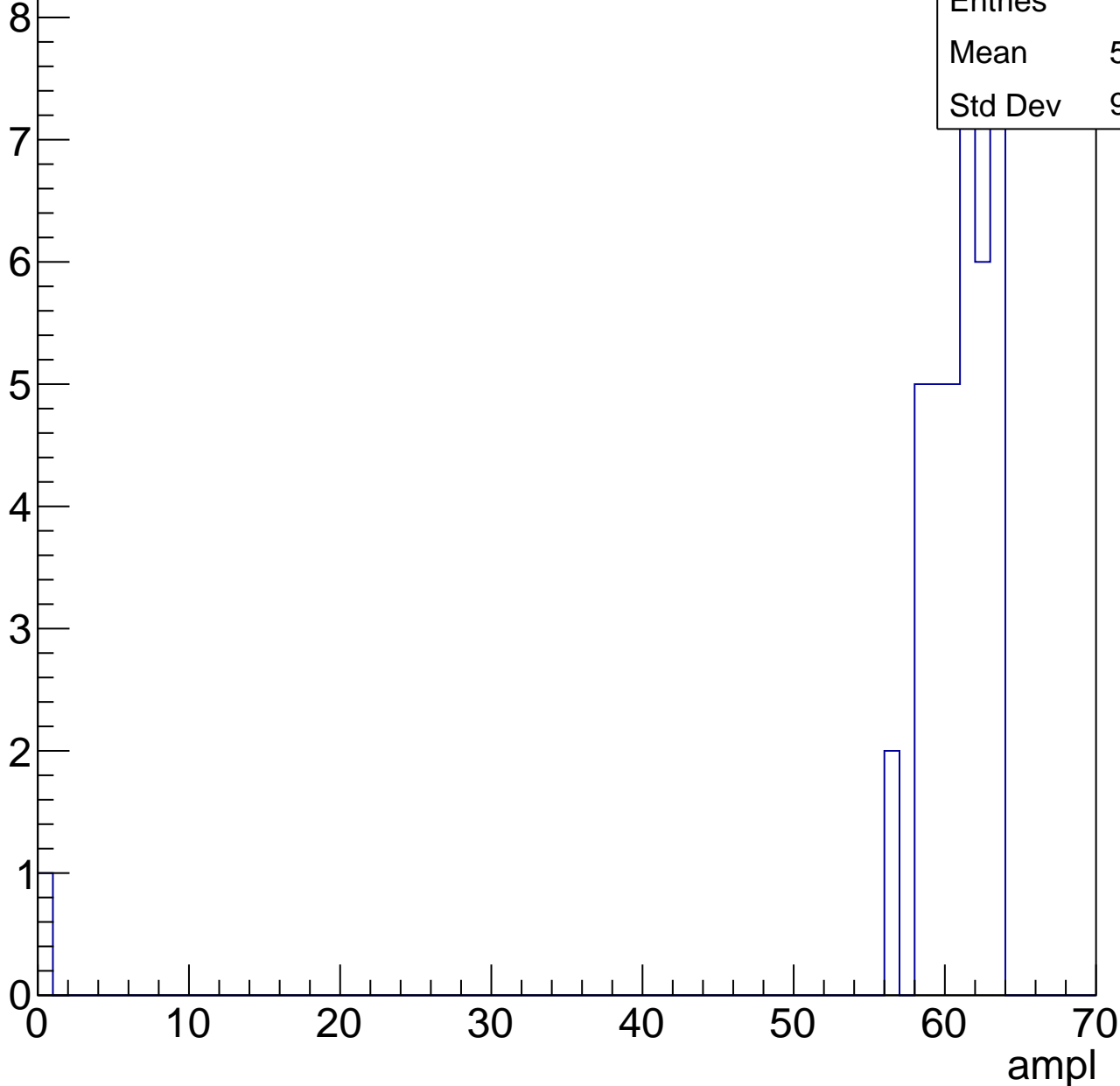
Entries	64
Mean	55.91
Std Dev	3.229

B1L101S, U17-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.02
Std Dev	9.648



B1L101S, U17-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch22, adc0

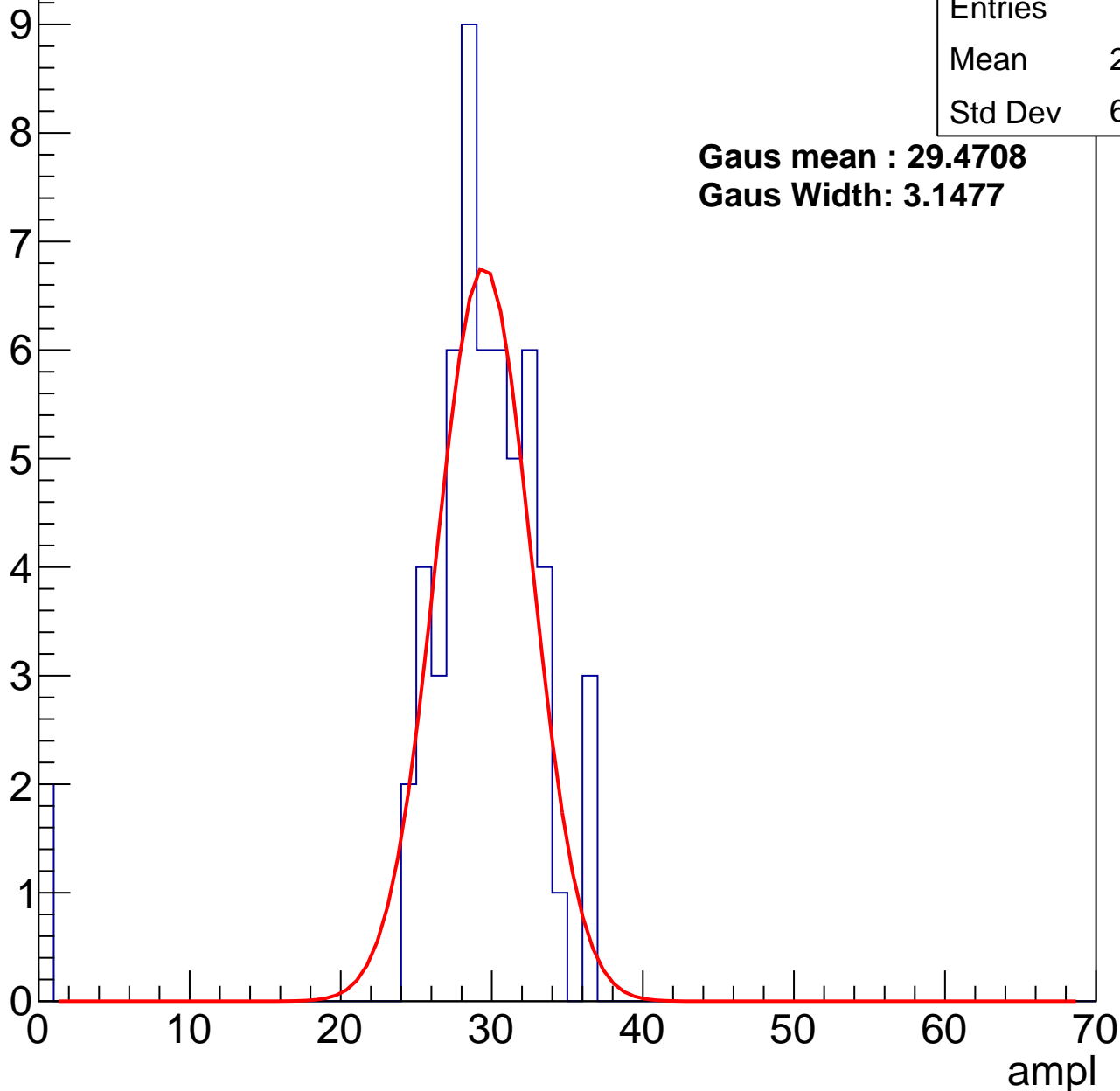
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.33
Std Dev	6.137

Gaus mean : 29.4708

Gaus Width: 3.1477



B1L101S, U17-ch22, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	36.4
Std Dev	3.583

Gaus mean : 36.8580

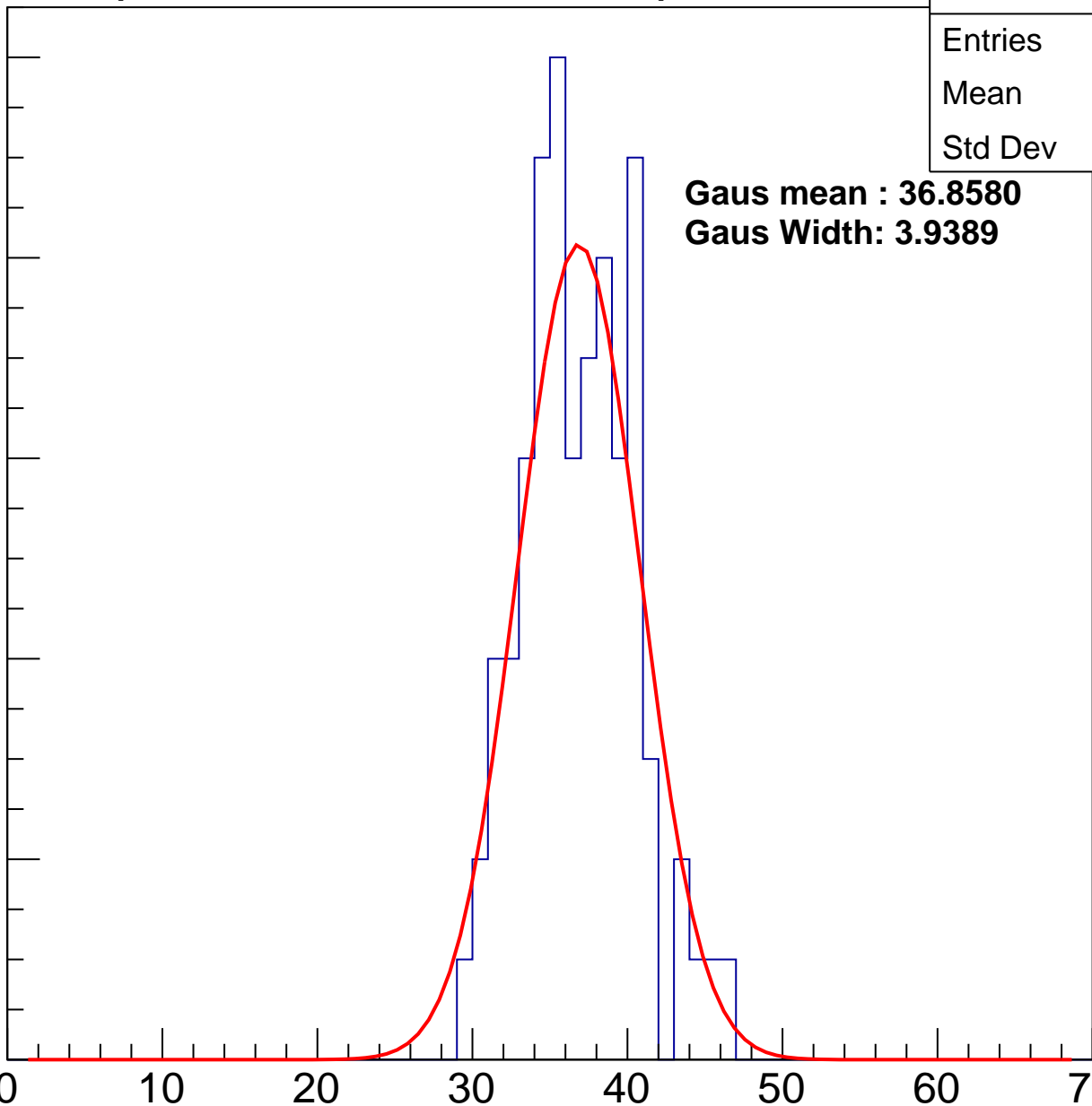
Gaus Width: 3.9389

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch22, adc2

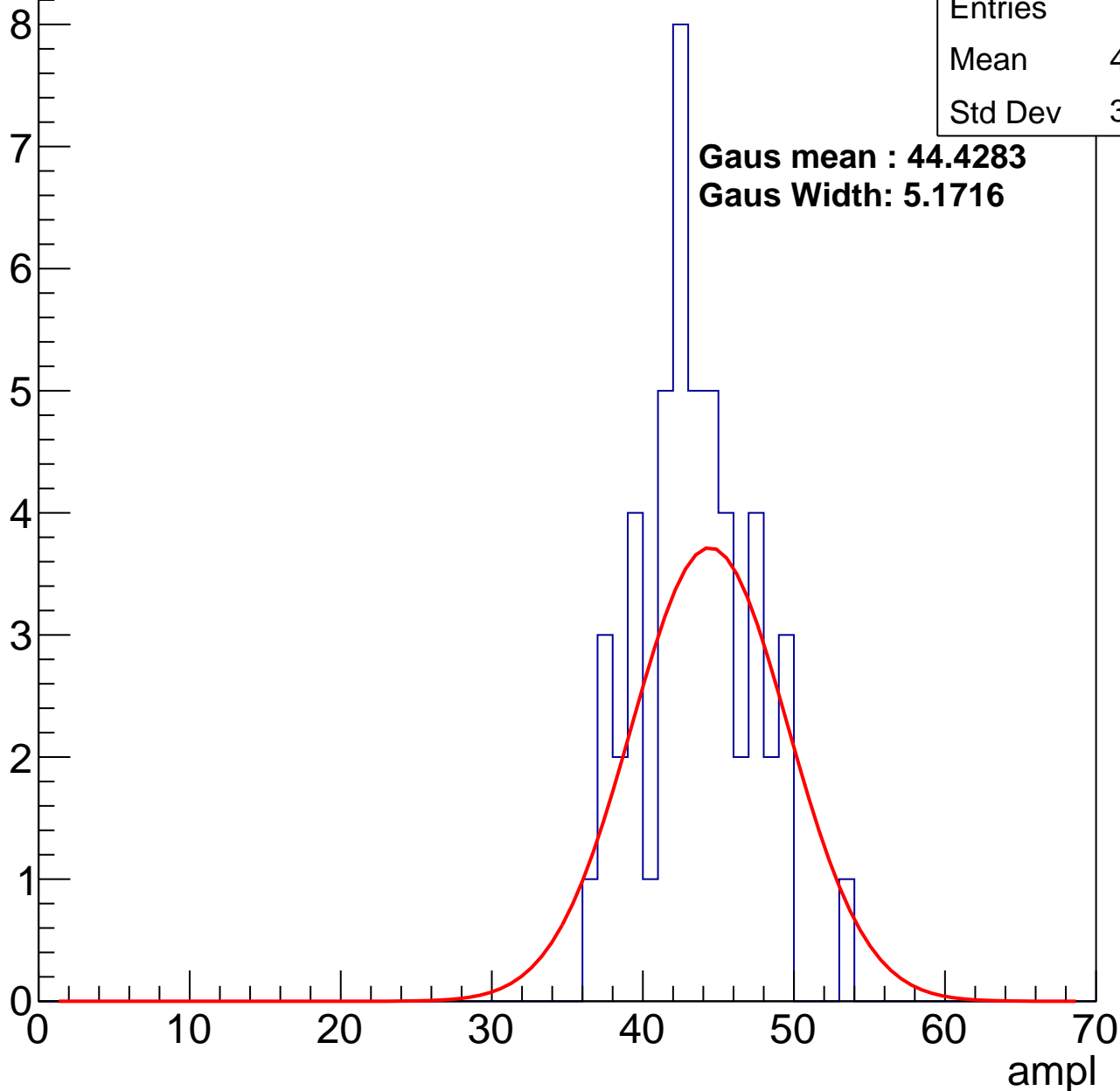
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	43.02
Std Dev	3.658

Gaus mean : 44.4283

Gaus Width: 5.1716

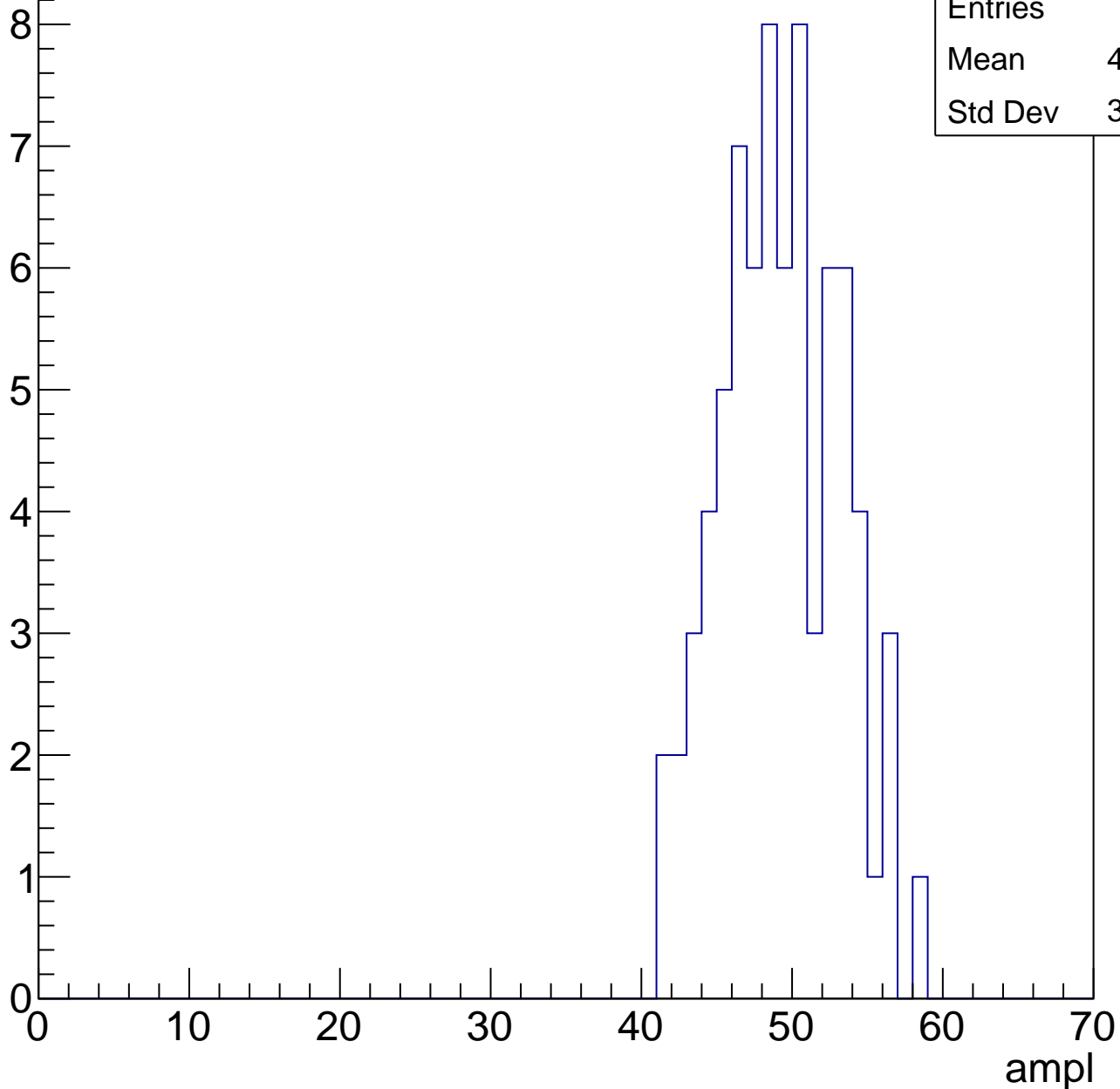


B1L101S, U17-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	48.77
Std Dev	3.897

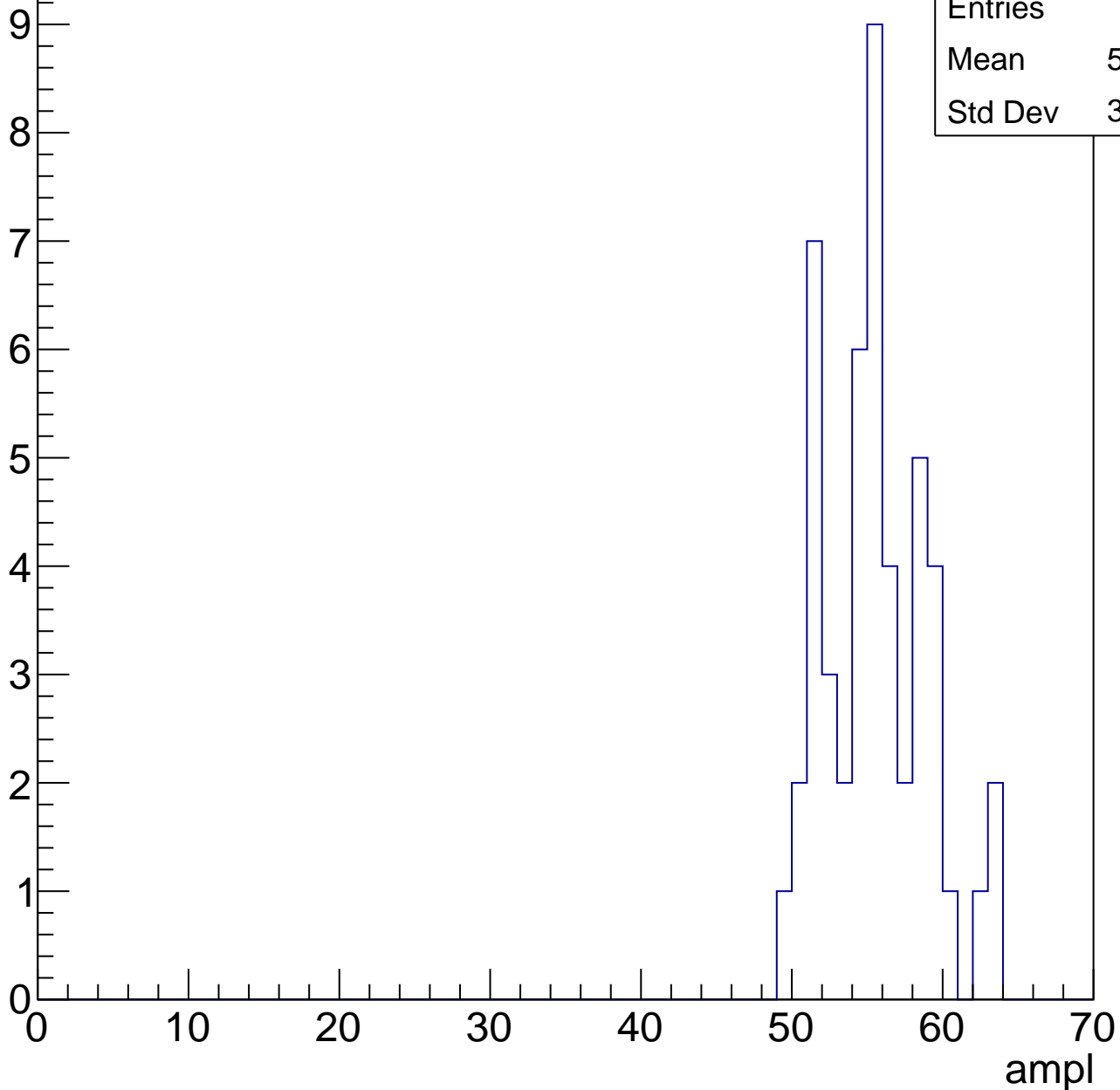


B1L101S, U17-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

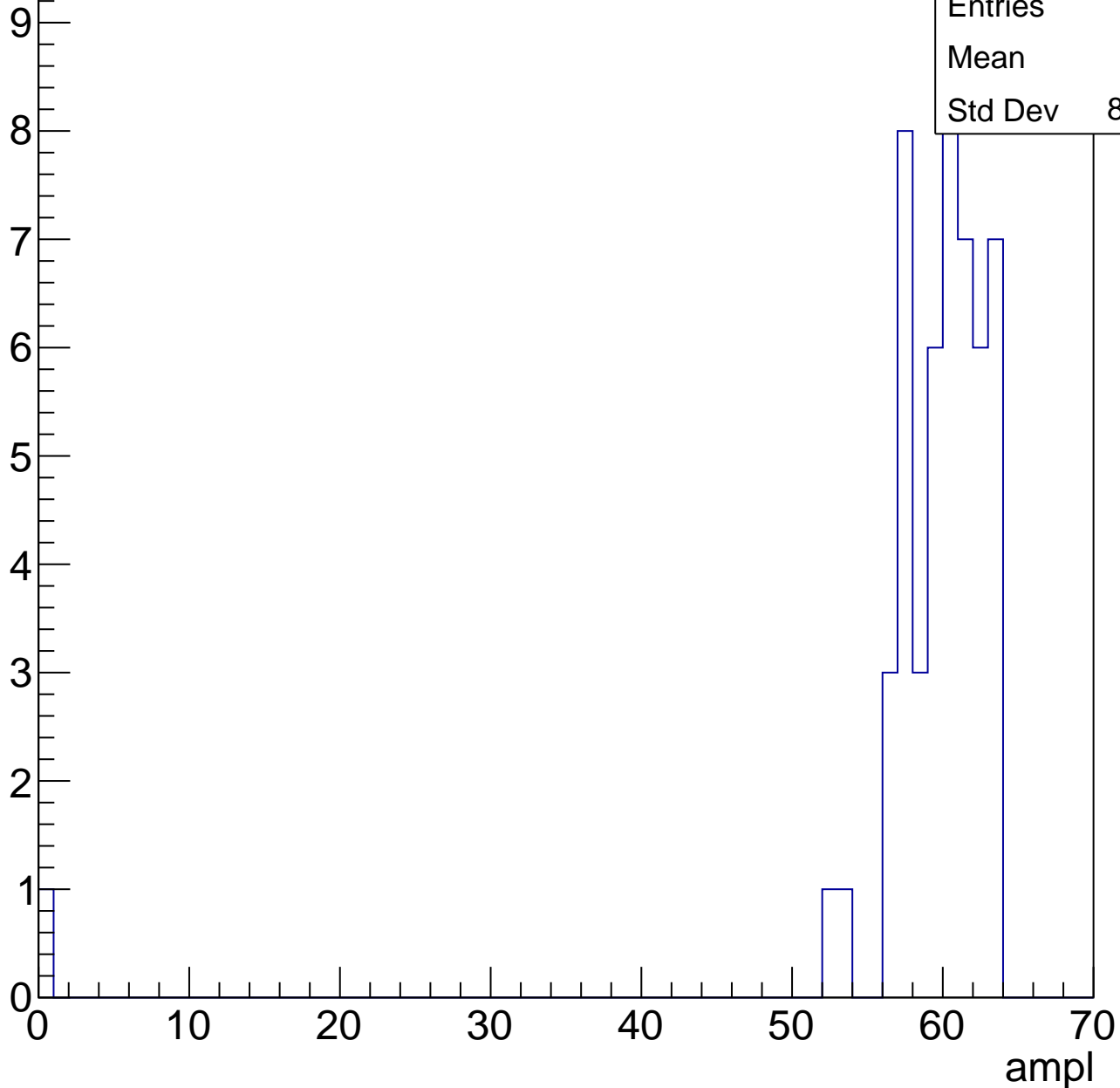
Entries	49
Mean	55.08
Std Dev	3.386



B1L101S, U17-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

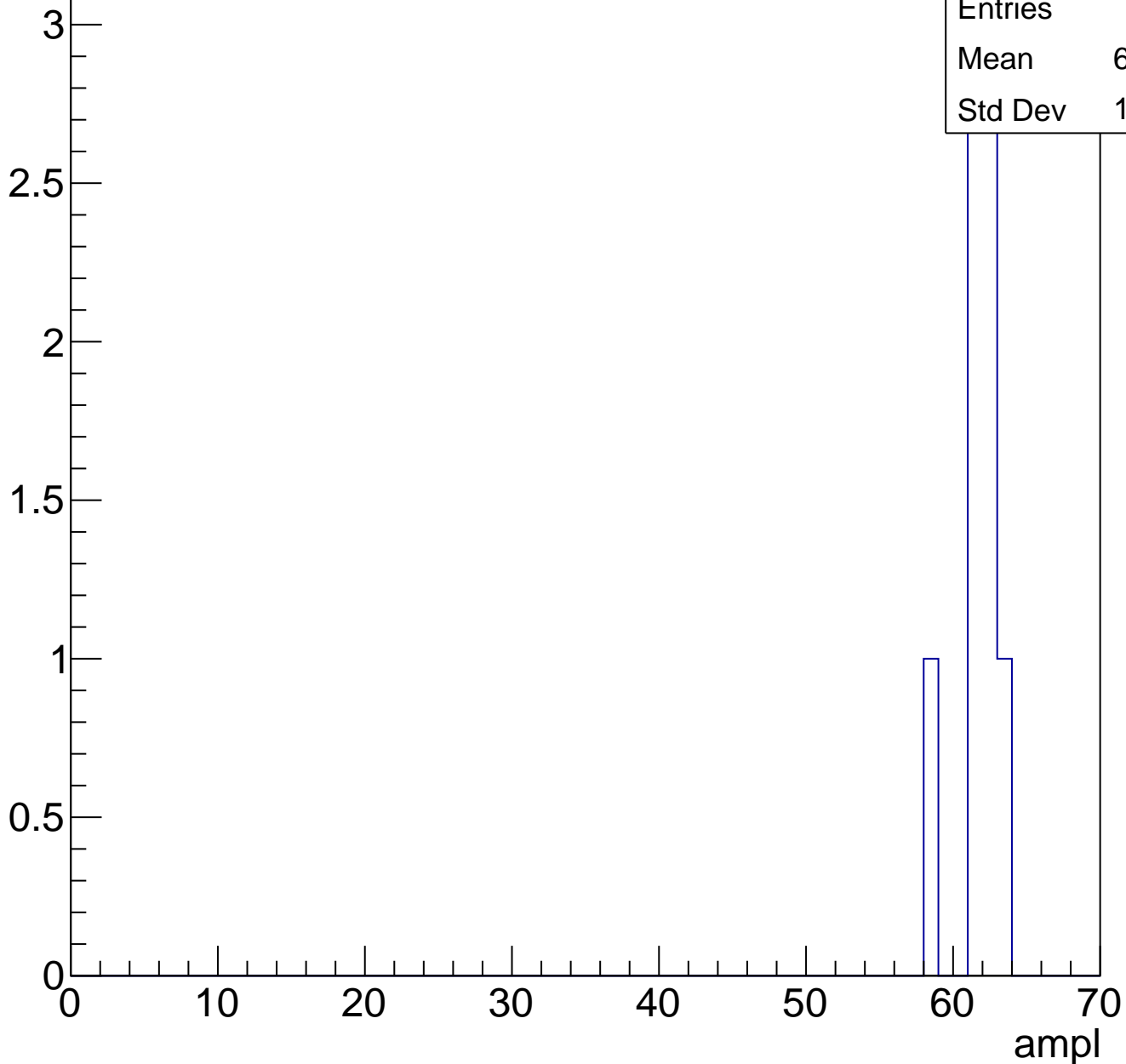
Entry



B1L101S, U17-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch23, adc0

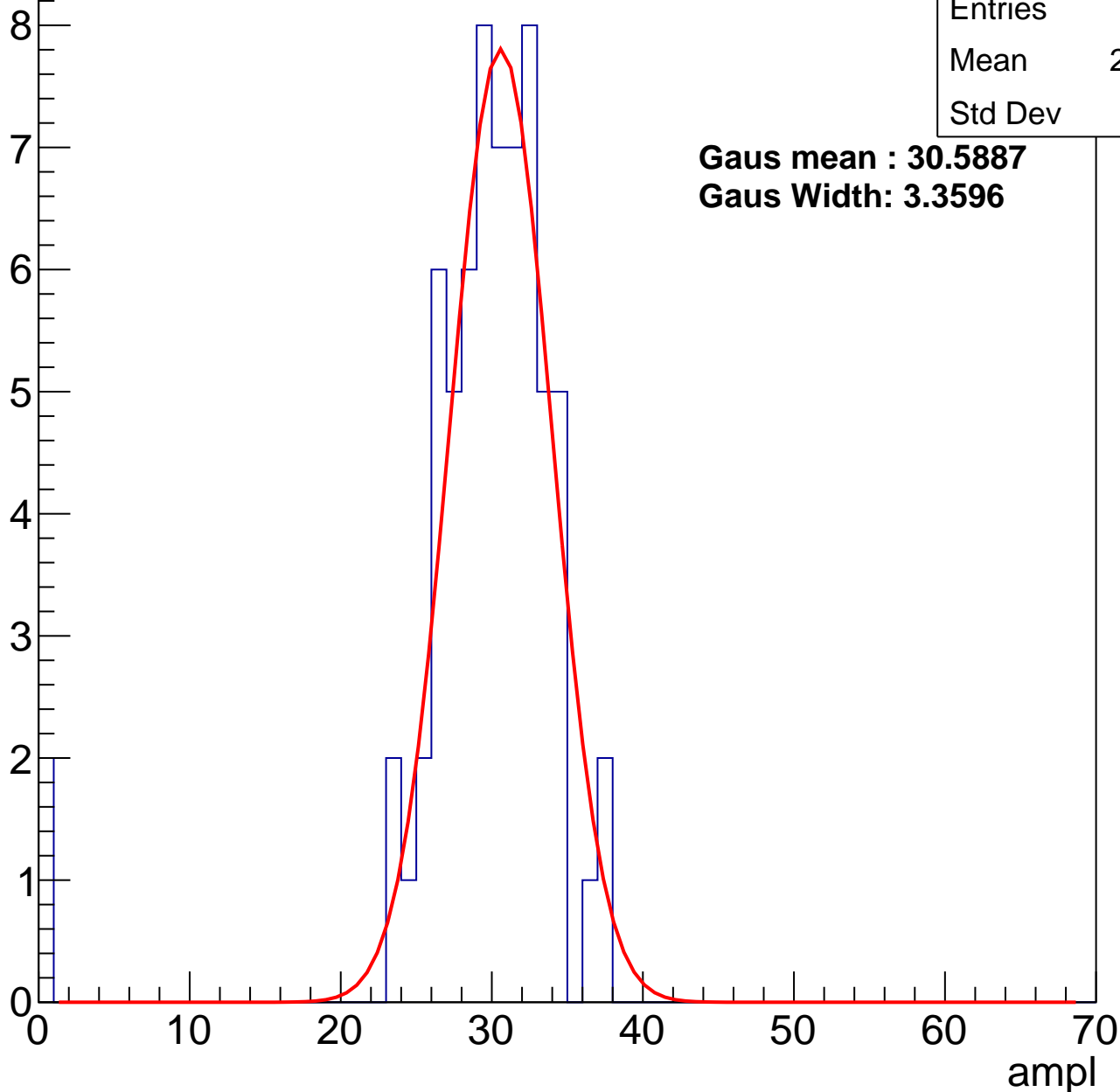
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.94
Std Dev	5.96

Gaus mean : 30.5887

Gaus Width: 3.3596



B1L101S, U17-ch23, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	36.53
Std Dev	4.171

Gaus mean : 37.5066

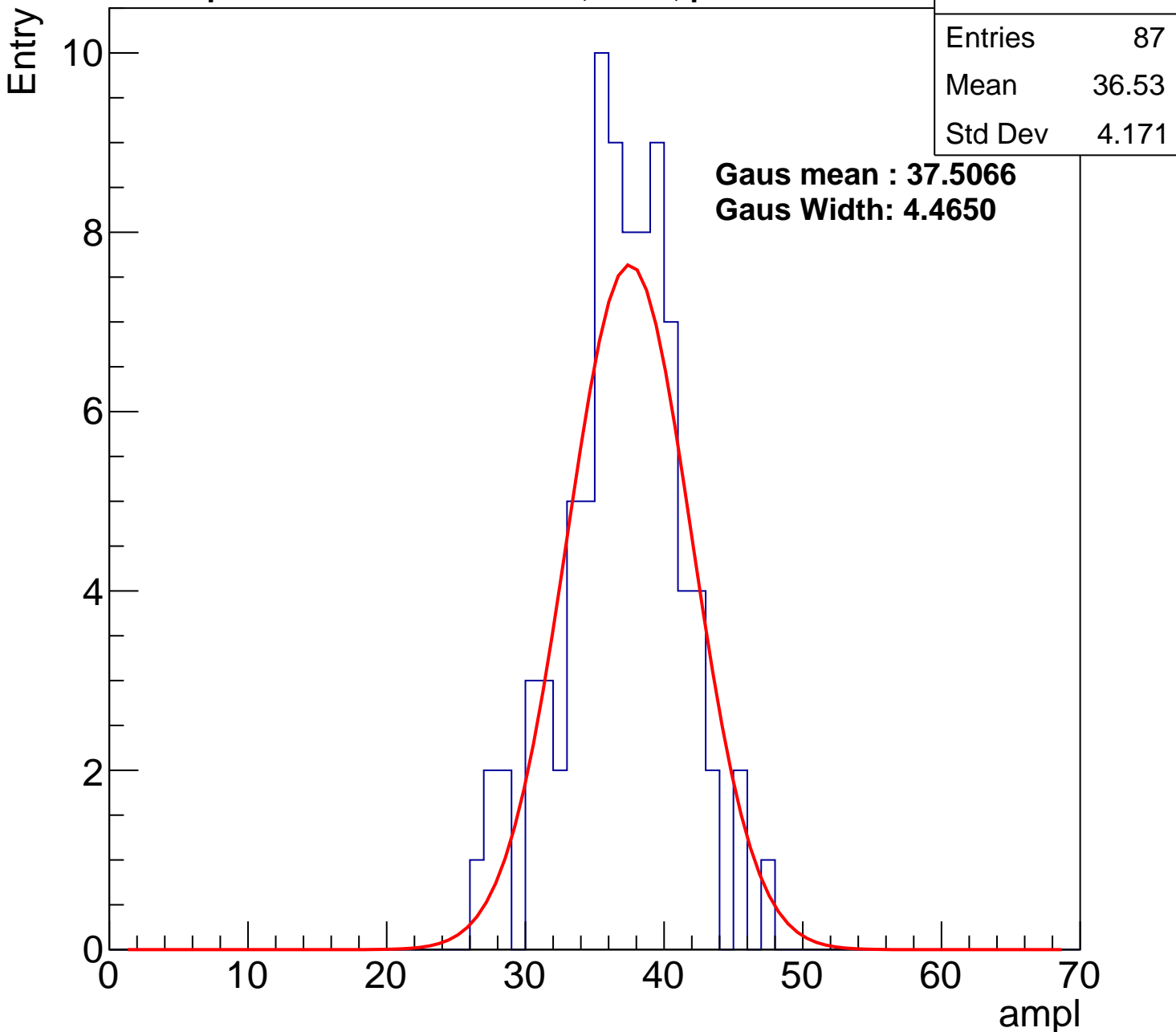
Gaus Width: 4.4650

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch23, adc2

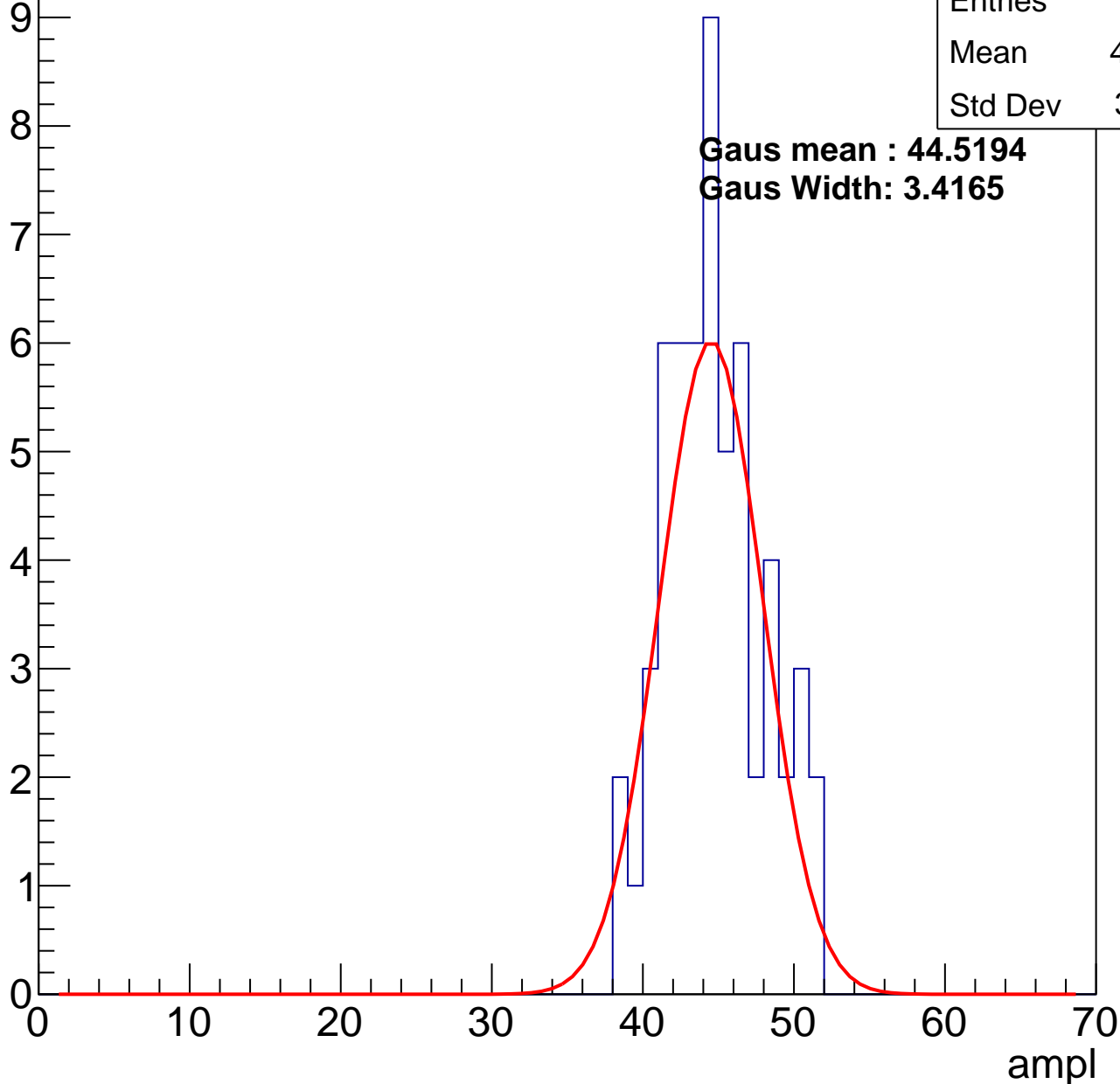
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.28
Std Dev	3.221

Gaus mean : 44.5194

Gaus Width: 3.4165

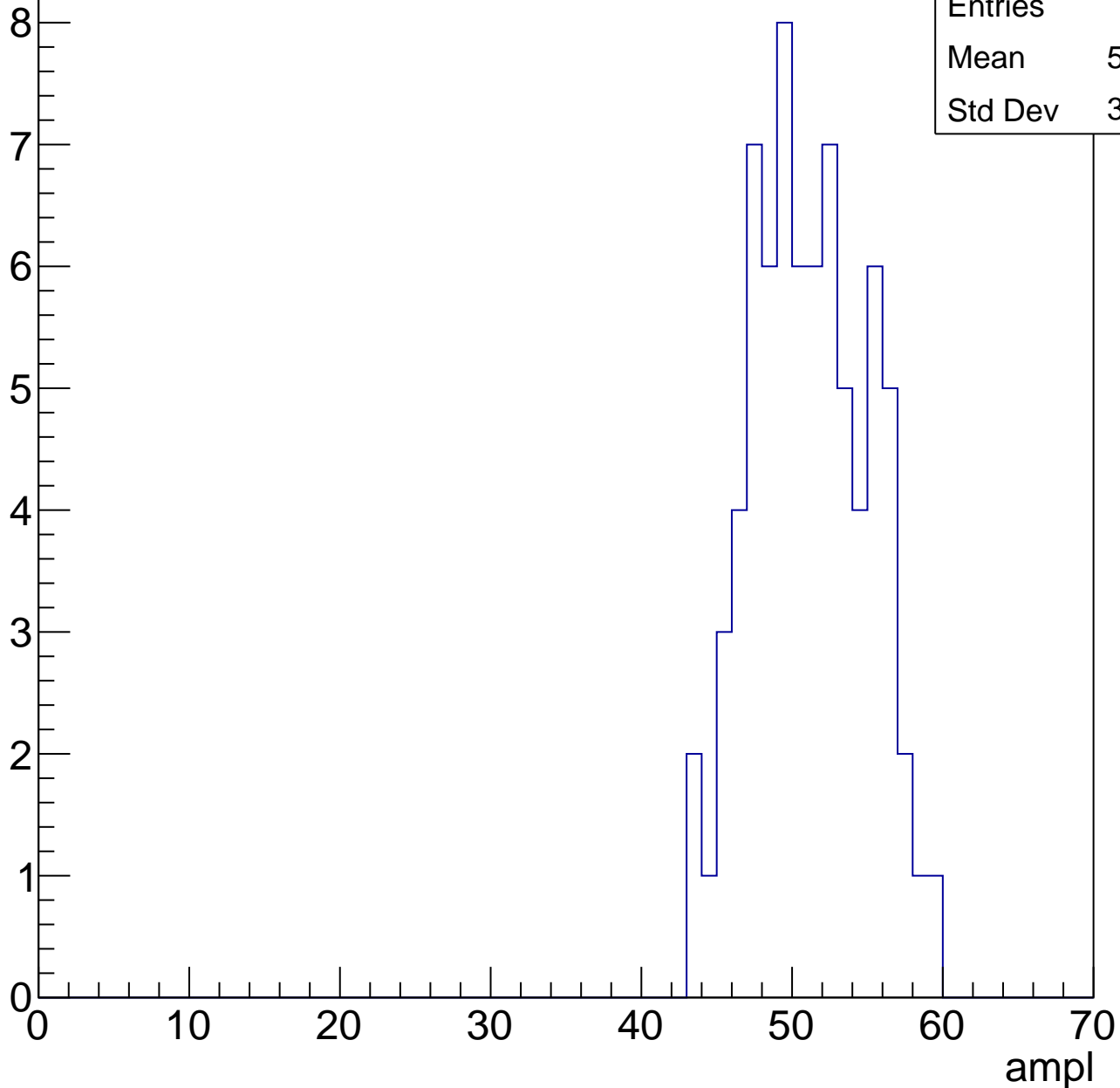


B1L101S, U17-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	50.68
Std Dev	3.767

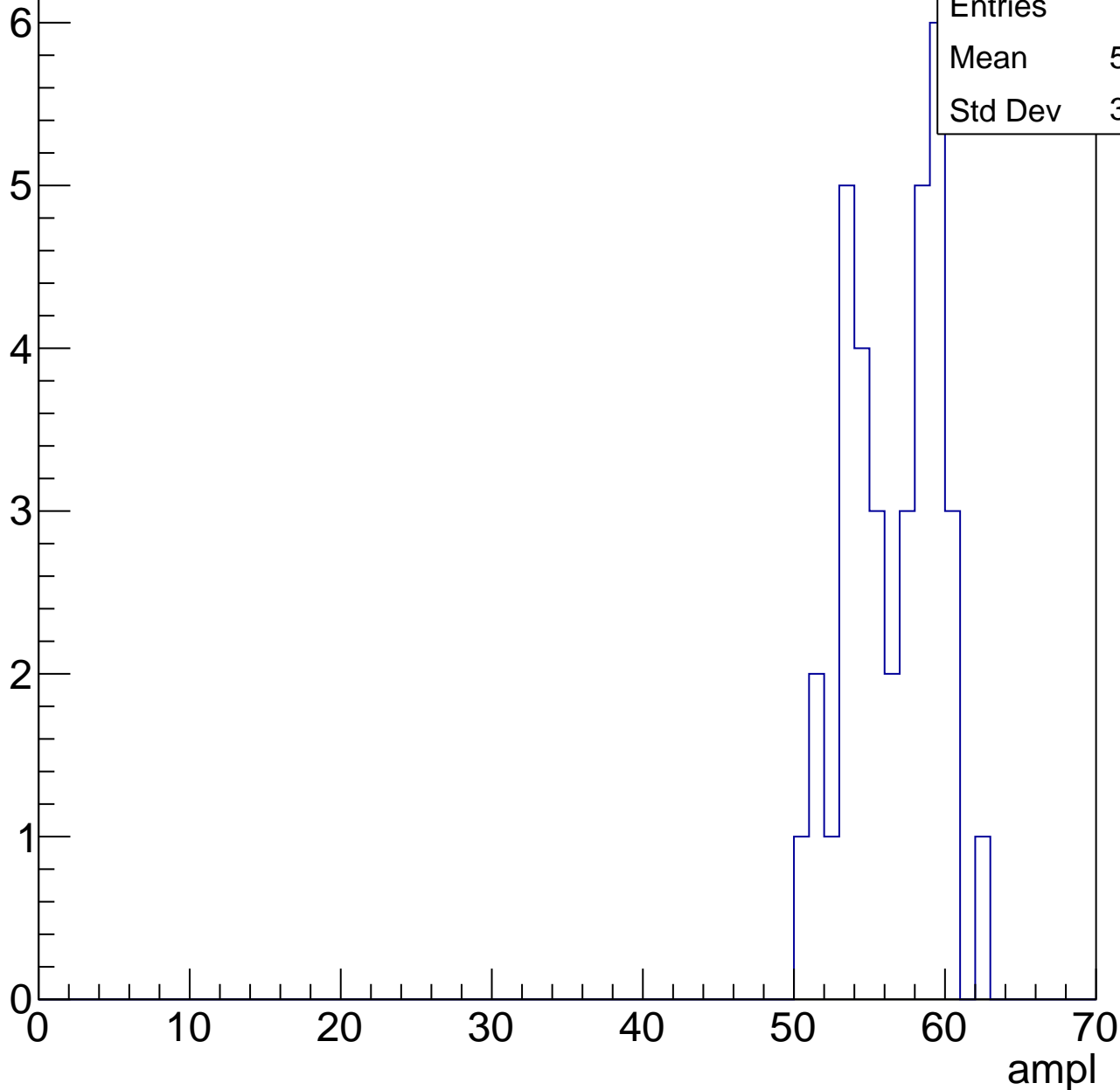


B1L101S, U17-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	56.08
Std Dev	3.013



B1L101S, U17-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

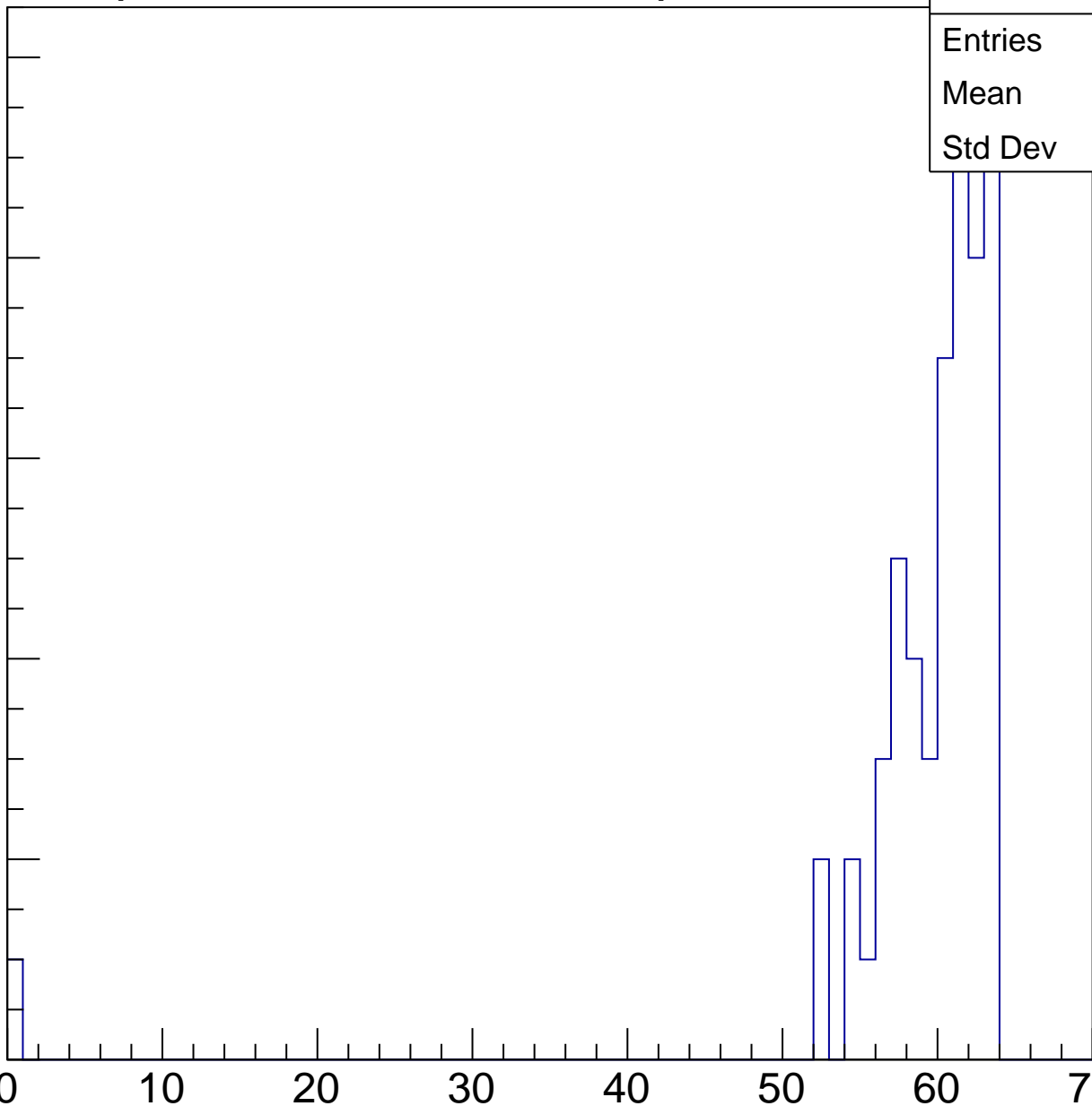
0

Entries	56
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Mean	58.66
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Std Dev	8.418
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ampl



B1L101S, U17-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch24, adc0

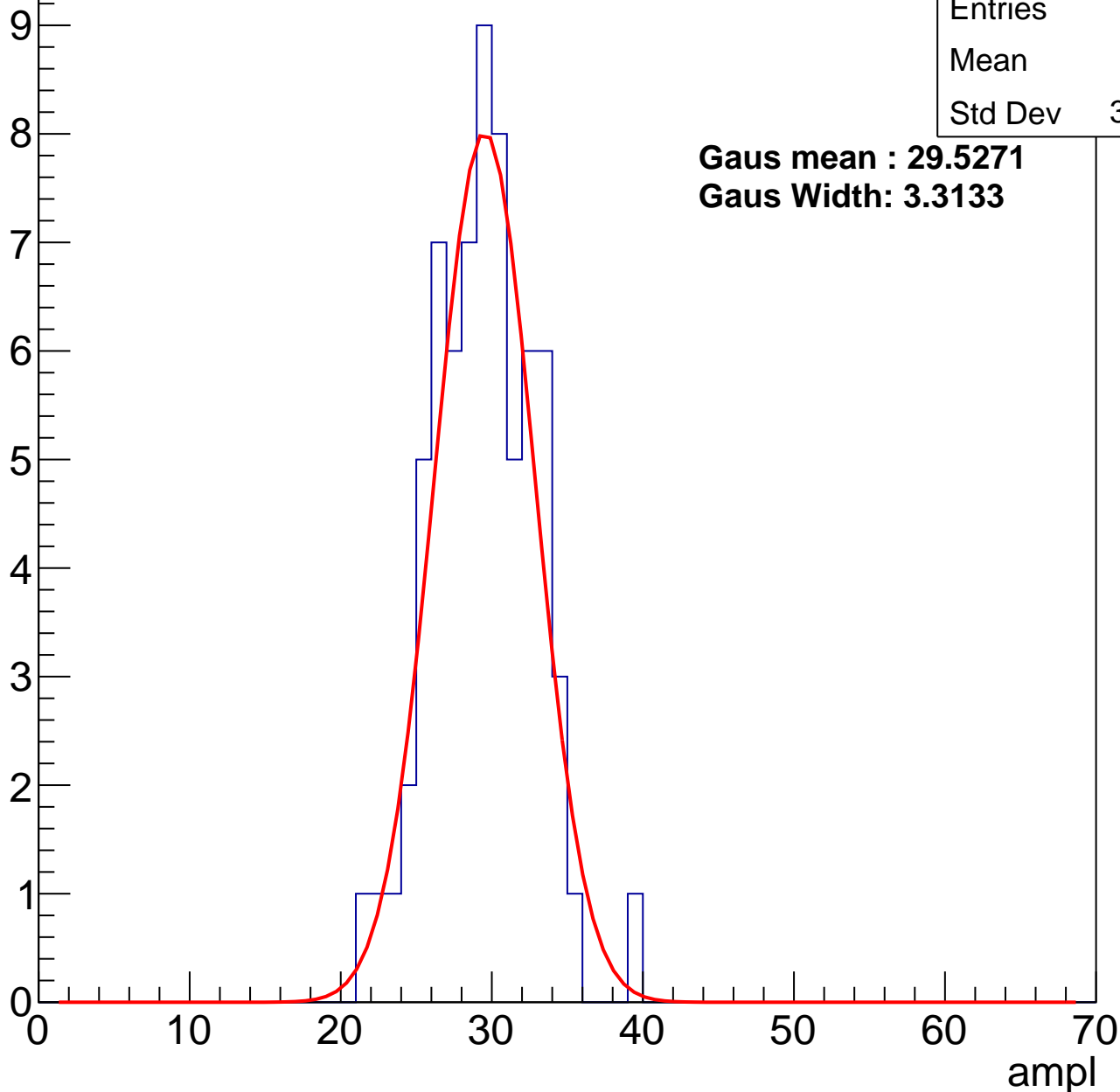
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29
Std Dev	3.323

Gaus mean : 29.5271

Gaus Width: 3.3133



B1L101S, U17-ch24, adc1

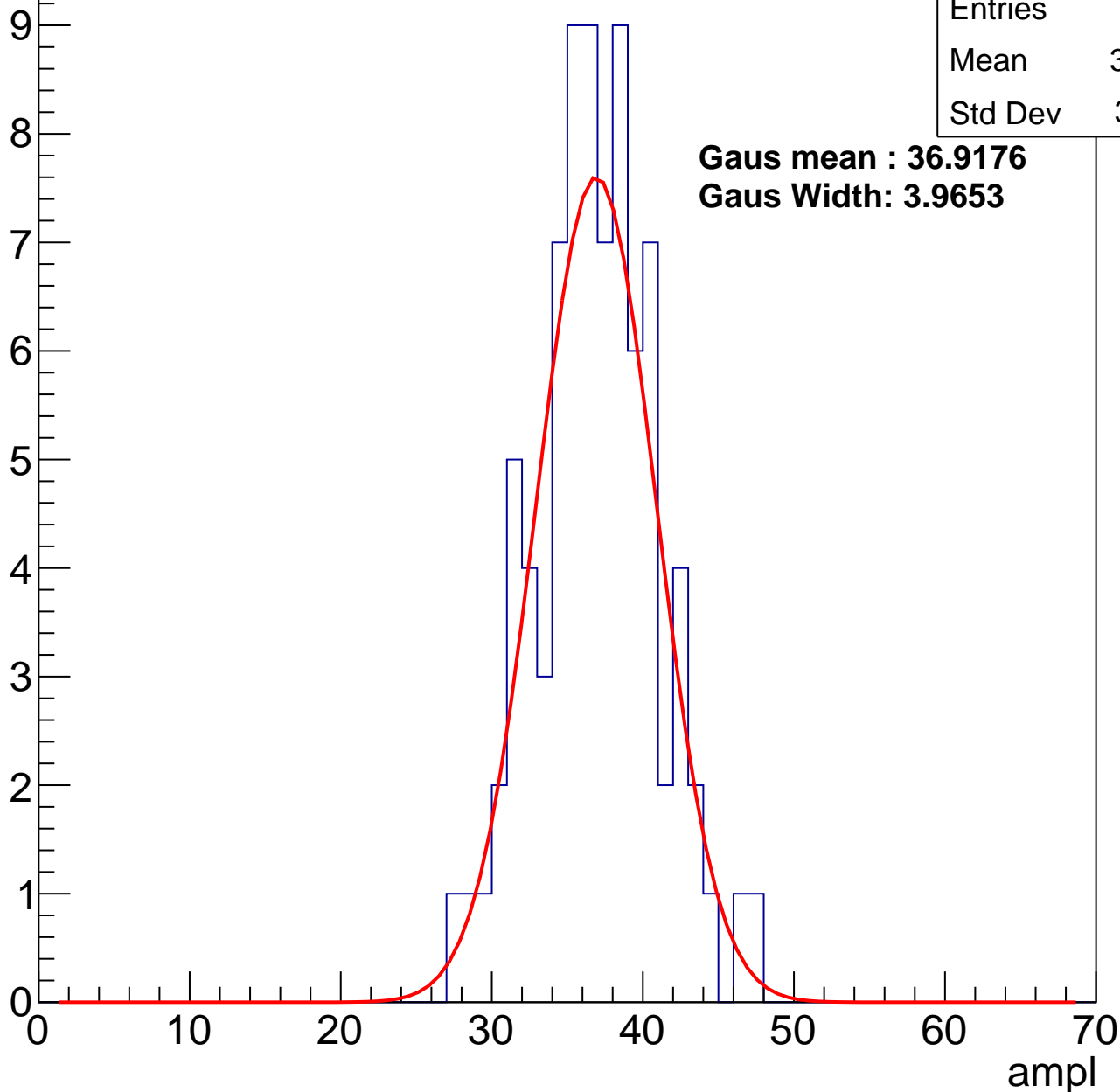
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.48
Std Dev	3.961

Gaus mean : 36.9176

Gaus Width: 3.9653



B1L101S, U17-ch24, adc2

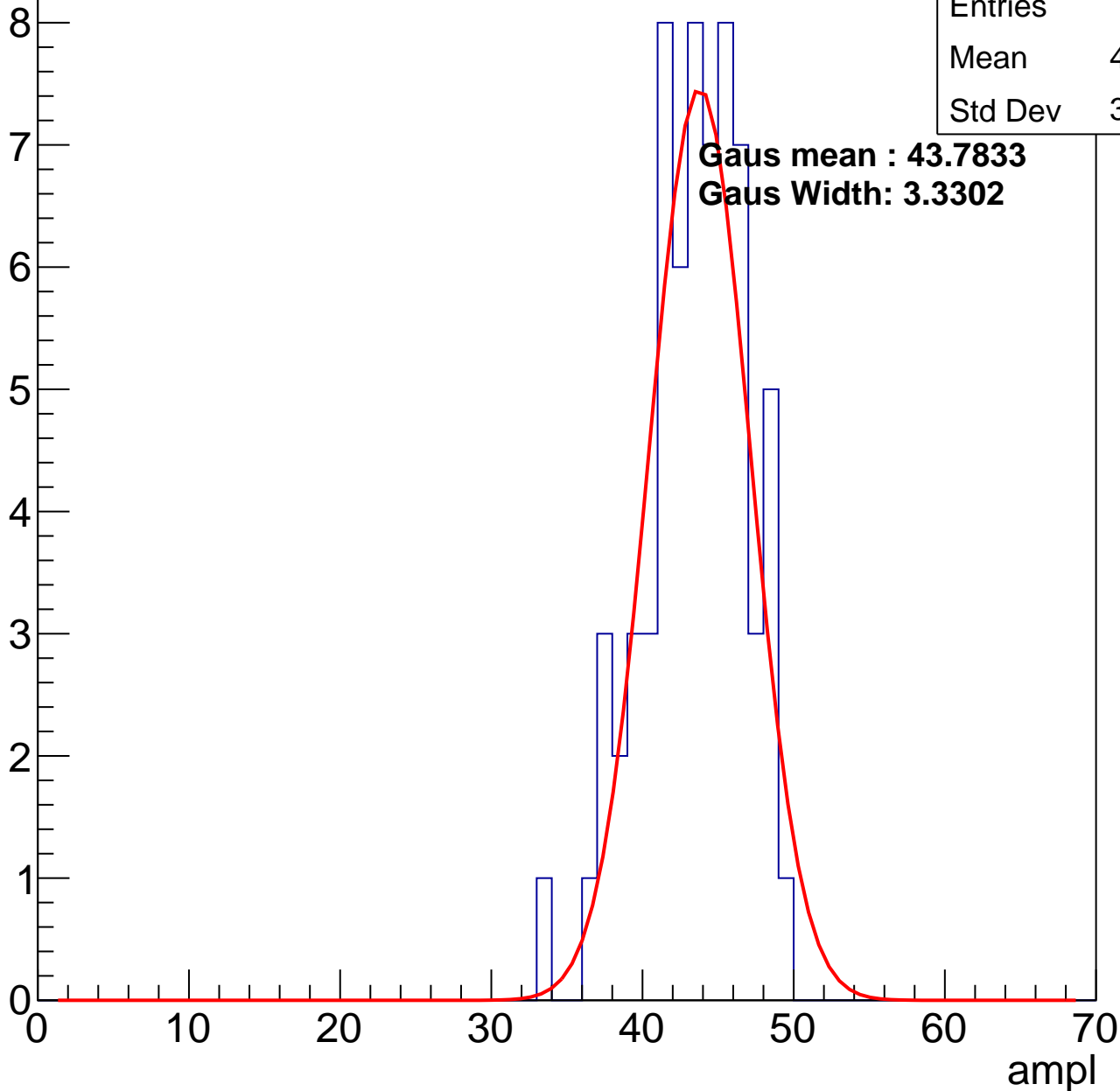
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.98
Std Dev	3.328

Gaus mean : 43.7833

Gaus Width: 3.3302

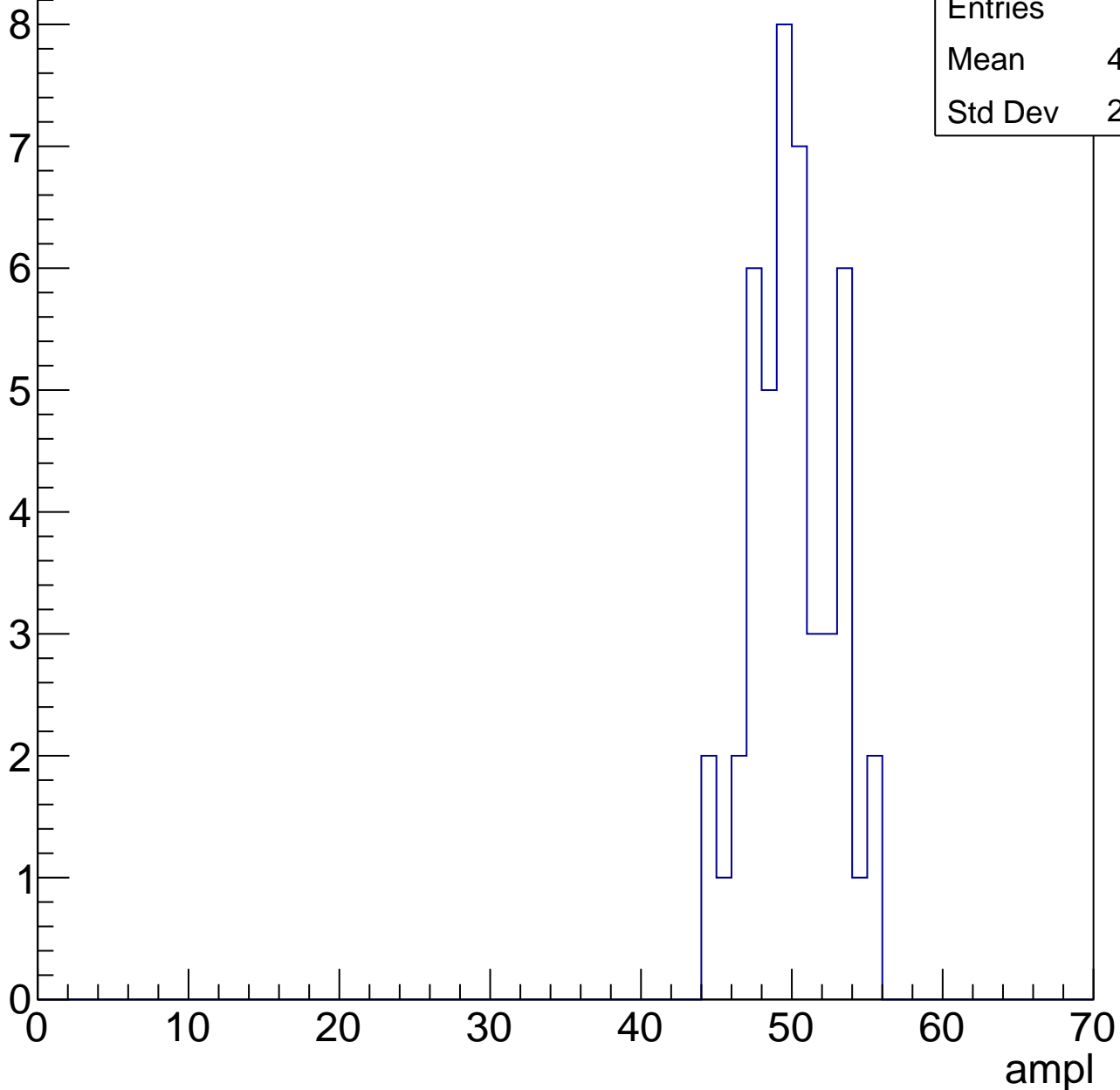


B1L101S, U17-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	49.57
Std Dev	2.708

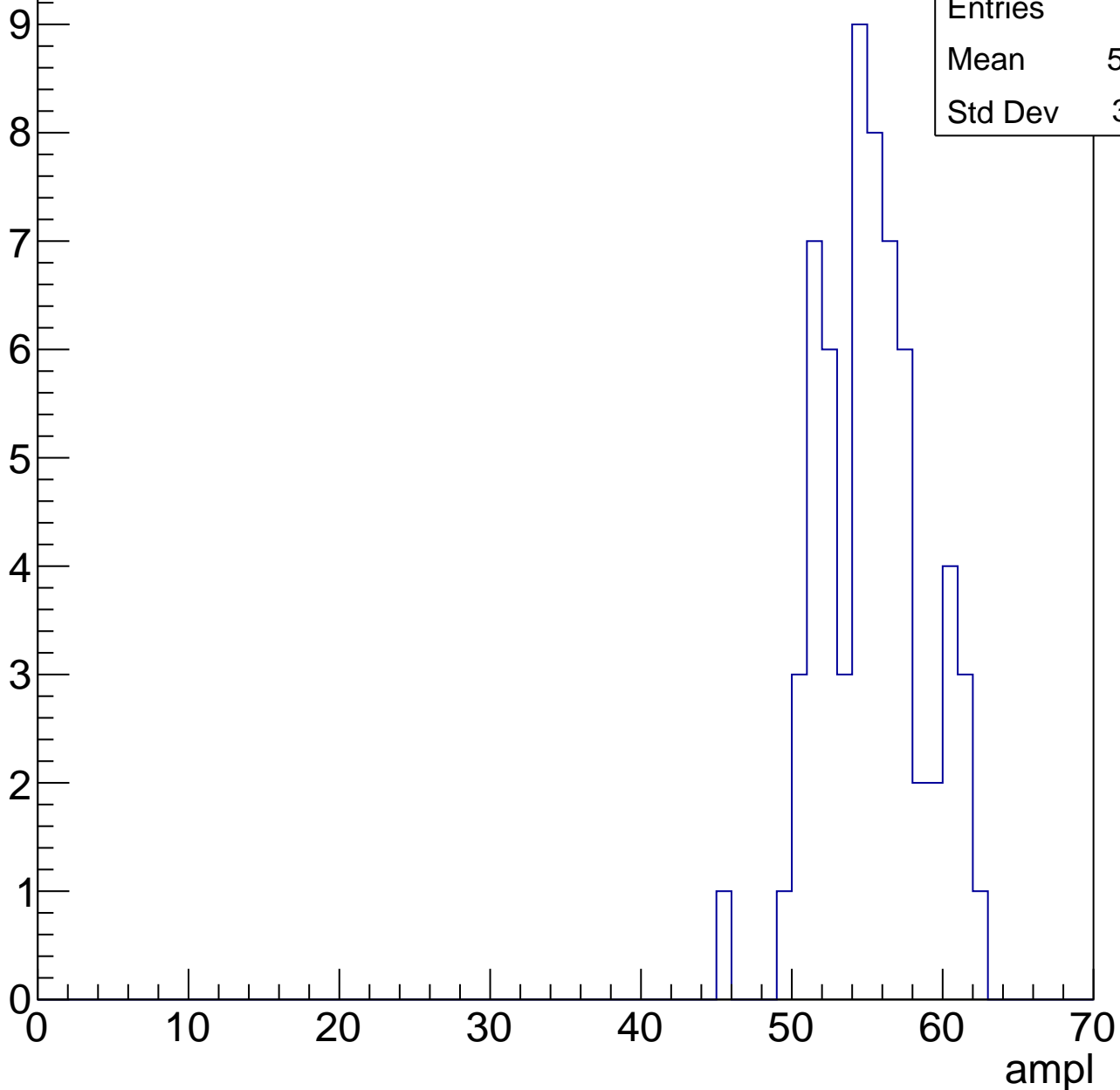


B1L101S, U17-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	54.78
Std Dev	3.411

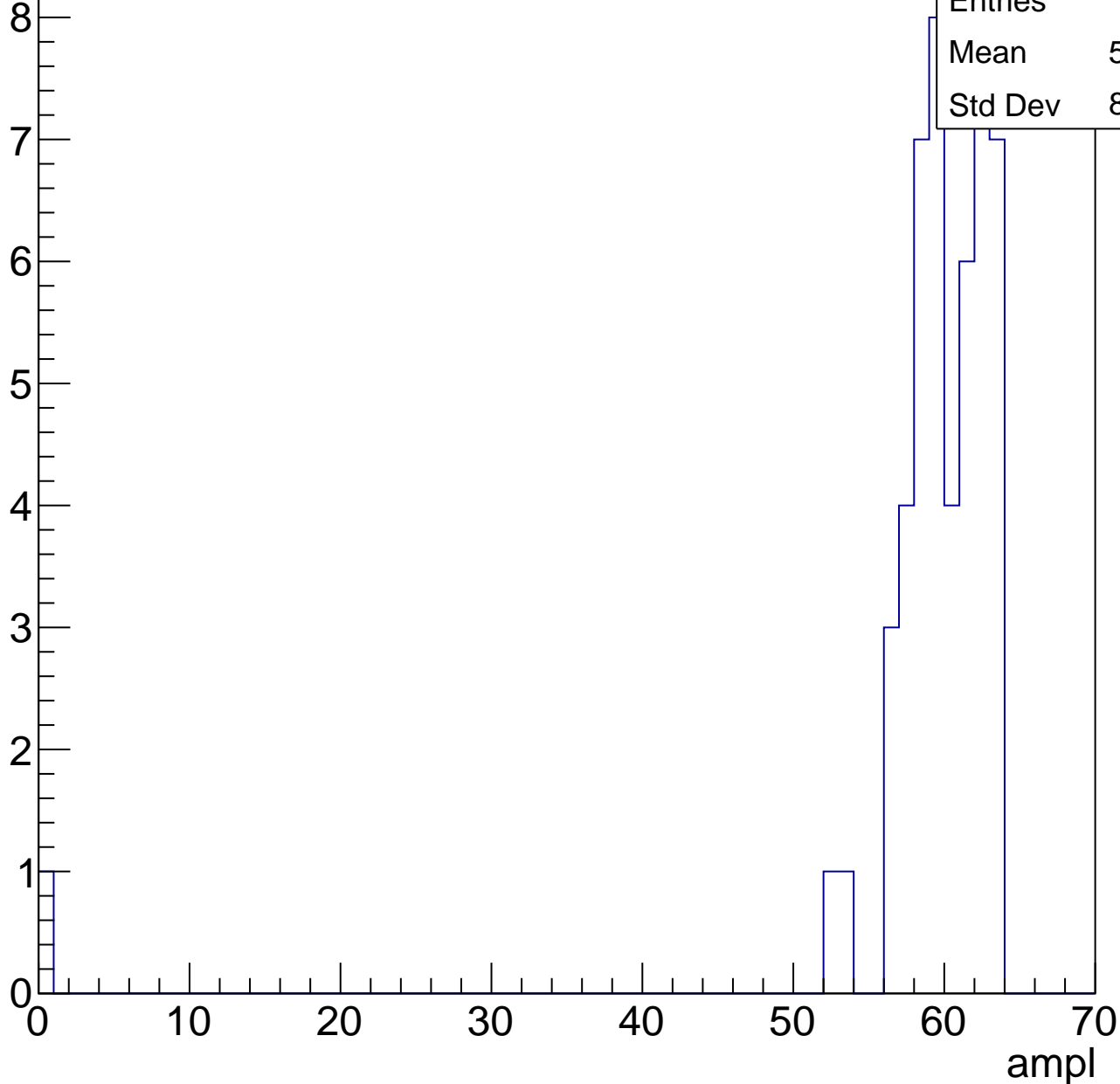


B1L101S, U17-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

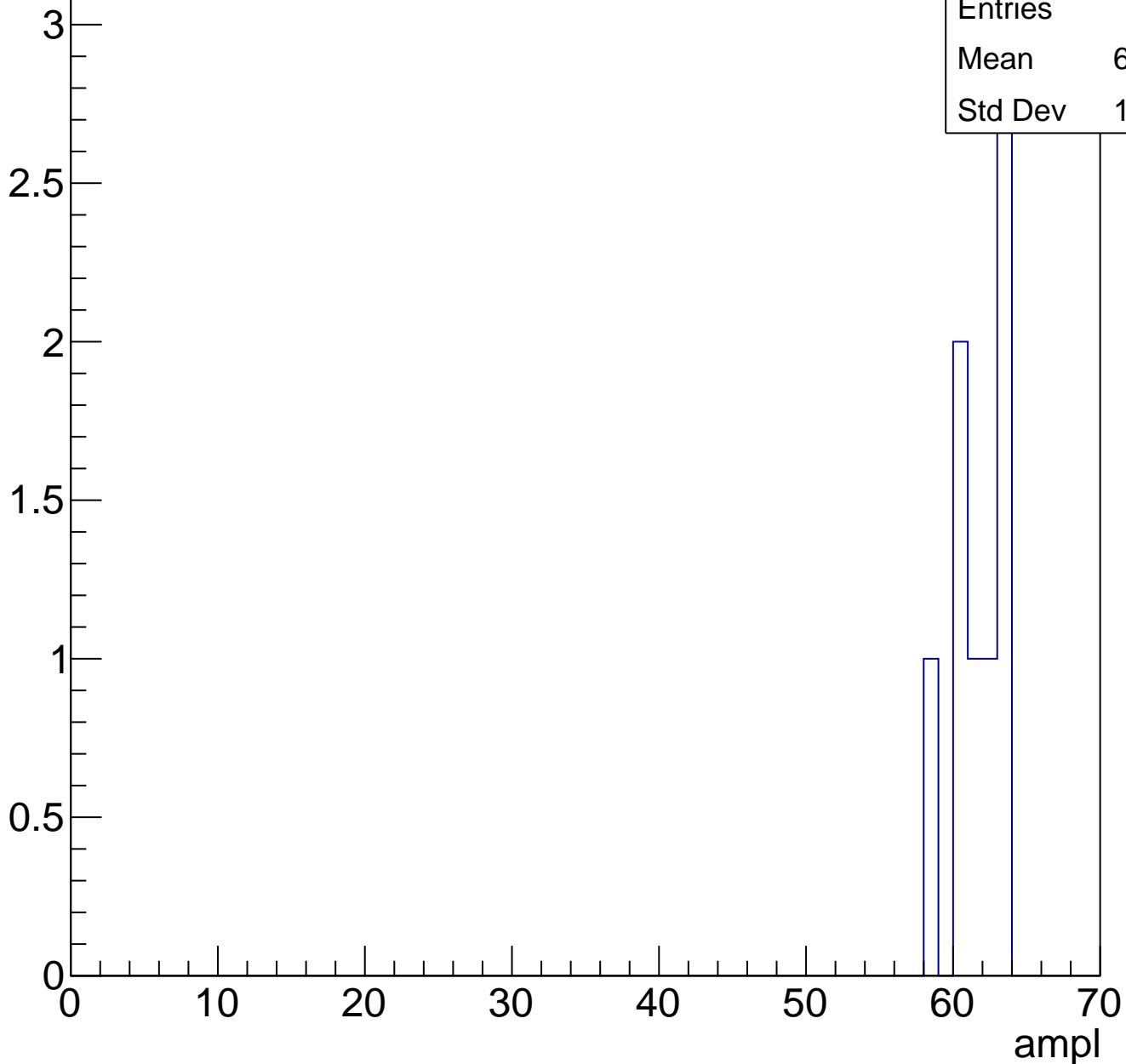
Entries	50
Mean	58.44
Std Dev	8.732



B1L101S, U17-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch25, adc0

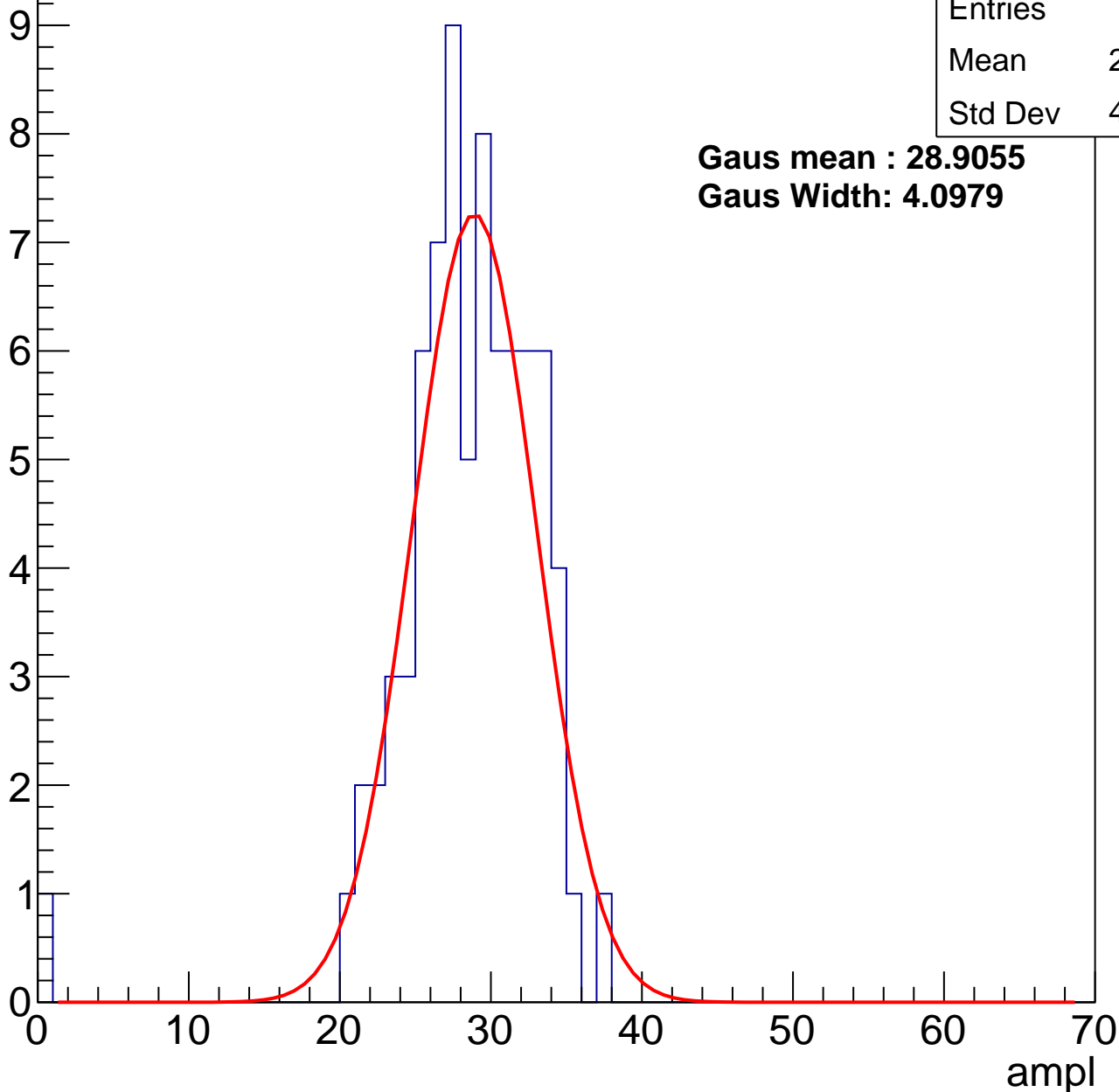
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.03
Std Dev	4.886

Gaus mean : 28.9055

Gaus Width: 4.0979



B1L101S, U17-ch25, adc1

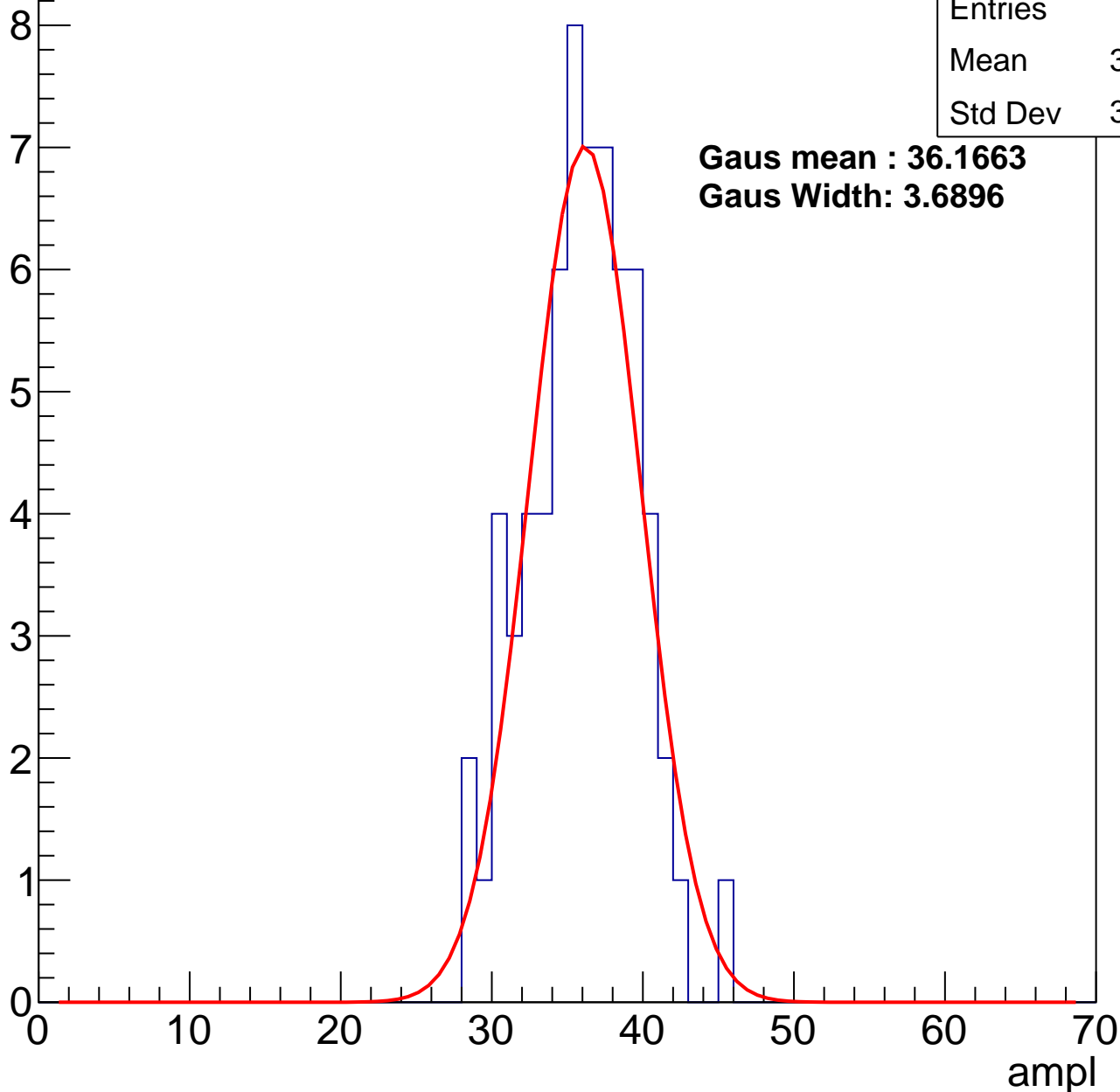
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.52
Std Dev	3.543

Gaus mean : 36.1663

Gaus Width: 3.6896



B1L101S, U17-ch25, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	53
Mean	41.42
Std Dev	3.456

Gaus mean : 41.6853

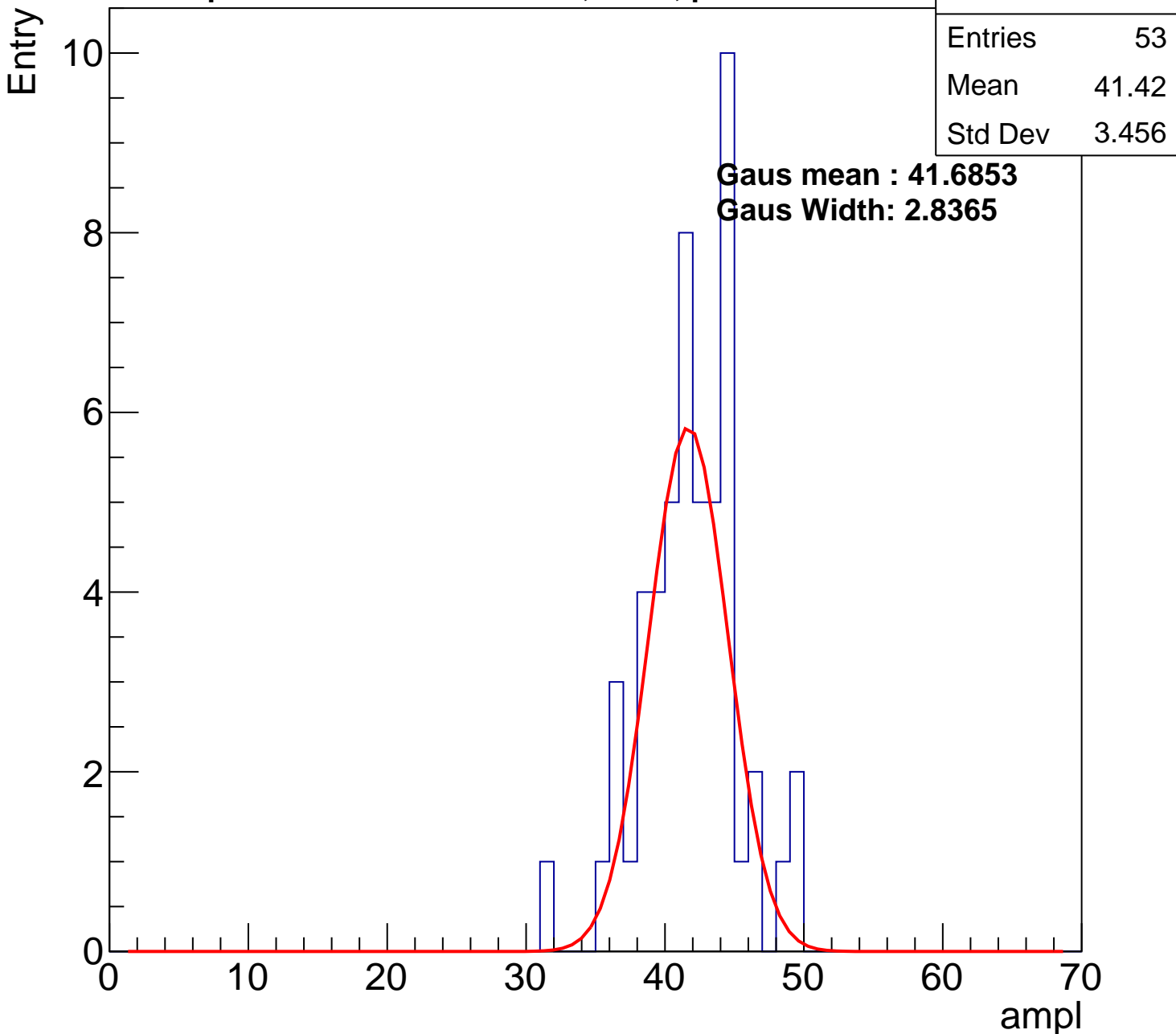
Gaus Width: 2.8365

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	47.74
Std Dev	3.596

Entry

10

8

6

4

2

0

0

10

20

30

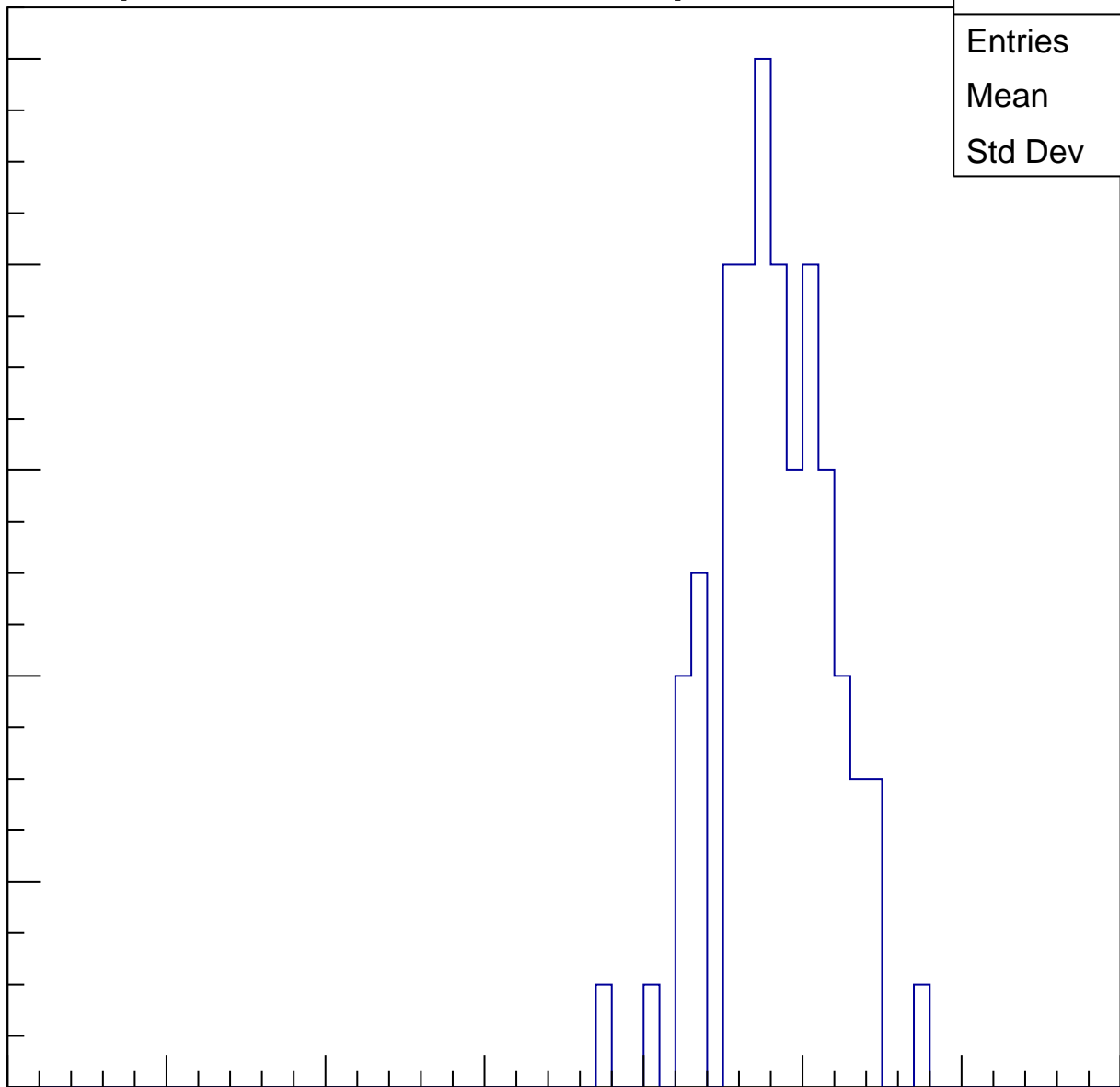
40

50

60

70

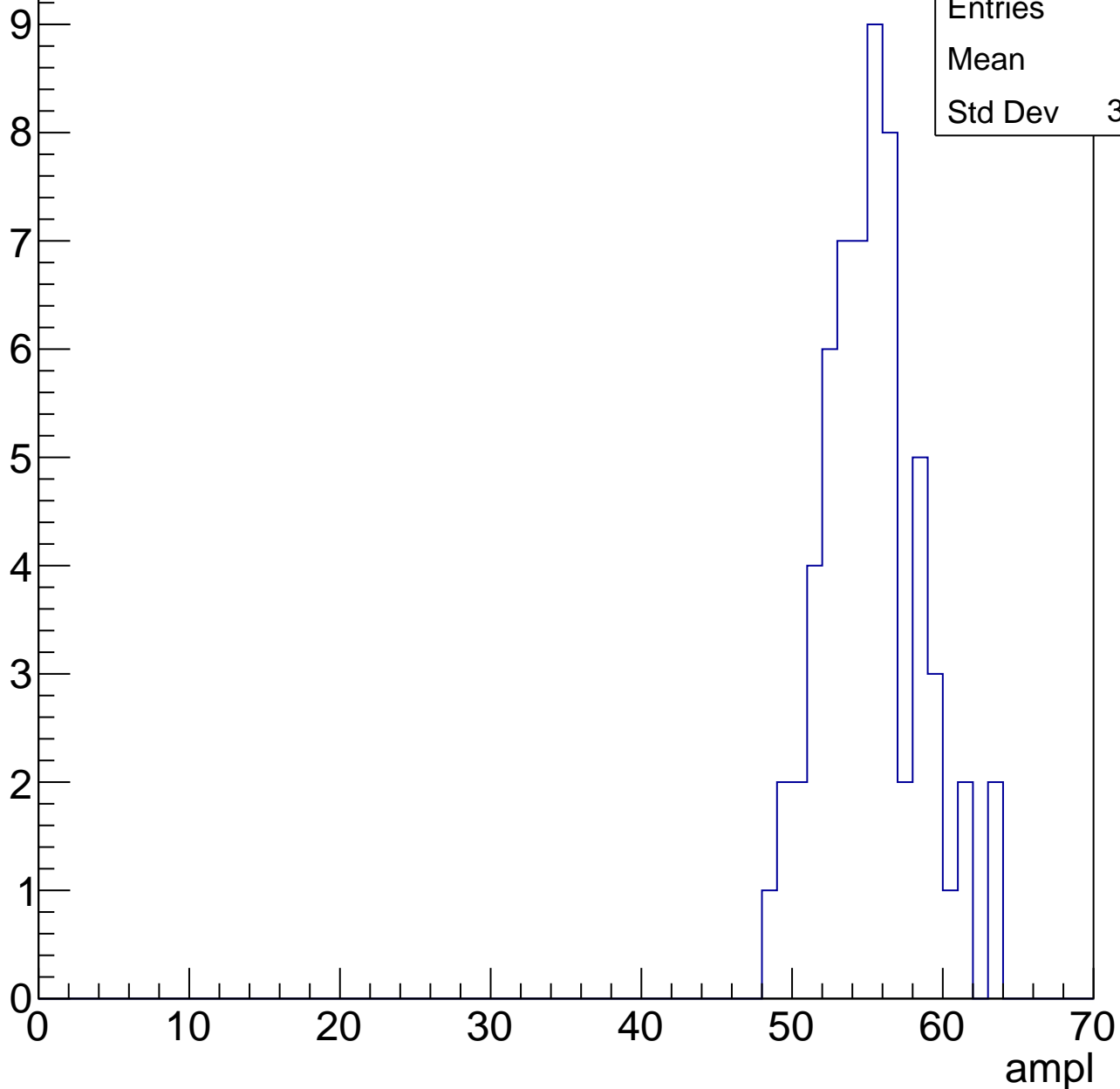
ampl



B1L101S, U17-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch25, adc5

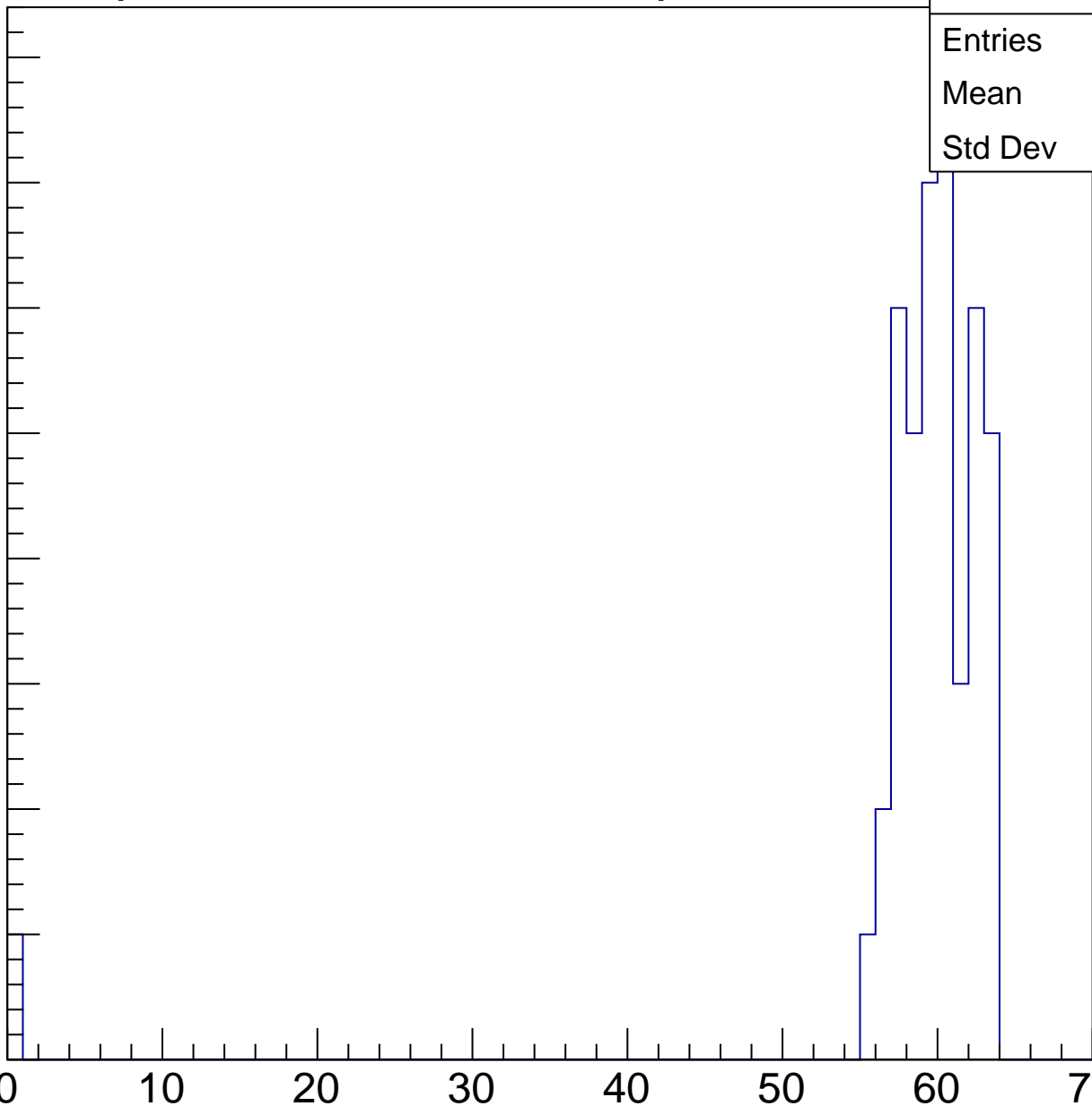
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.23
Std Dev	9.135

ampl

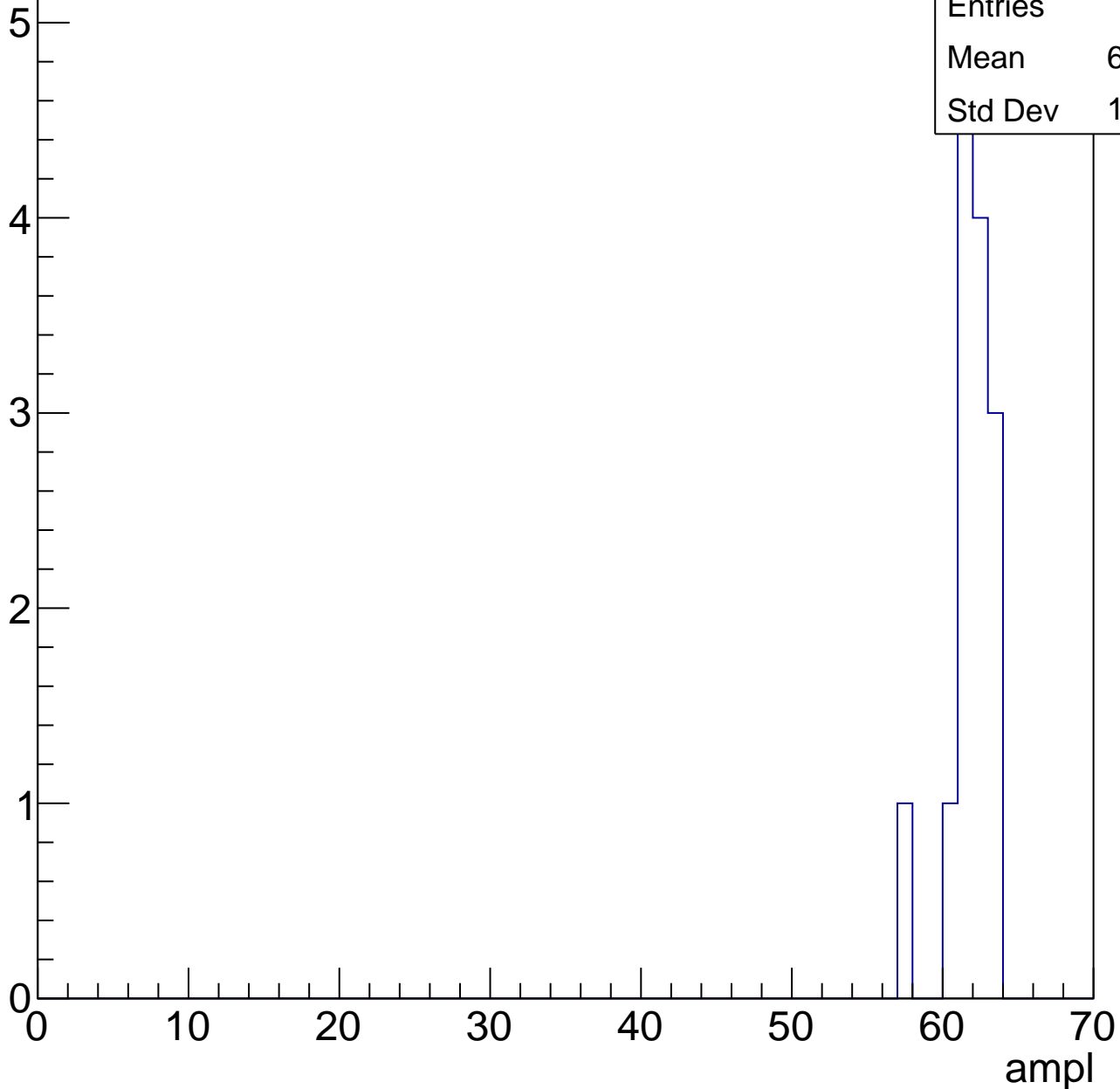


B1L101S, U17-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.36
Std Dev	1.493



B1L101S, U17-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch26, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	28.34
Std Dev	3.35

Gaus mean : 29.5610

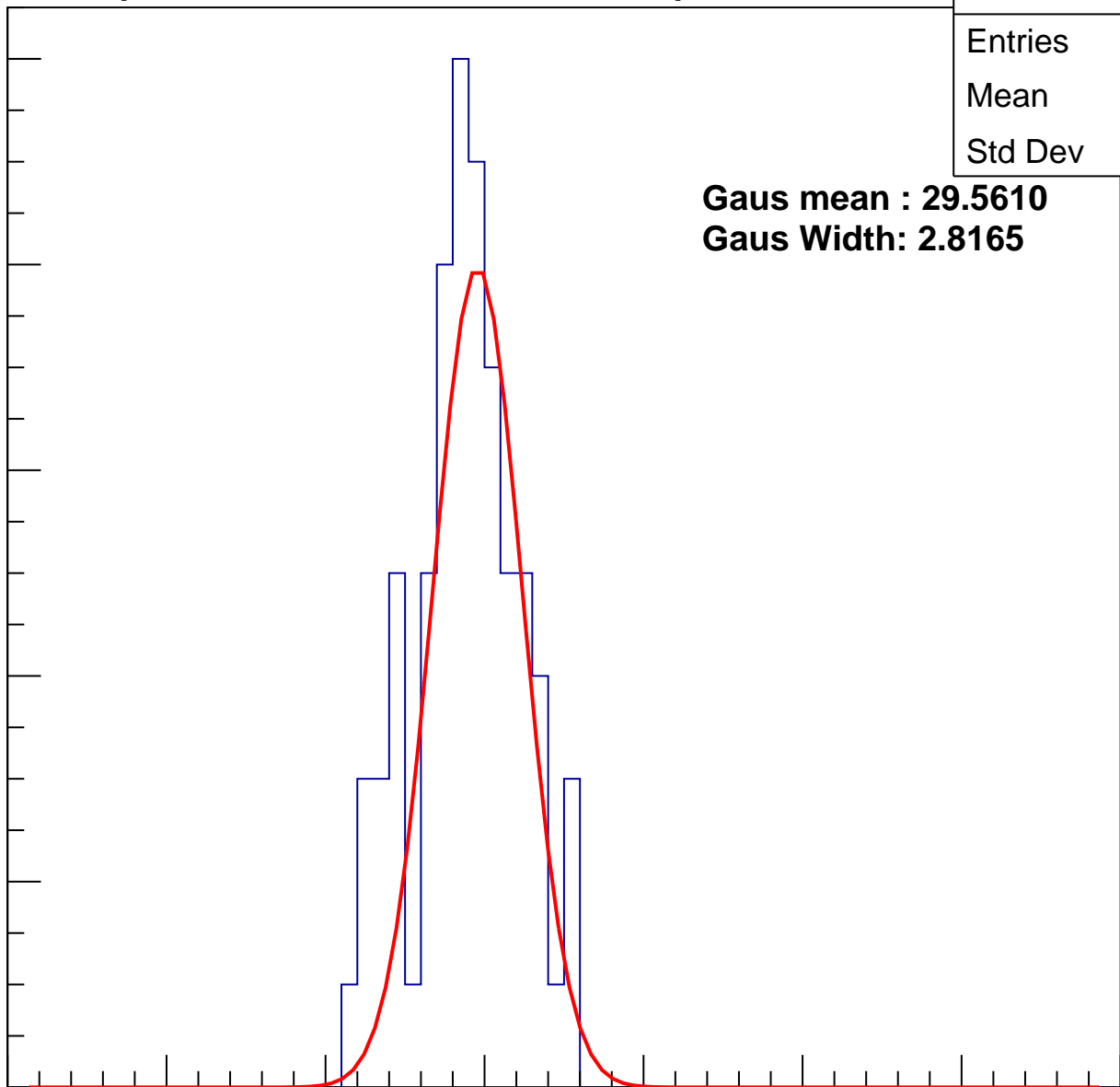
Gaus Width: 2.8165

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch26, adc1

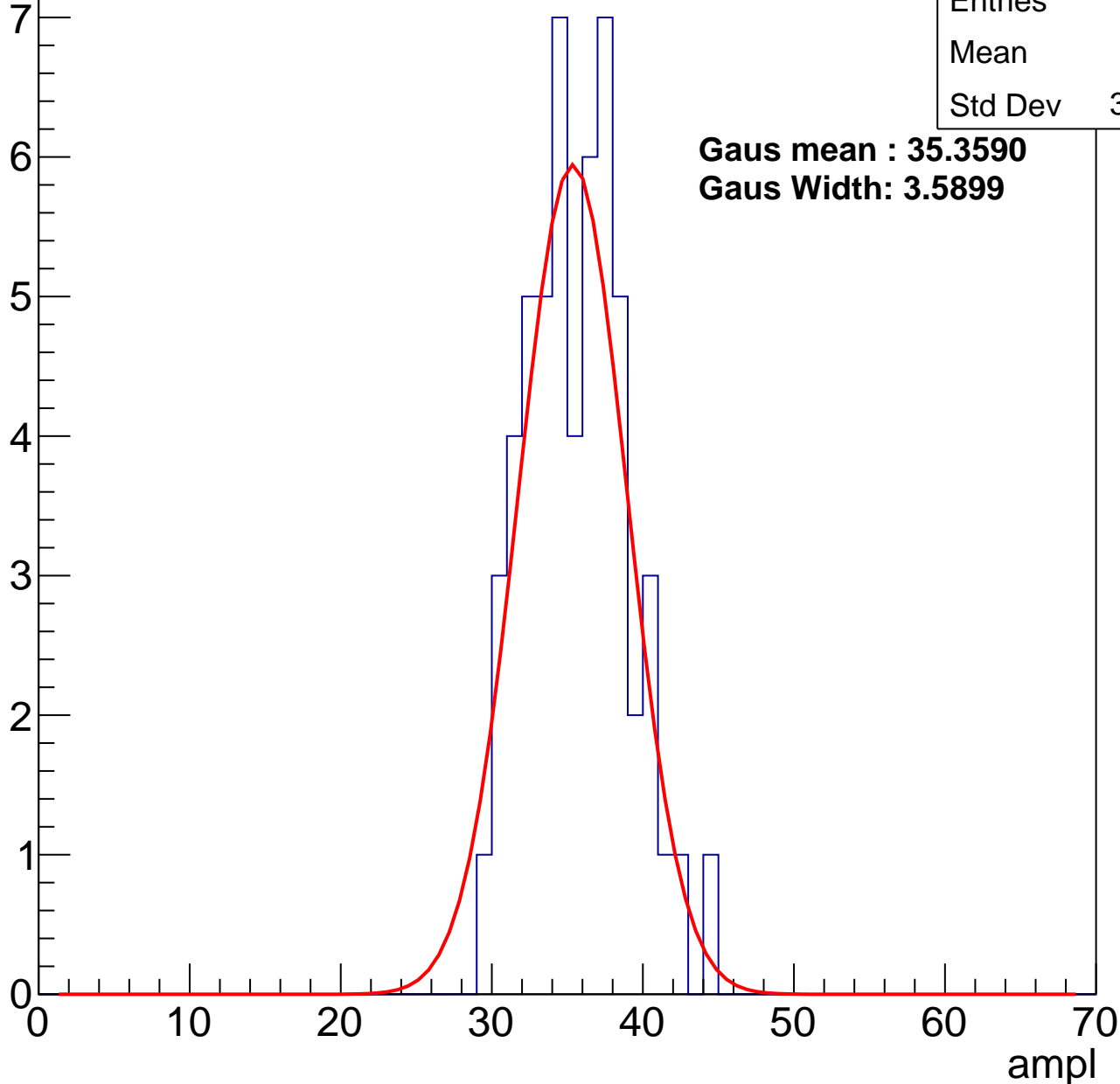
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.2
Std Dev	3.294

Gaus mean : 35.3590

Gaus Width: 3.5899



B1L101S, U17-ch26, adc2

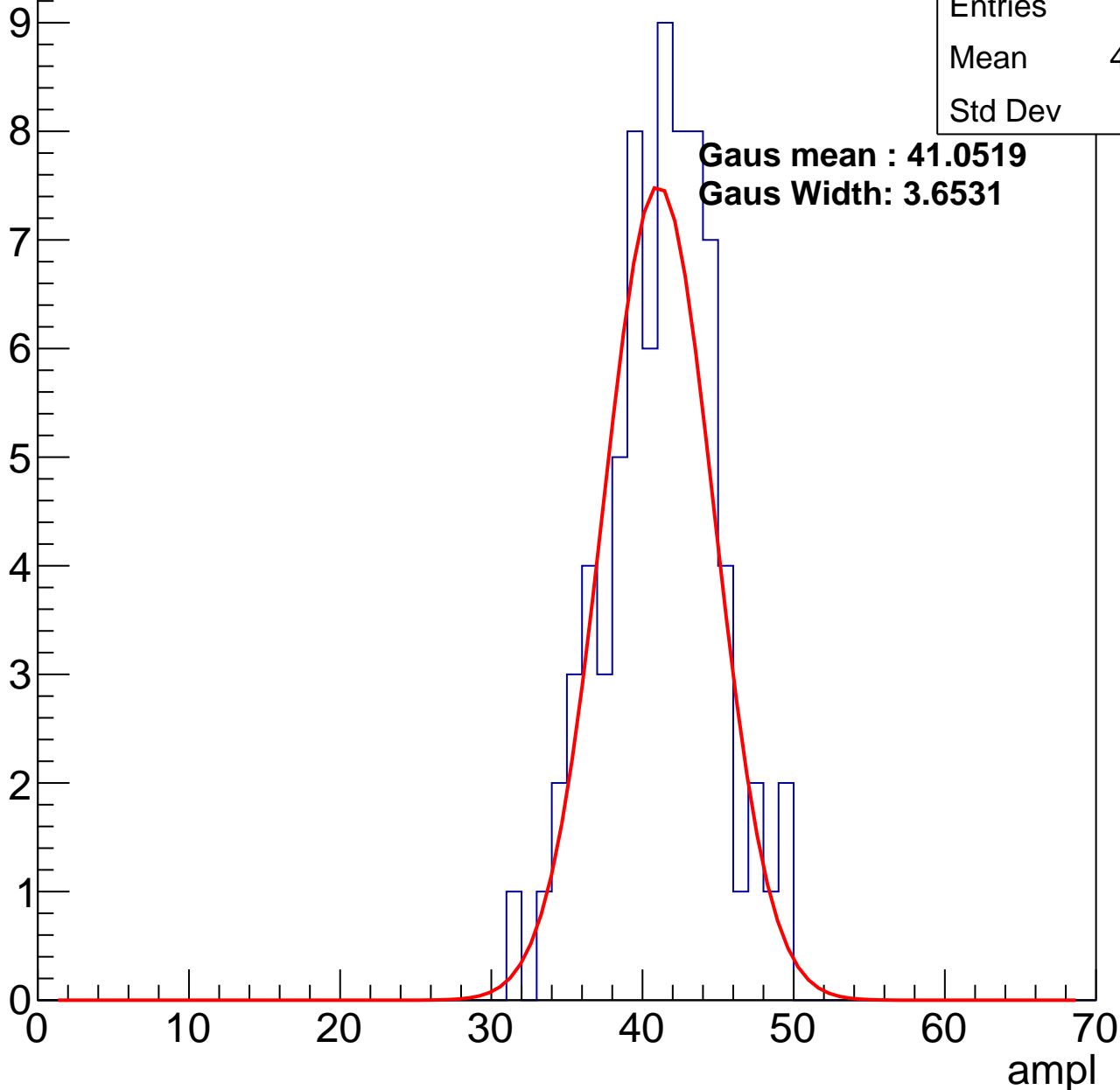
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	40.76
Std Dev	3.72

Gaus mean : 41.0519

Gaus Width: 3.6531

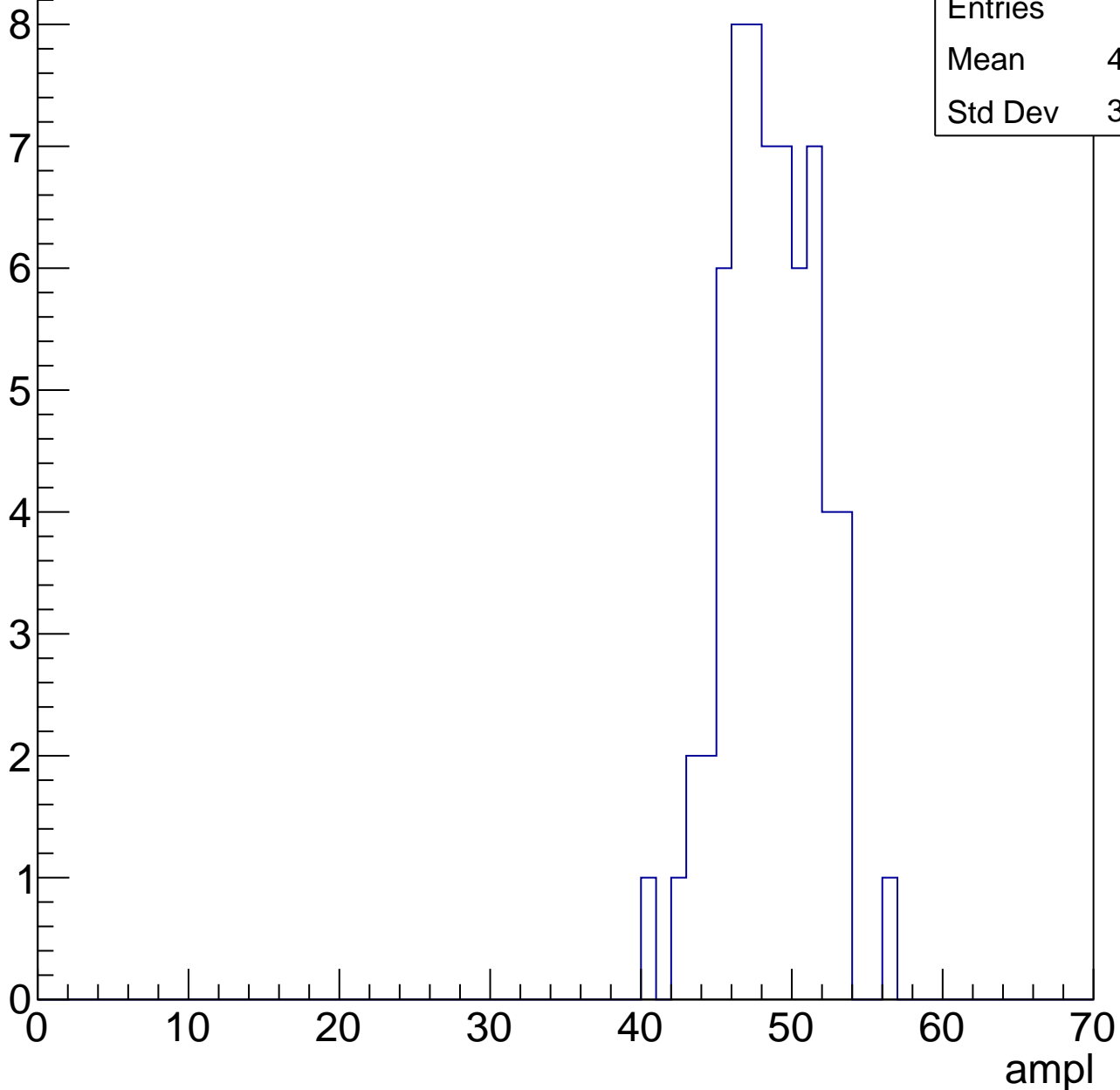


B1L101S, U17-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.16
Std Dev	3.053

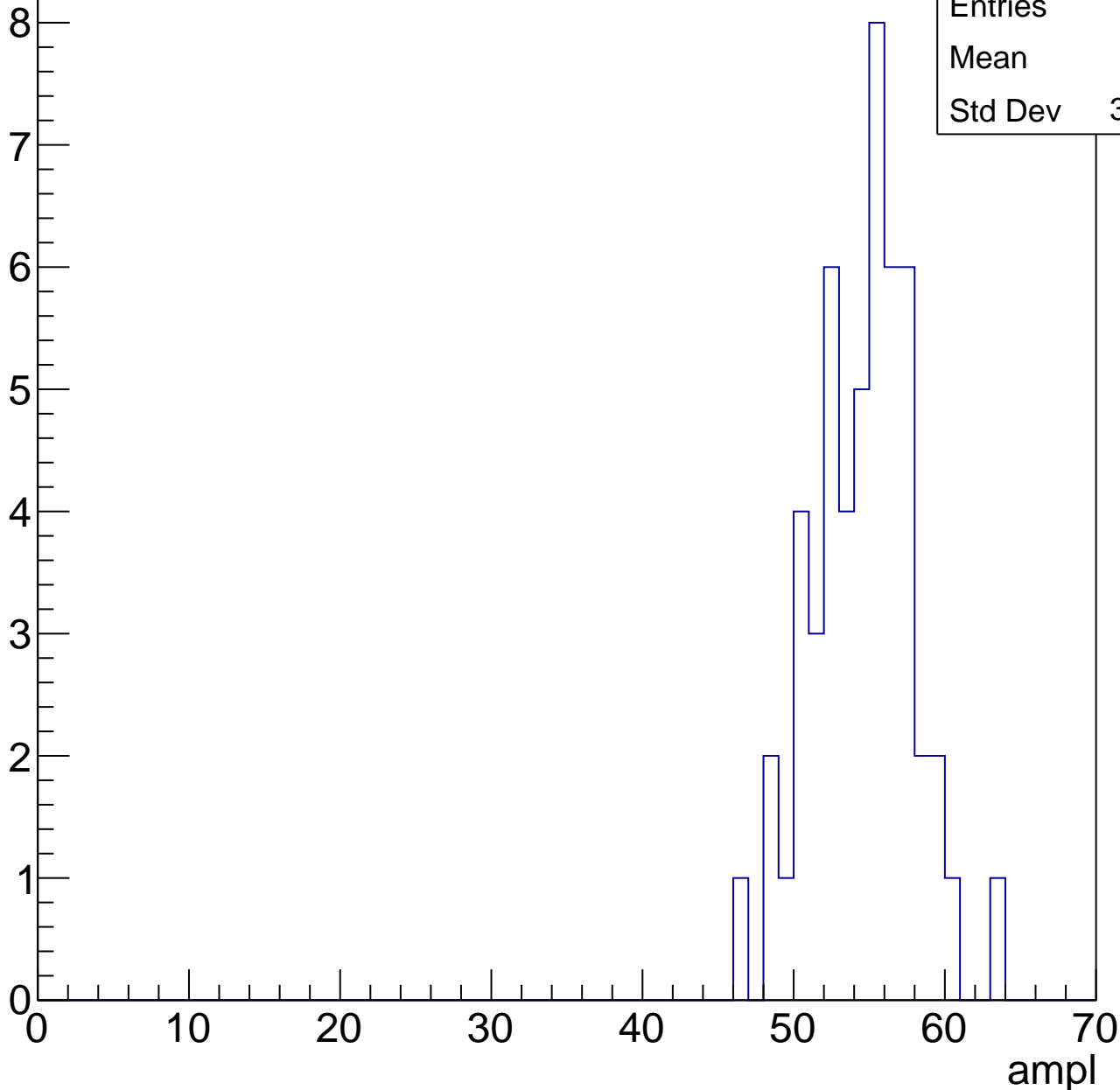


B1L101S, U17-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.1
Std Dev	3.295



B1L101S, U17-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.11
Std Dev	7.894

ampl

0

10

20

30

40

50

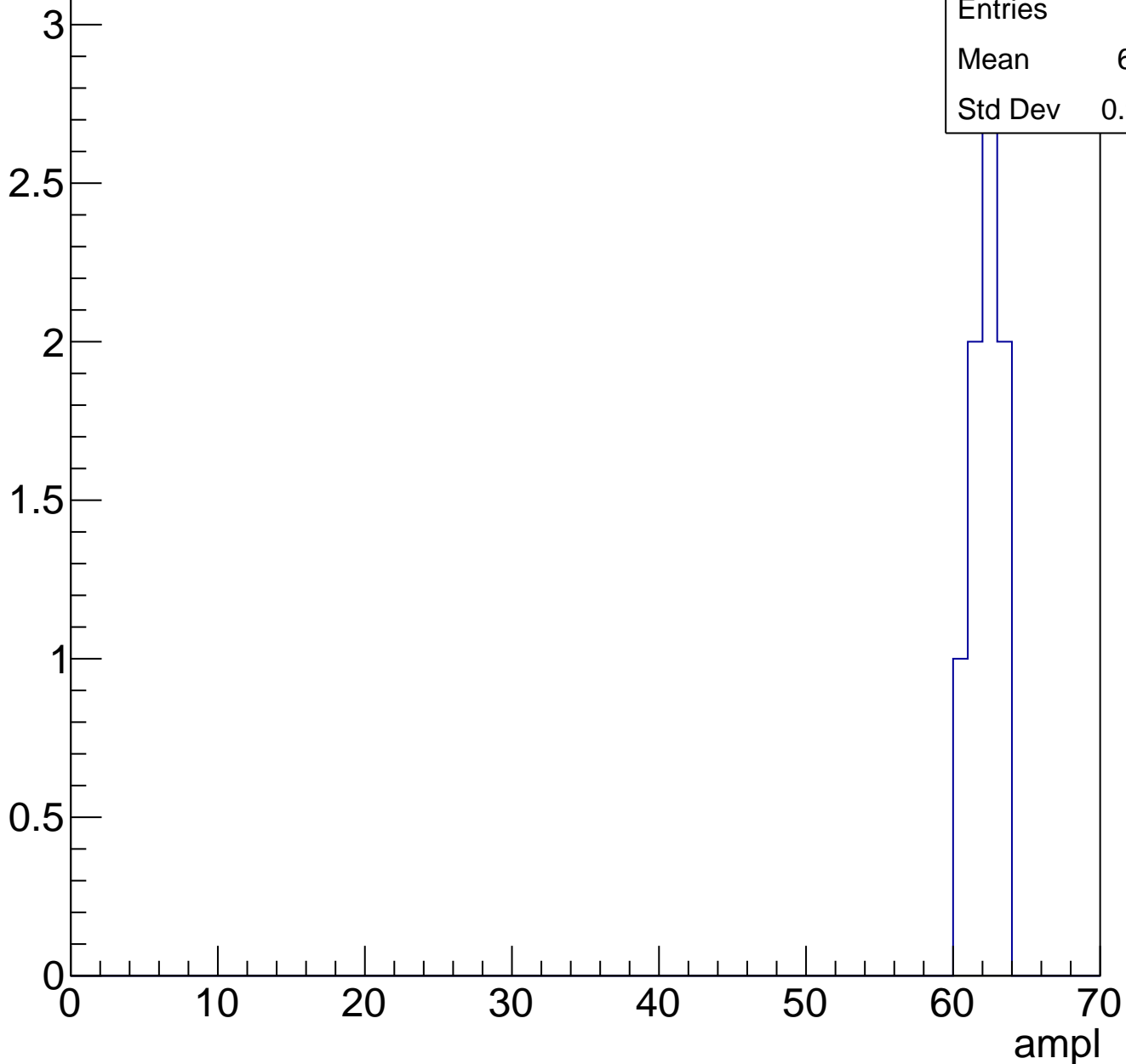
60

70

B1L101S, U17-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	61.75
Std Dev	0.9682

B1L101S, U17-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch27, adc0

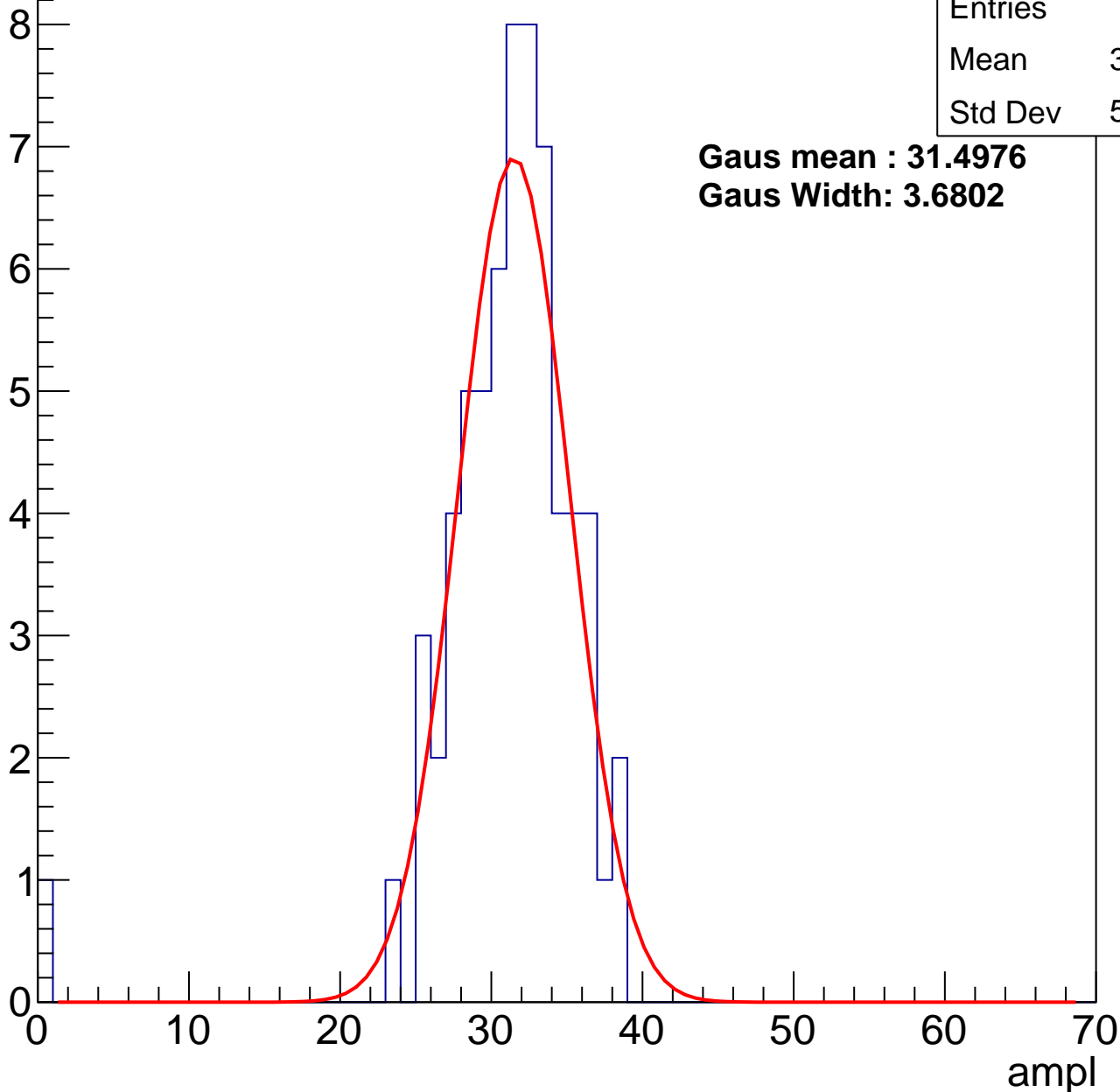
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.63
Std Dev	5.083

Gaus mean : 31.4976

Gaus Width: 3.6802



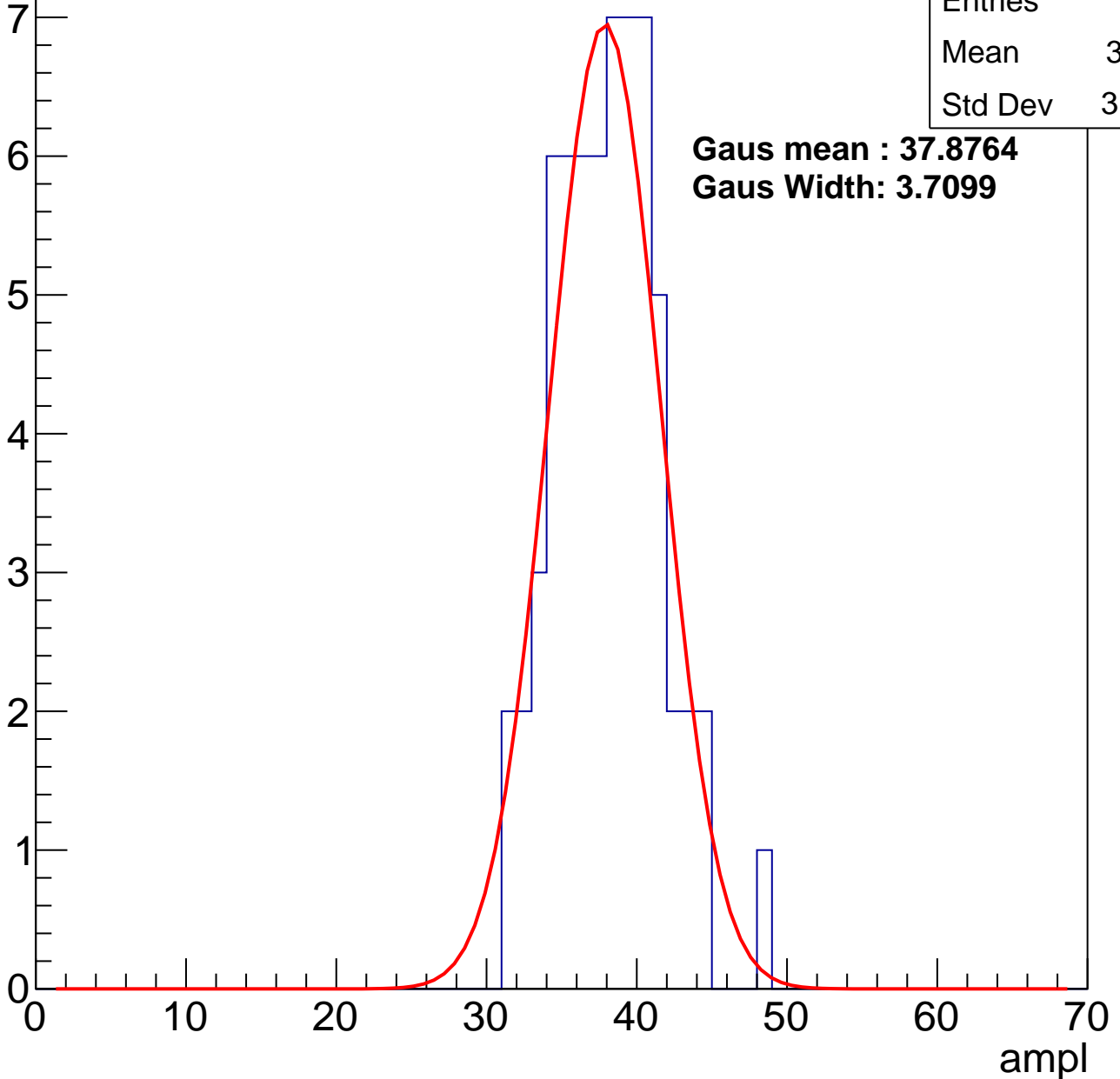
B1L101S, U17-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.61
Std Dev	3.422

Gaus mean : 37.8764
Gaus Width: 3.7099

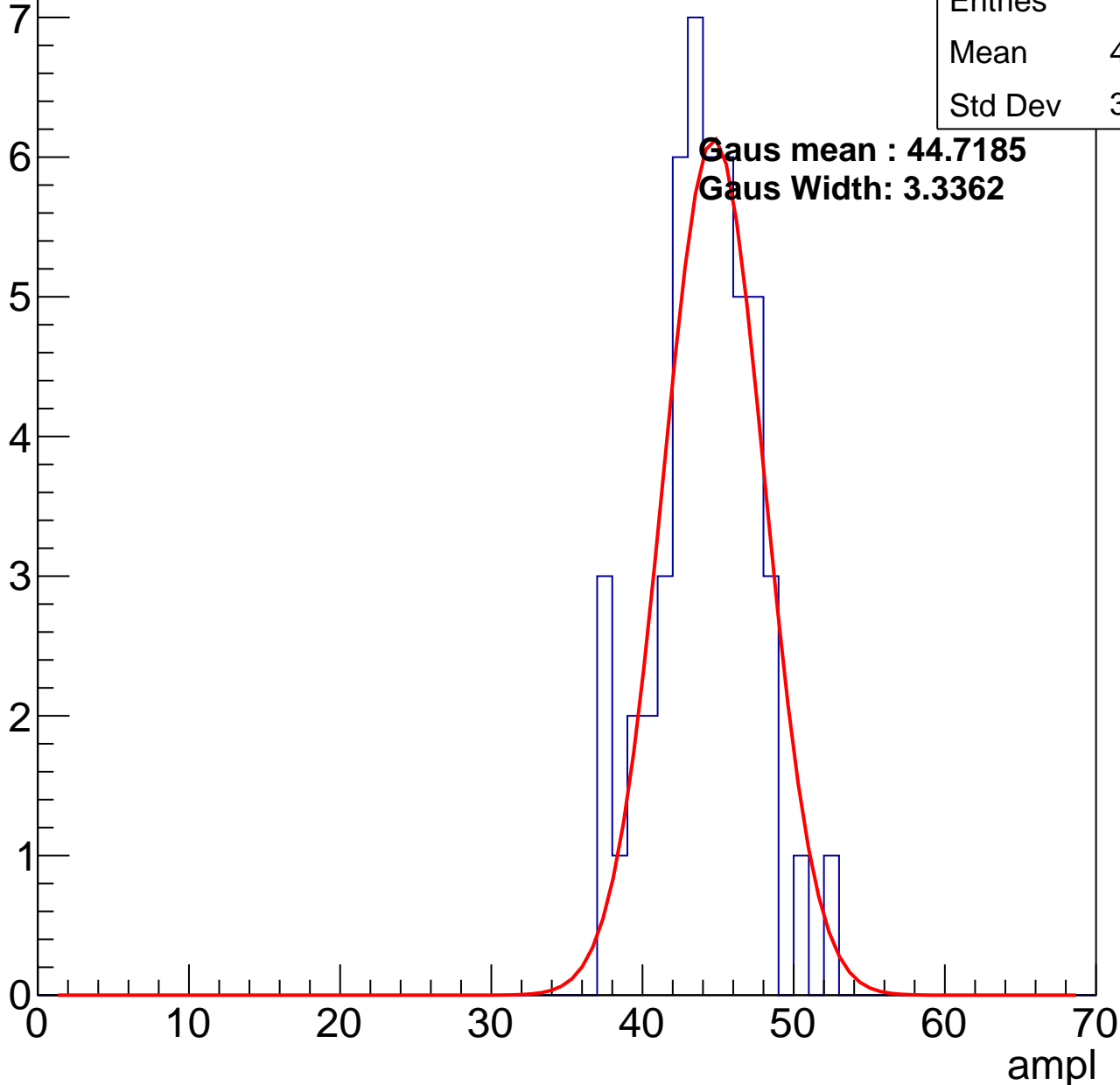


B1L101S, U17-ch27, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

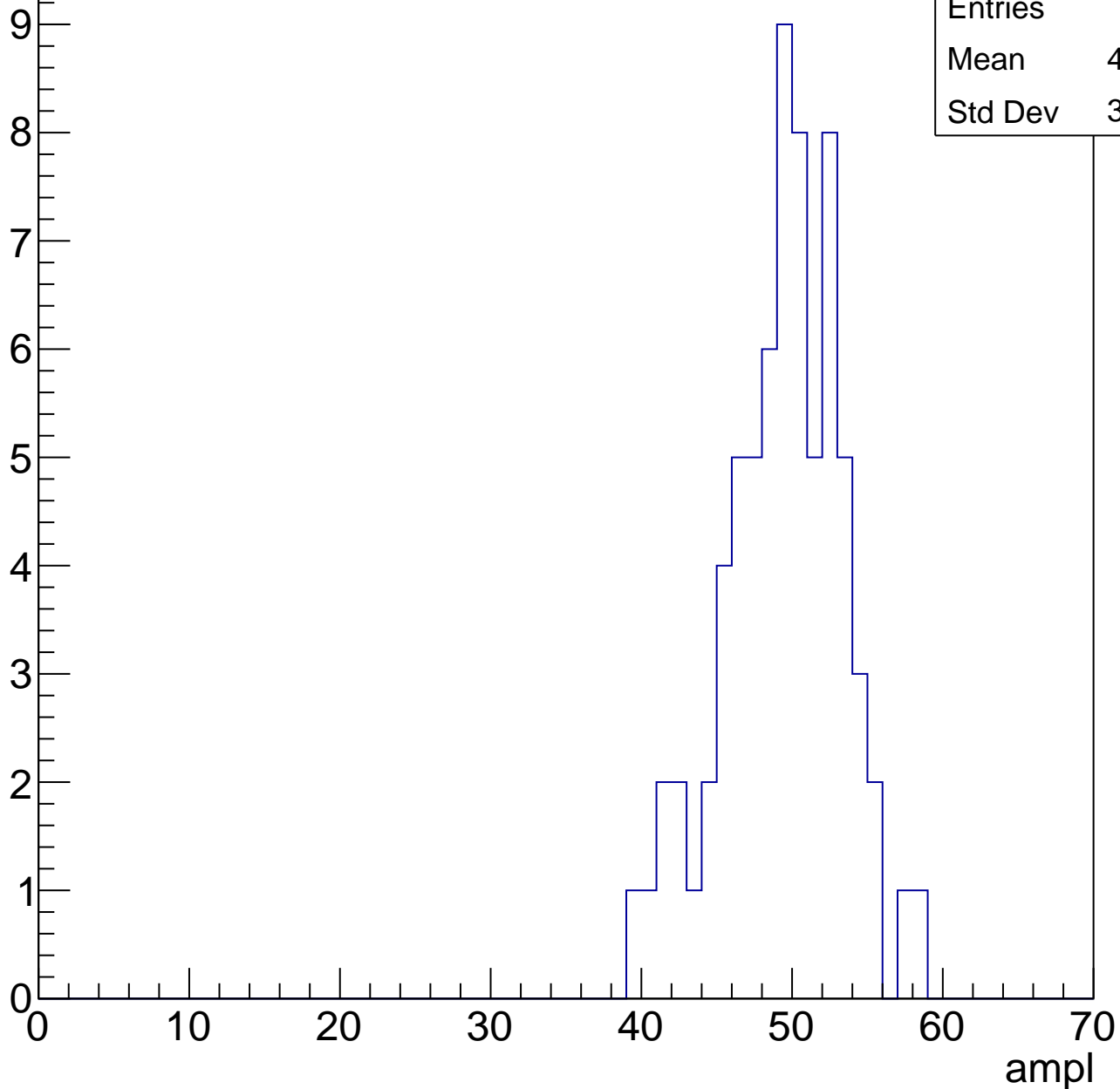
Entries	51
Mean	43.69
Std Dev	3.245



B1L101S, U17-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

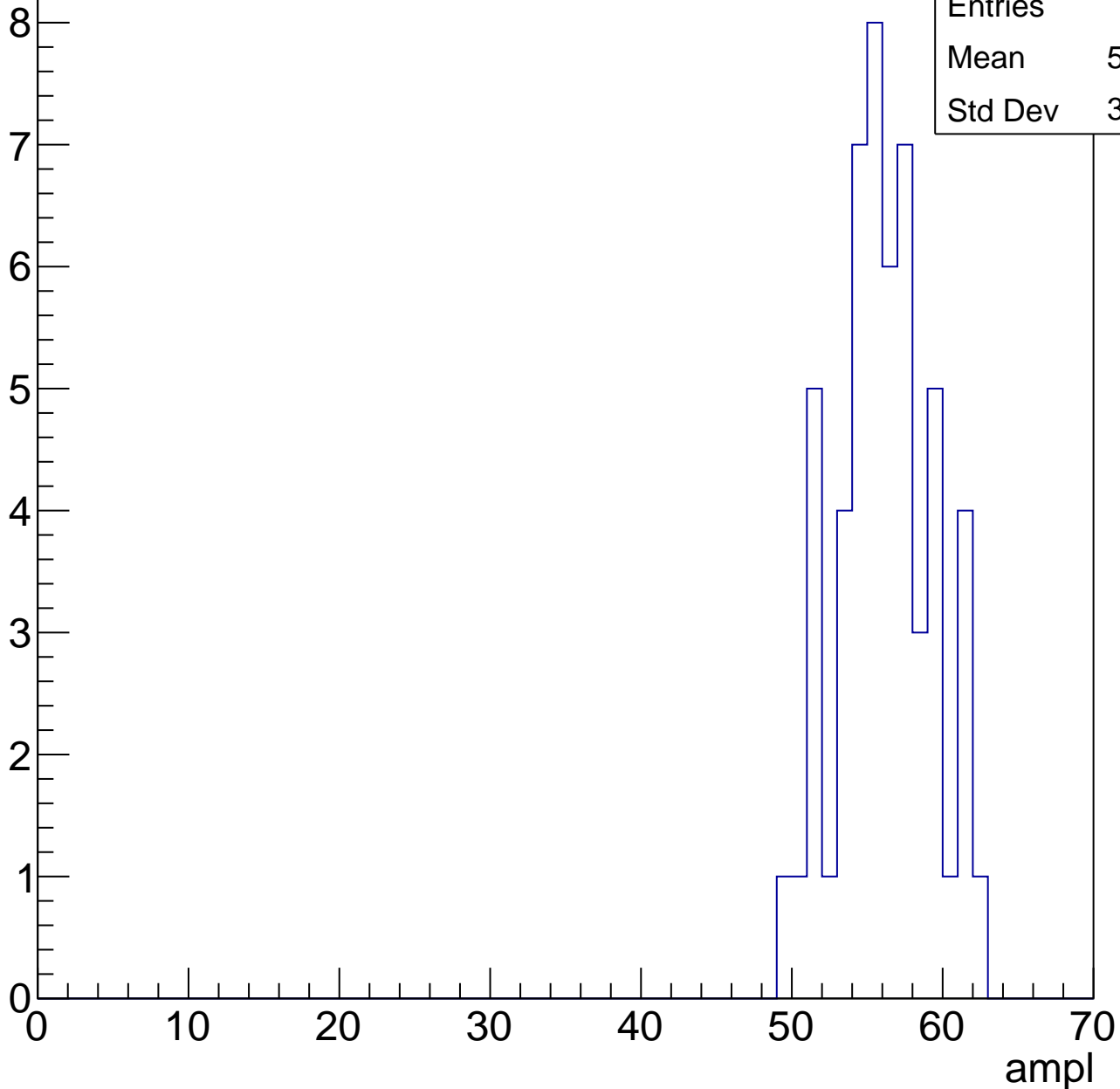


B1L101S, U17-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.67
Std Dev	3.067

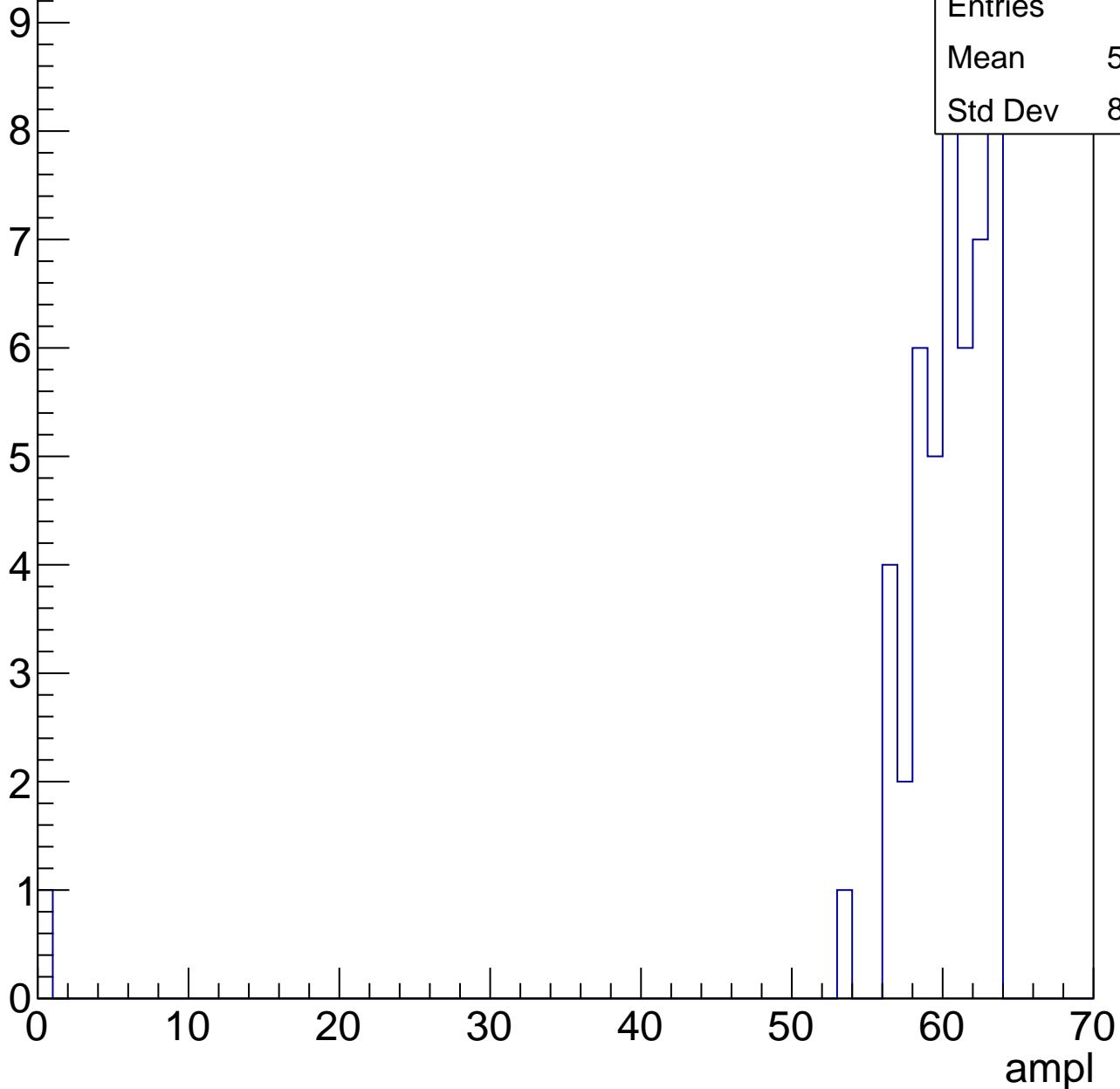


B1L101S, U17-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.73
Std Dev	8.792



B1L101S, U17-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch28, adc0

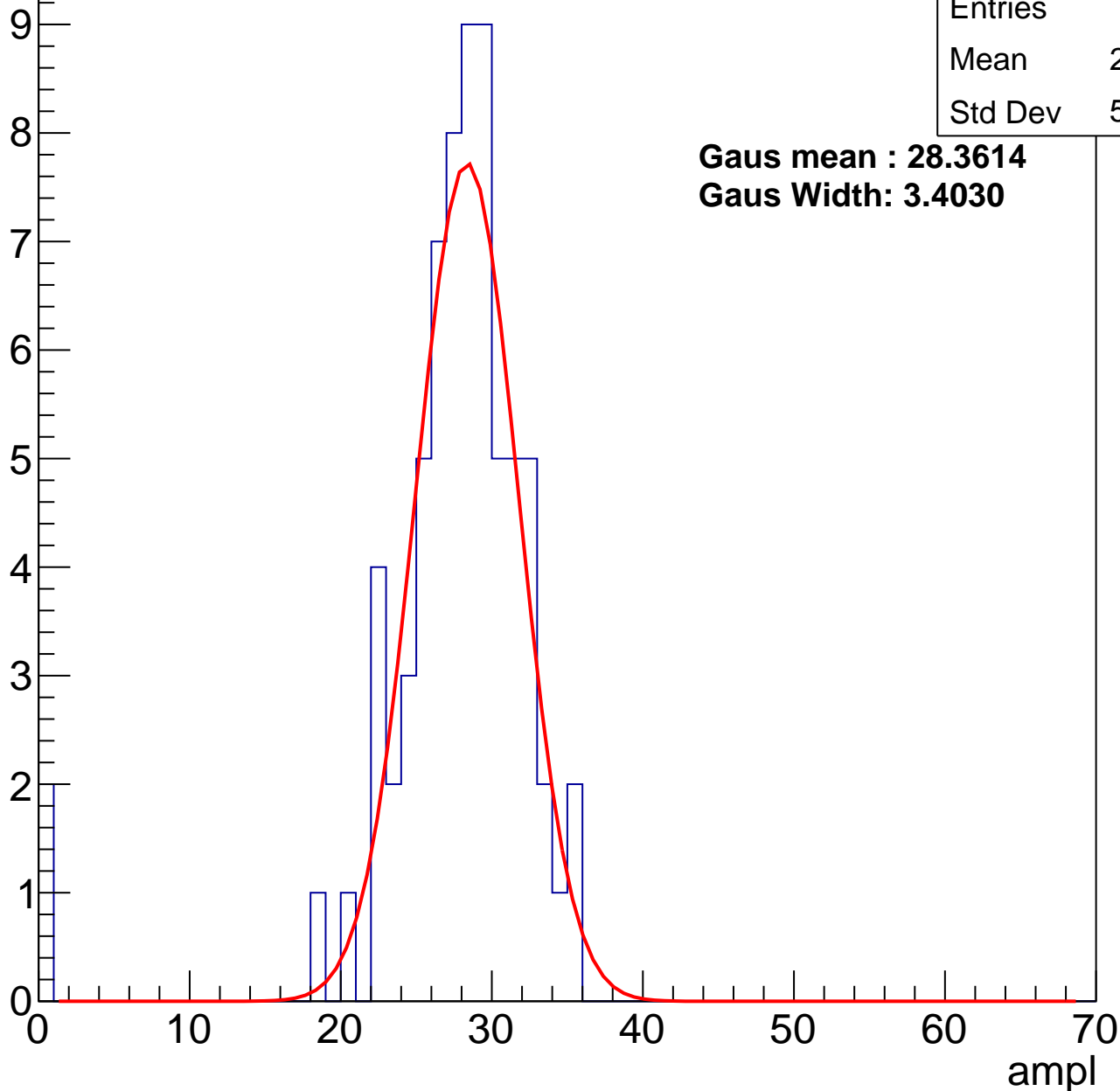
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	26.97
Std Dev	5.714

Gaus mean : 28.3614

Gaus Width: 3.4030



B1L101S, U17-ch28, adc1

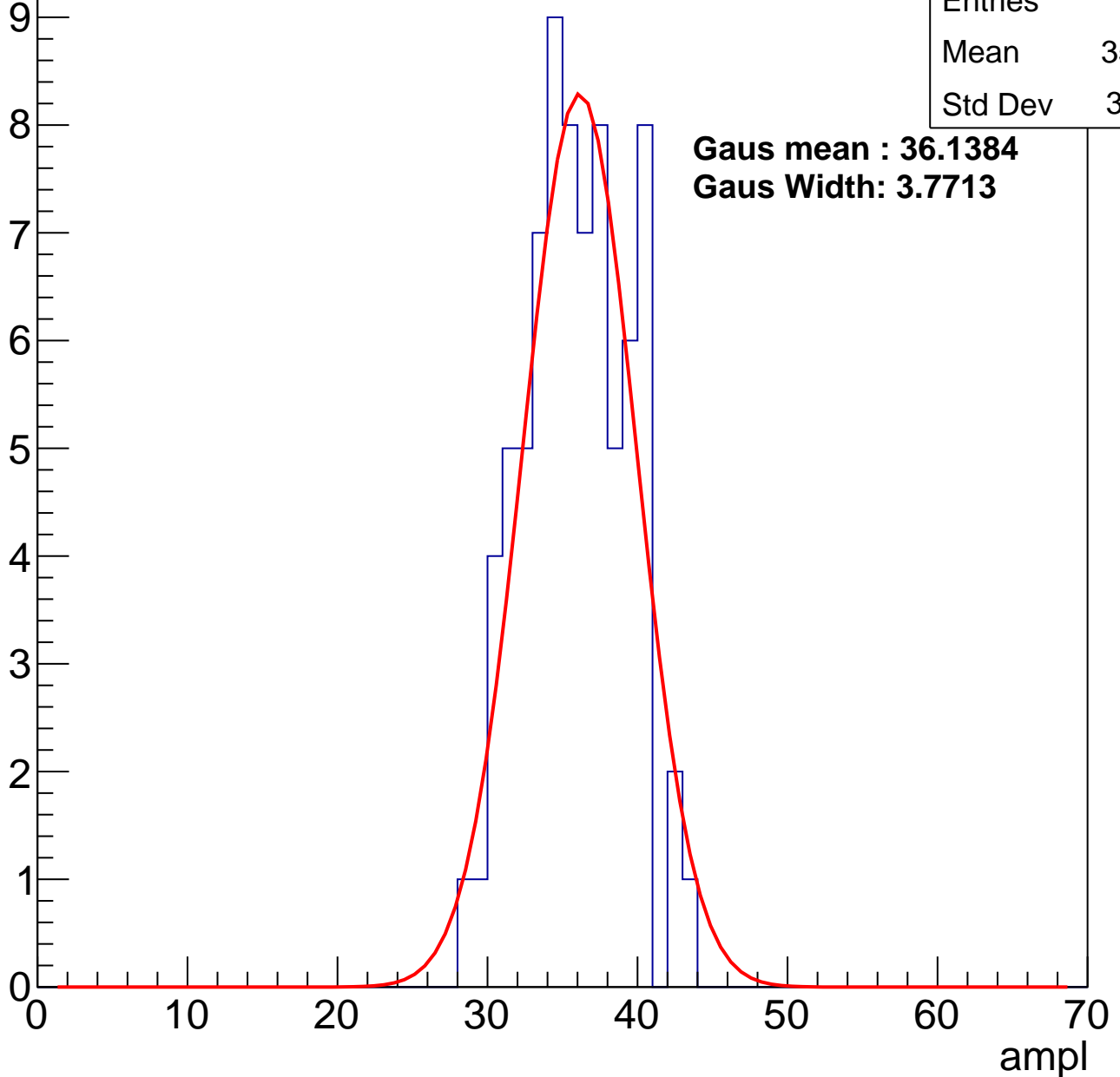
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	35.43
Std Dev	3.371

Gaus mean : 36.1384

Gaus Width: 3.7713



B1L101S, U17-ch28, adc2

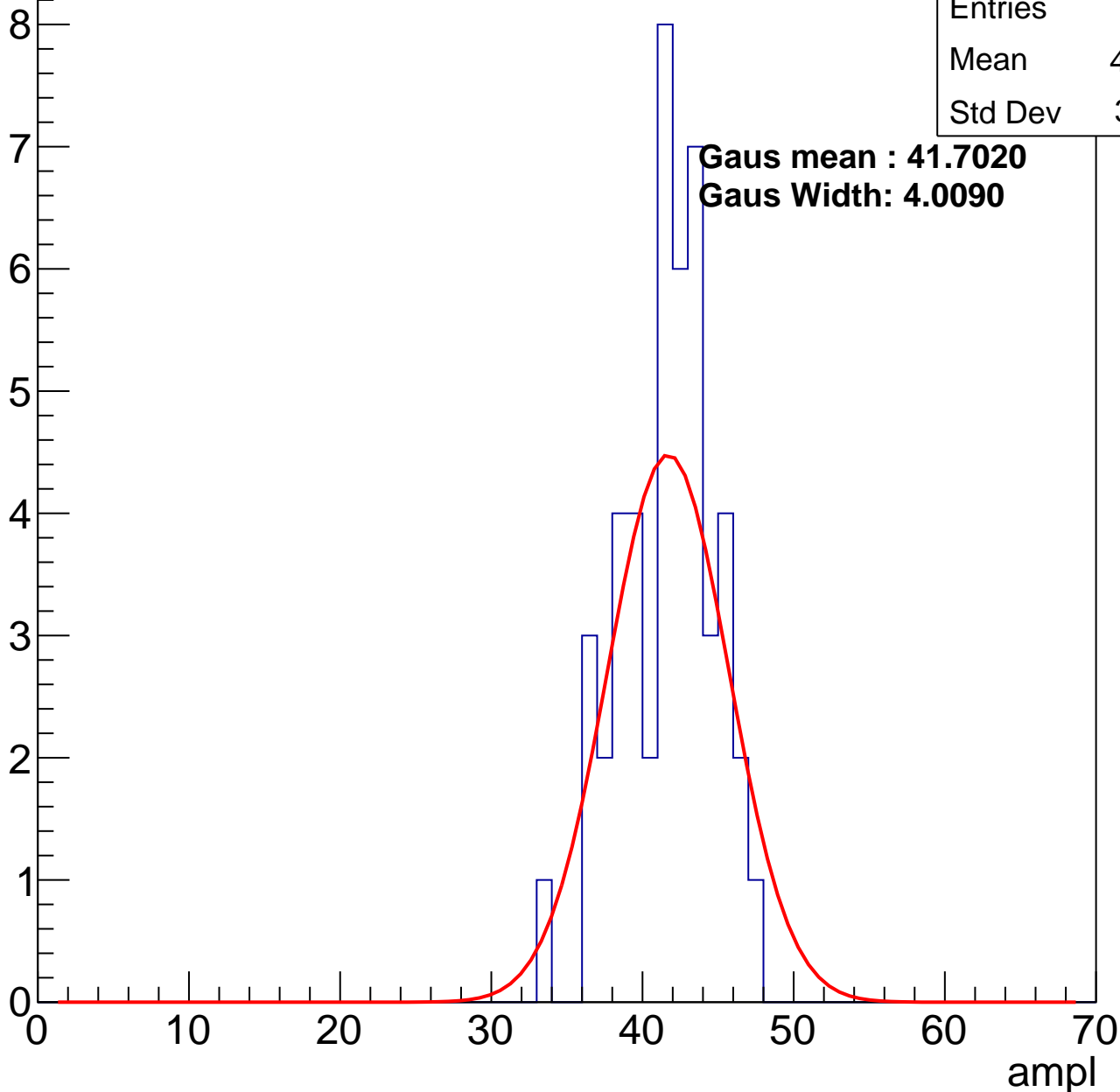
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	41.17
Std Dev	3.041

Gaus mean : 41.7020

Gaus Width: 4.0090



B1L101S, U17-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	48.1
Std Dev	3.528

Entry

10

8

6

4

2

0

0

10

20

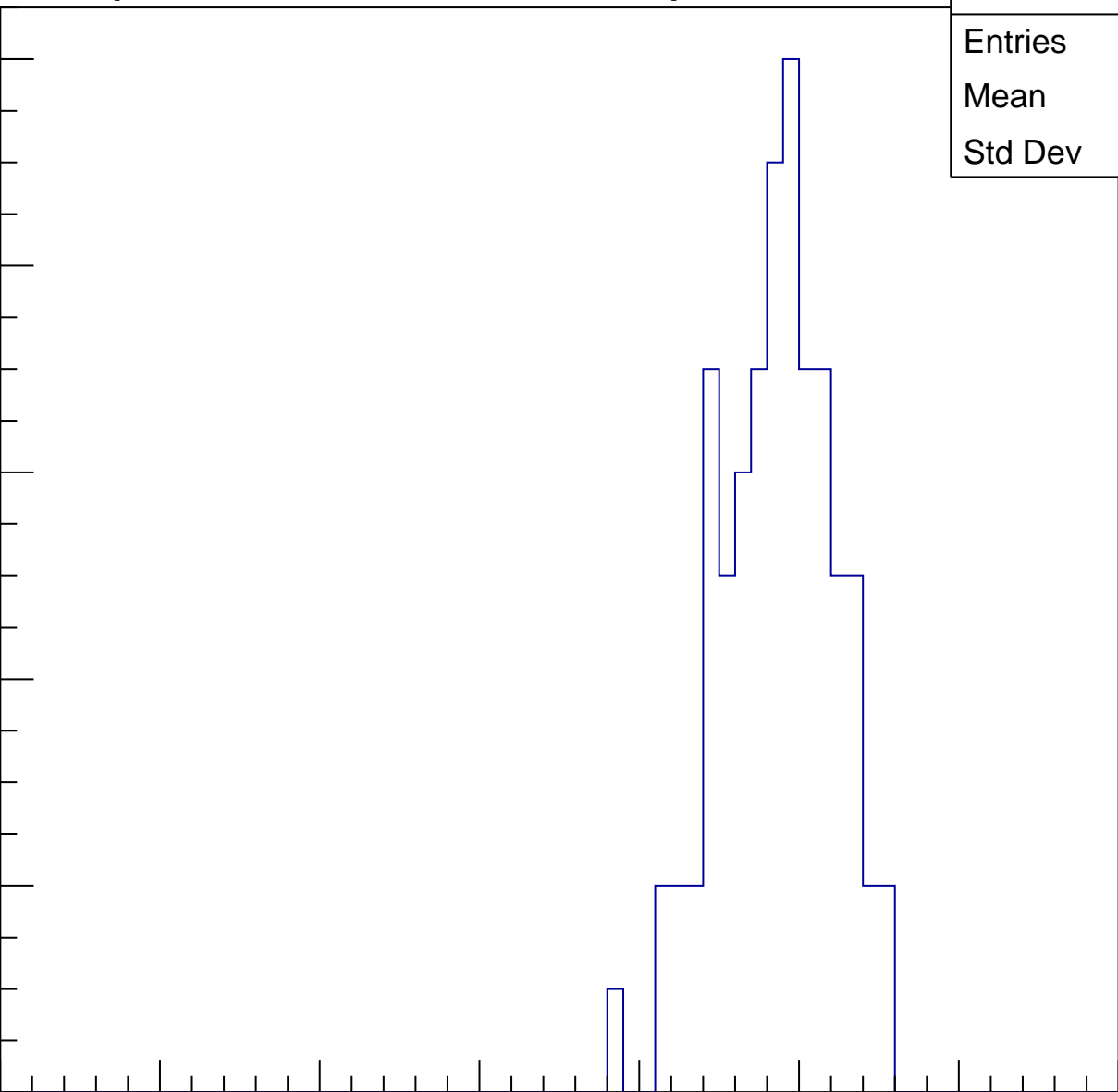
30

40

50

60

ampl

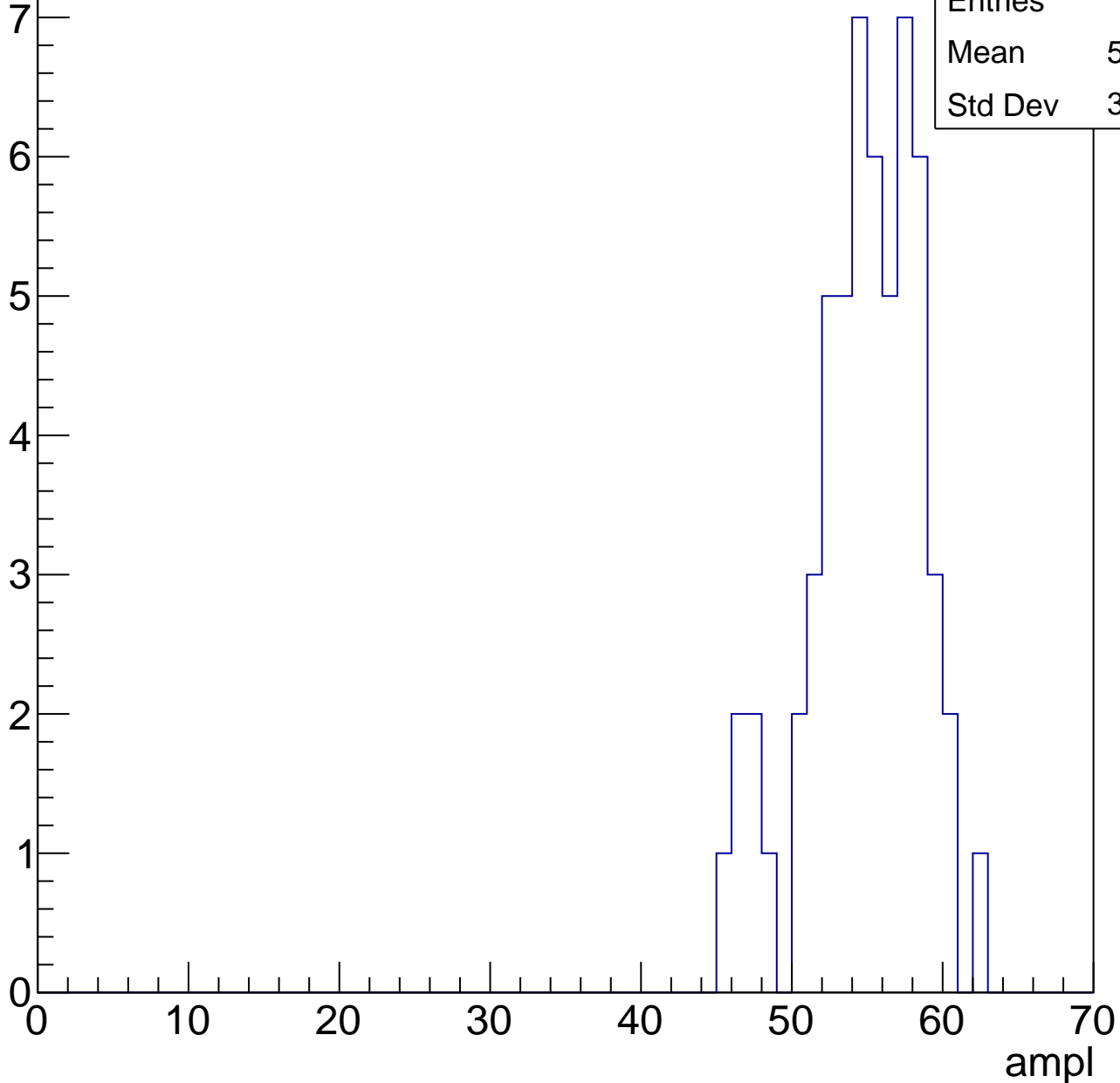


B1L101S, U17-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	54.33
Std Dev	3.753

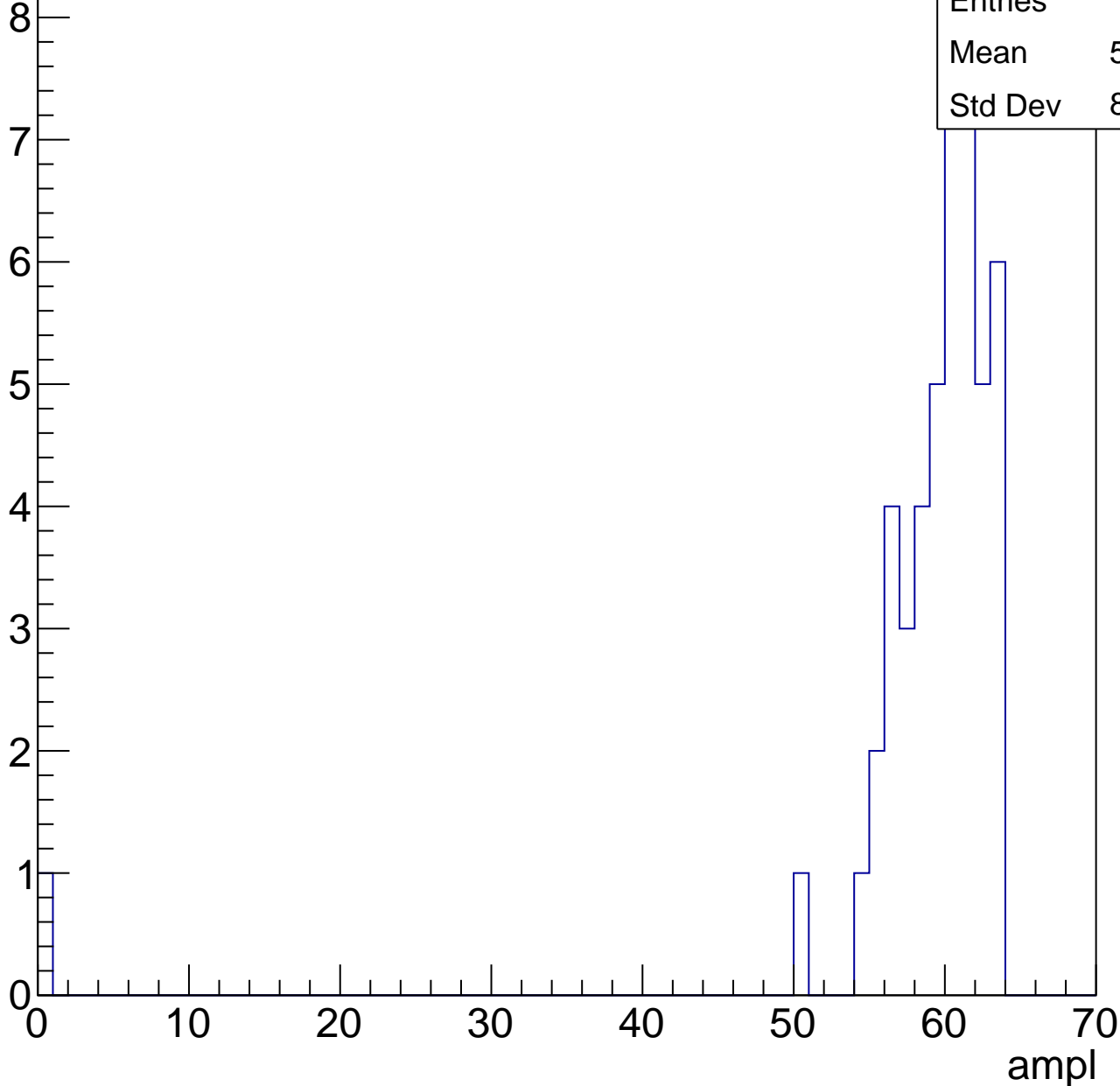


B1L101S, U17-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

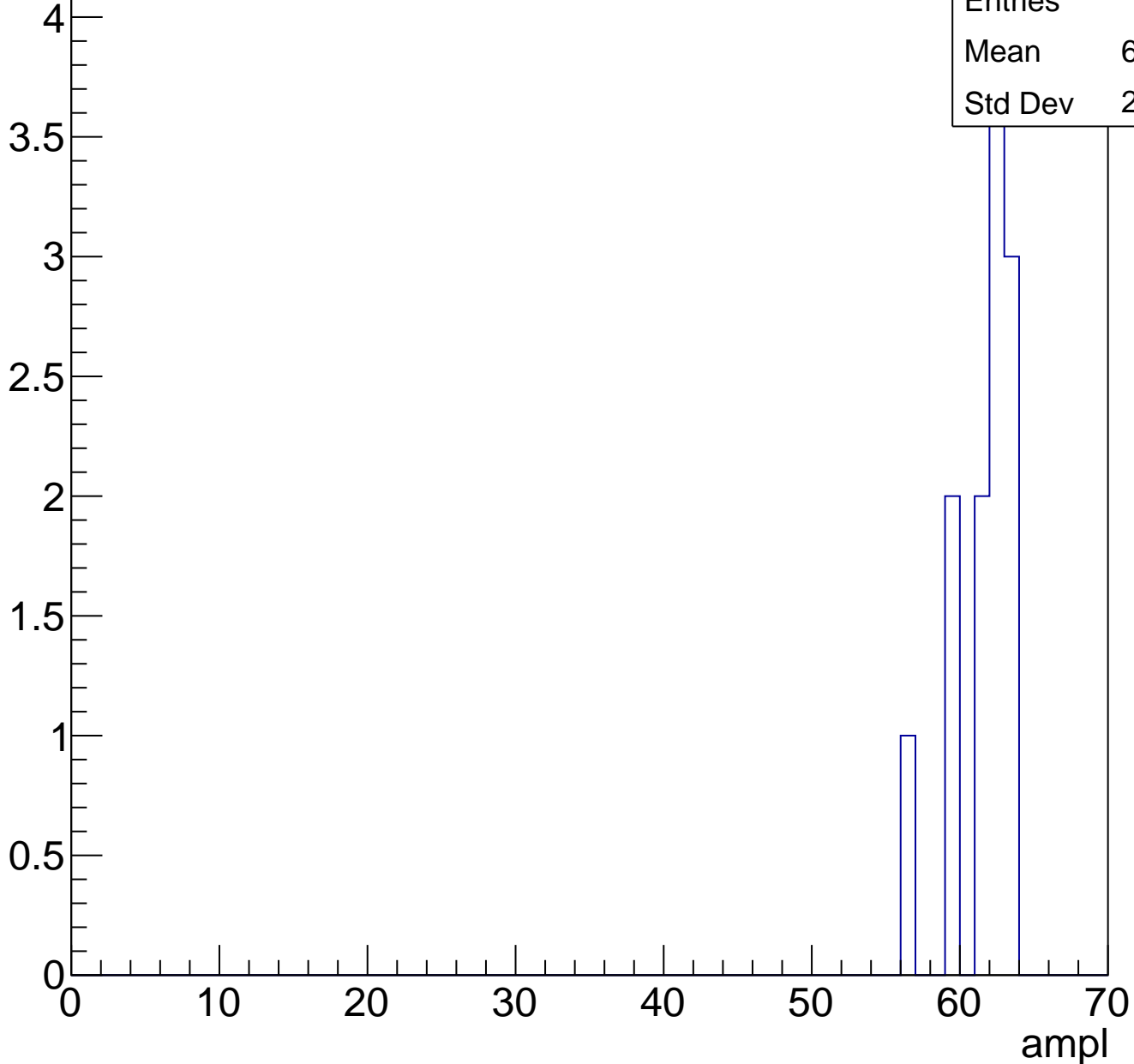
Entries	48
Mean	58.17
Std Dev	8.919



B1L101S, U17-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	12
Mean	61.08
Std Dev	2.019

B1L101S, U17-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch29, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	29.45
Std Dev	4.992

Gaus mean : 30.2235

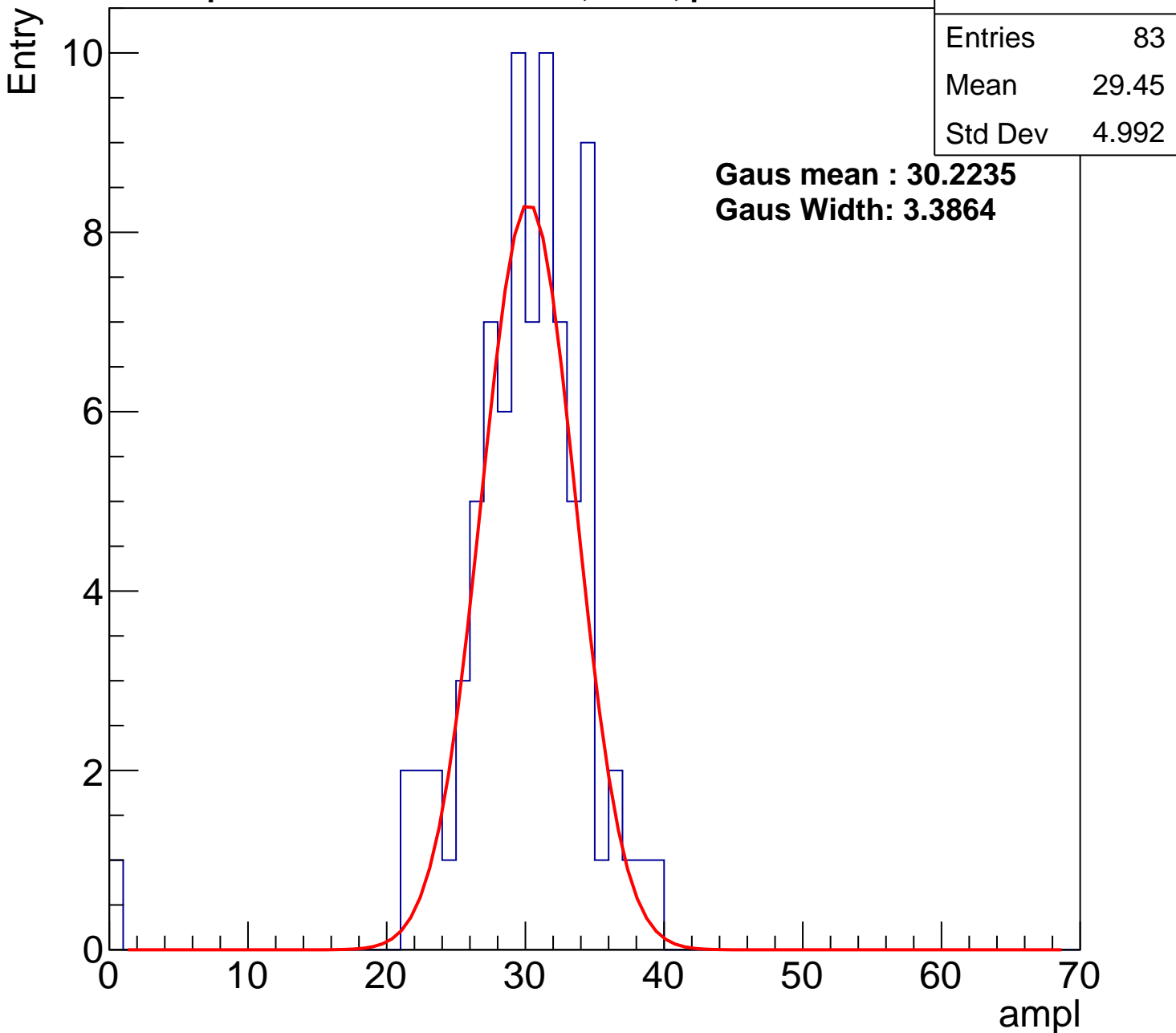
Gaus Width: 3.3864

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch29, adc1

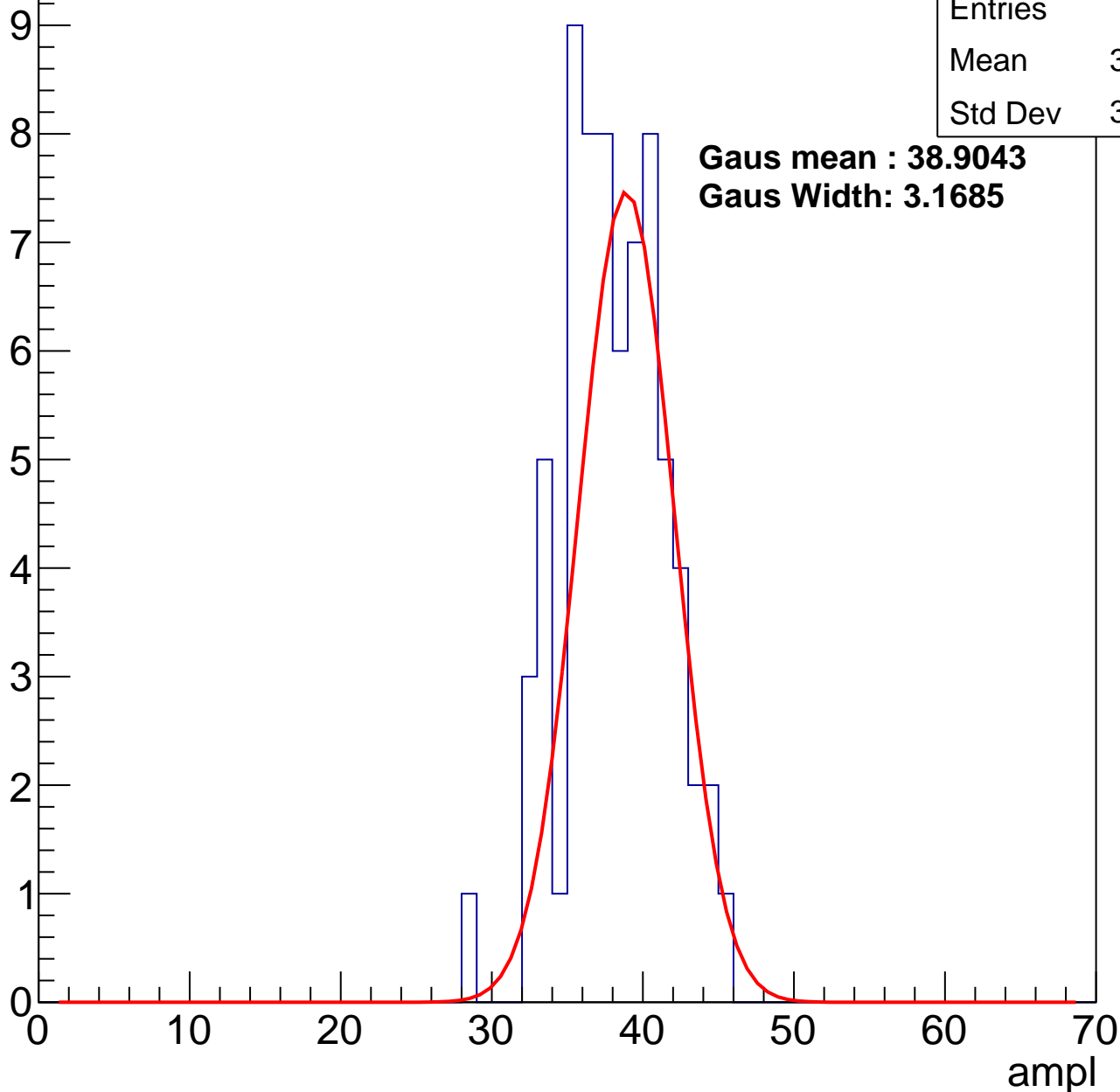
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.64
Std Dev	3.338

Gaus mean : 38.9043

Gaus Width: 3.1685



B1L101S, U17-ch29, adc2

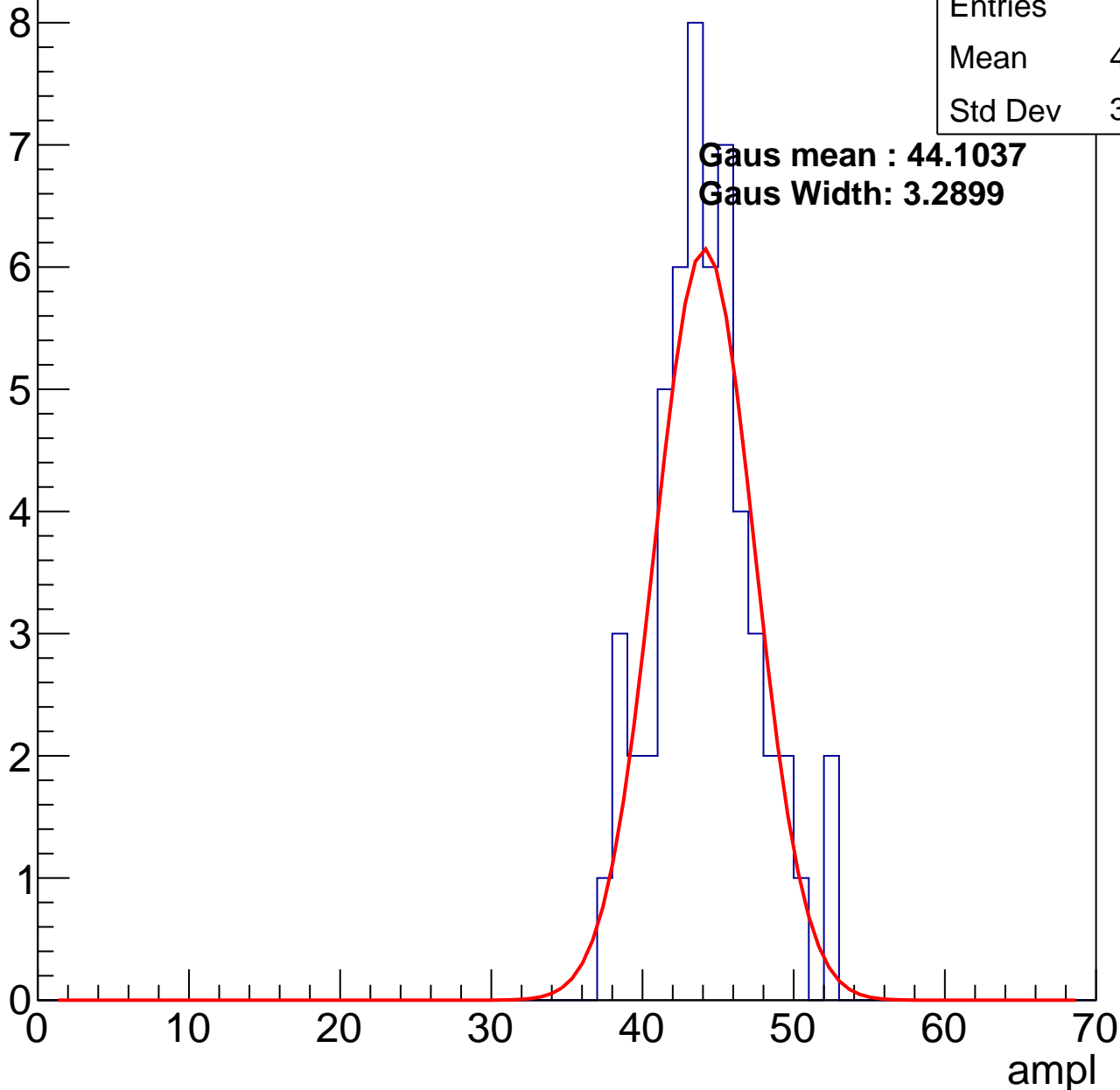
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.74
Std Dev	3.357

Gaus mean : 44.1037

Gaus Width: 3.2899

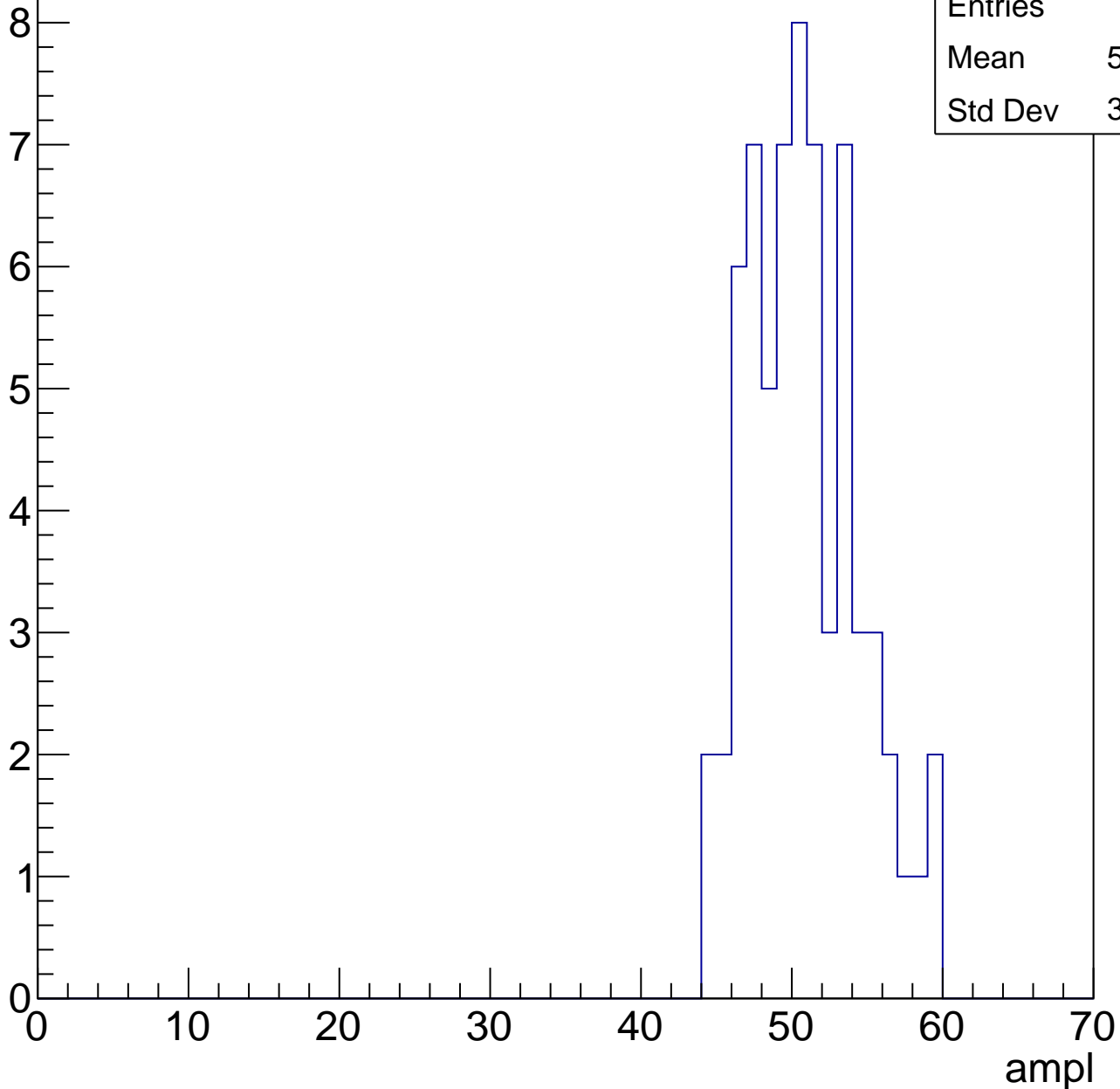


B1L101S, U17-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.33
Std Dev	3.594

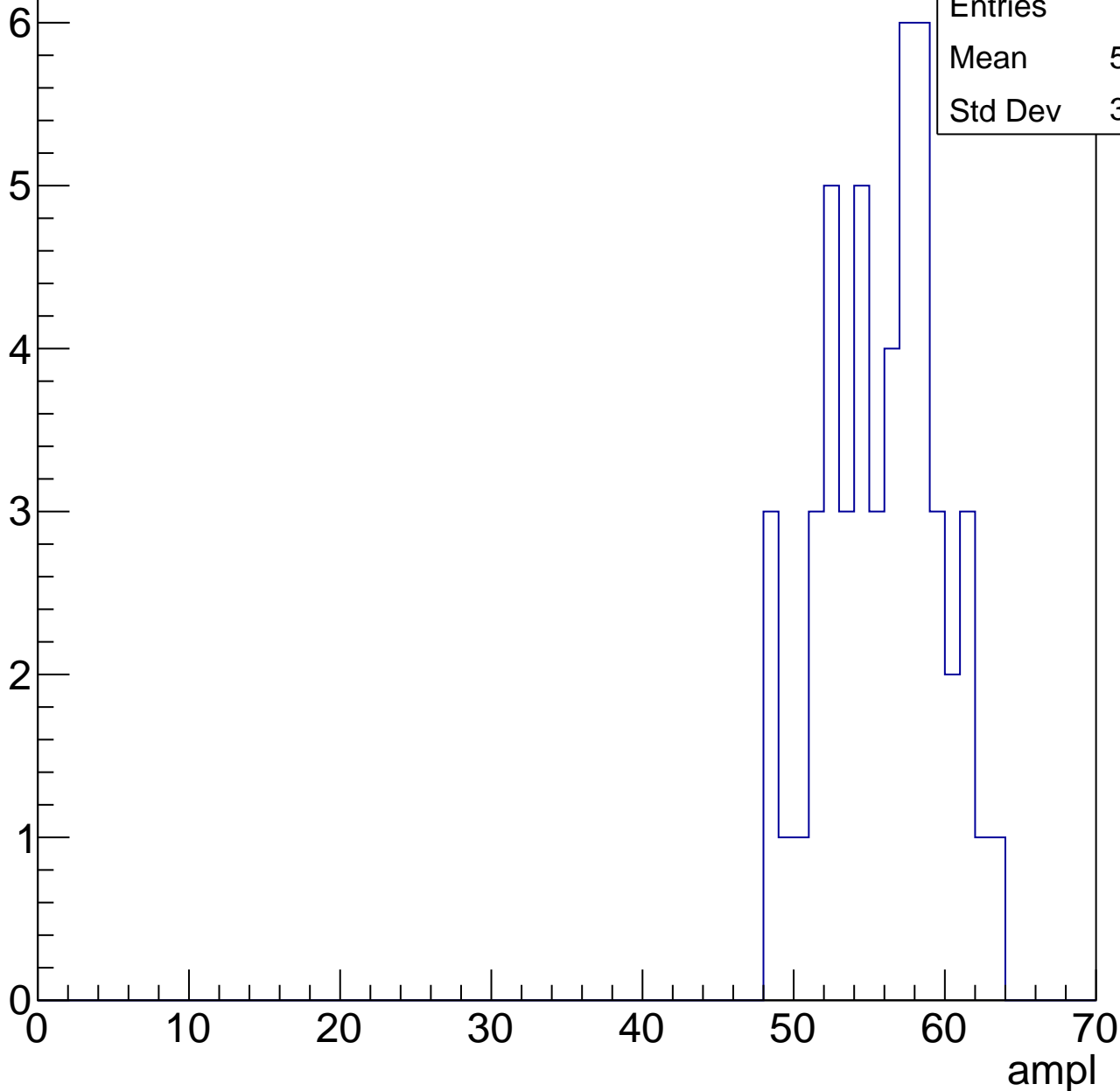


B1L101S, U17-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.38
Std Dev	3.773



B1L101S, U17-ch29, adc5

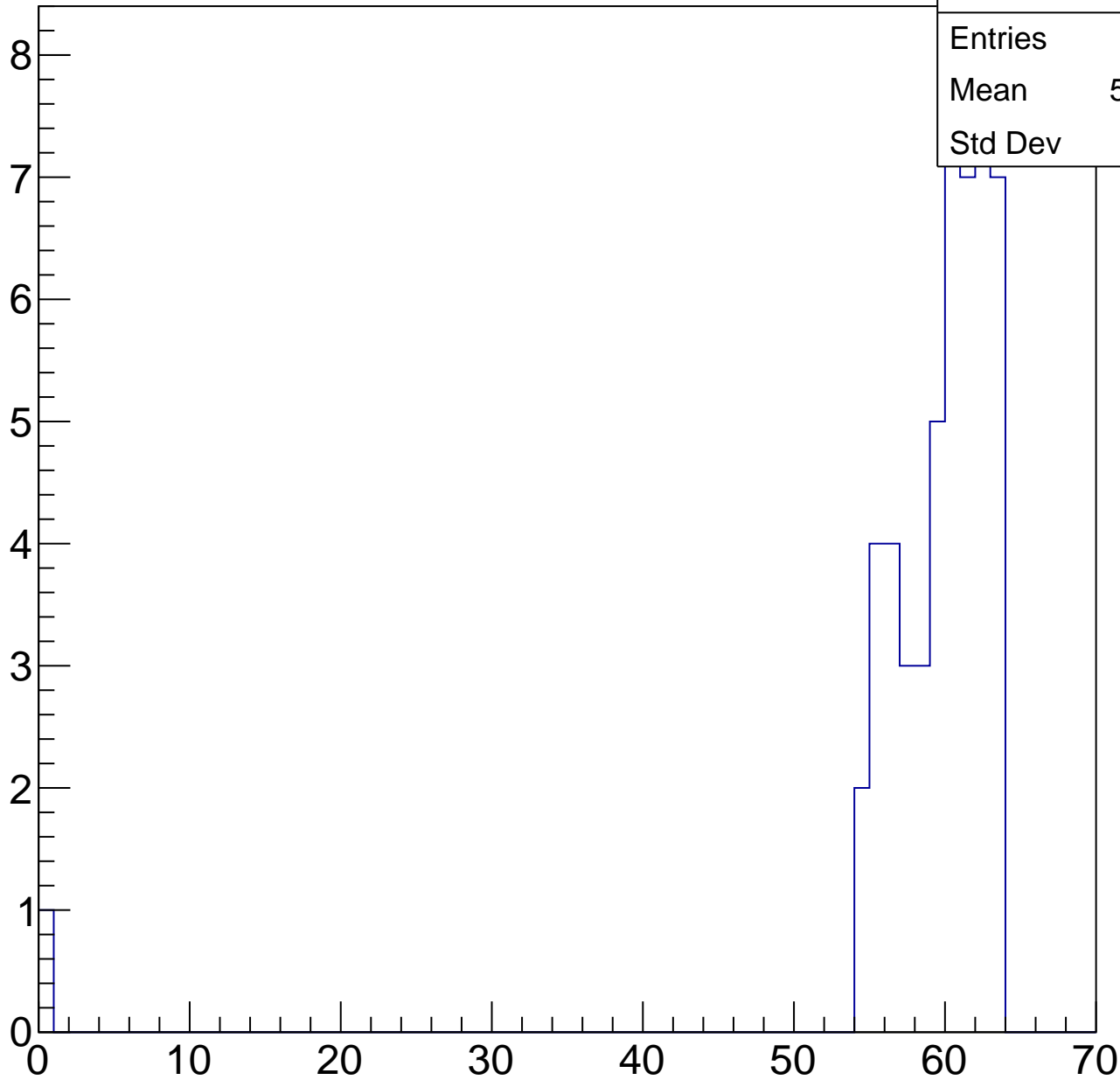
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.38
Std Dev	8.6

ampl



B1L101S, U17-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch30, adc0

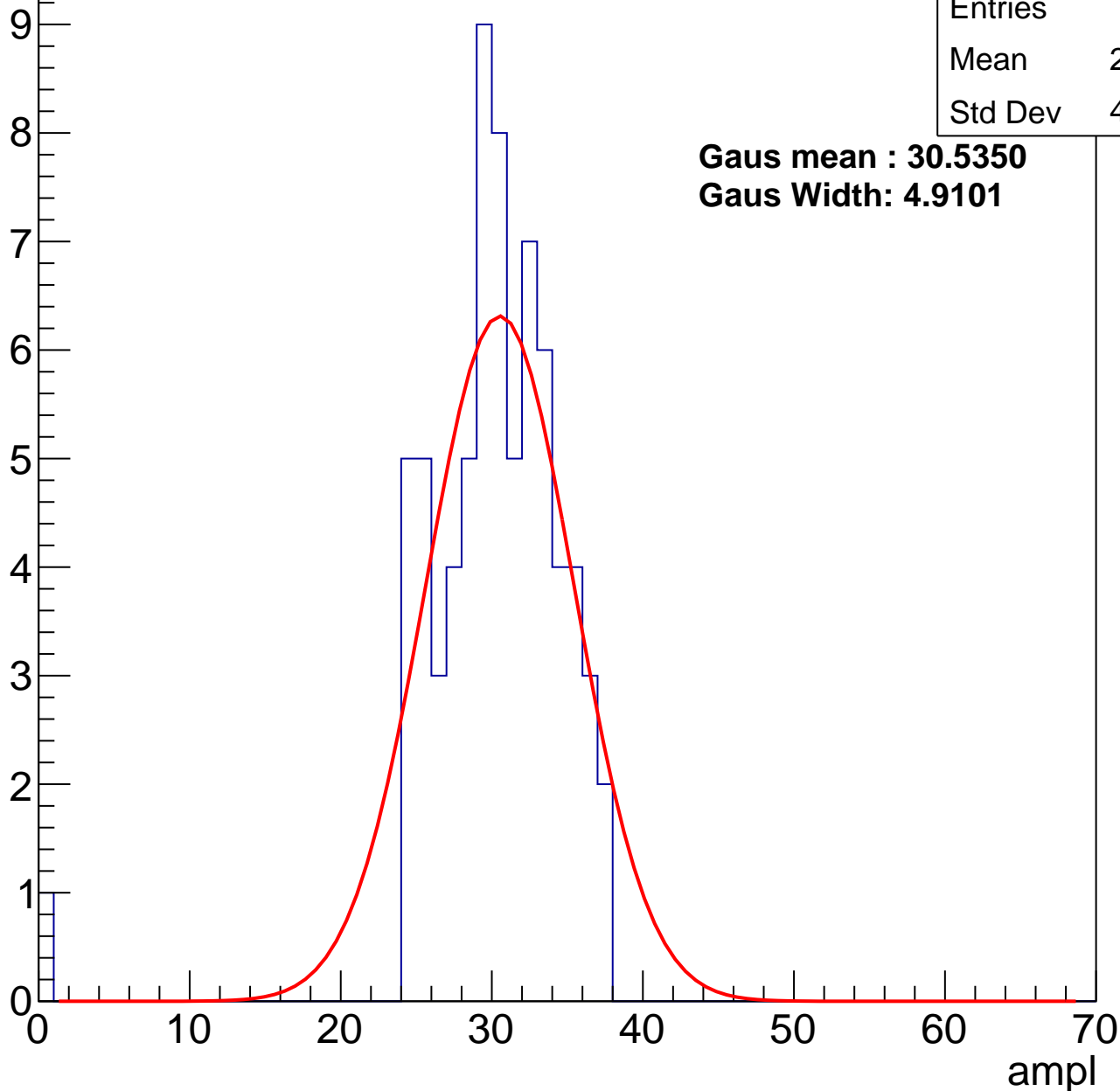
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.68
Std Dev	4.989

Gaus mean : 30.5350

Gaus Width: 4.9101



B1L101S, U17-ch30, adc1

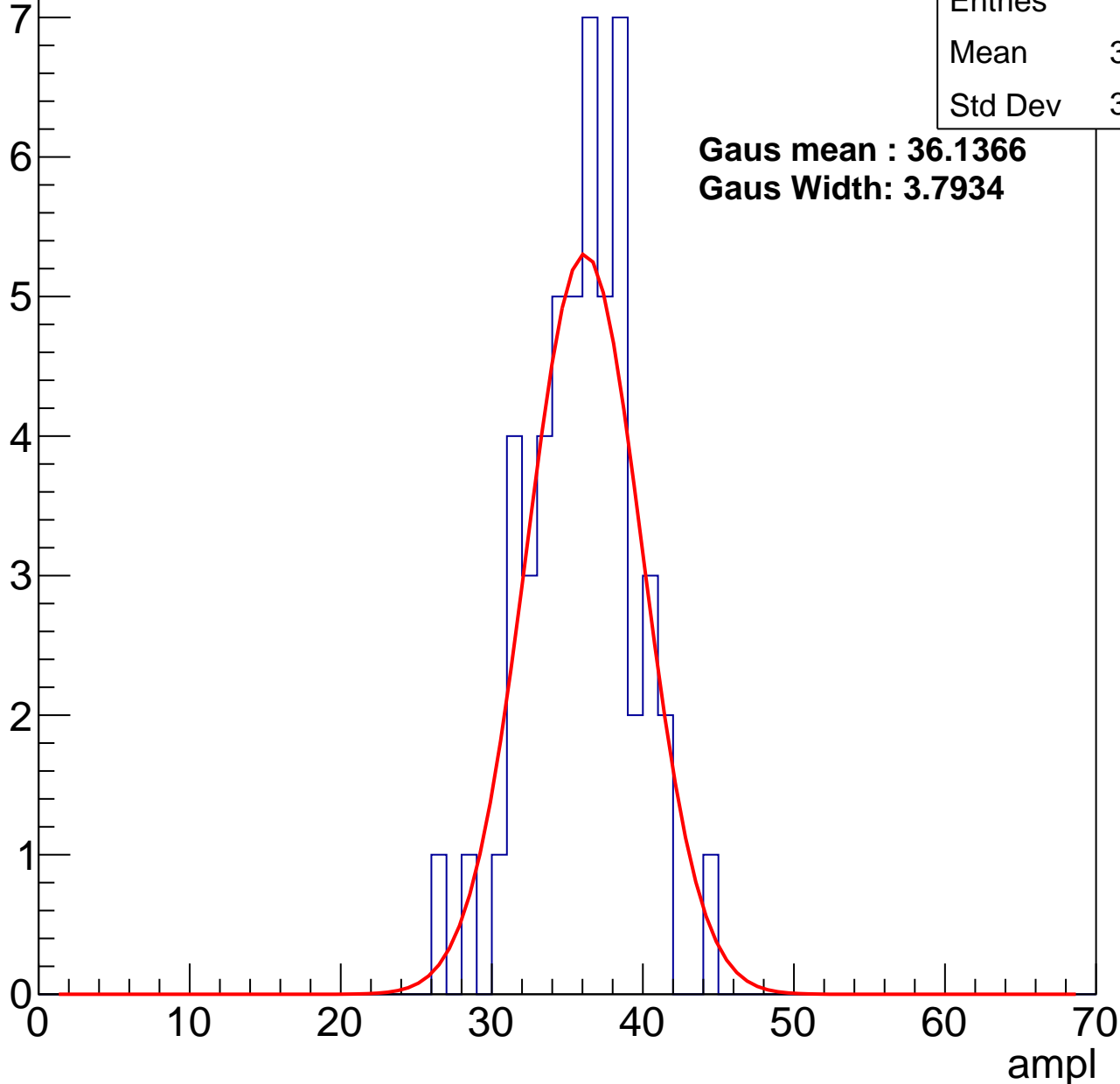
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.45
Std Dev	3.449

Gaus mean : 36.1366

Gaus Width: 3.7934



B1L101S, U17-ch30, adc2

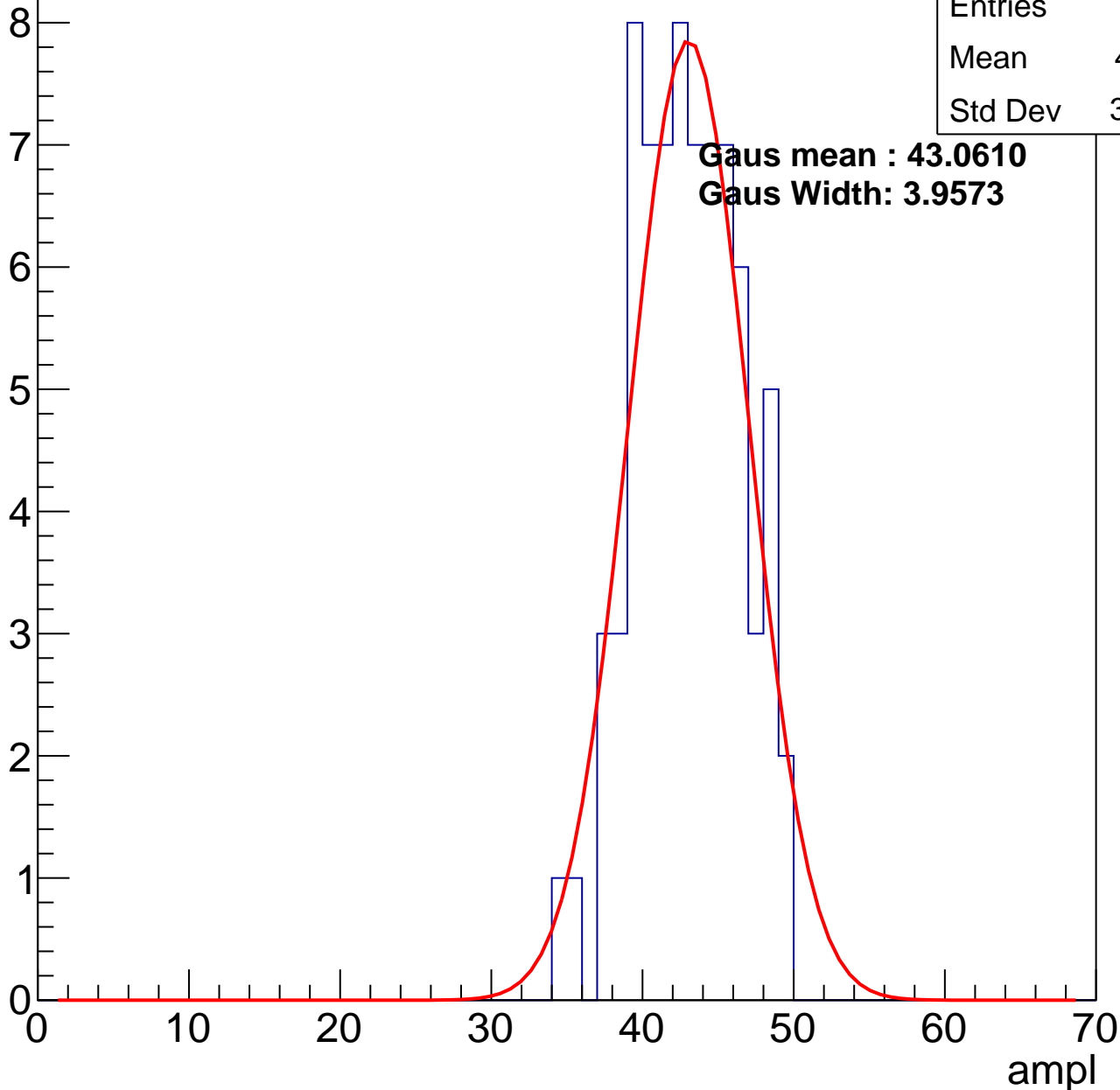
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	42.51
Std Dev	3.407

Gaus mean : 43.0610

Gaus Width: 3.9573

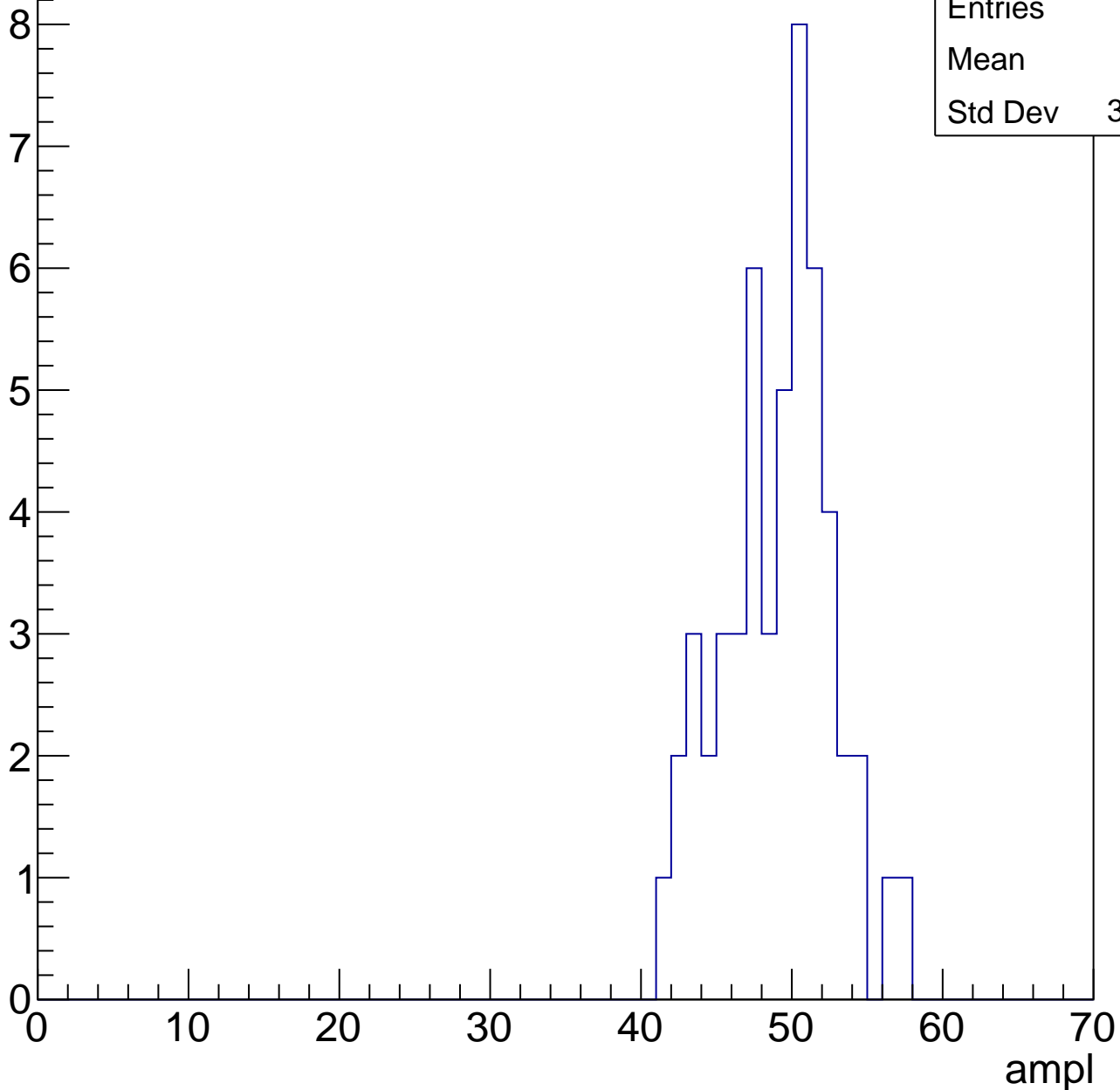


B1L101S, U17-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

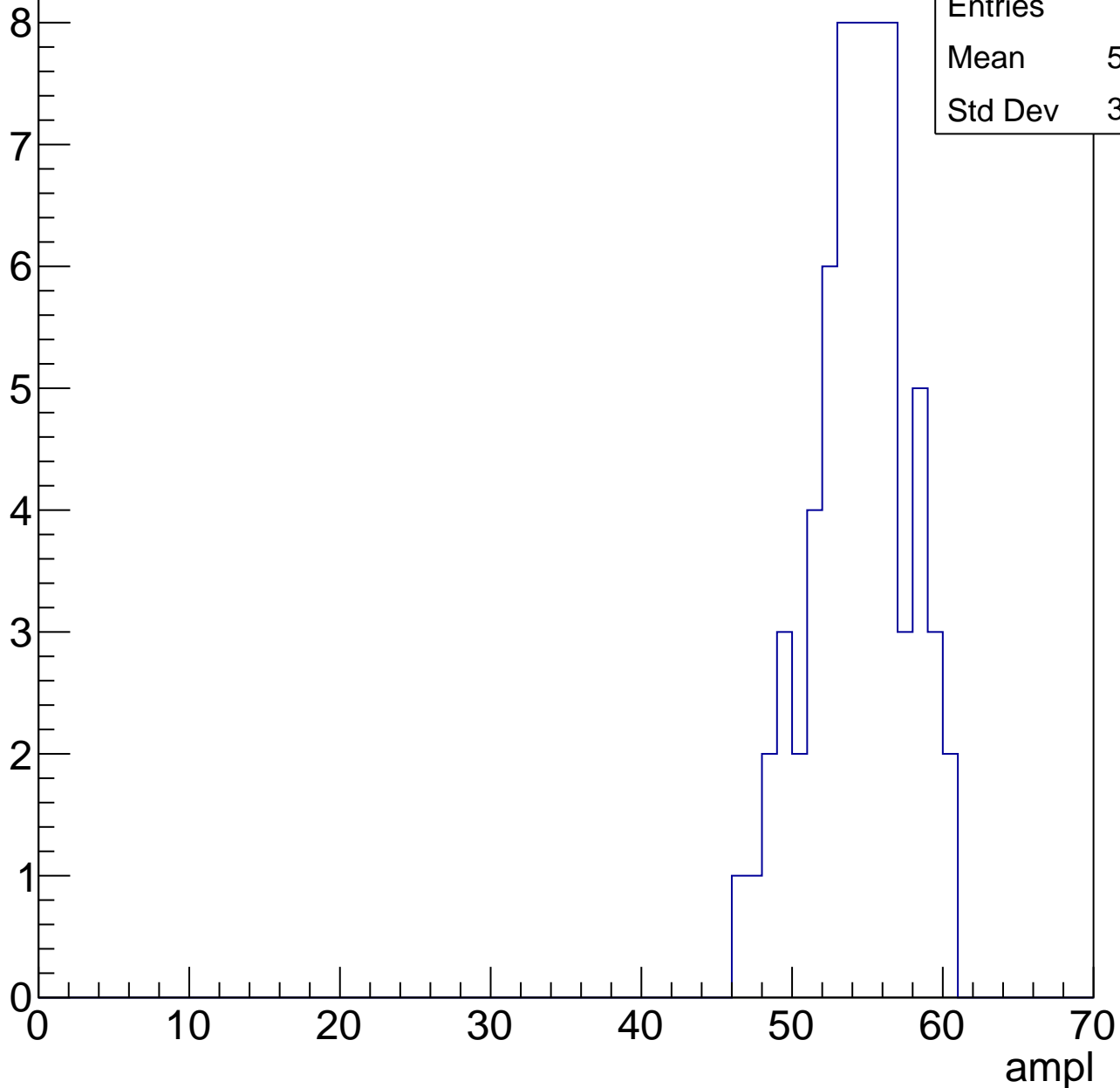
Entries	52
Mean	48.6
Std Dev	3.607



B1L101S, U17-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

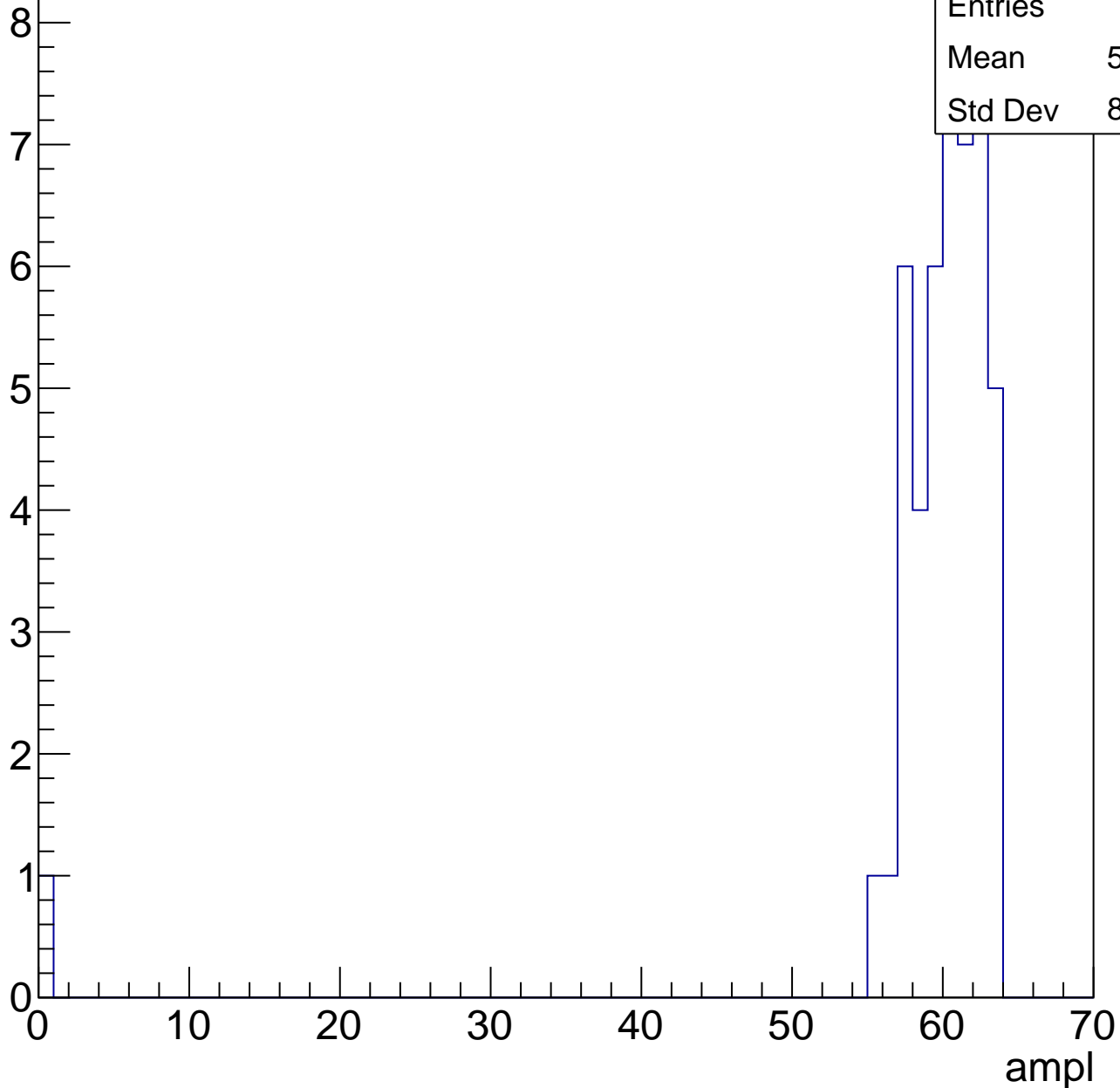


Entries	64
Mean	53.97
Std Dev	3.197

B1L101S, U17-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

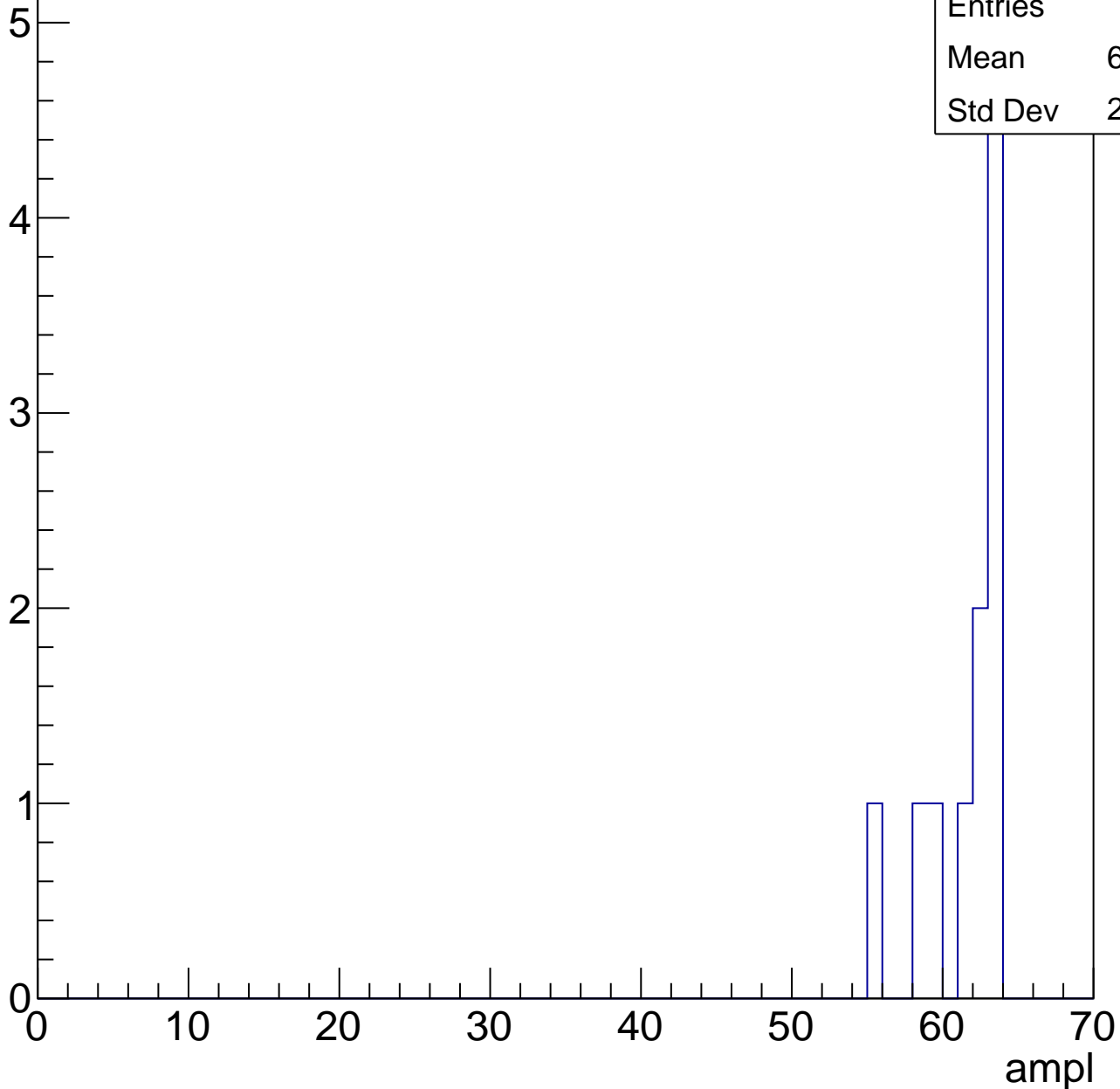


B1L101S, U17-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

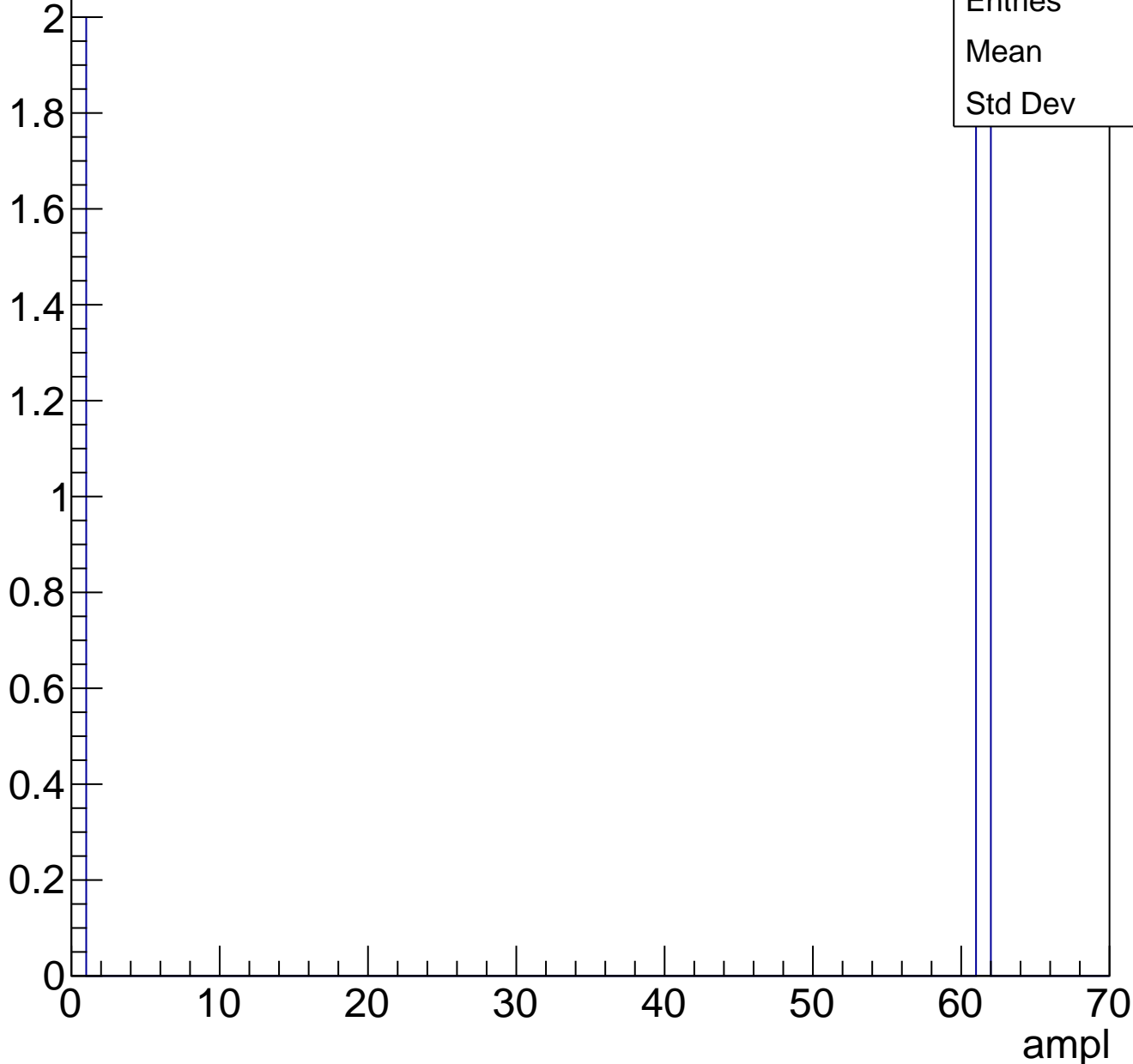
Entries	11
Mean	61.09
Std Dev	2.539



B1L101S, U17-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch31, adc0

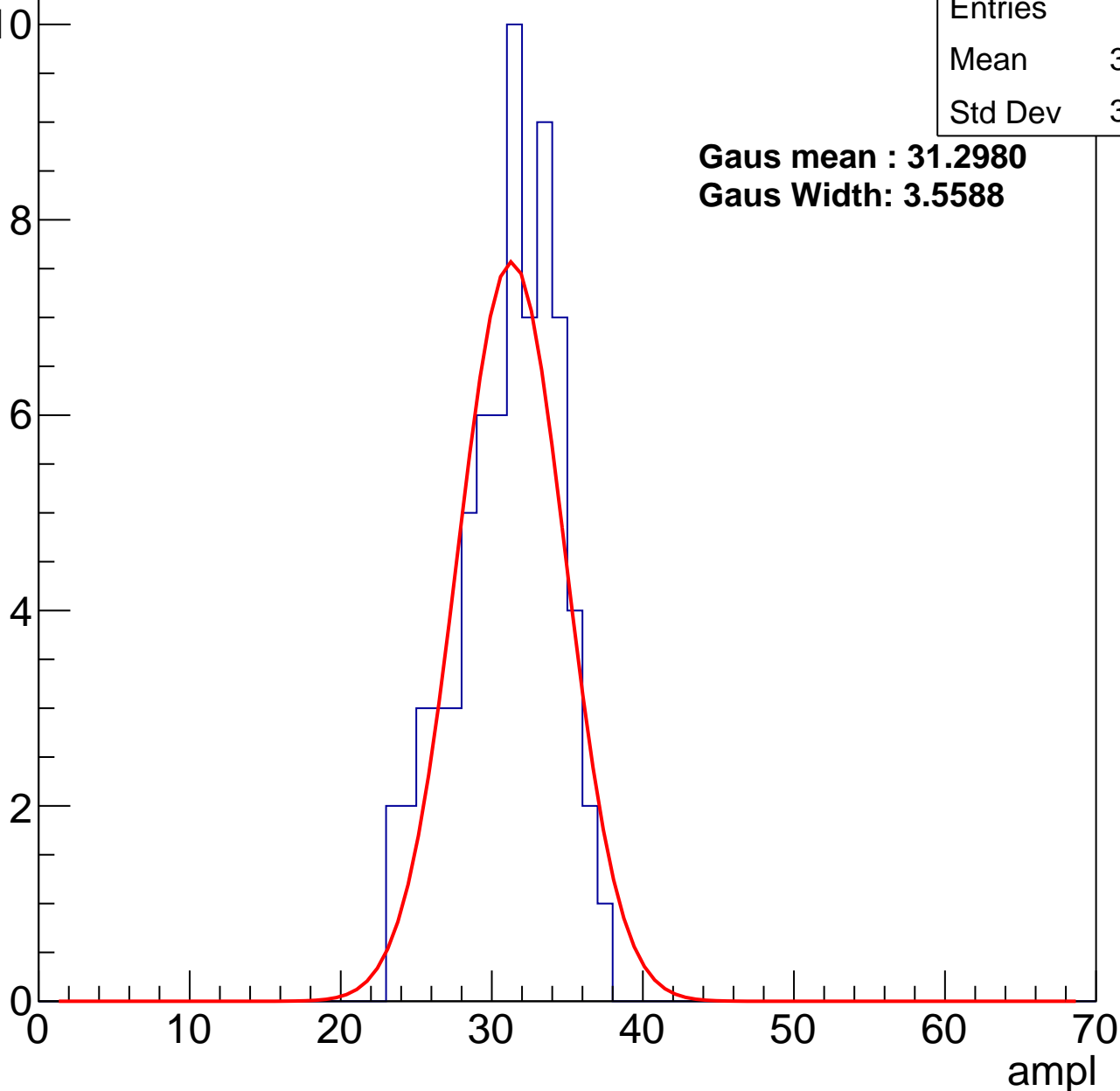
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.57
Std Dev	3.328

Gaus mean : 31.2980

Gaus Width: 3.5588



B1L101S, U17-ch31, adc1

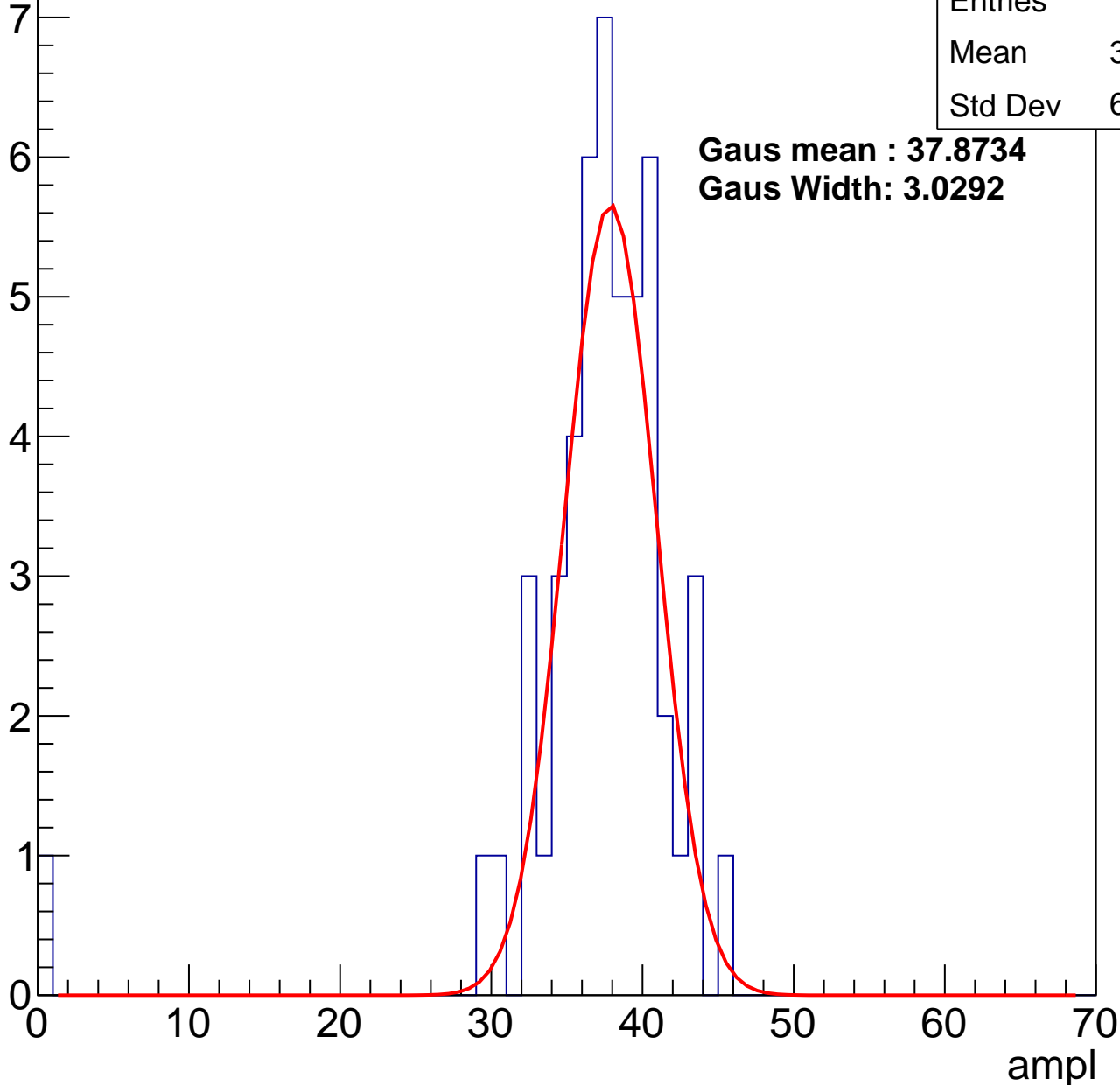
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	36.56
Std Dev	6.197

Gaus mean : 37.8734

Gaus Width: 3.0292



B1L101S, U17-ch31, adc2

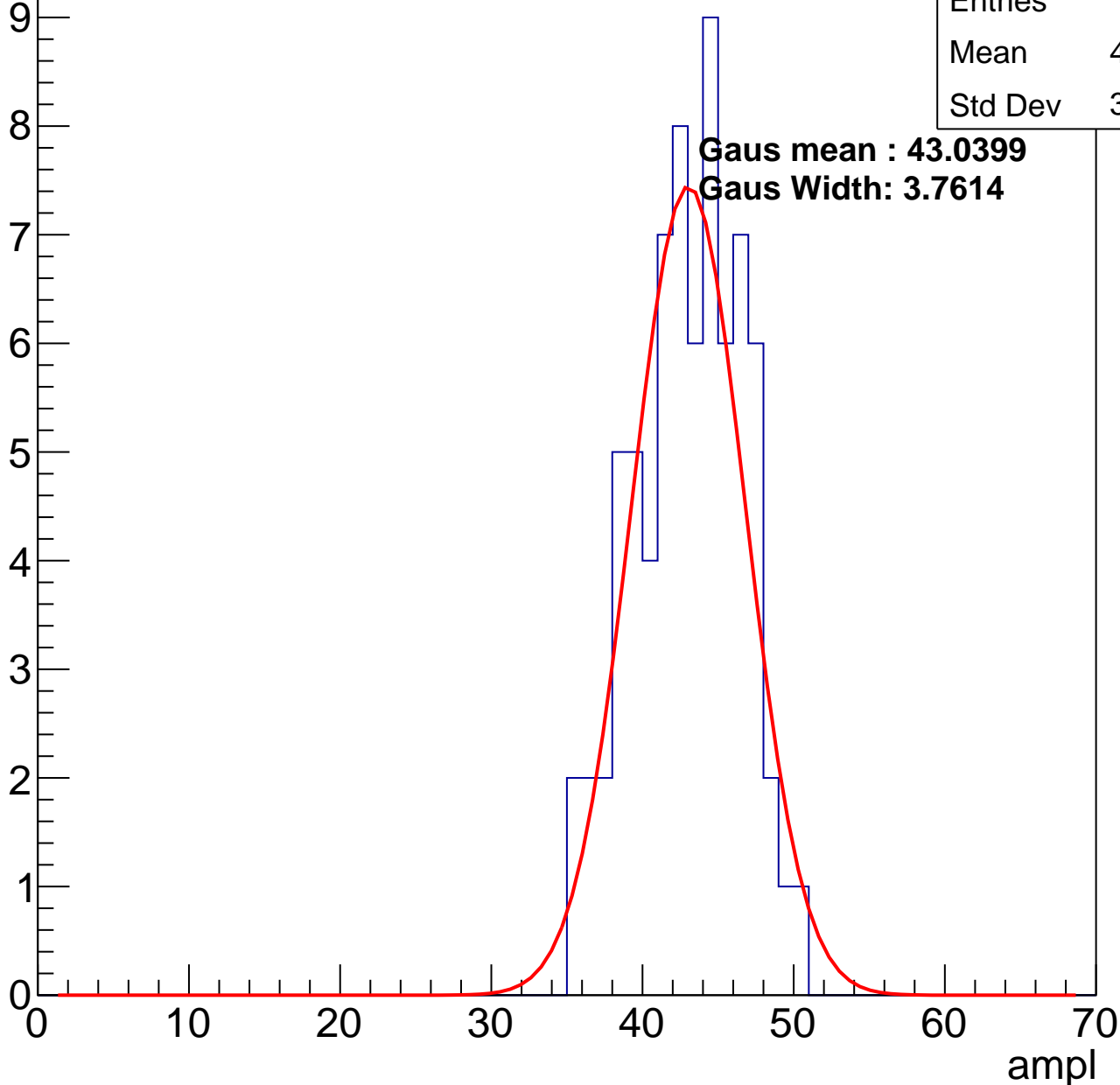
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	42.56
Std Dev	3.488

Gaus mean : 43.0399

Gaus Width: 3.7614

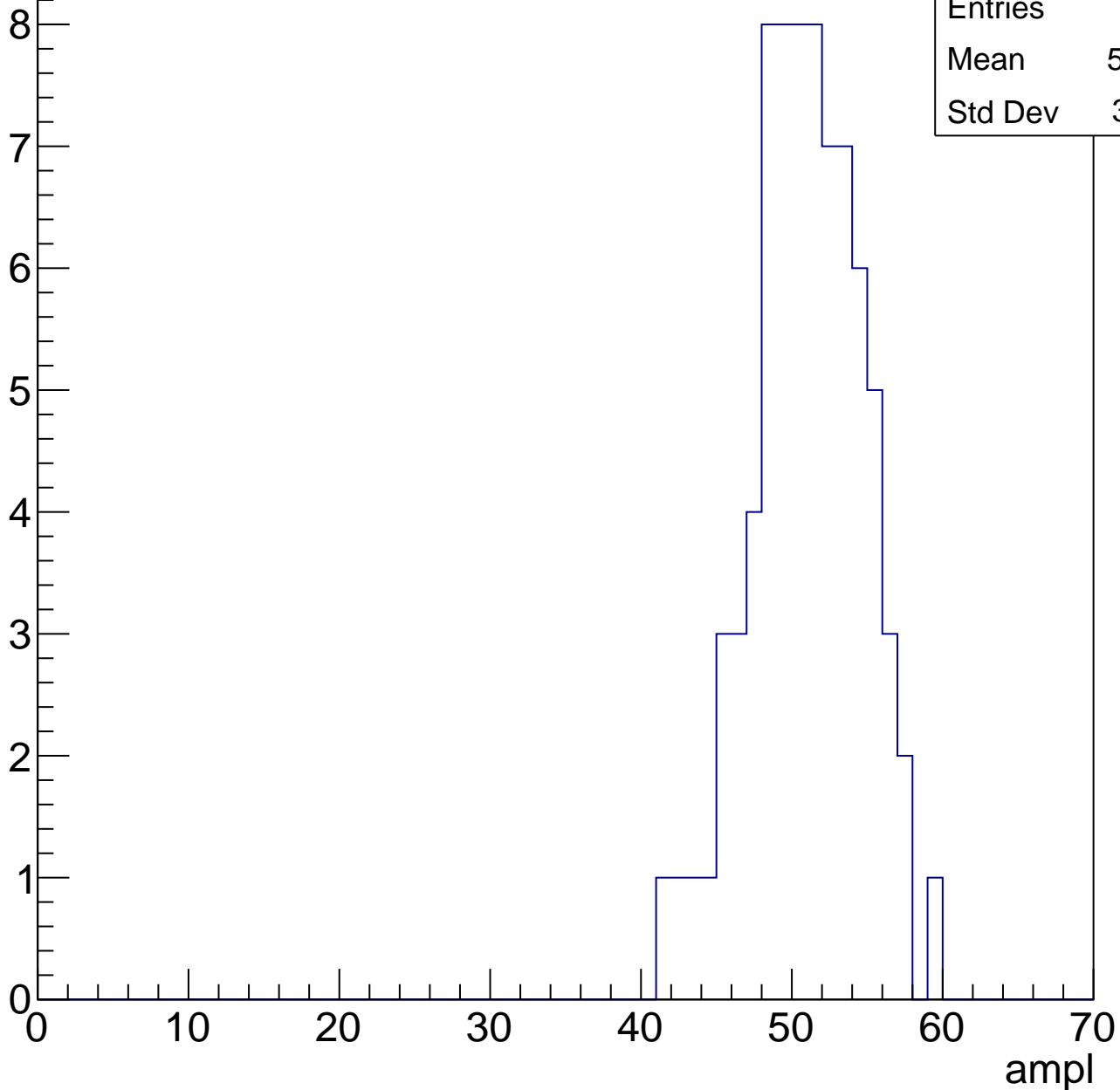


B1L101S, U17-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50.52
Std Dev	3.631

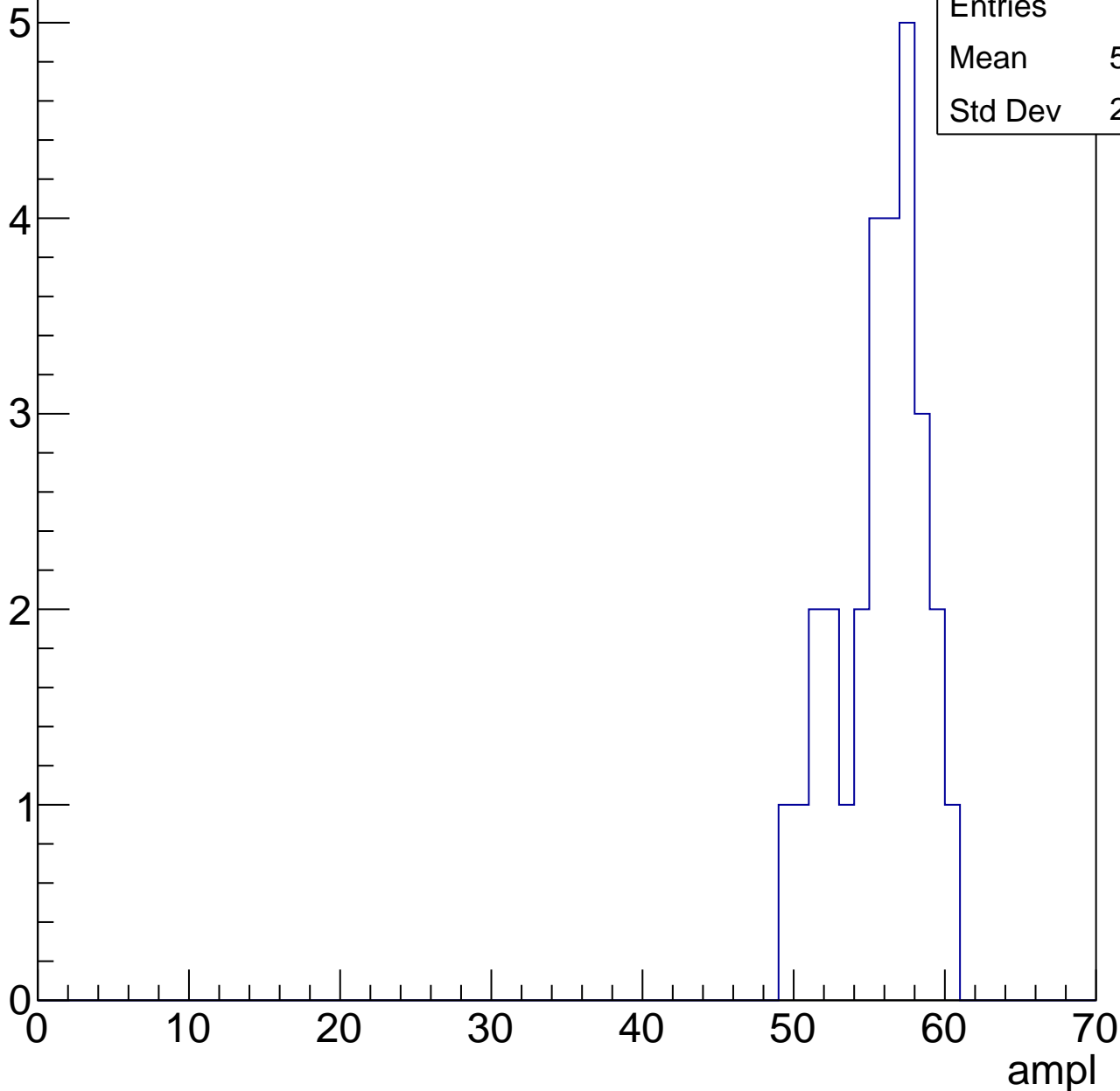


B1L101S, U17-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	55.25
Std Dev	2.824

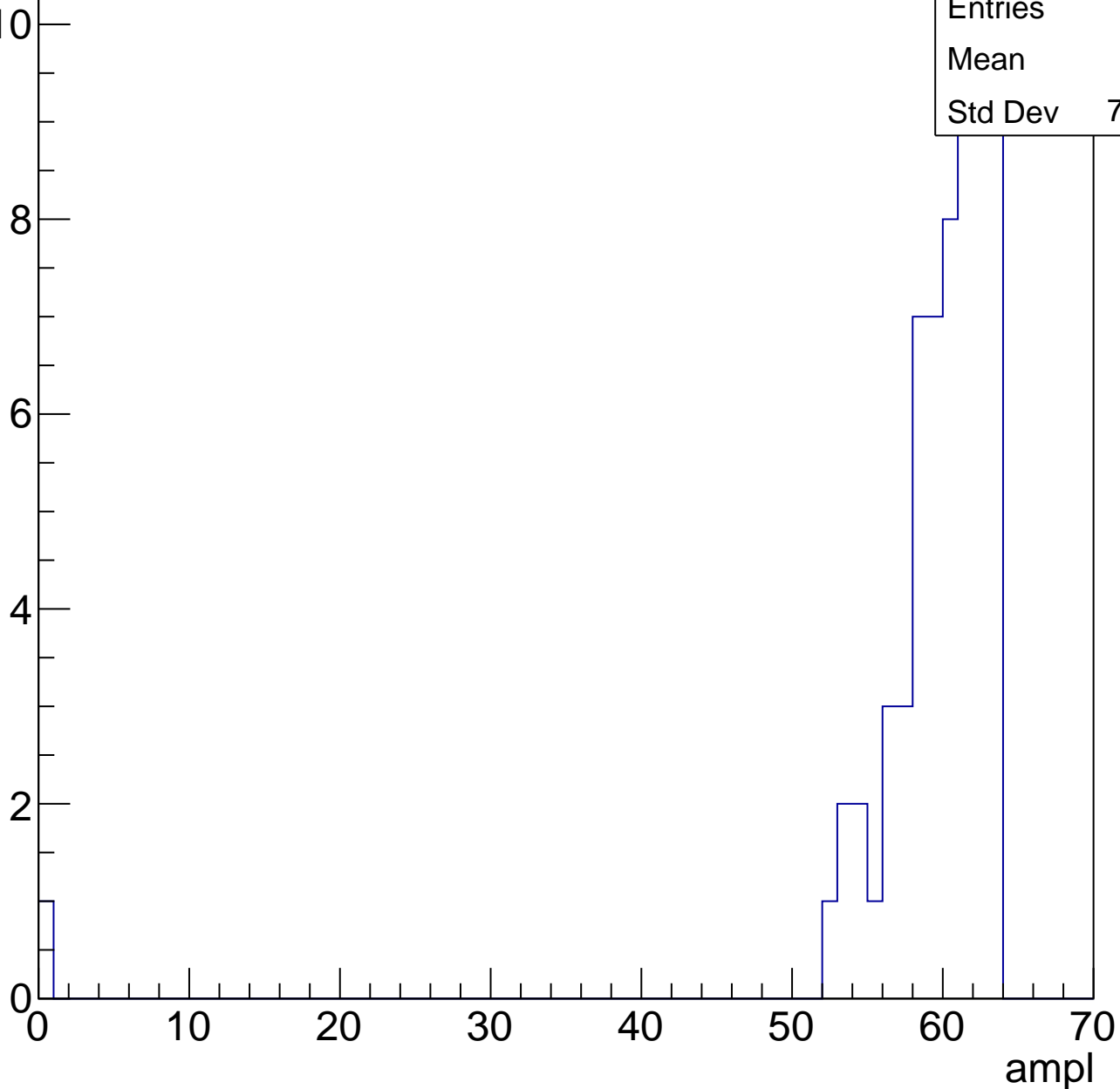


B1L101S, U17-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	58.7
Std Dev	7.903



B1L101S, U17-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch32, adc0

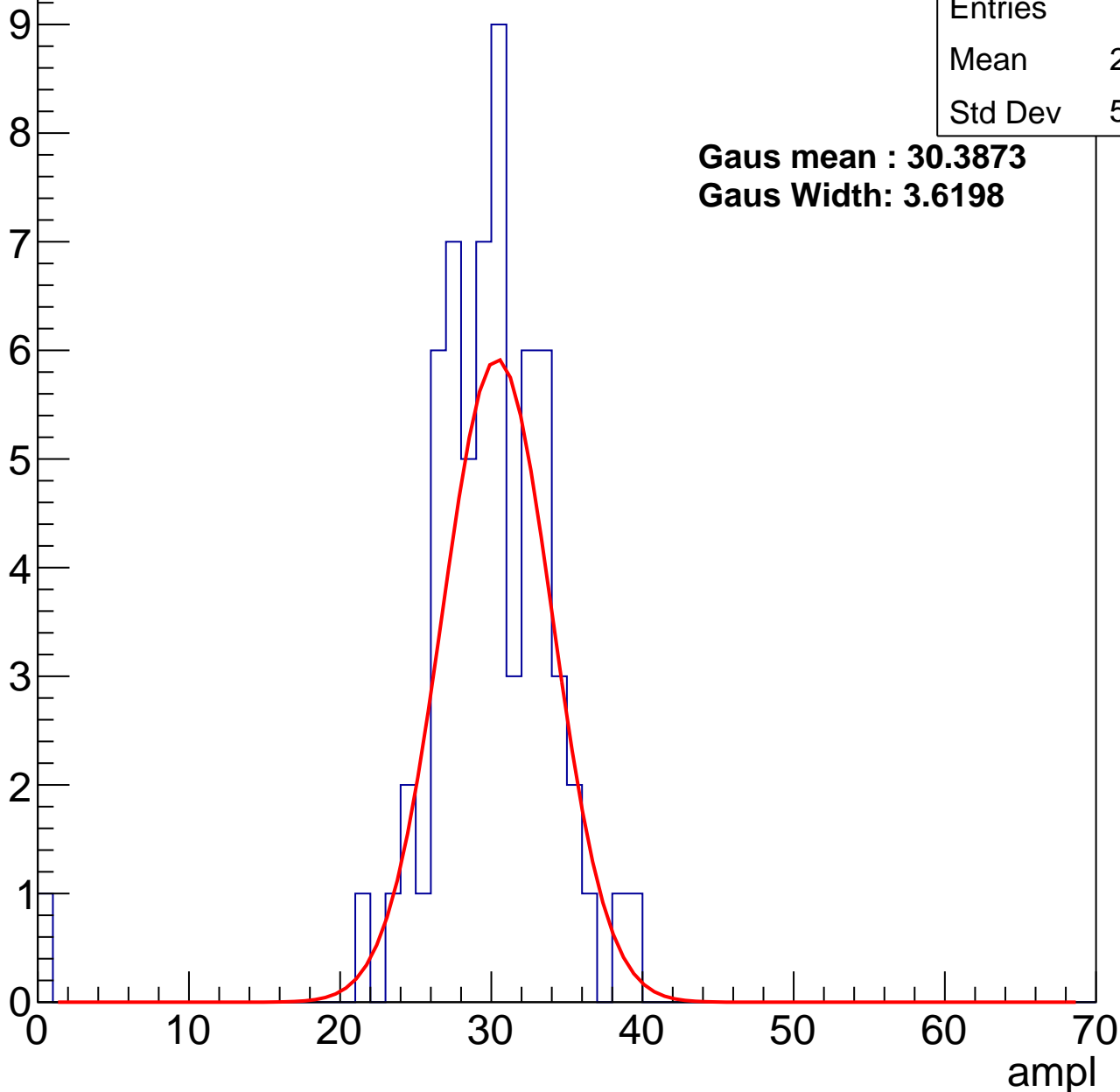
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.25
Std Dev	5.099

Gaus mean : 30.3873

Gaus Width: 3.6198



B1L101S, U17-ch32, adc1

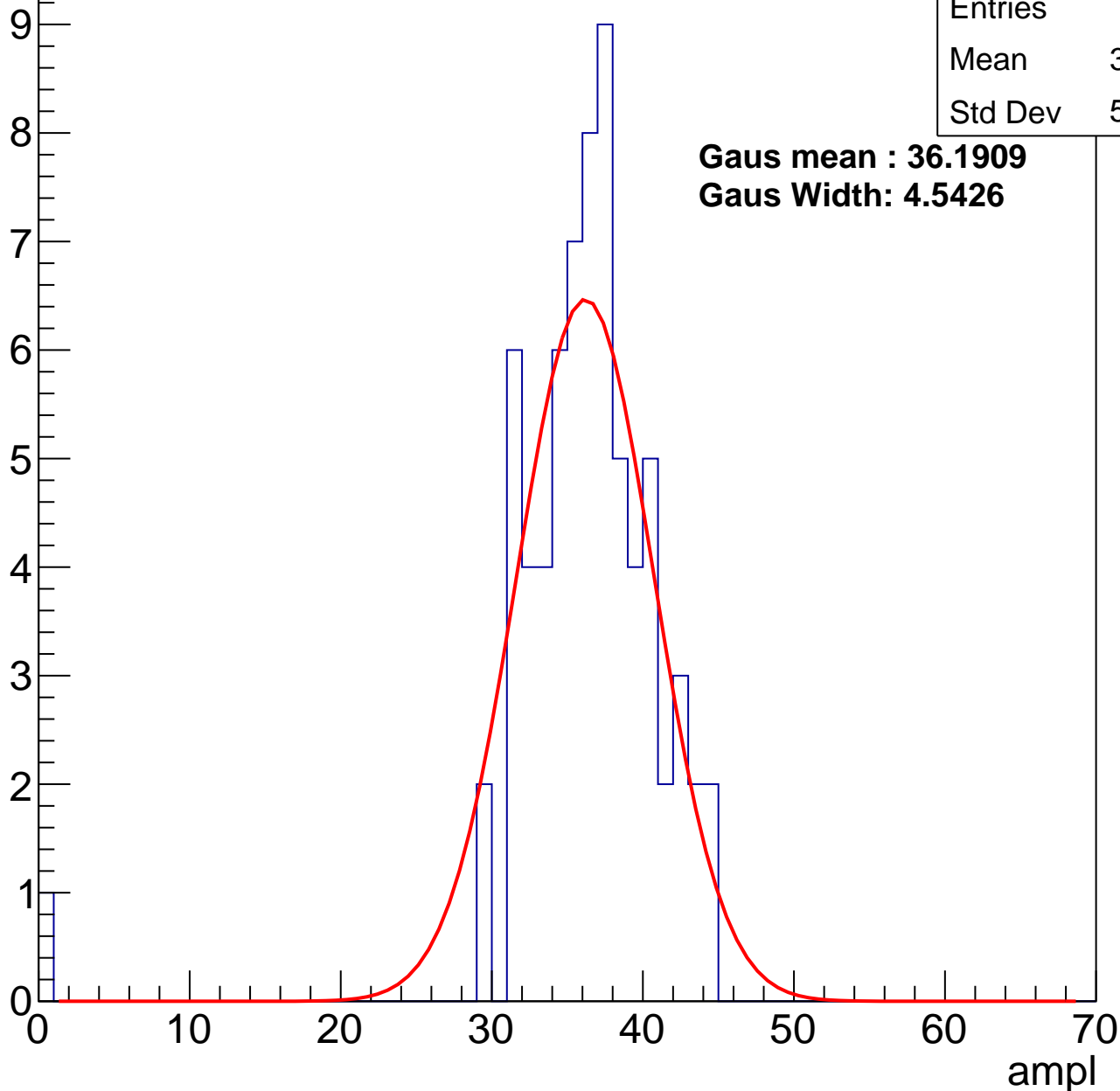
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.74
Std Dev	5.605

Gaus mean : 36.1909

Gaus Width: 4.5426



B1L101S, U17-ch32, adc2

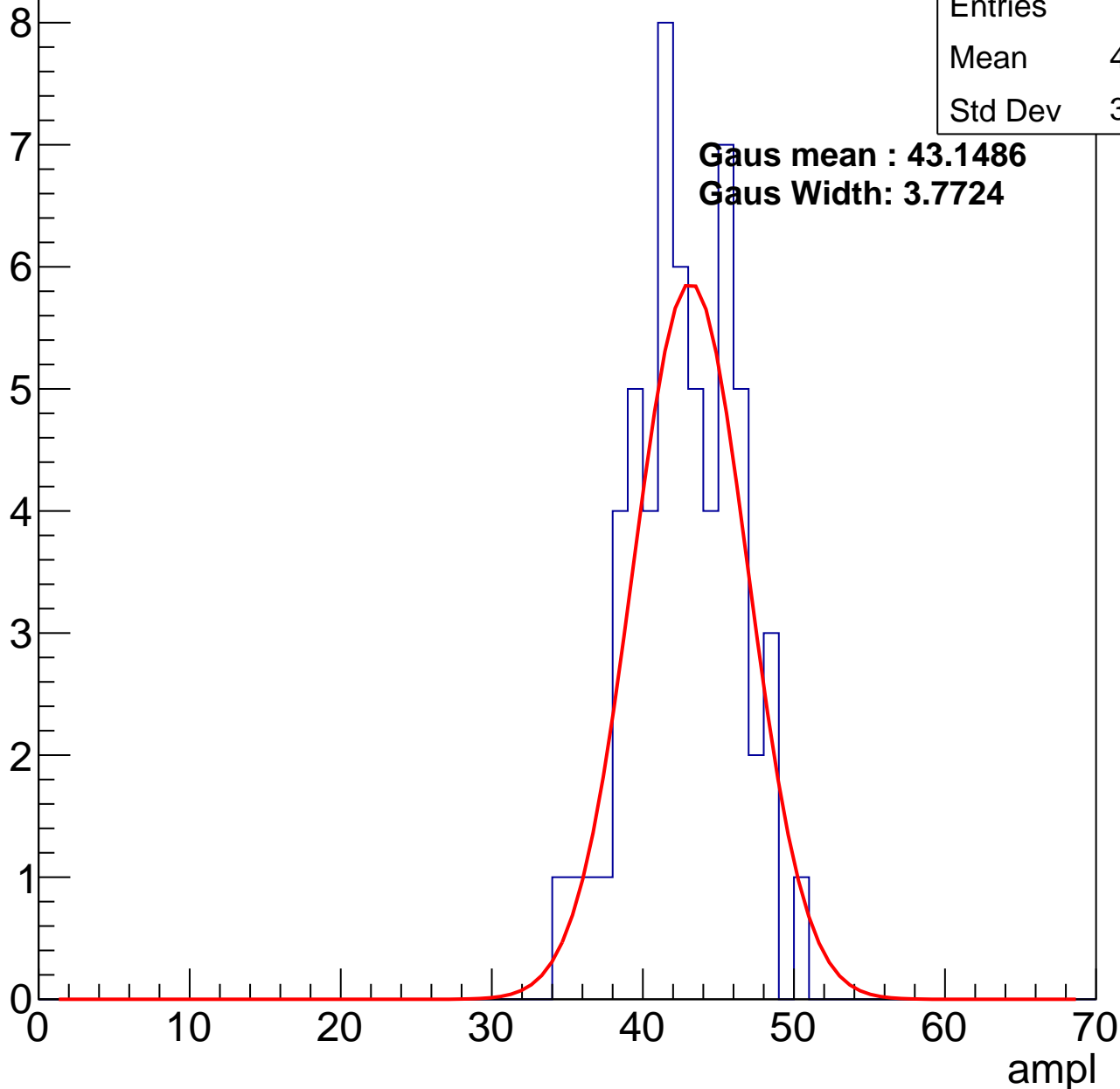
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.29
Std Dev	3.444

Gaus mean : 43.1486

Gaus Width: 3.7724

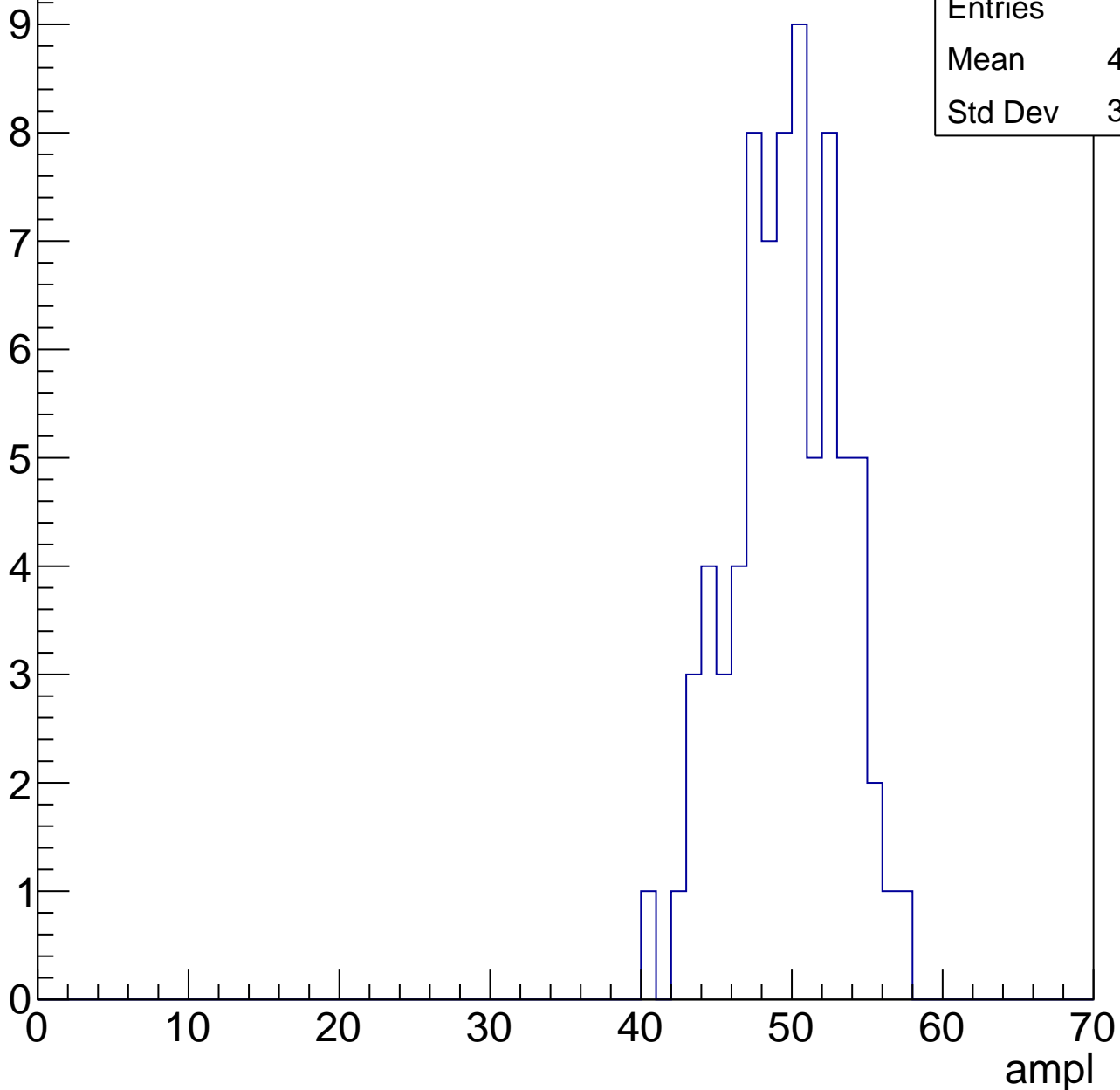


B1L101S, U17-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	49.19
Std Dev	3.558

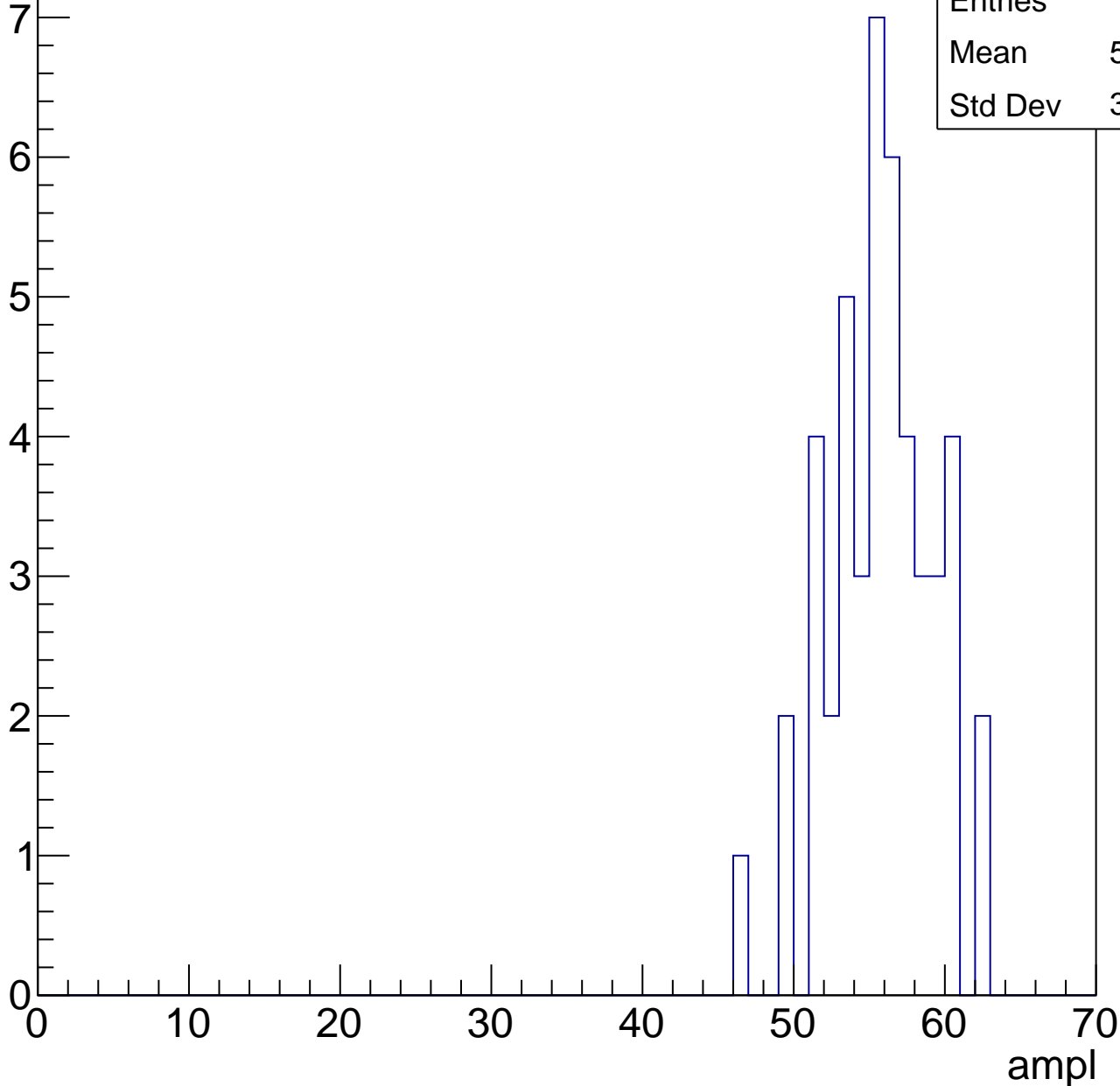


B1L101S, U17-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	55.28
Std Dev	3.456

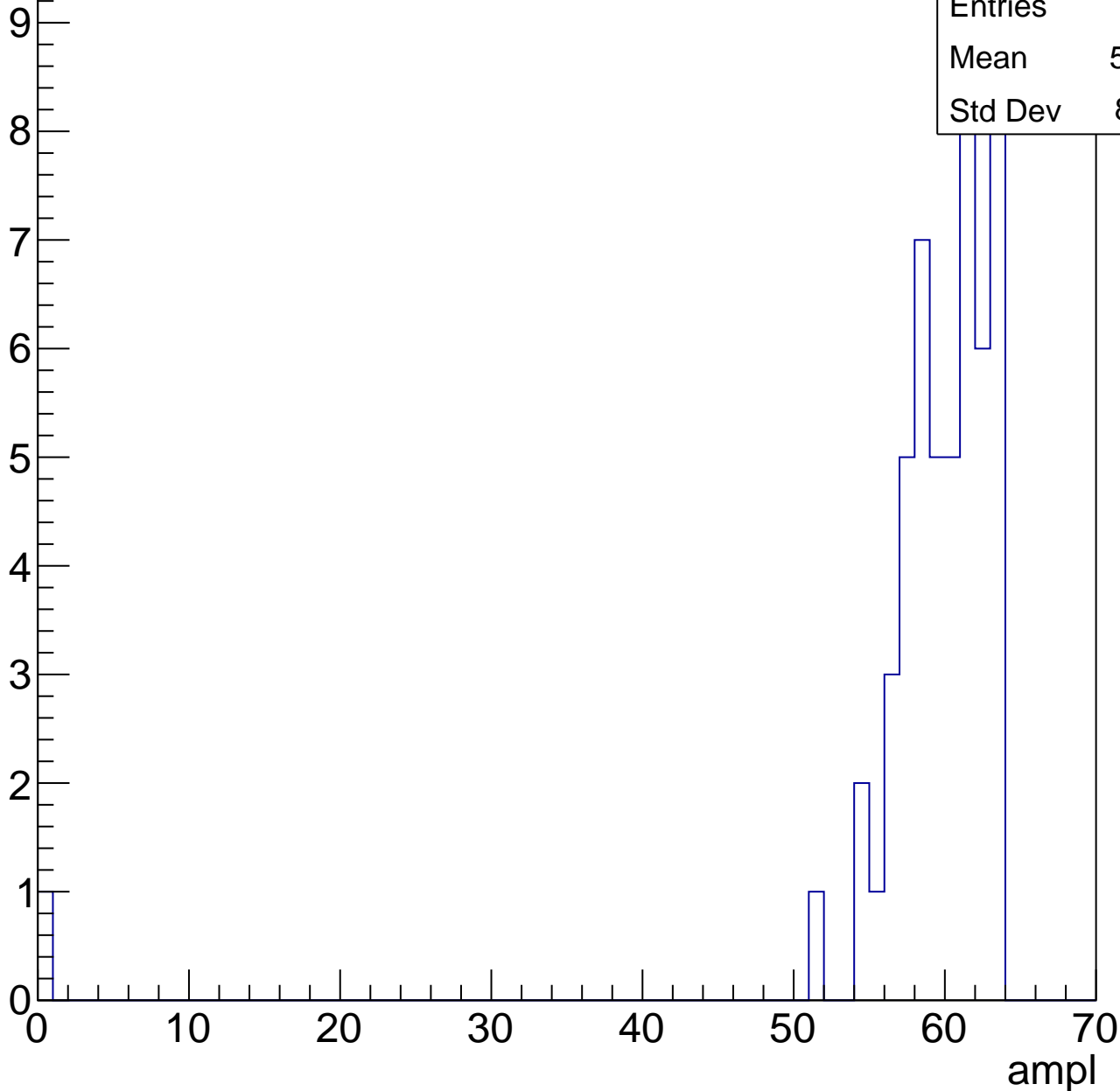


B1L101S, U17-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.36
Std Dev	8.541



B1L101S, U17-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	8
Mean	61.12
Std Dev	1.536

B1L101S, U17-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch33, adc0

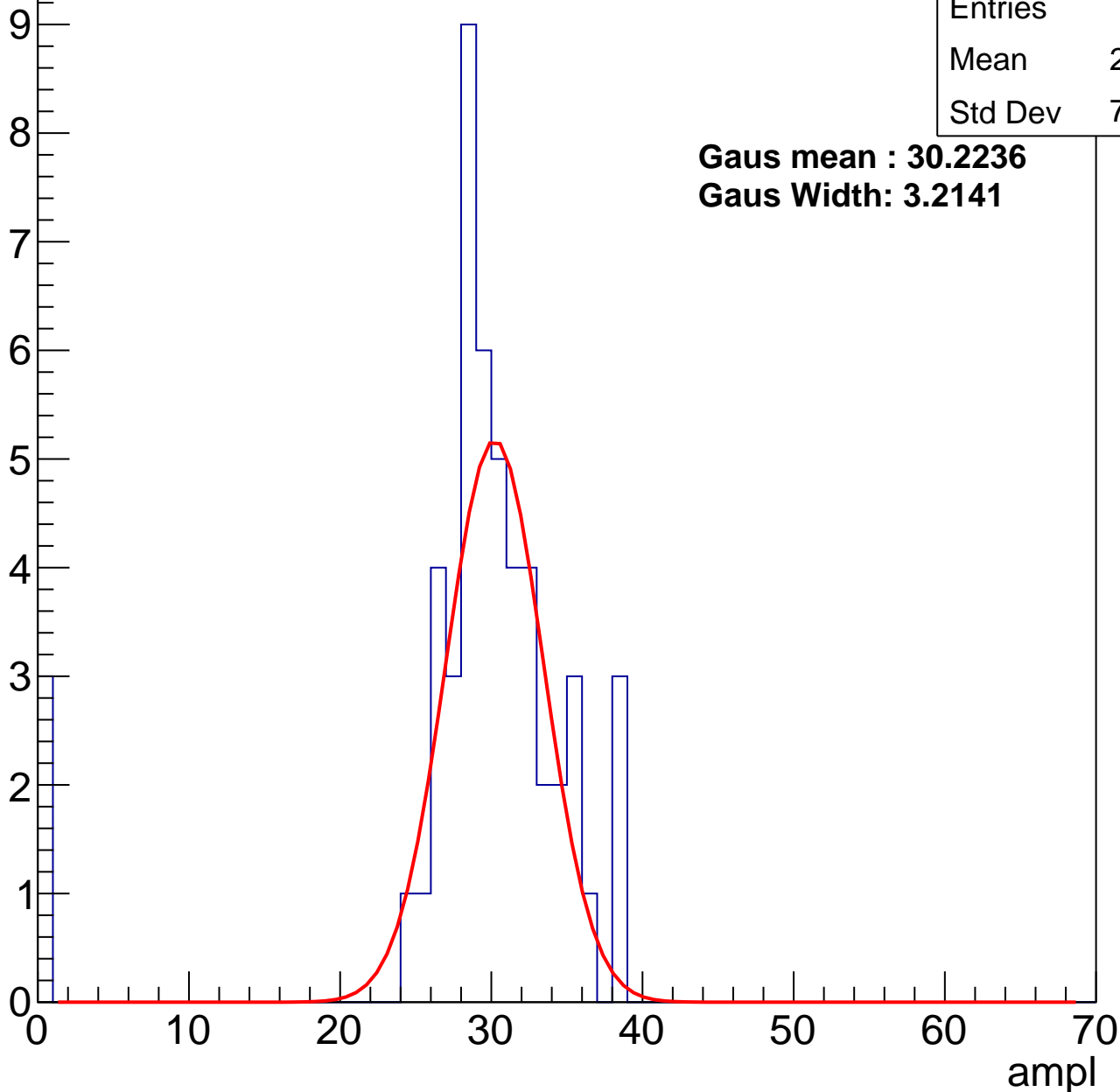
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	28.45
Std Dev	7.852

Gaus mean : 30.2236

Gaus Width: 3.2141



B1L101S, U17-ch33, adc1

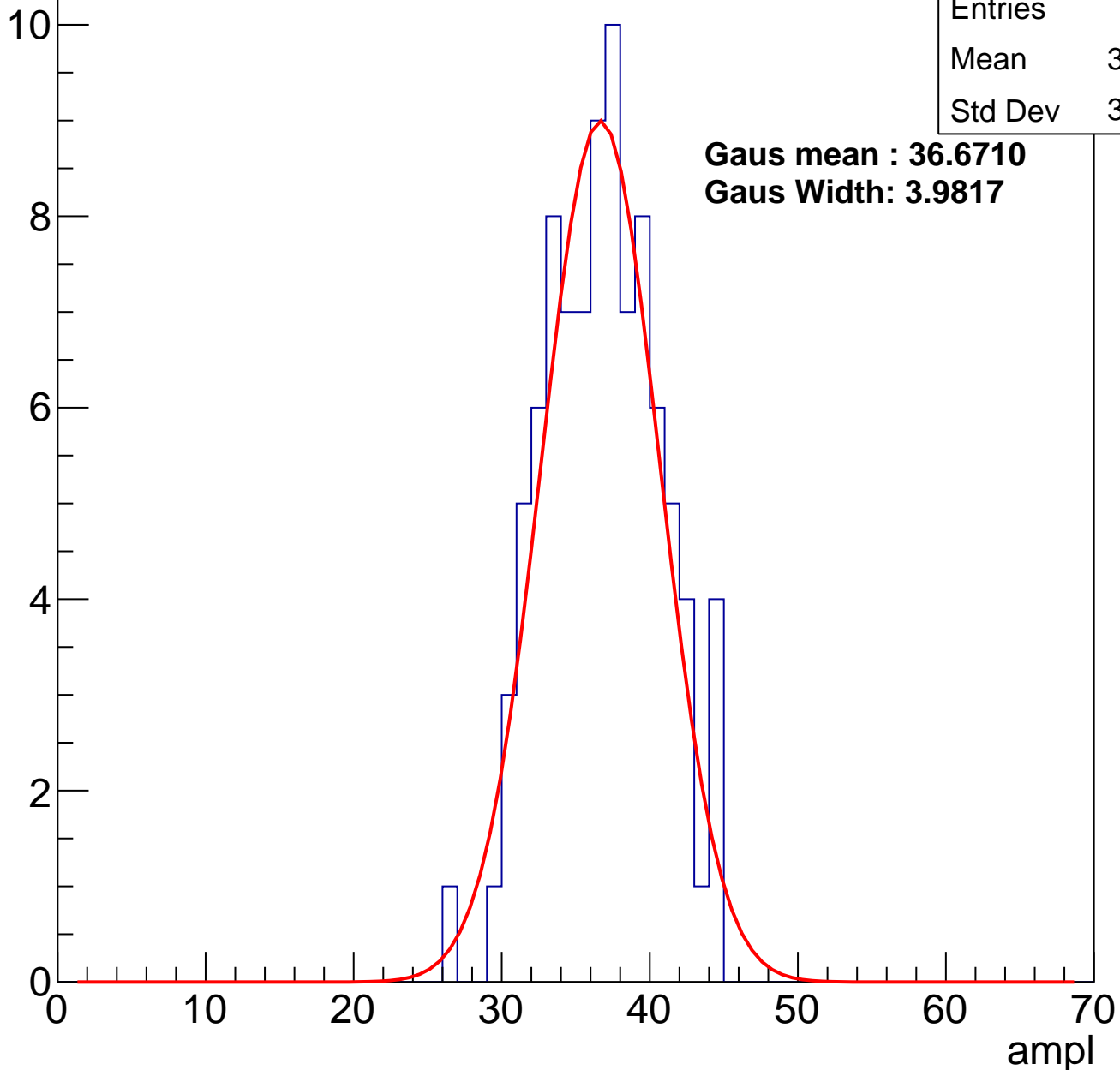
calib_packv5_042523_0143.root, FC#0, port D2

Entries	92
Mean	36.34
Std Dev	3.837

Gaus mean : 36.6710

Gaus Width: 3.9817

Entry



B1L101S, U17-ch33, adc2

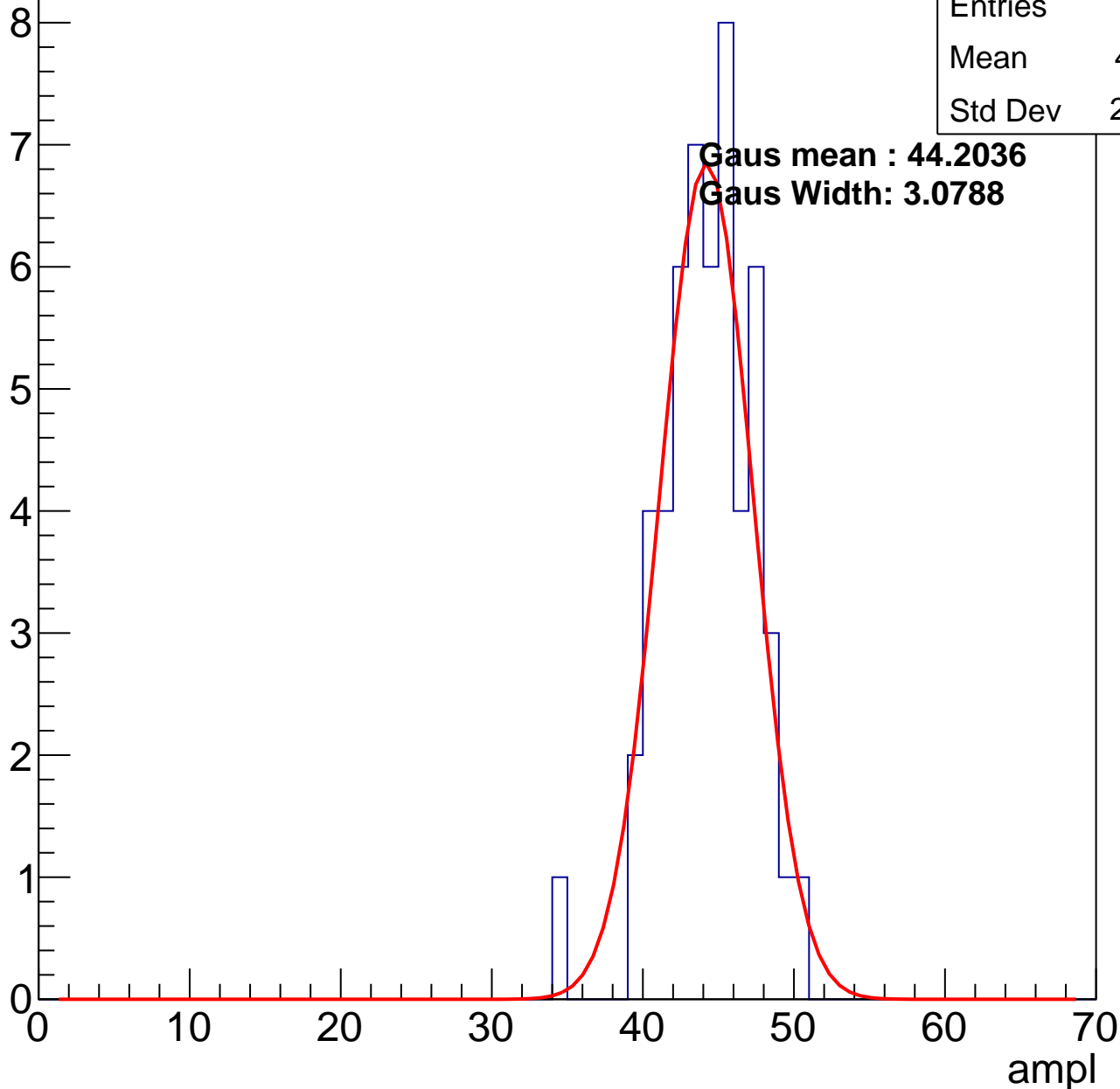
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	43.81
Std Dev	2.966

Gaus mean : 44.2036

Gaus Width: 3.0788

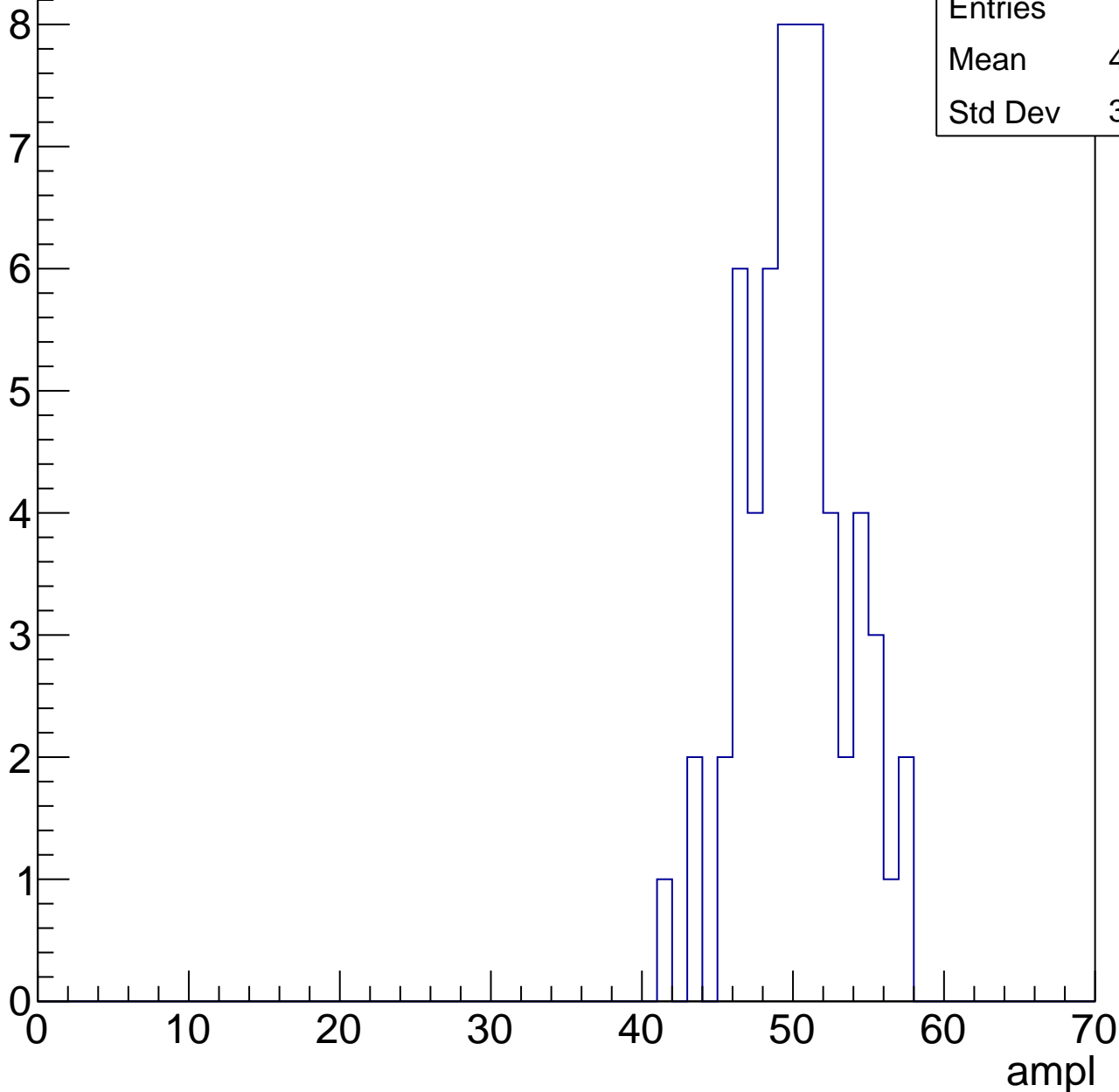


B1L101S, U17-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.74
Std Dev	3.397

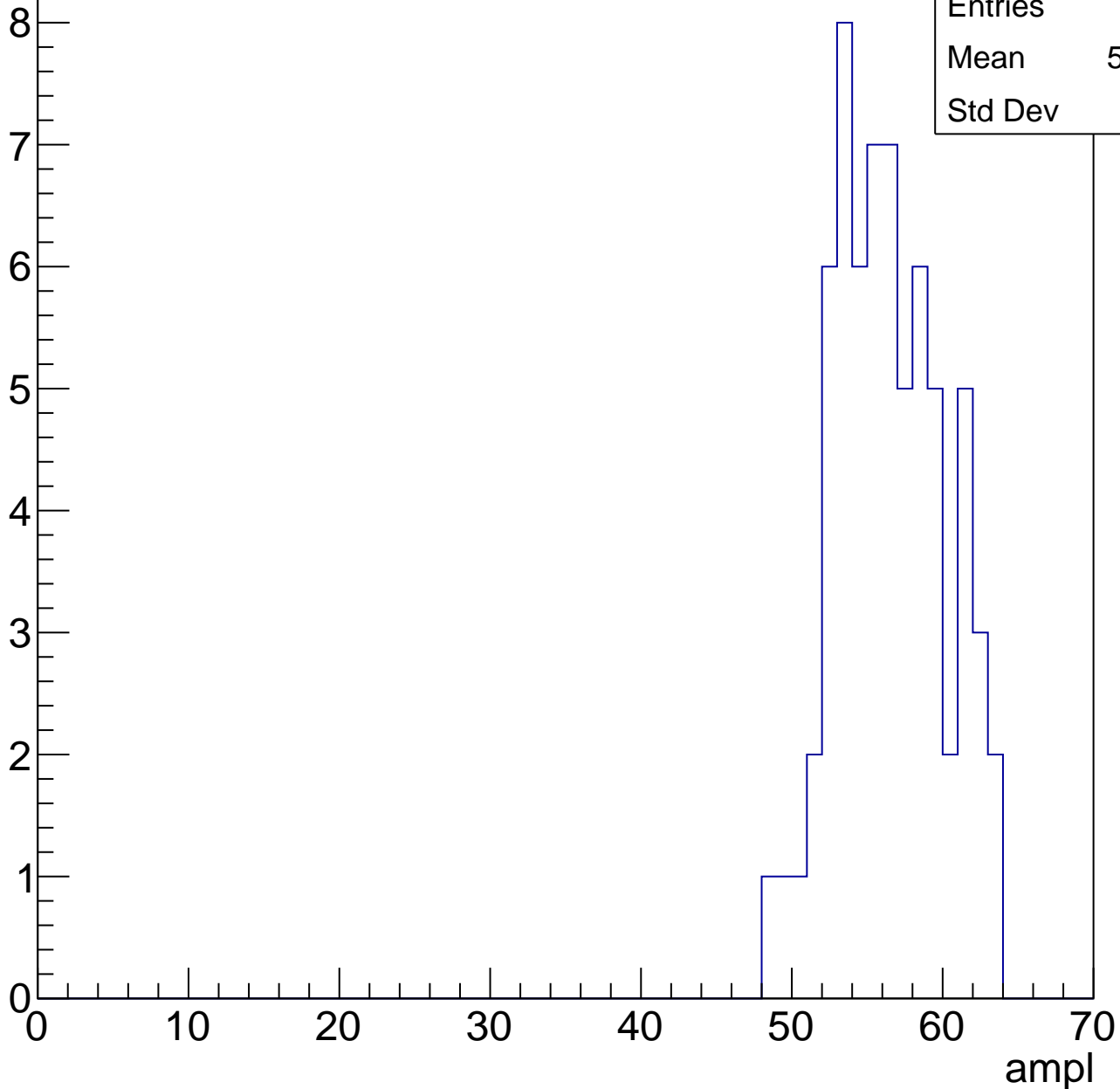


B1L101S, U17-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	55.99
Std Dev	3.53

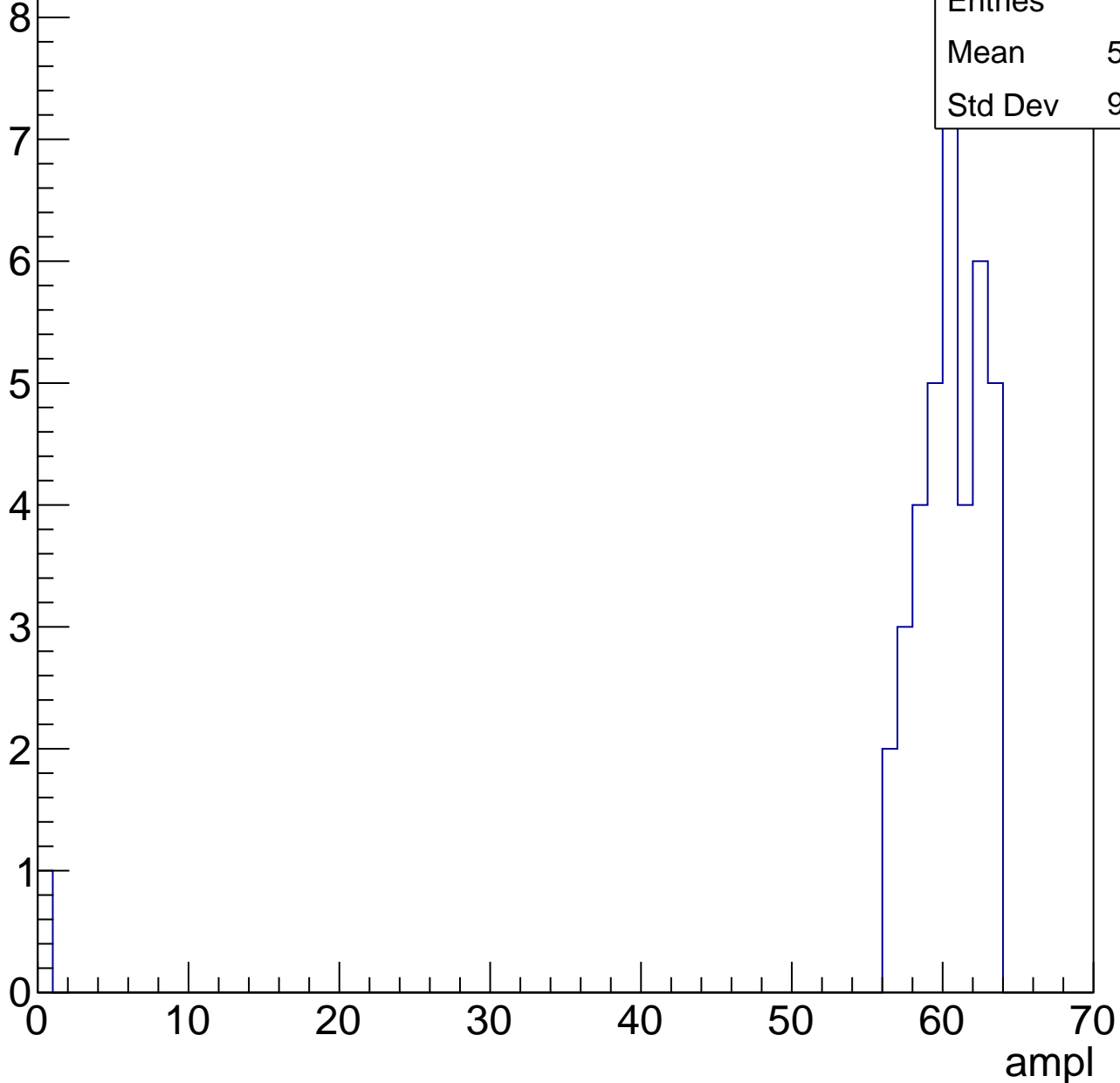


B1L101S, U17-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.45
Std Dev	9.816



B1L101S, U17-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch34, adc0

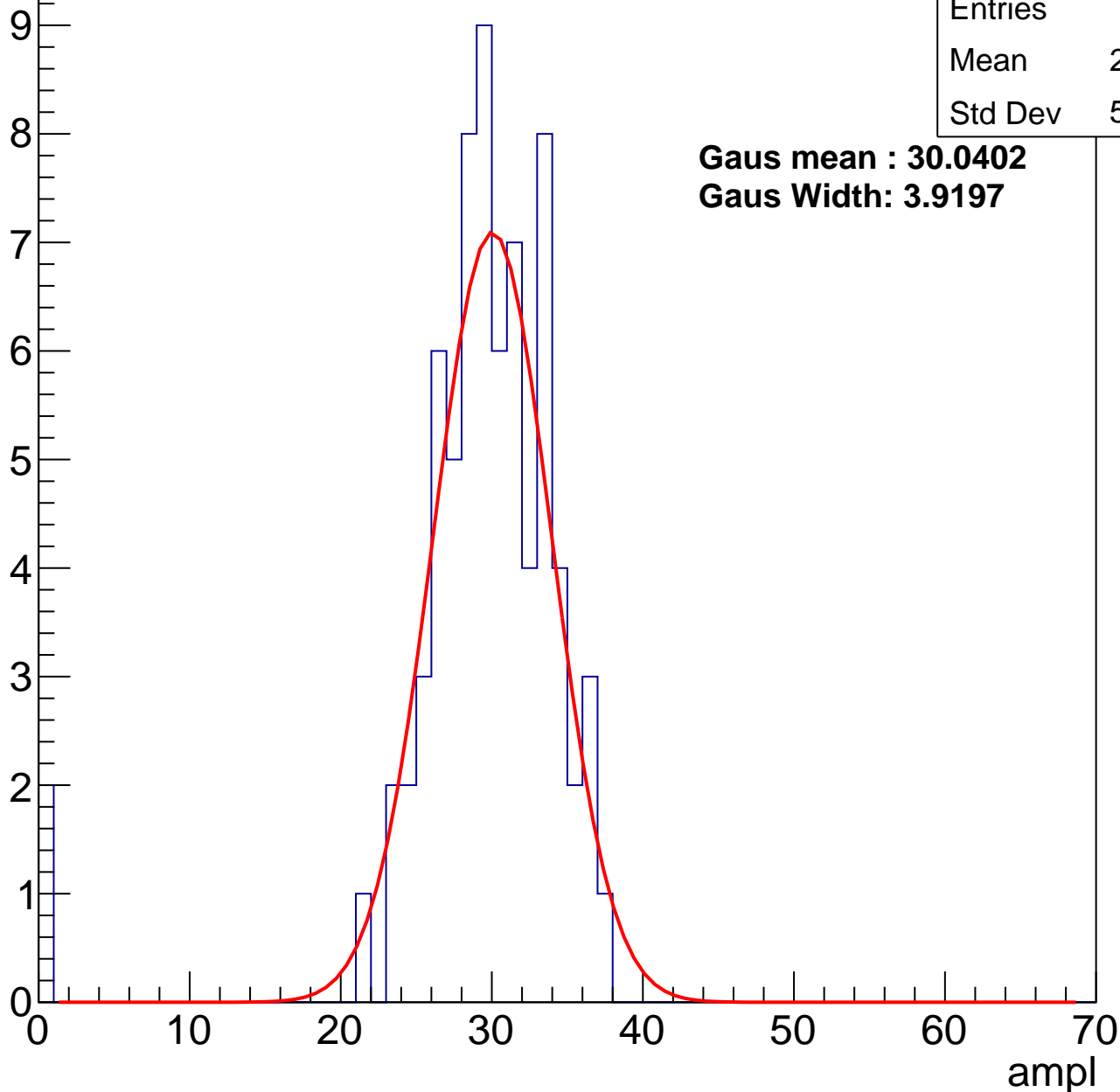
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.85
Std Dev	5.942

Gaus mean : 30.0402

Gaus Width: 3.9197



B1L101S, U17-ch34, adc1

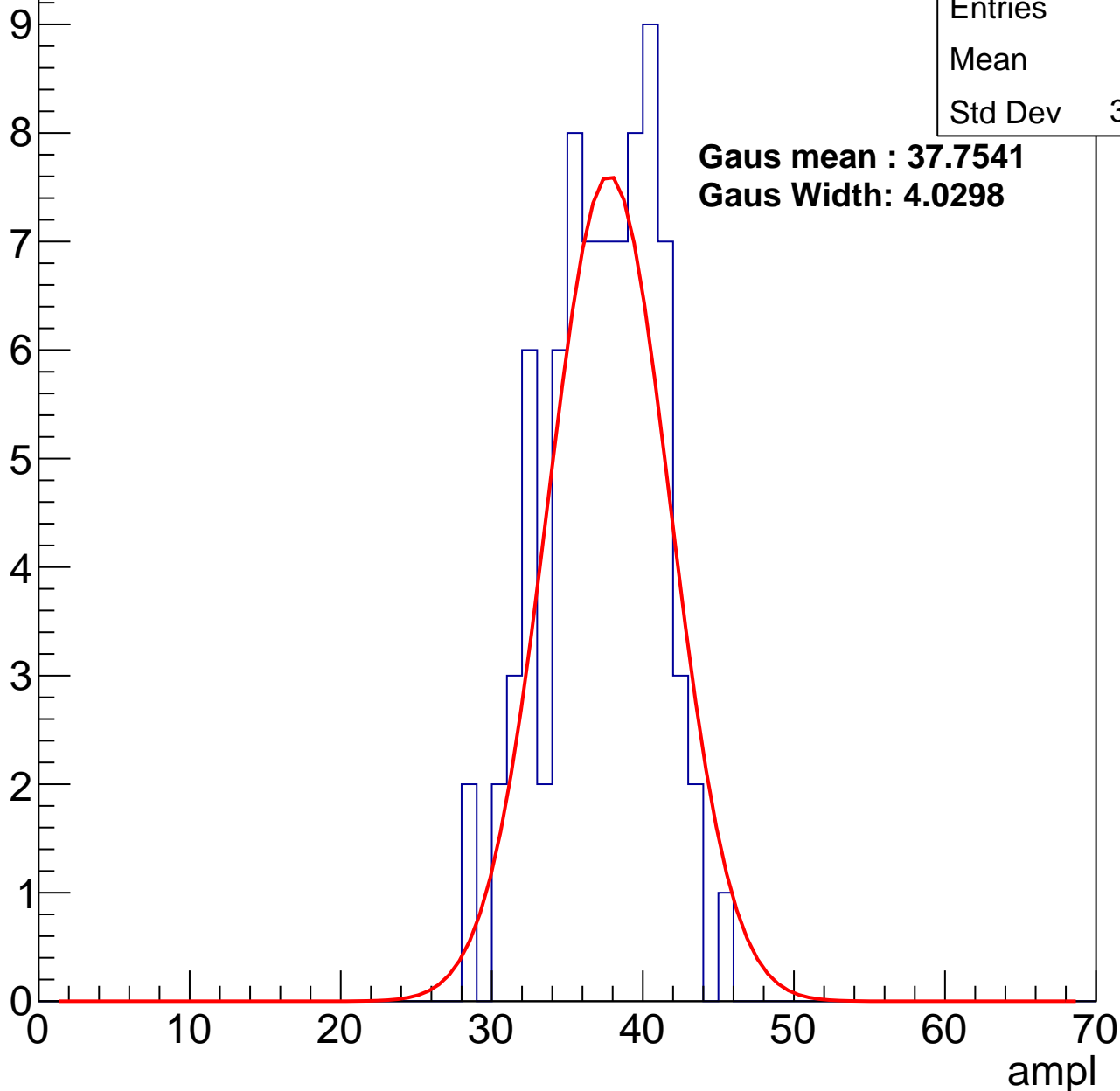
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.8
Std Dev	3.679

Gaus mean : 37.7541

Gaus Width: 4.0298



B1L101S, U17-ch34, adc2

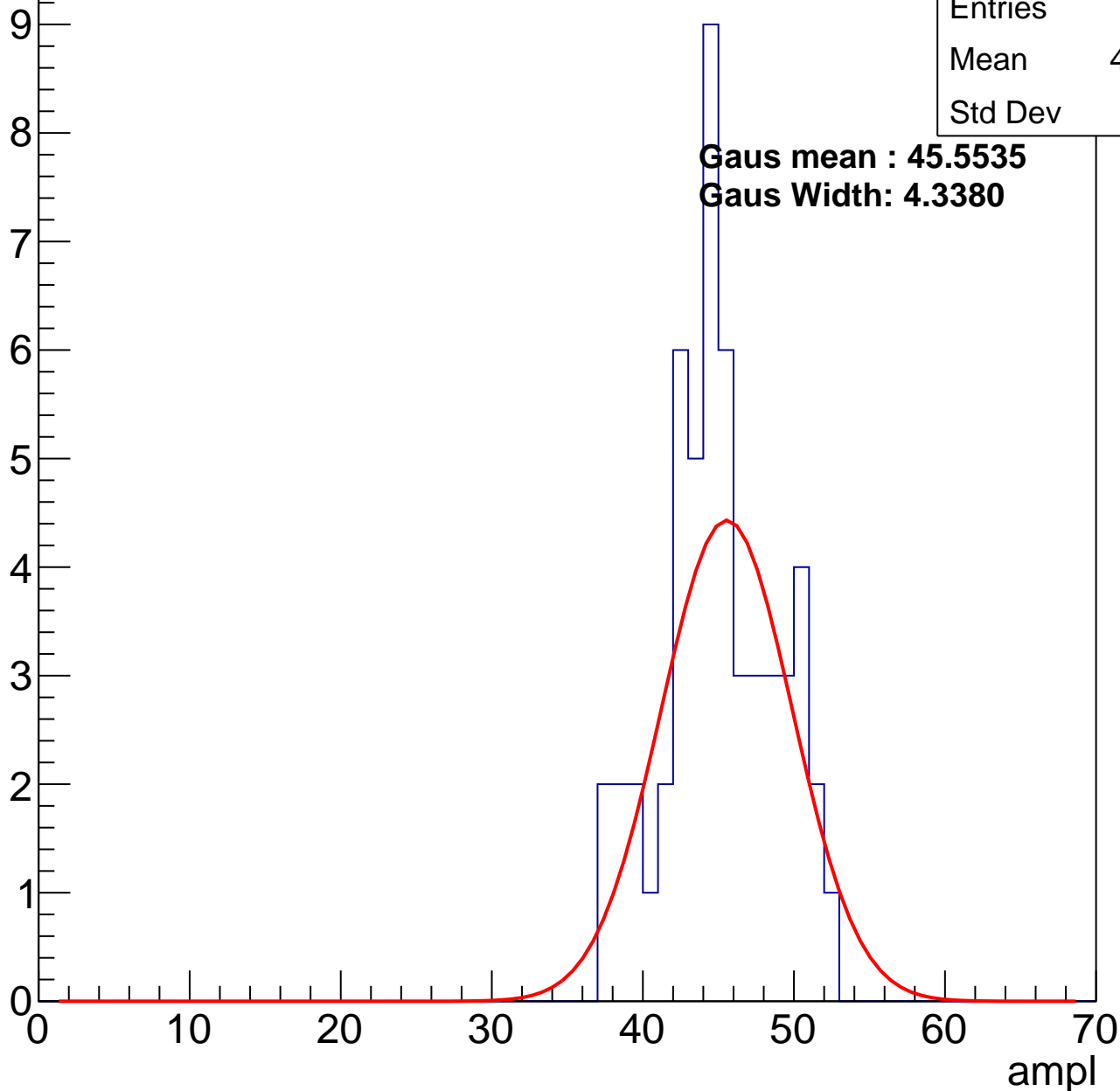
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.57
Std Dev	3.7

Gaus mean : 45.5535

Gaus Width: 4.3380

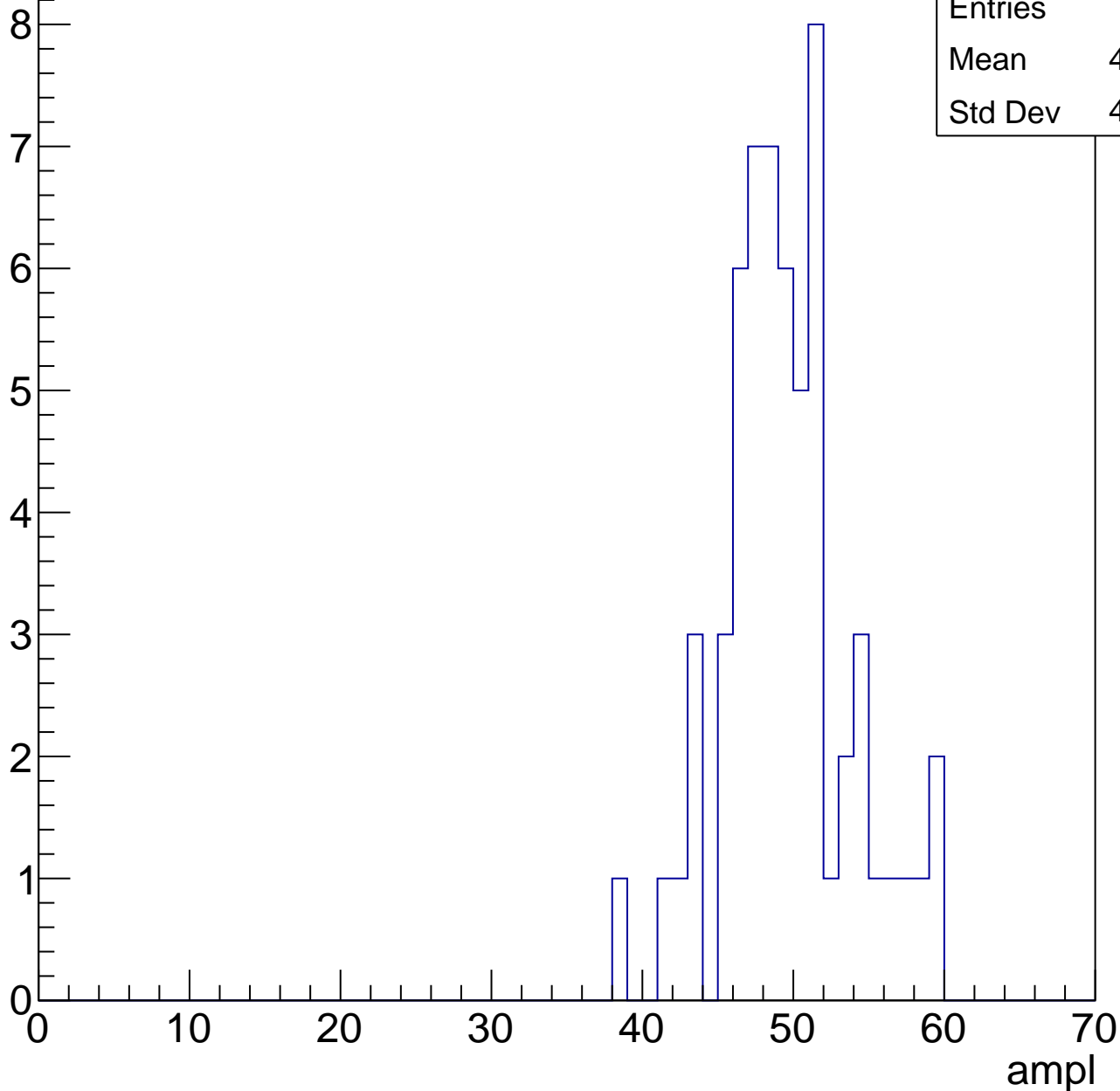


B1L101S, U17-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.03
Std Dev	4.207

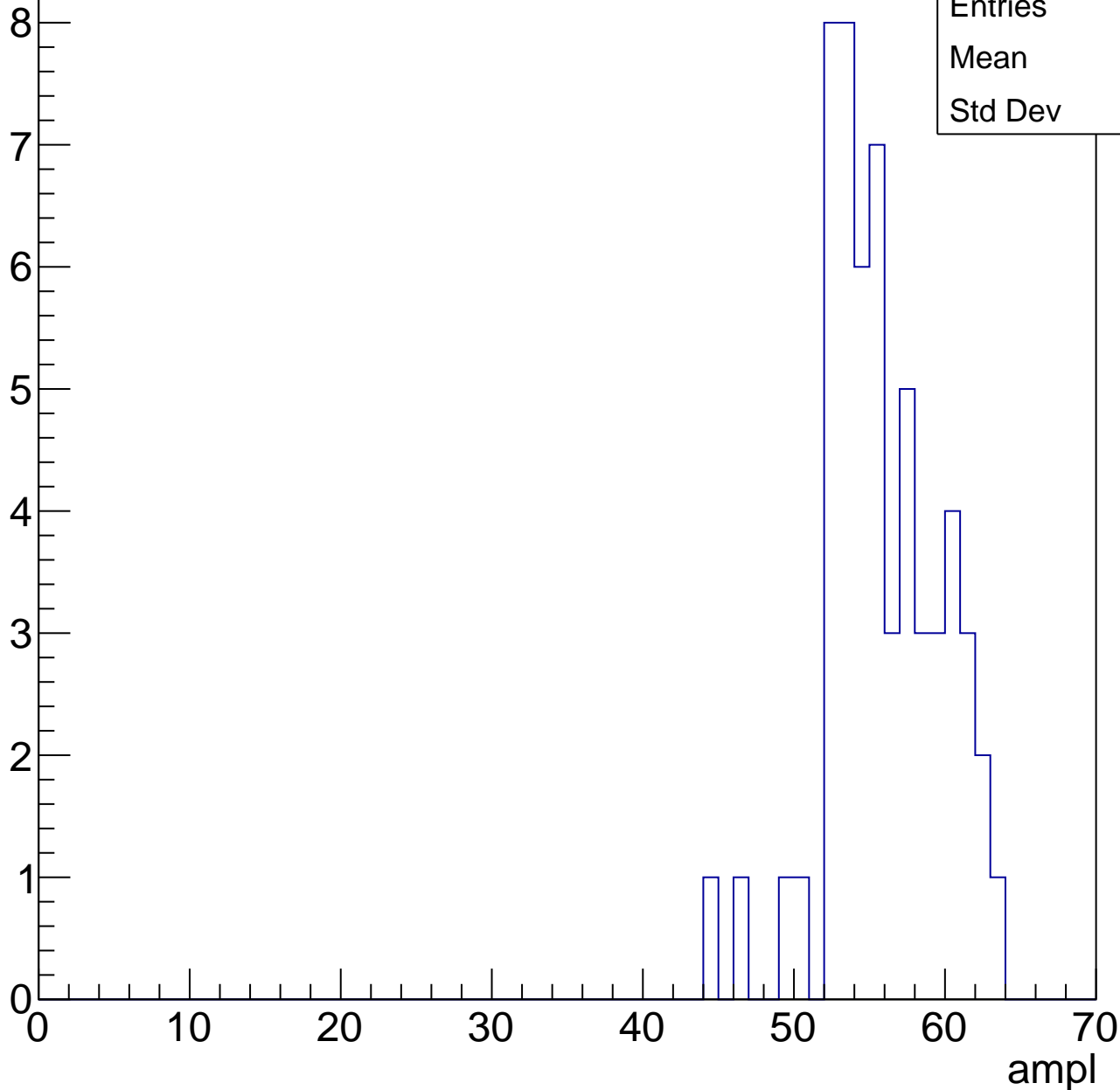


B1L101S, U17-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	55.3
Std Dev	3.83

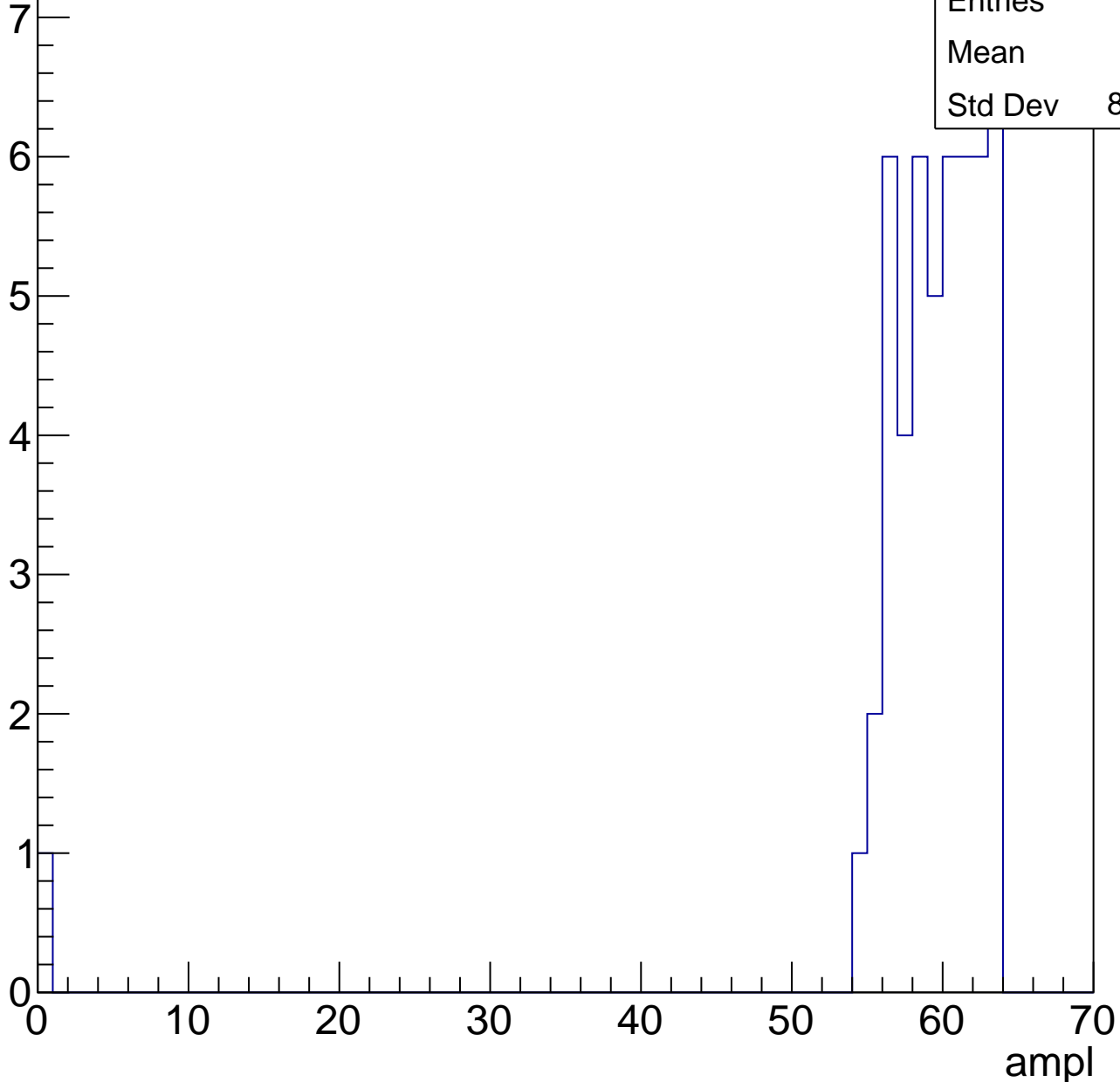


B1L101S, U17-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.2
Std Dev	8.693



B1L101S, U17-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch35, adc0

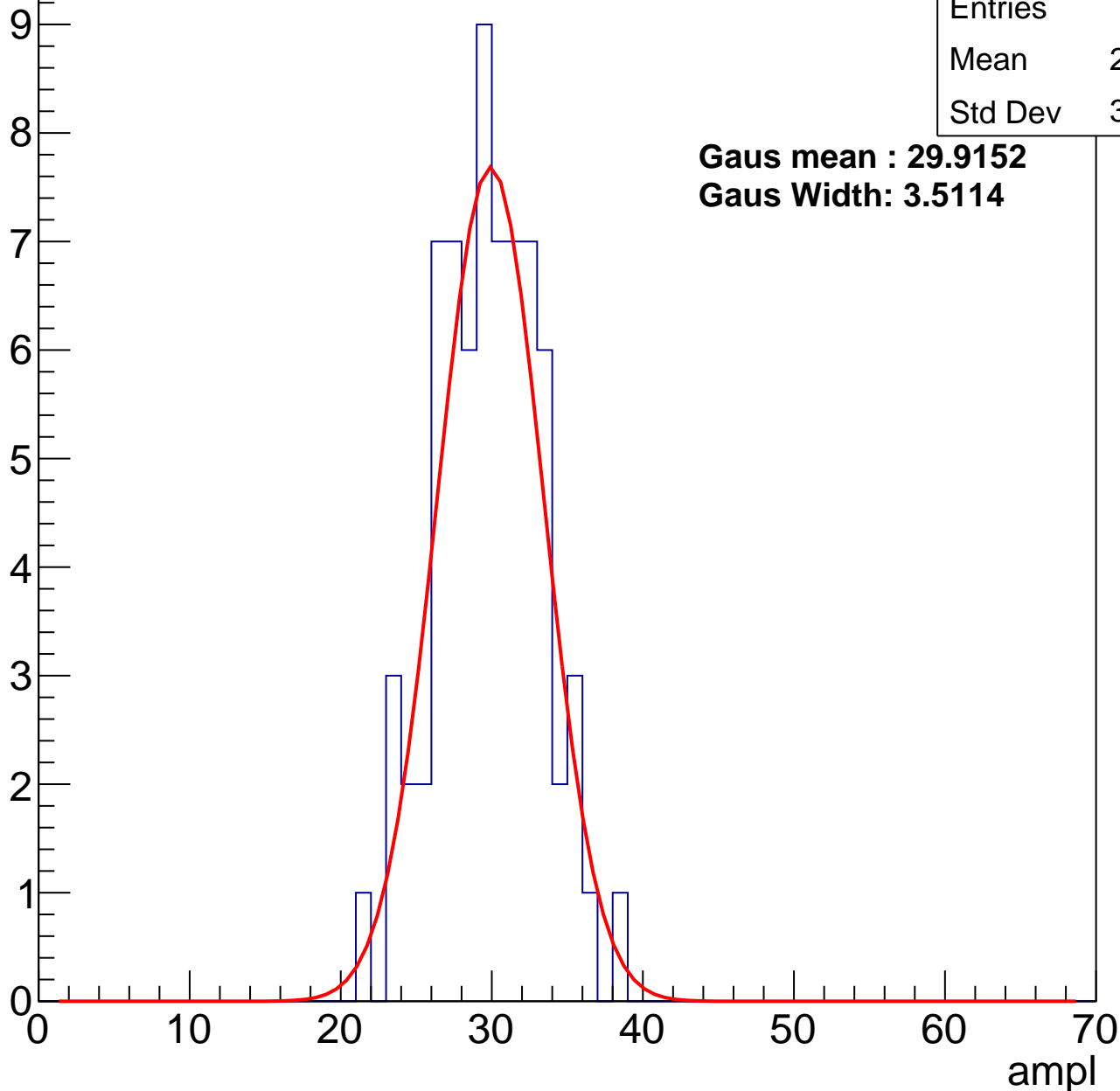
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.35
Std Dev	3.407

Gaus mean : 29.9152

Gaus Width: 3.5114



B1L101S, U17-ch35, adc1

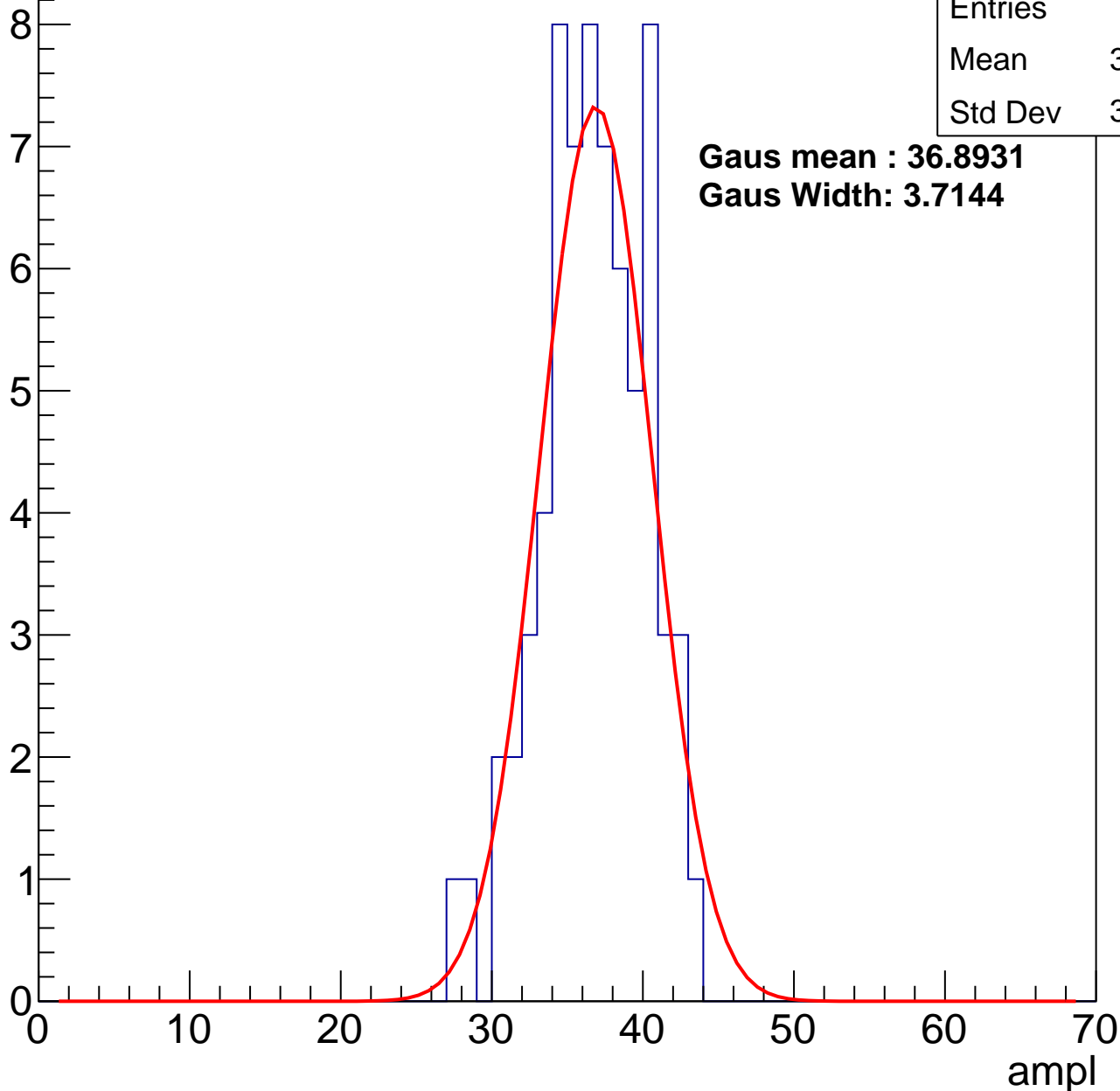
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.29
Std Dev	3.448

Gaus mean : 36.8931

Gaus Width: 3.7144



B1L101S, U17-ch35, adc2

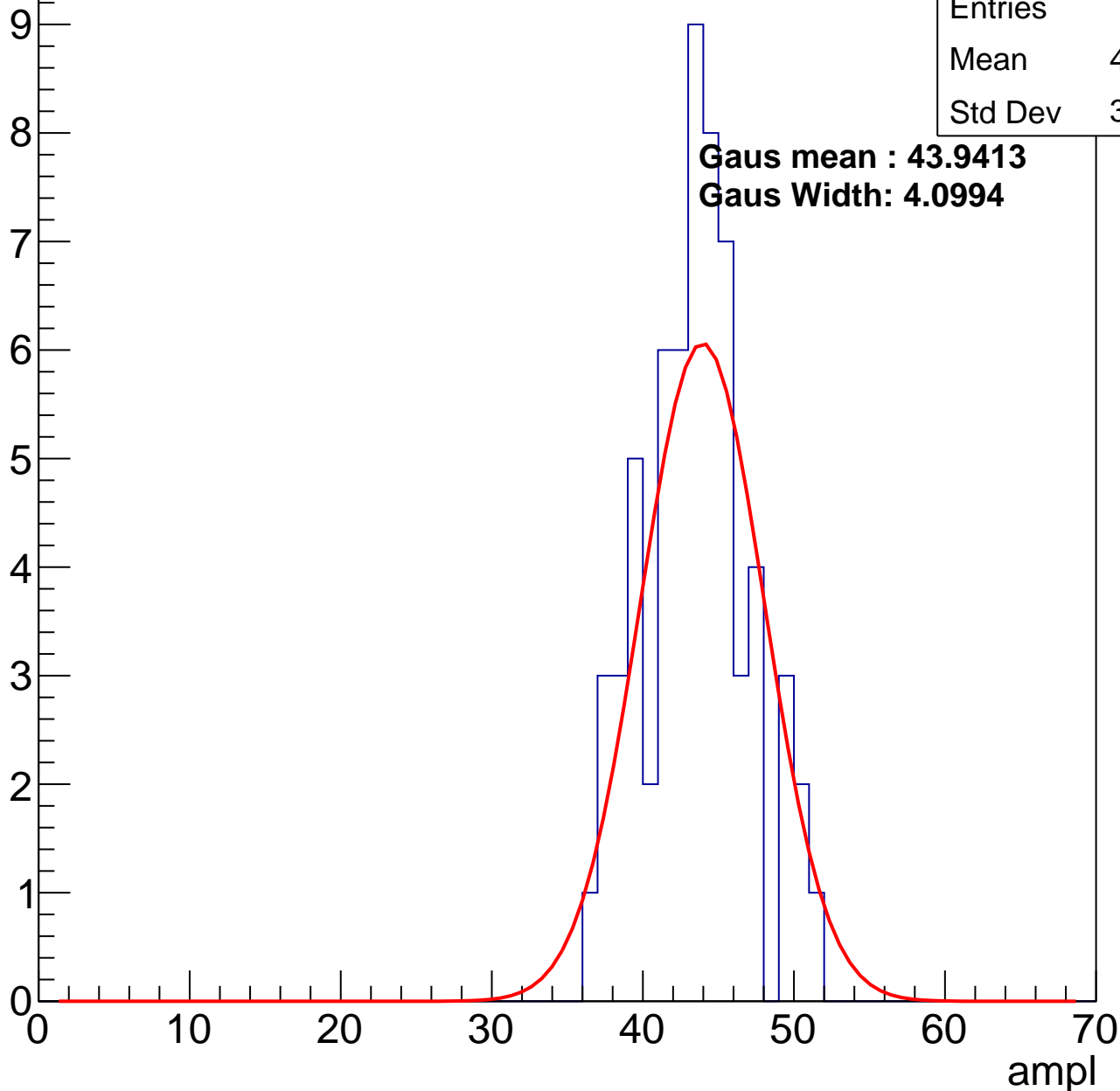
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.05
Std Dev	3.466

Gaus mean : 43.9413

Gaus Width: 4.0994

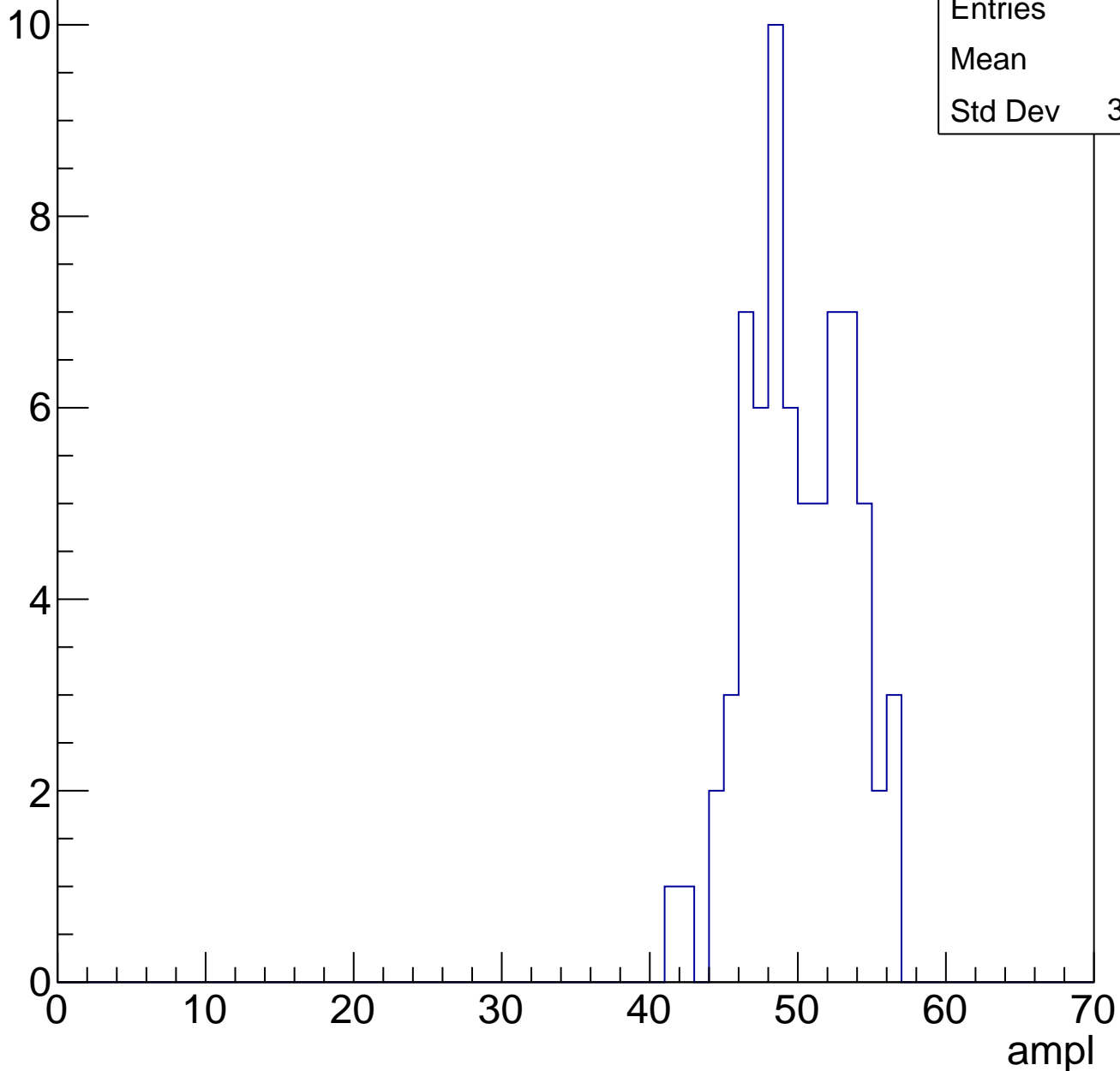


B1L101S, U17-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.6
Std Dev	3.437

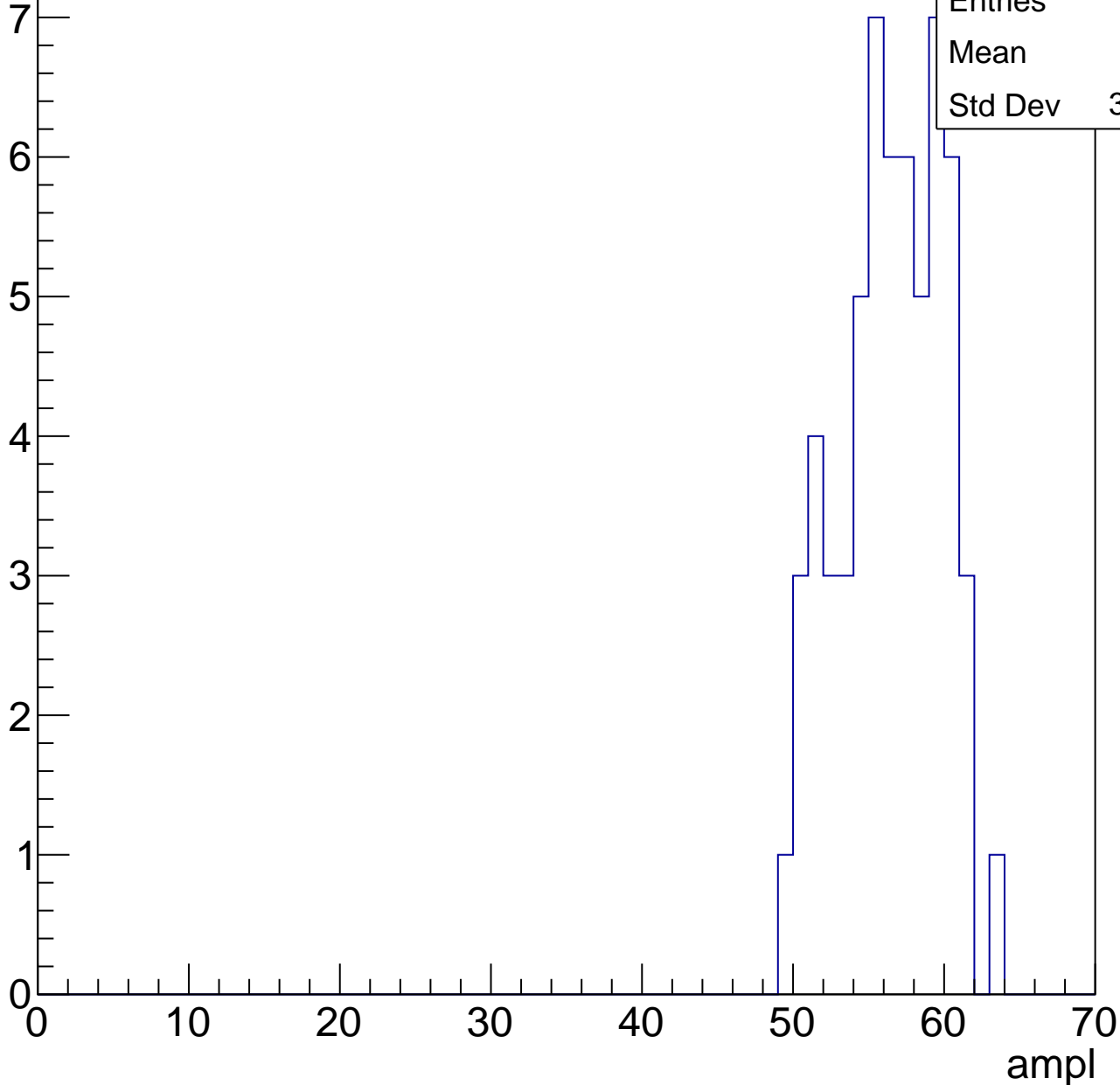


B1L101S, U17-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56
Std Dev	3.337

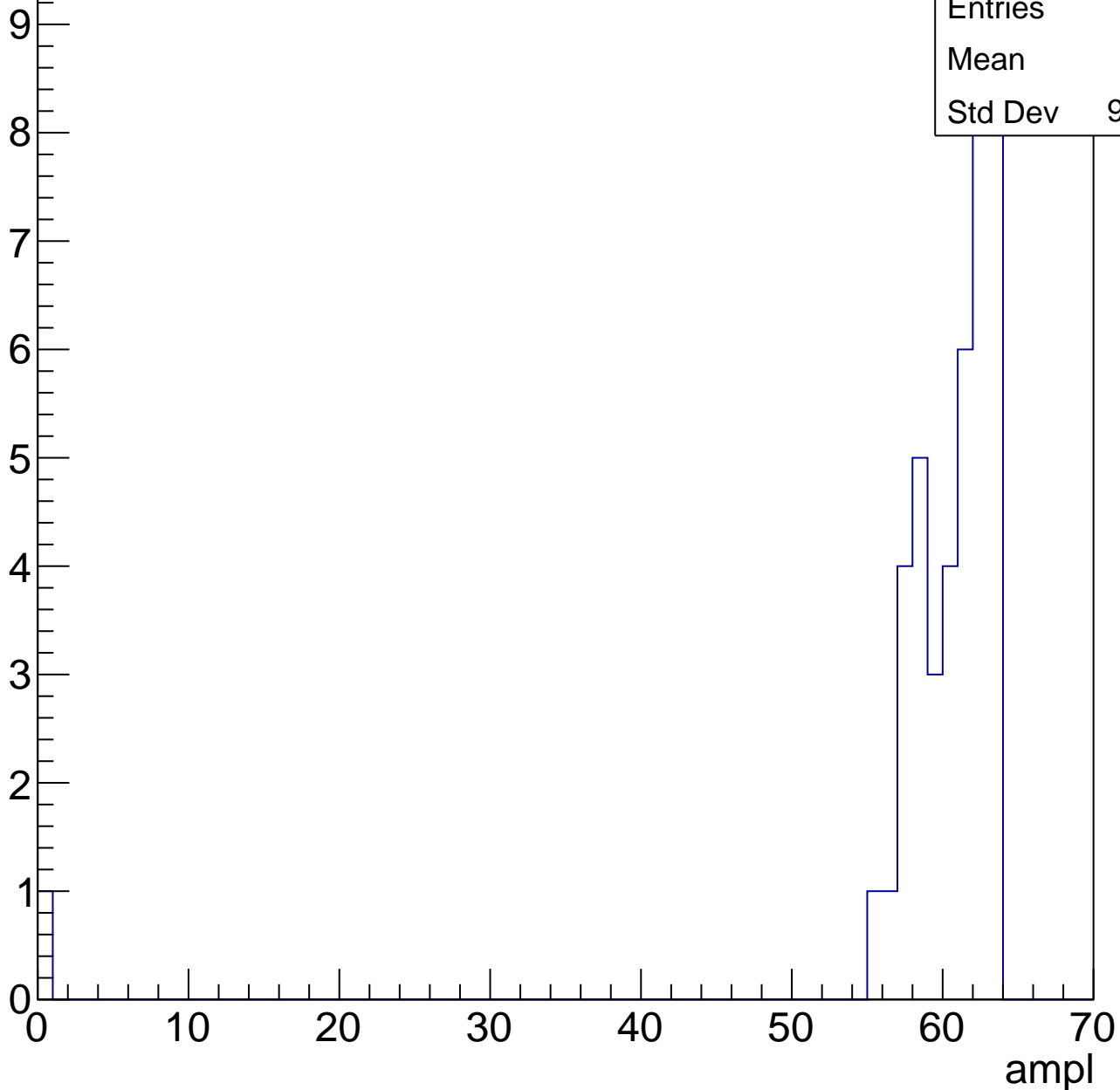


B1L101S, U17-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.9
Std Dev	9.466



B1L101S, U17-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch36, adc0

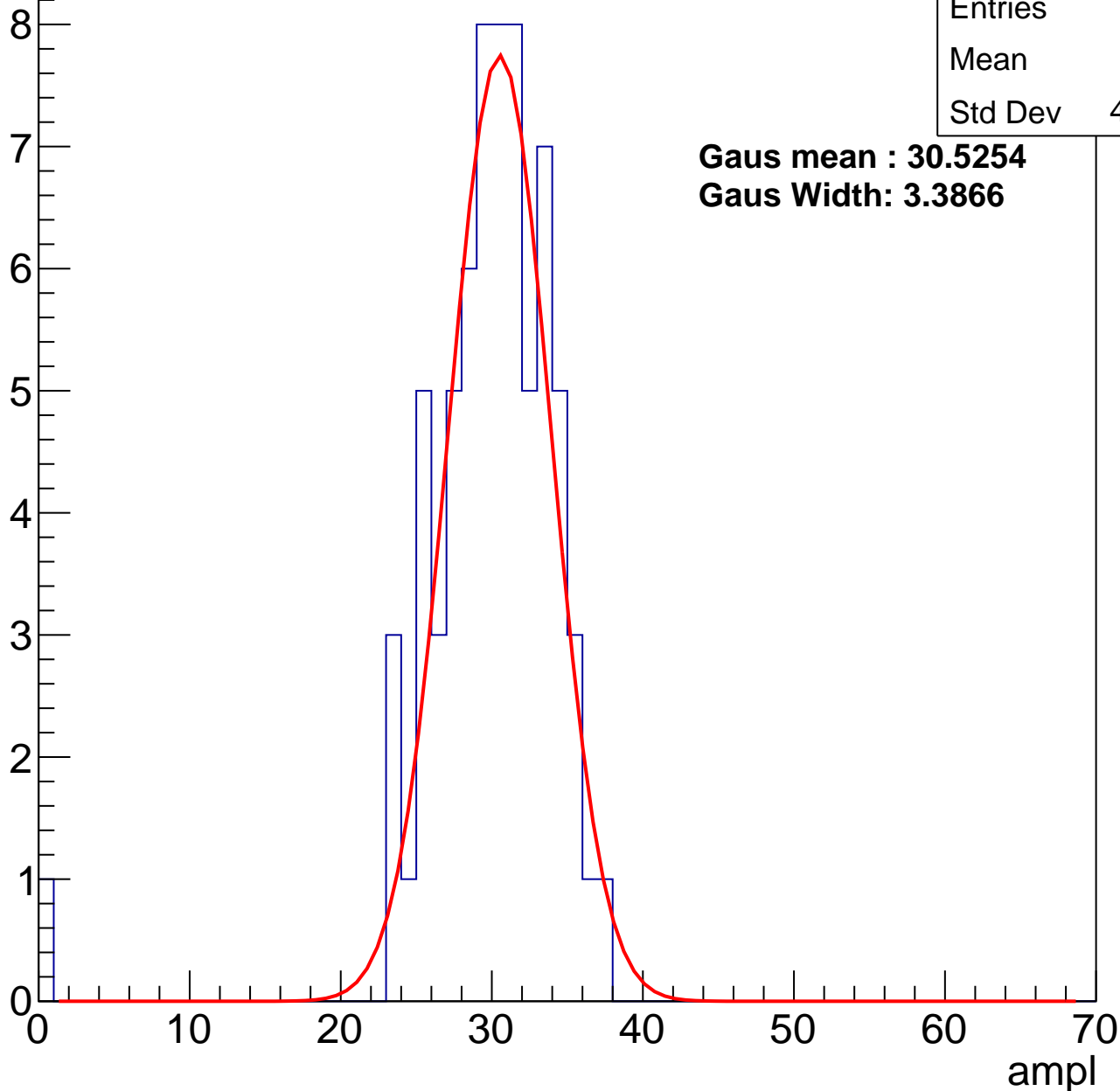
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.4
Std Dev	4.839

Gaus mean : 30.5254

Gaus Width: 3.3866



B1L101S, U17-ch36, adc1

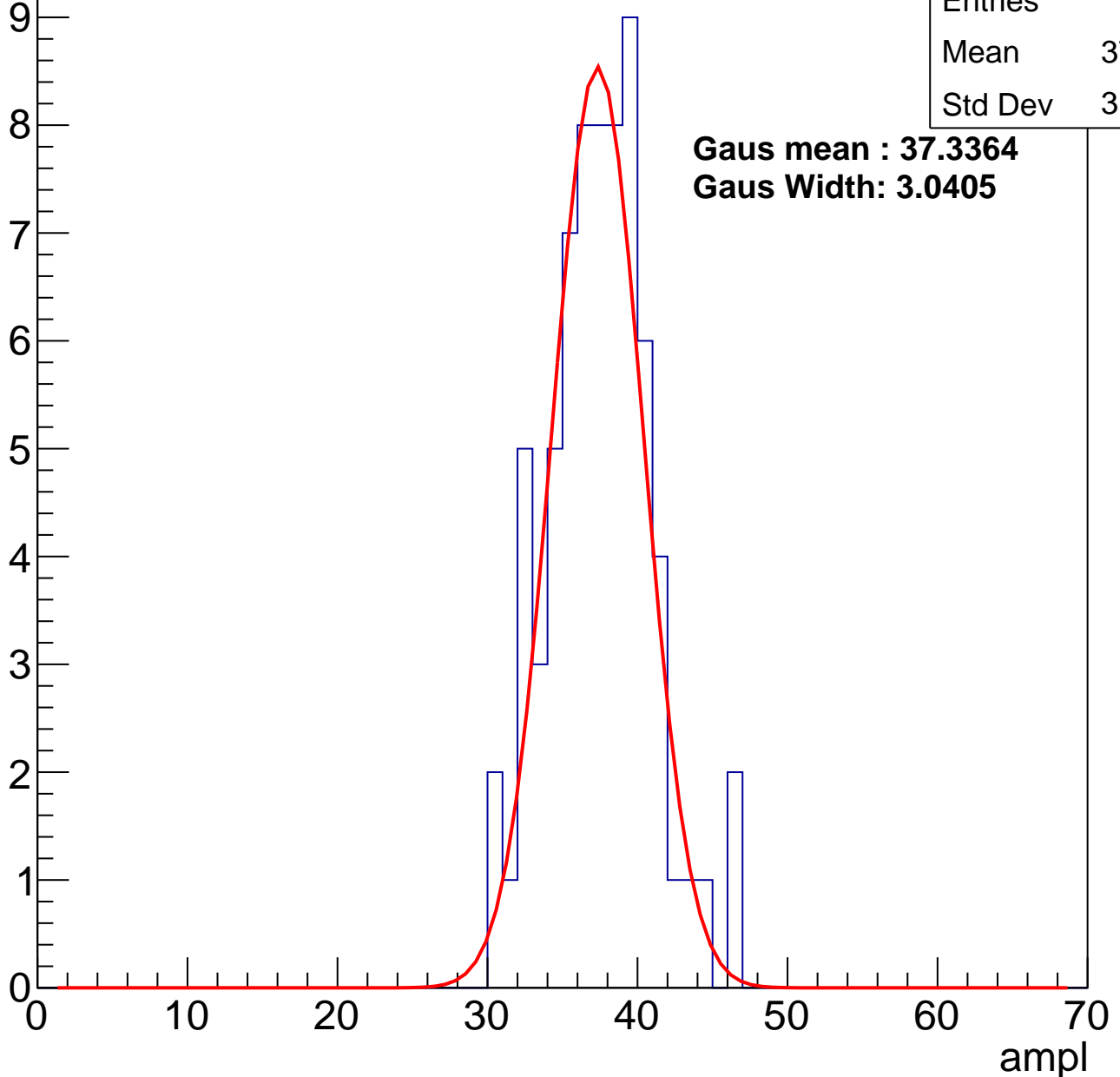
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.03
Std Dev	3.402

Gaus mean : 37.3364

Gaus Width: 3.0405



B1L101S, U17-ch36, adc2

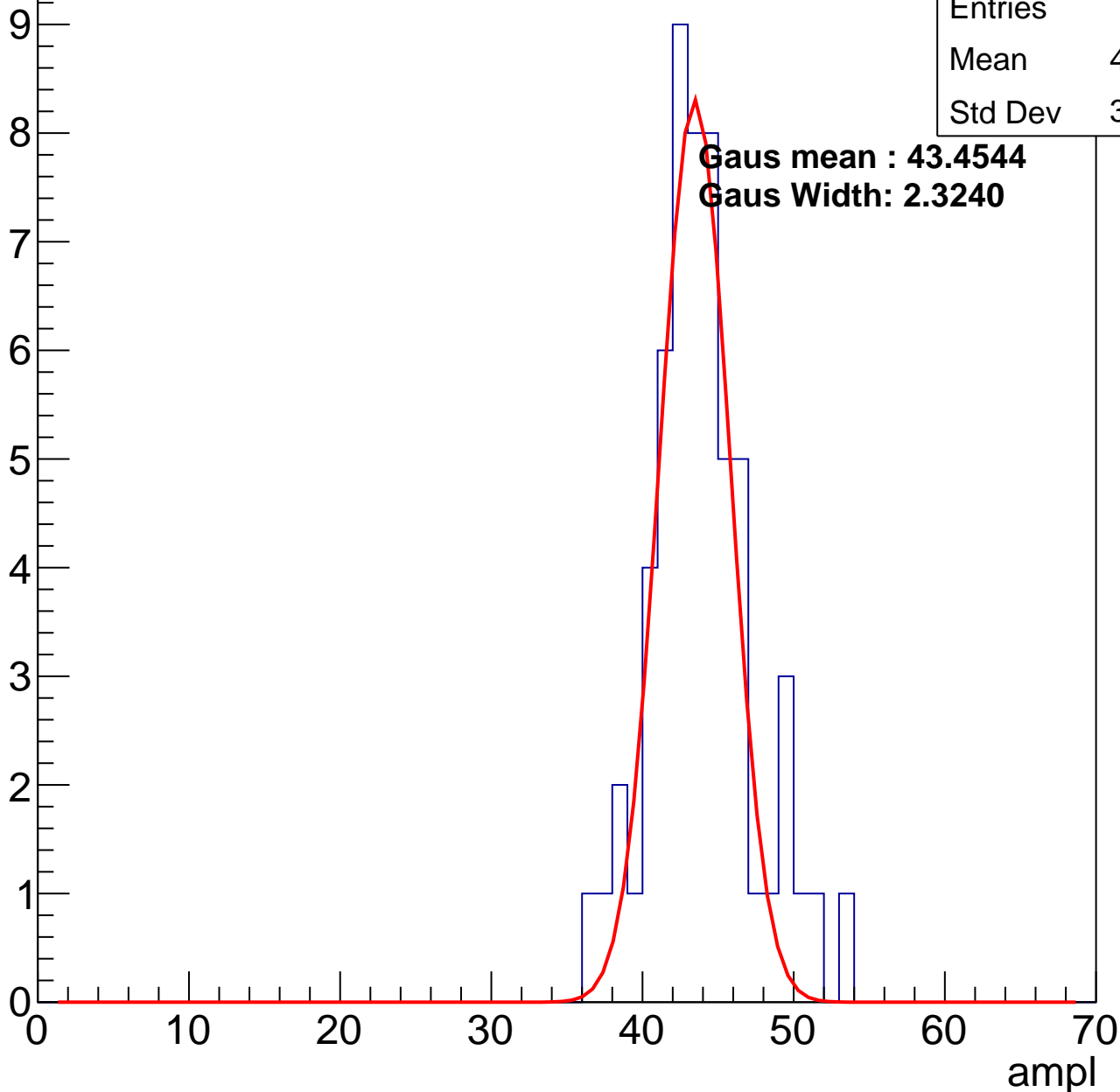
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.43
Std Dev	3.333

Gaus mean : 43.4544

Gaus Width: 2.3240

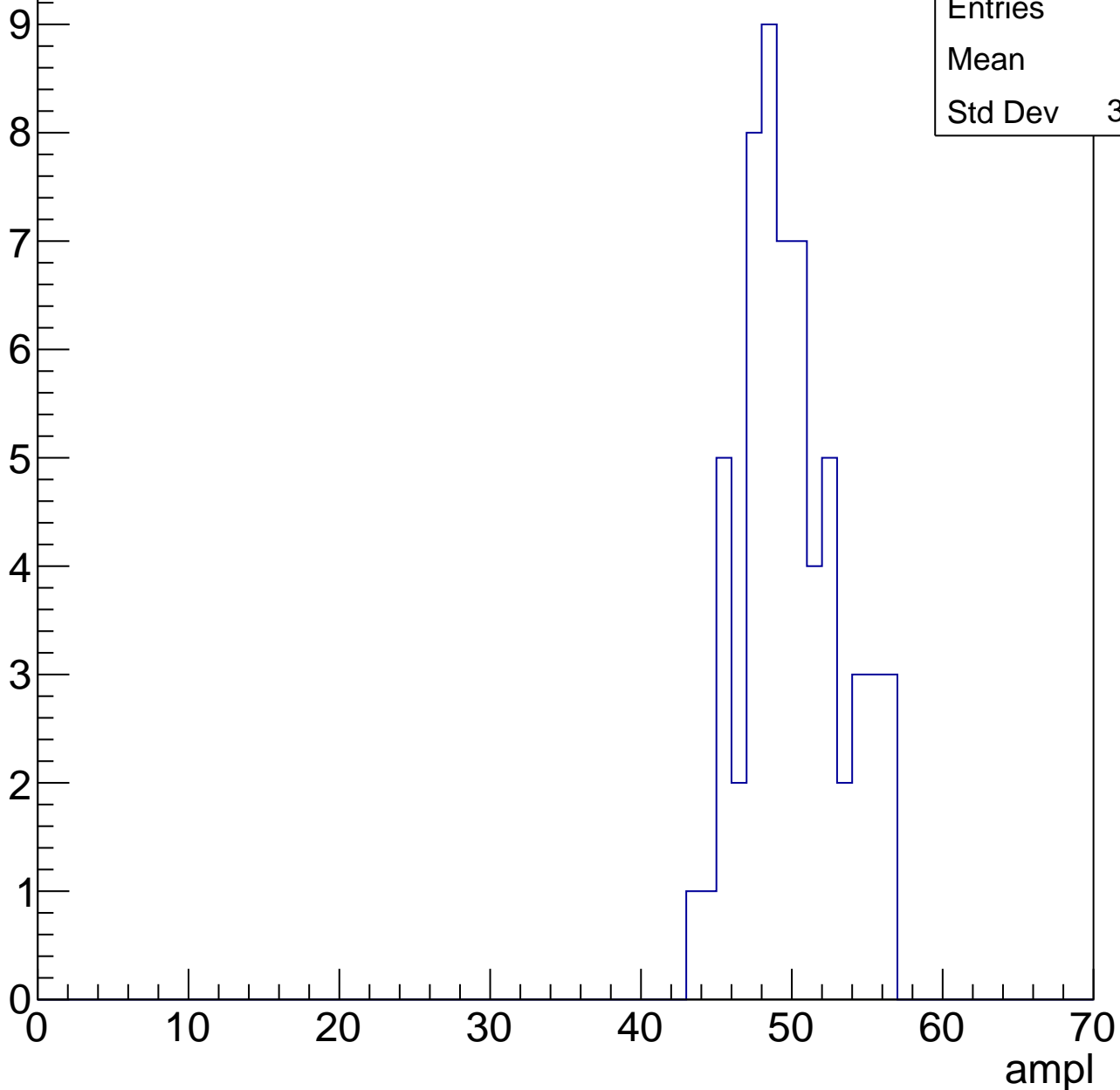


B1L101S, U17-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.5
Std Dev	3.202

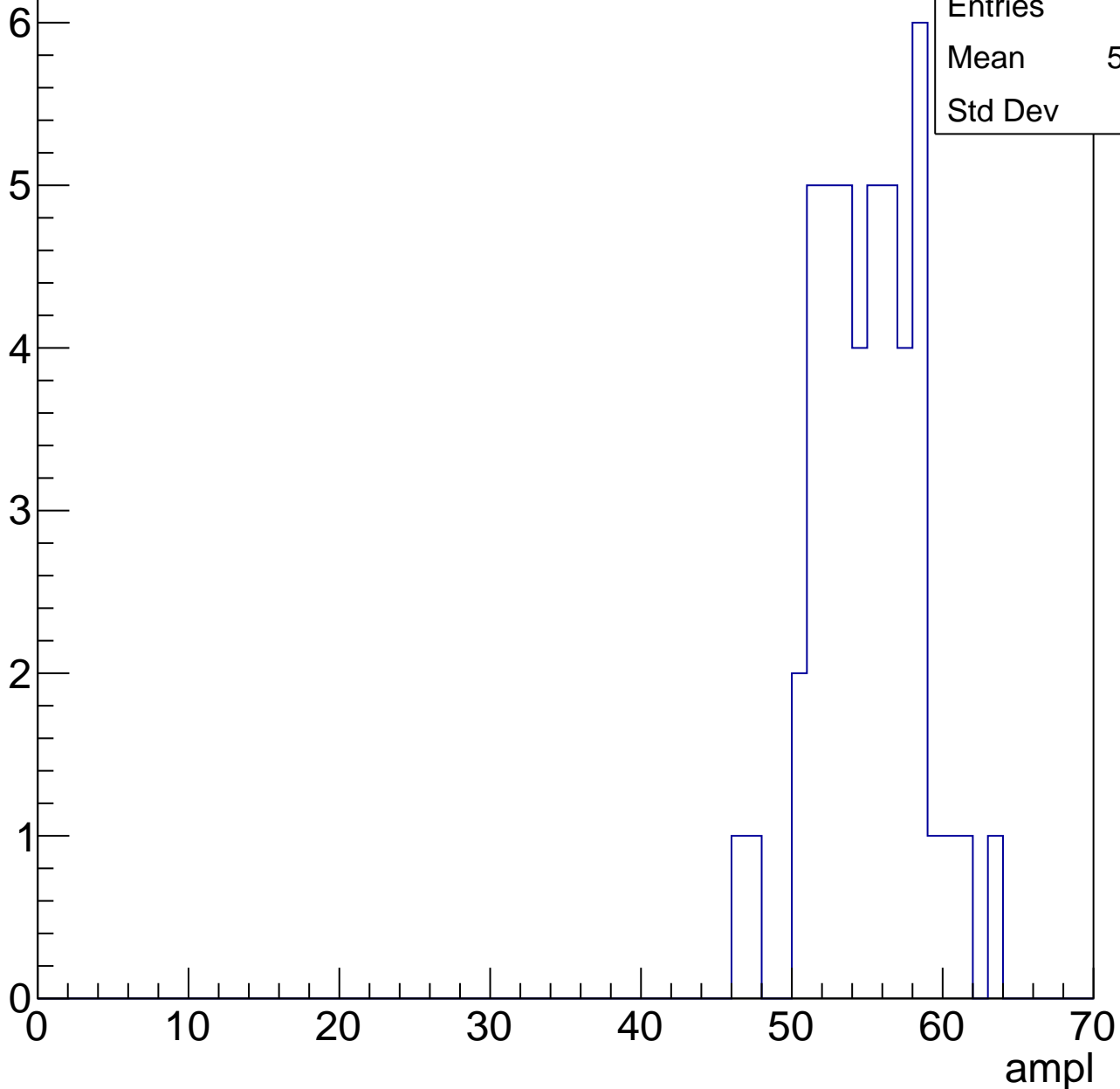


B1L101S, U17-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	54.53
Std Dev	3.42



B1L101S, U17-ch36, adc5

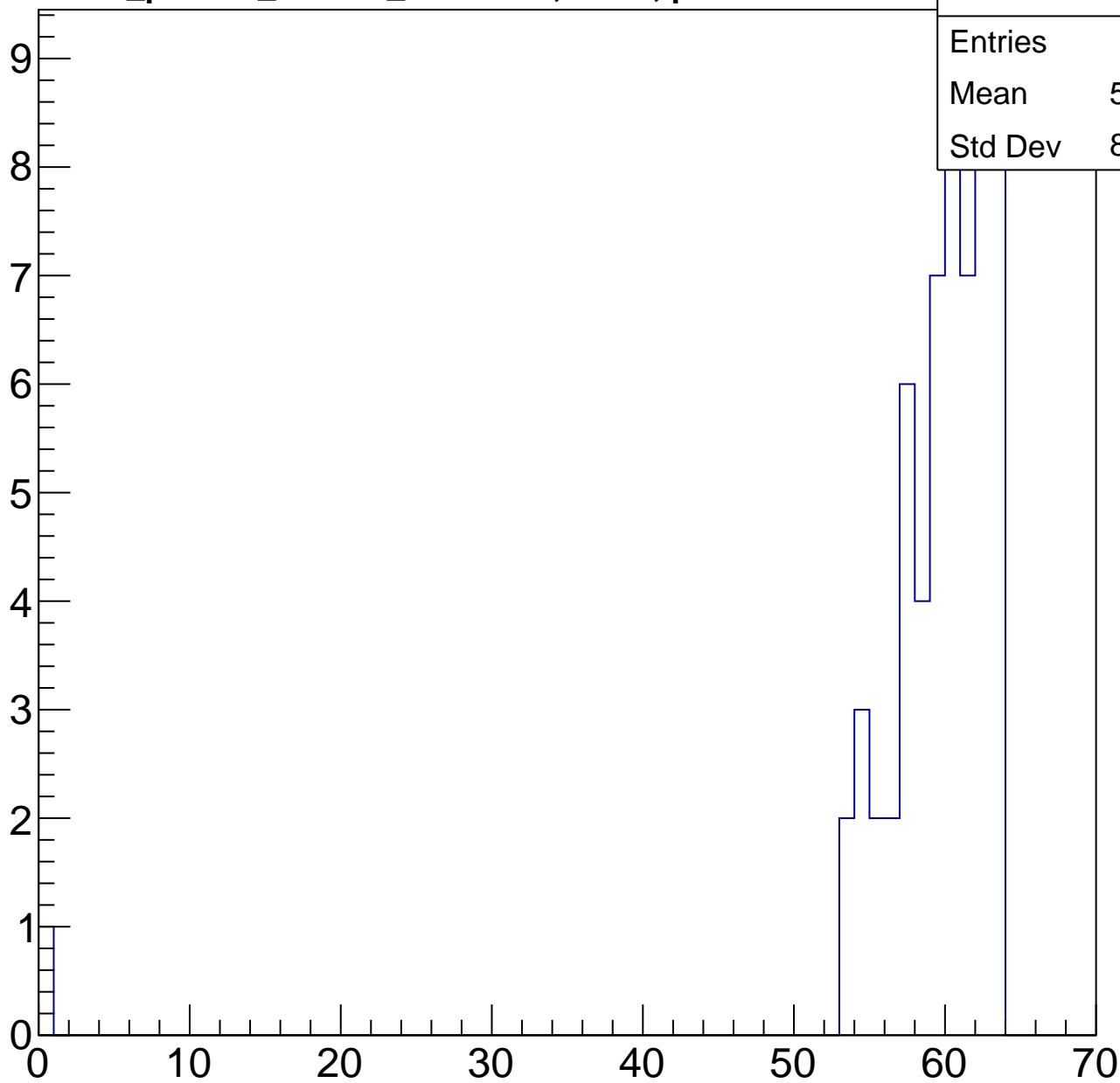
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.43
Std Dev	8.096

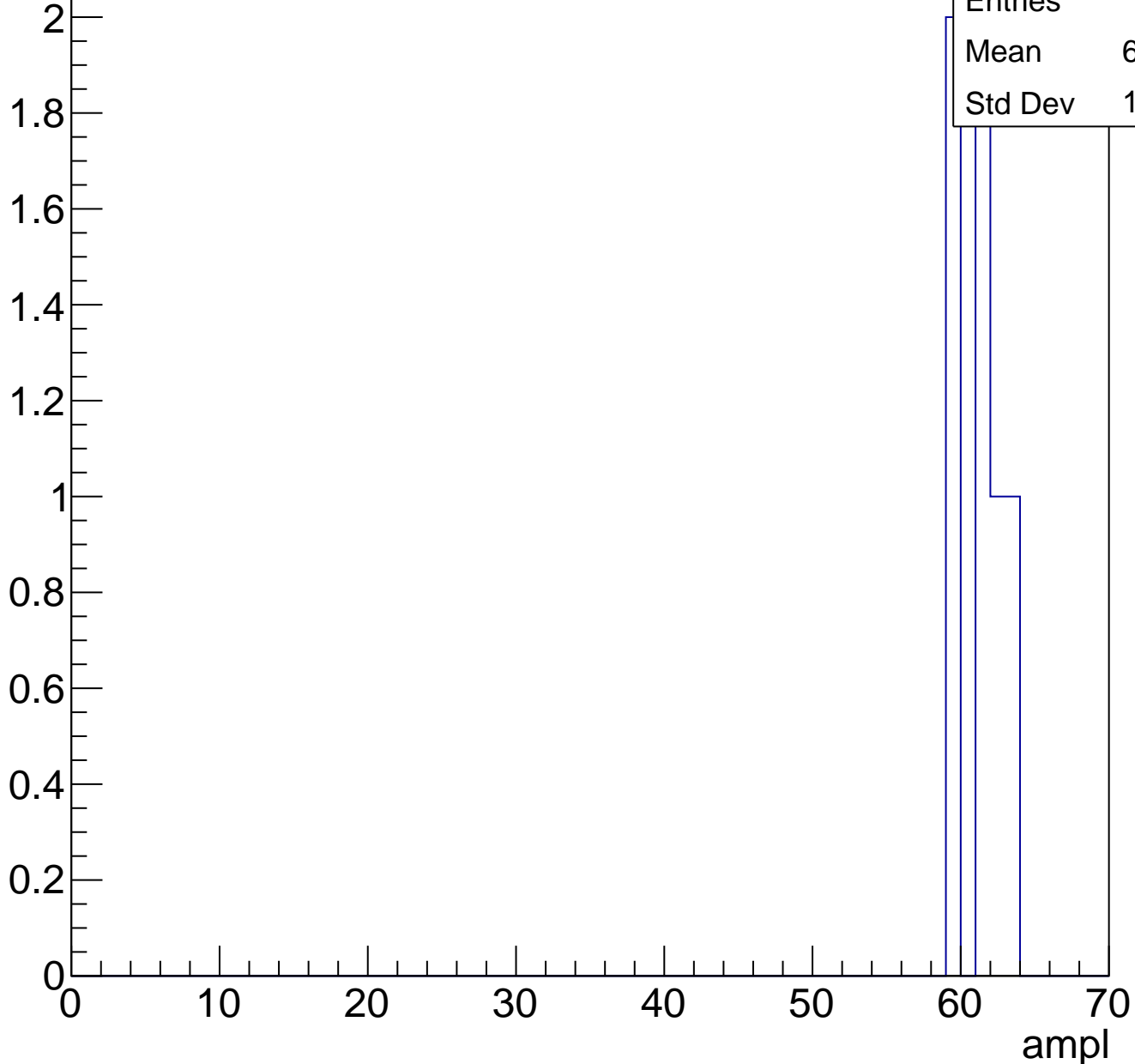
ampl



B1L101S, U17-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch37, adc0

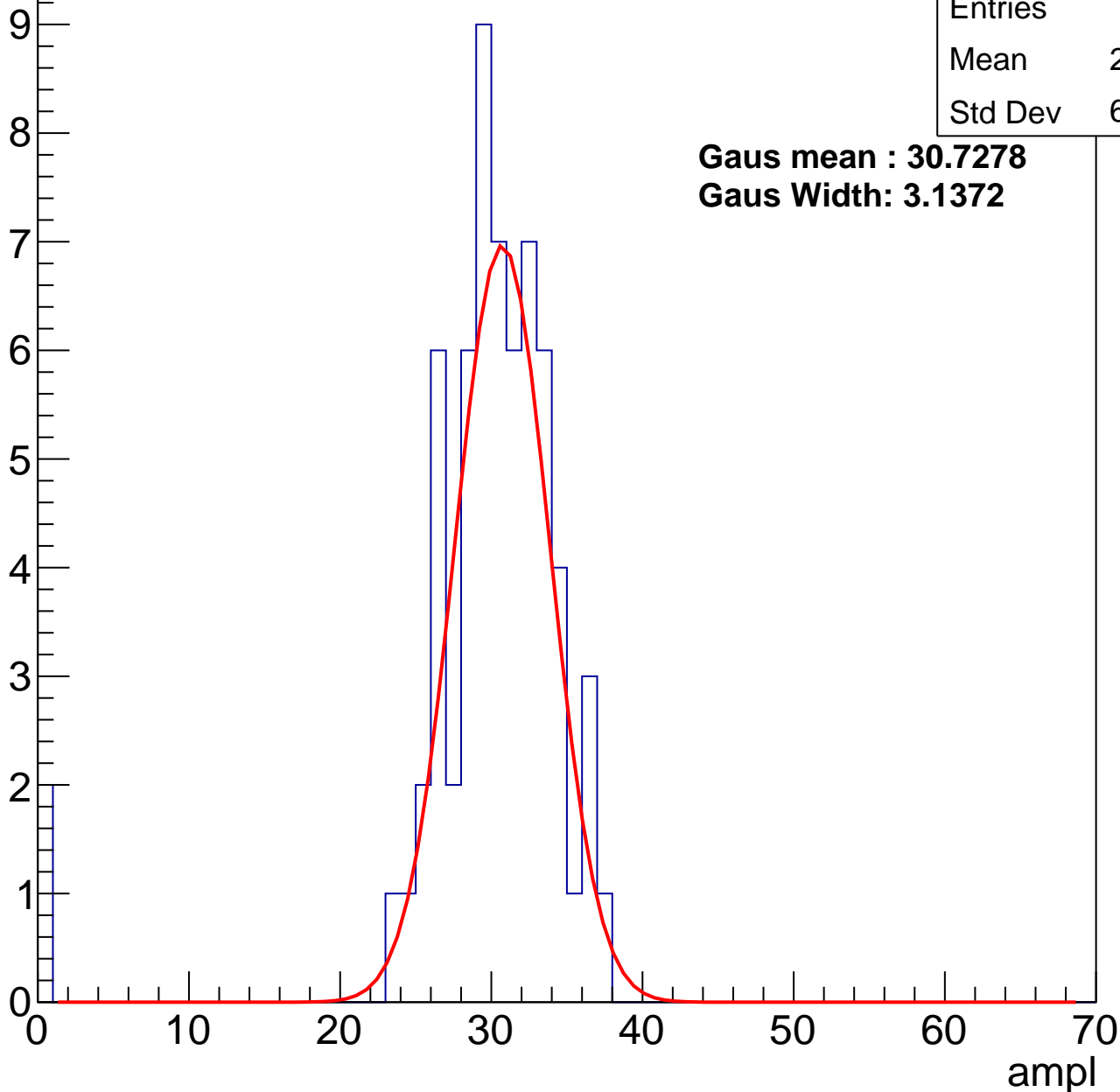
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.22
Std Dev	6.094

Gaus mean : 30.7278

Gaus Width: 3.1372



B1L101S, U17-ch37, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	37.15
Std Dev	5.549

Gaus mean : 37.5294

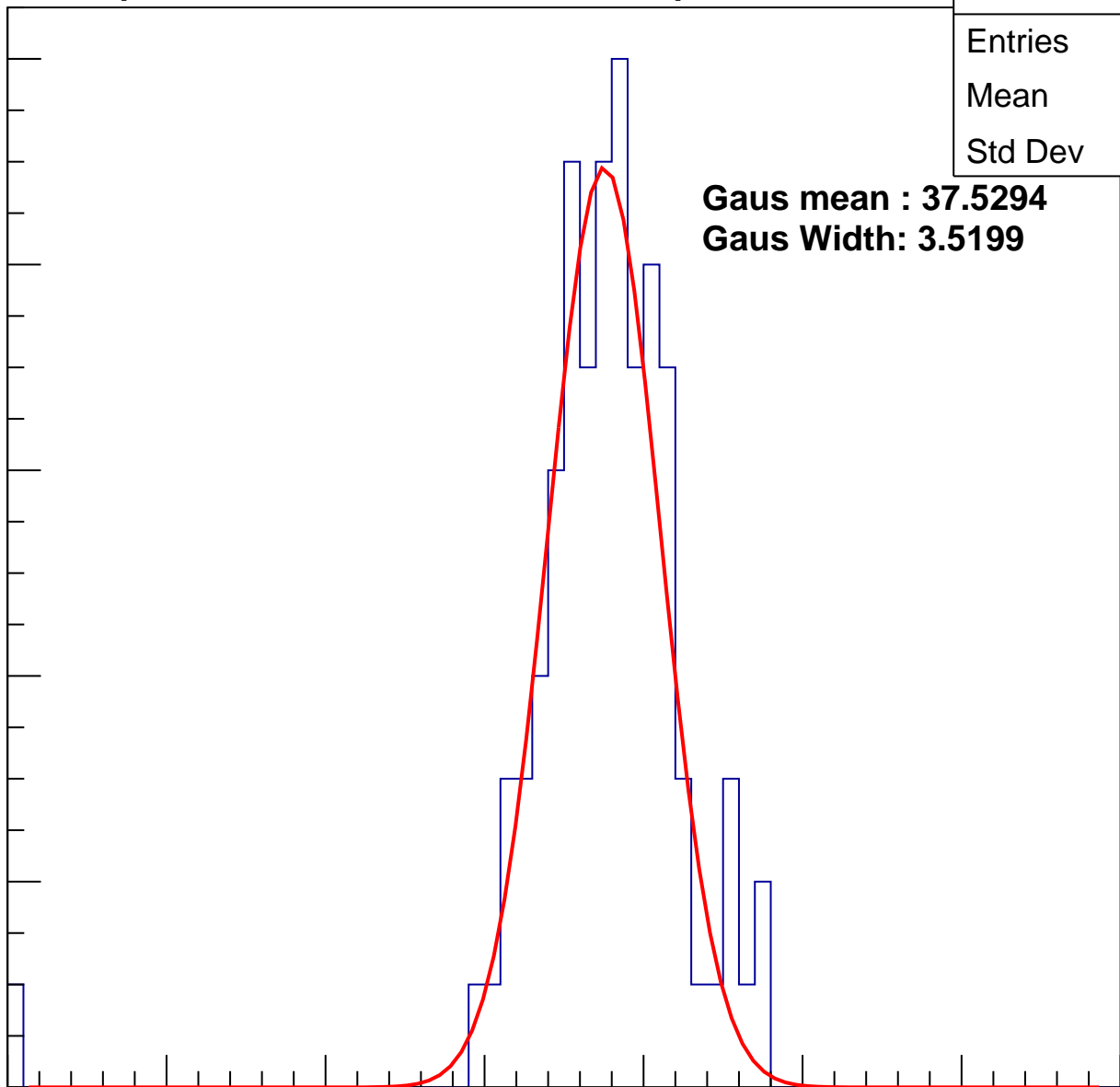
Gaus Width: 3.5199

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch37, adc2

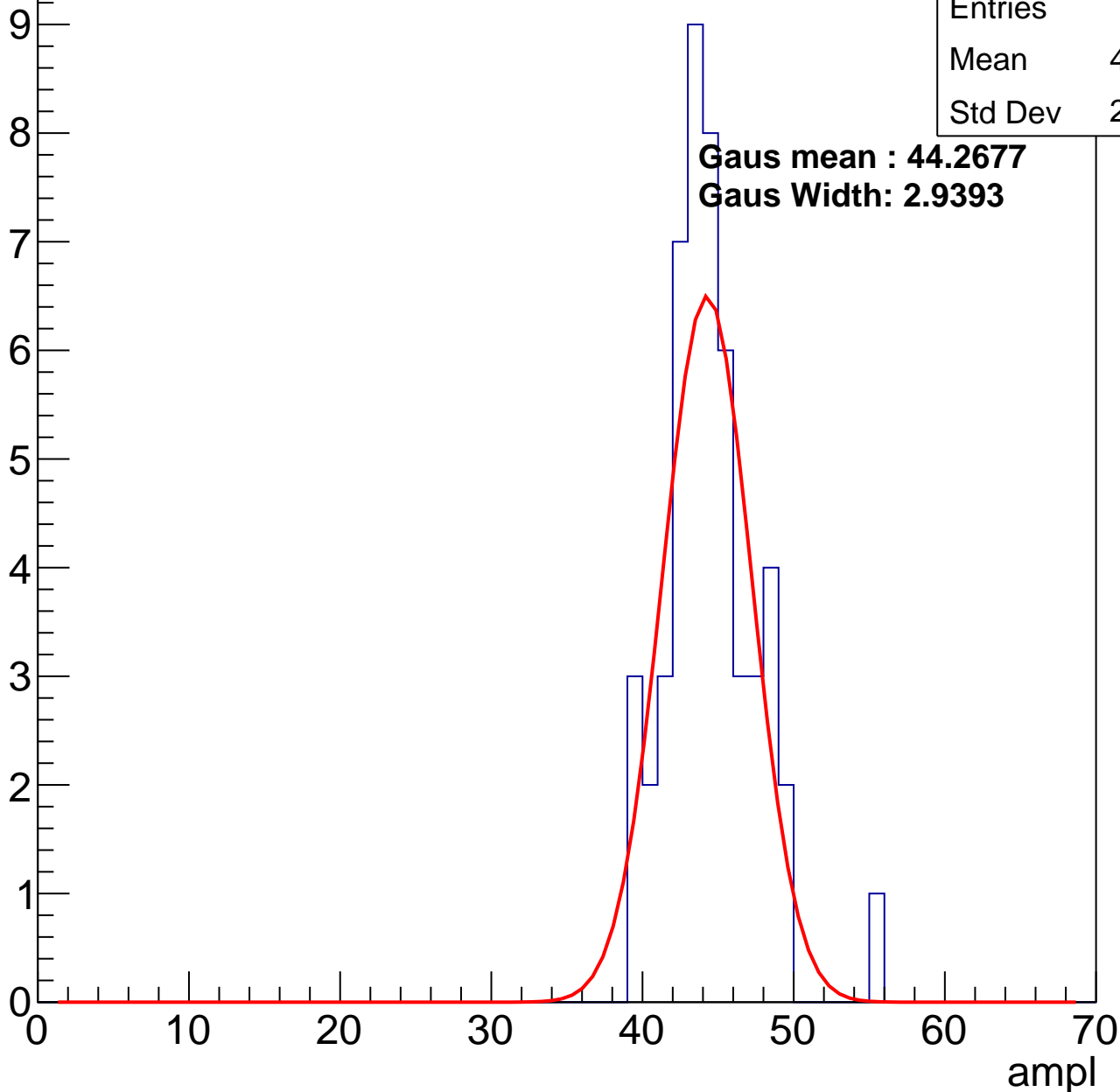
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	44.06
Std Dev	2.973

Gaus mean : 44.2677

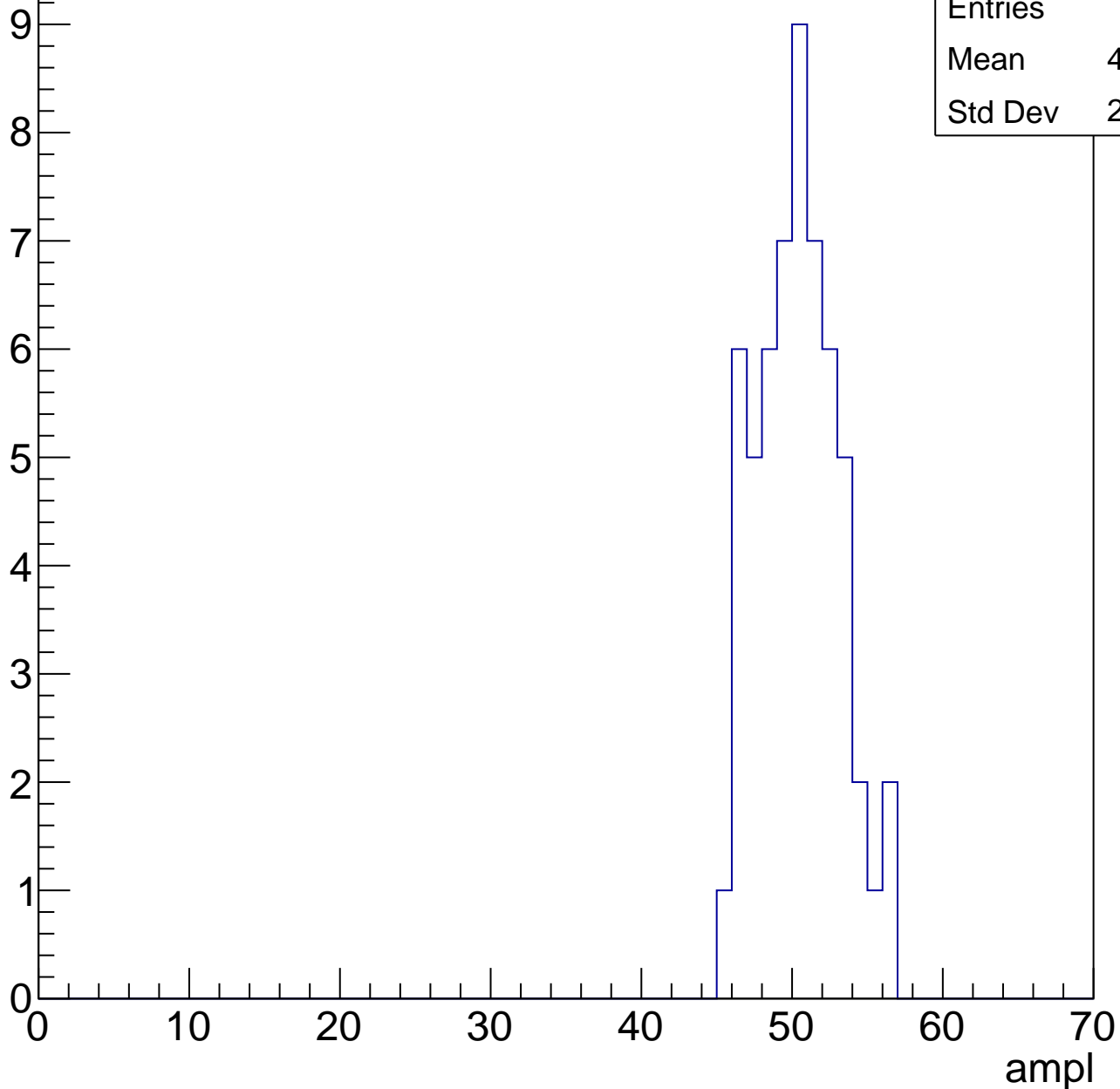
Gaus Width: 2.9393



B1L101S, U17-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	55.57
Std Dev	7.234

Entry

10

8

6

4

2

0

ampl

0

10

20

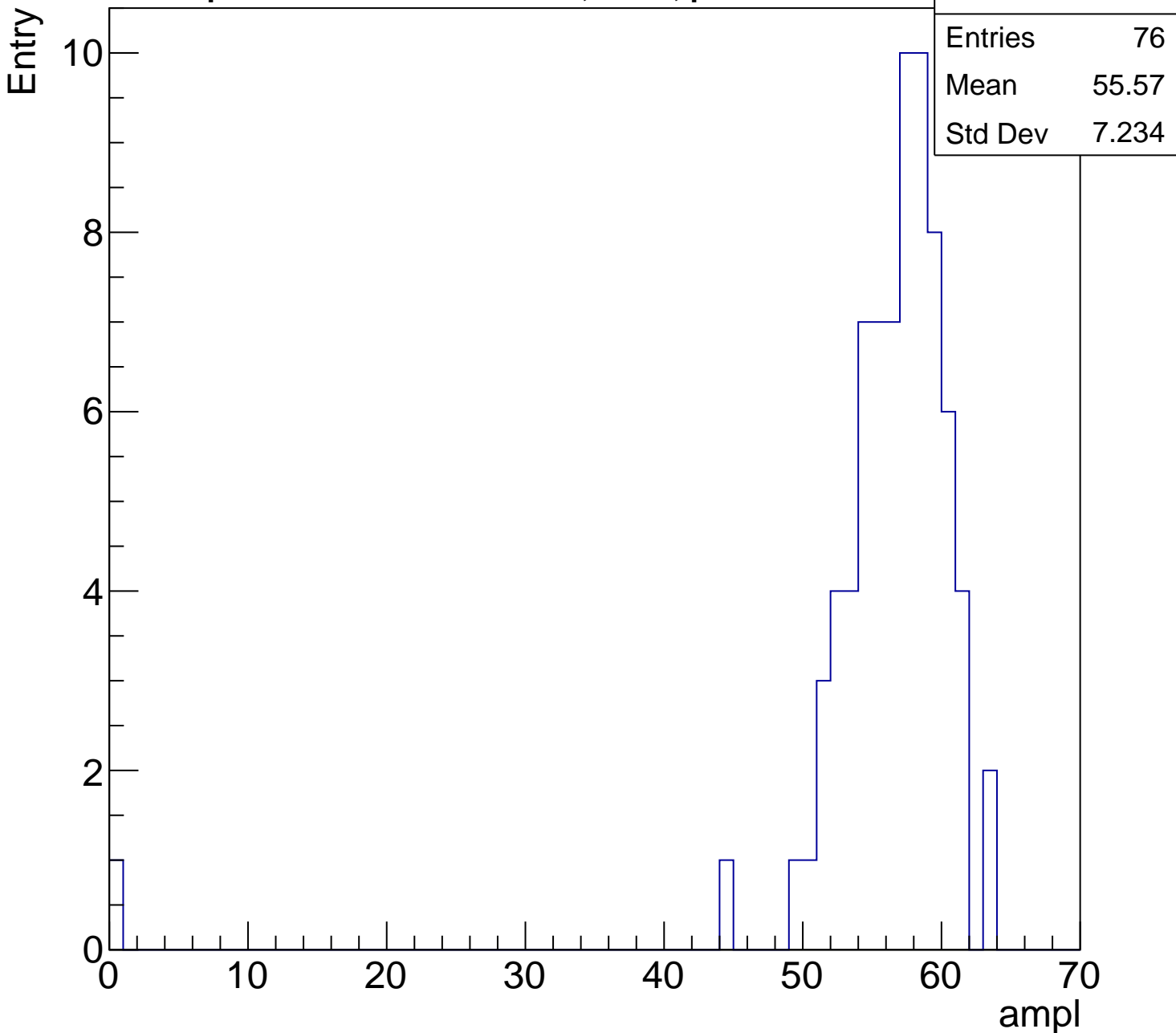
30

40

50

60

70

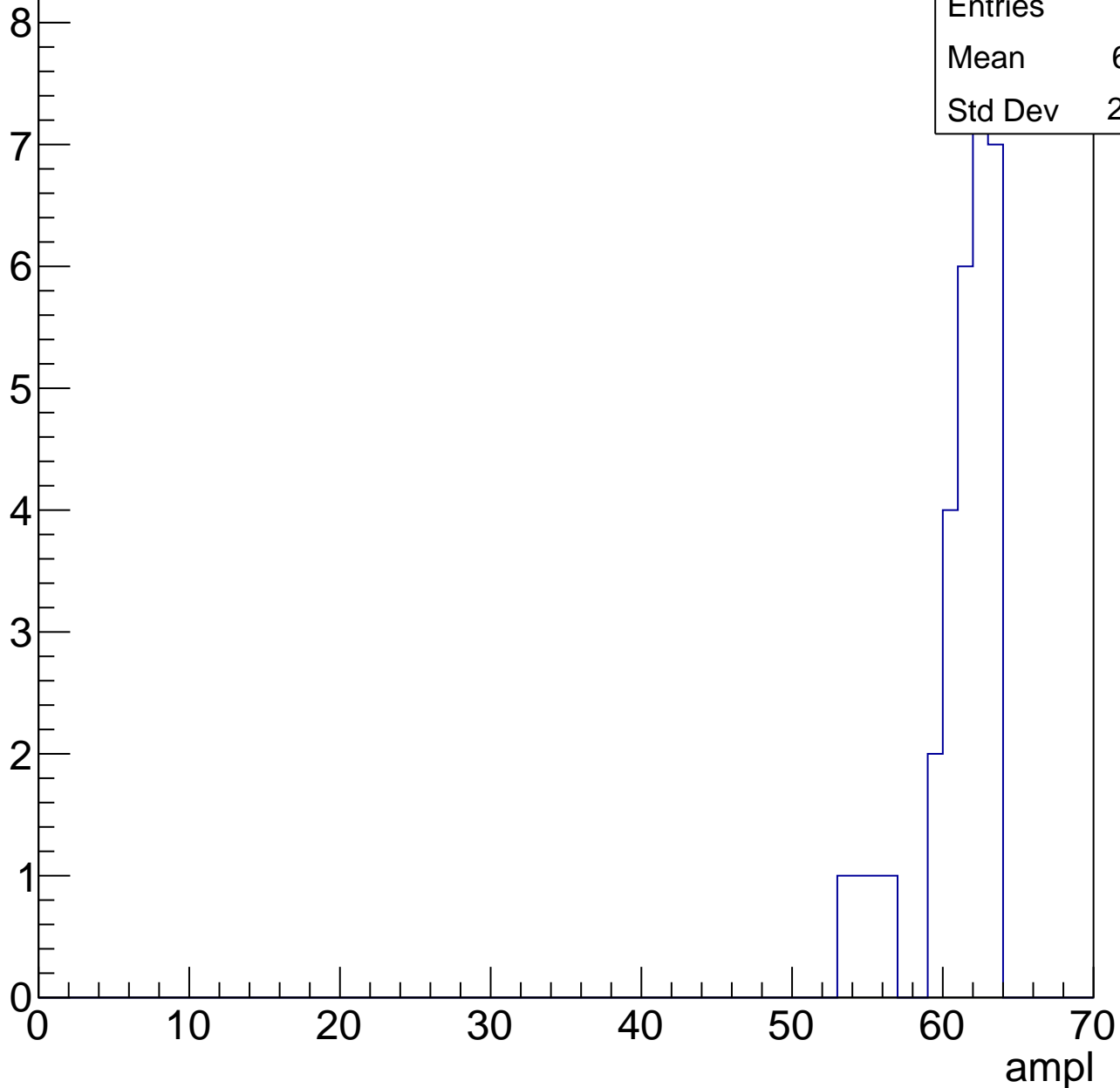


B1L101S, U17-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.61
Std Dev	2.648



B1L101S, U17-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch38, adc0

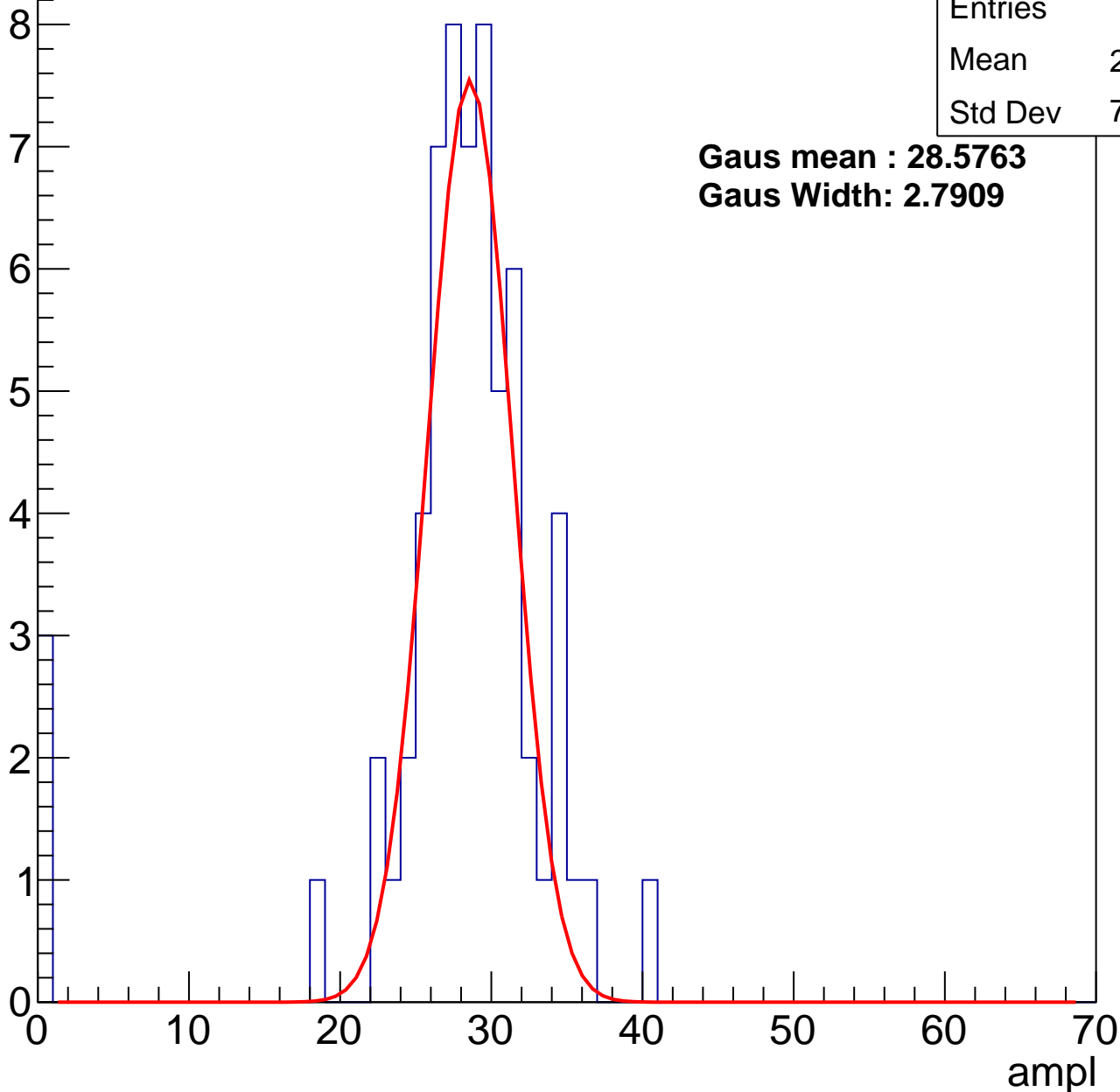
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.17
Std Dev	7.015

Gaus mean : 28.5763

Gaus Width: 2.7909



B1L101S, U17-ch38, adc1

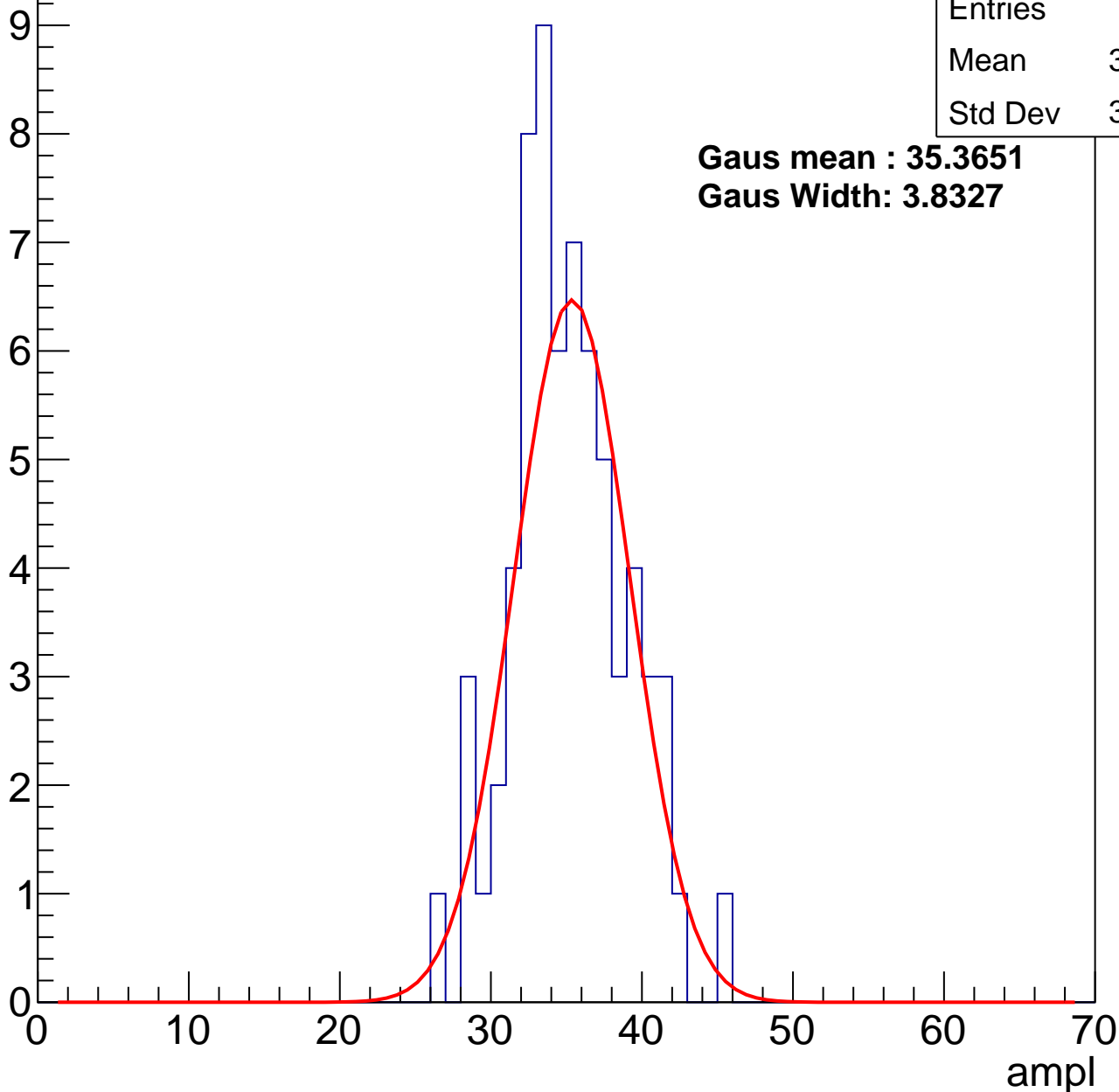
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.72
Std Dev	3.737

Gaus mean : 35.3651

Gaus Width: 3.8327



B1L101S, U17-ch38, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	42.13
Std Dev	3.556

Gaus mean : 42.2999

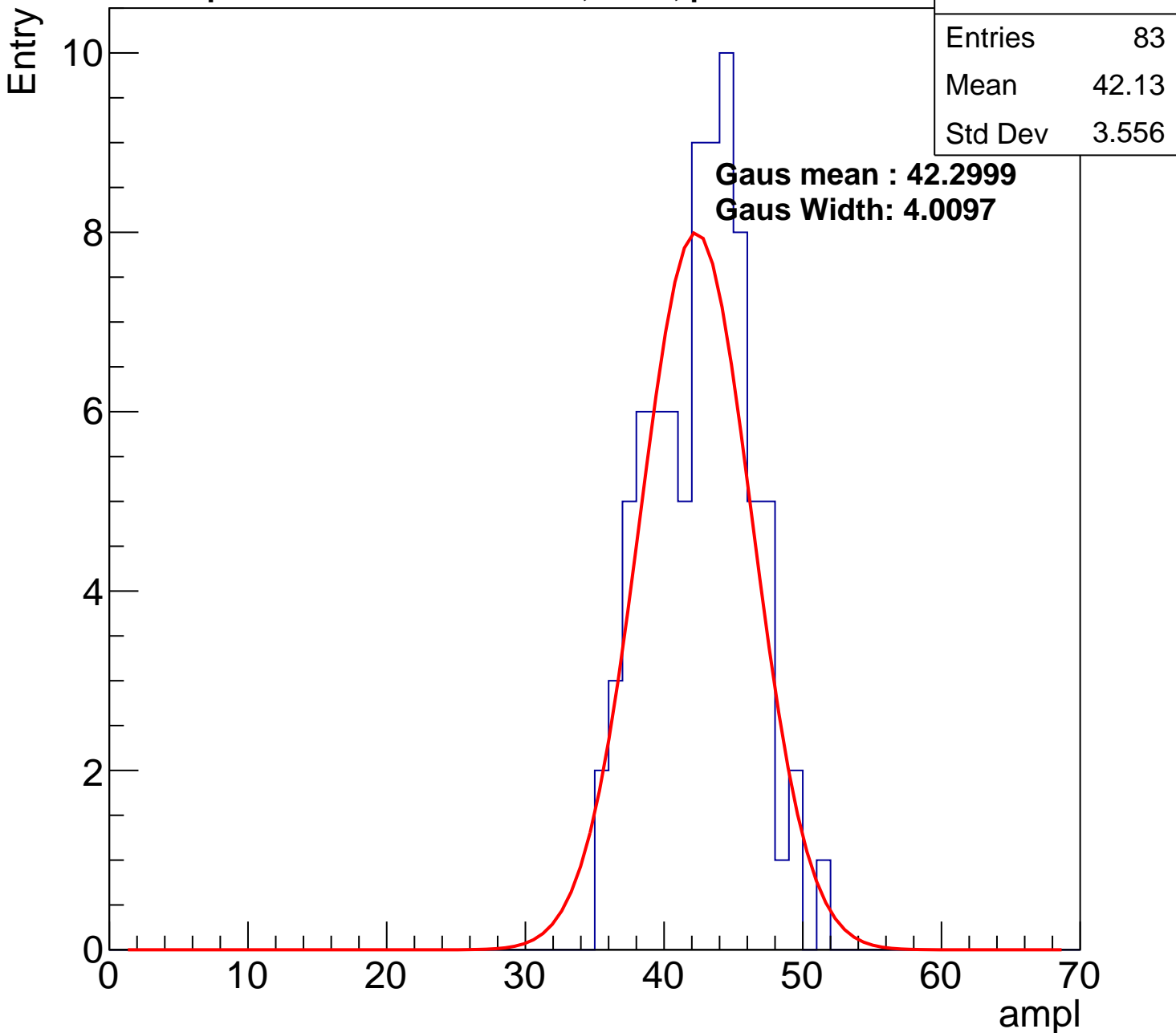
Gaus Width: 4.0097

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

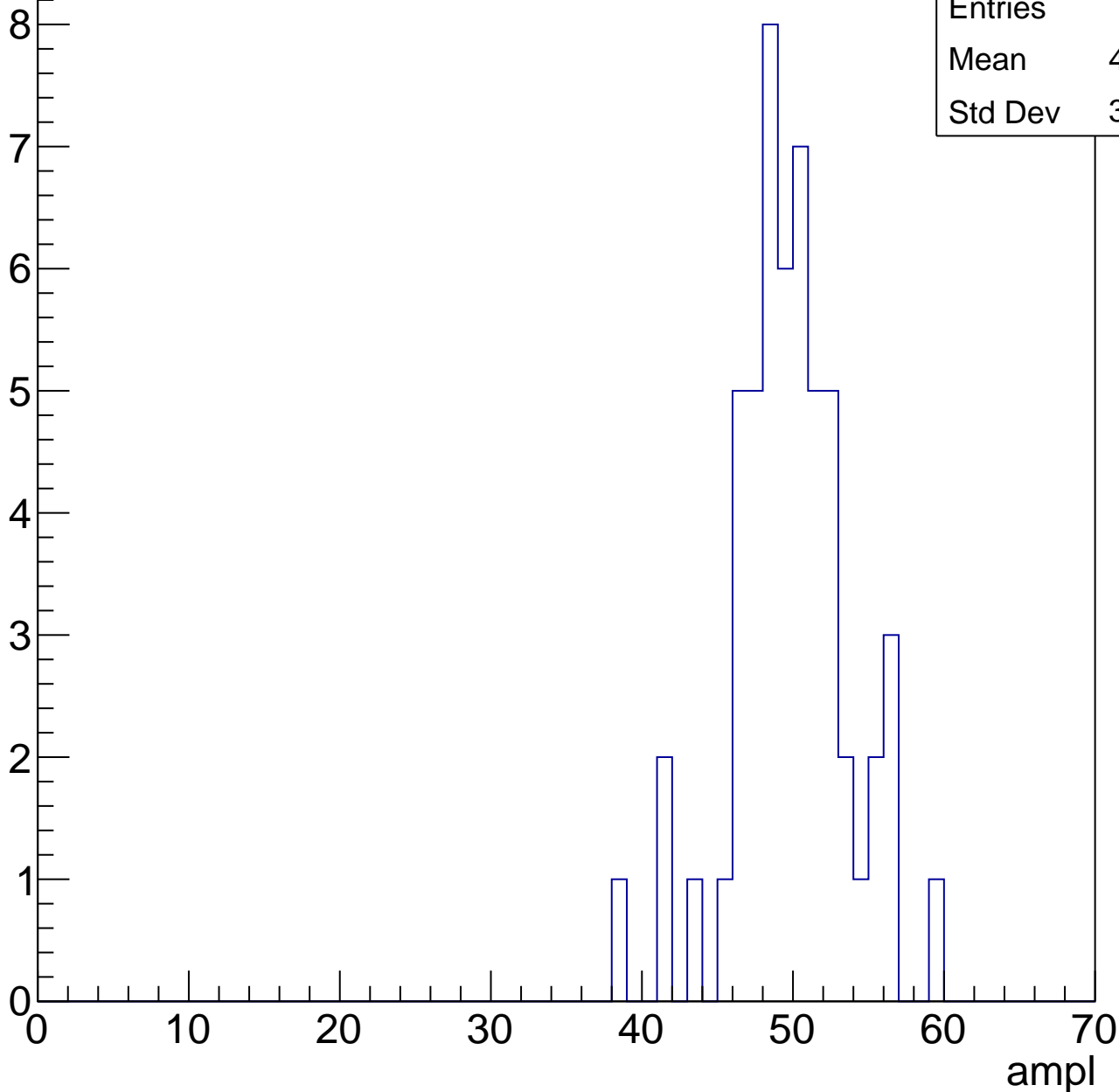


B1L101S, U17-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	49.33
Std Dev	3.852

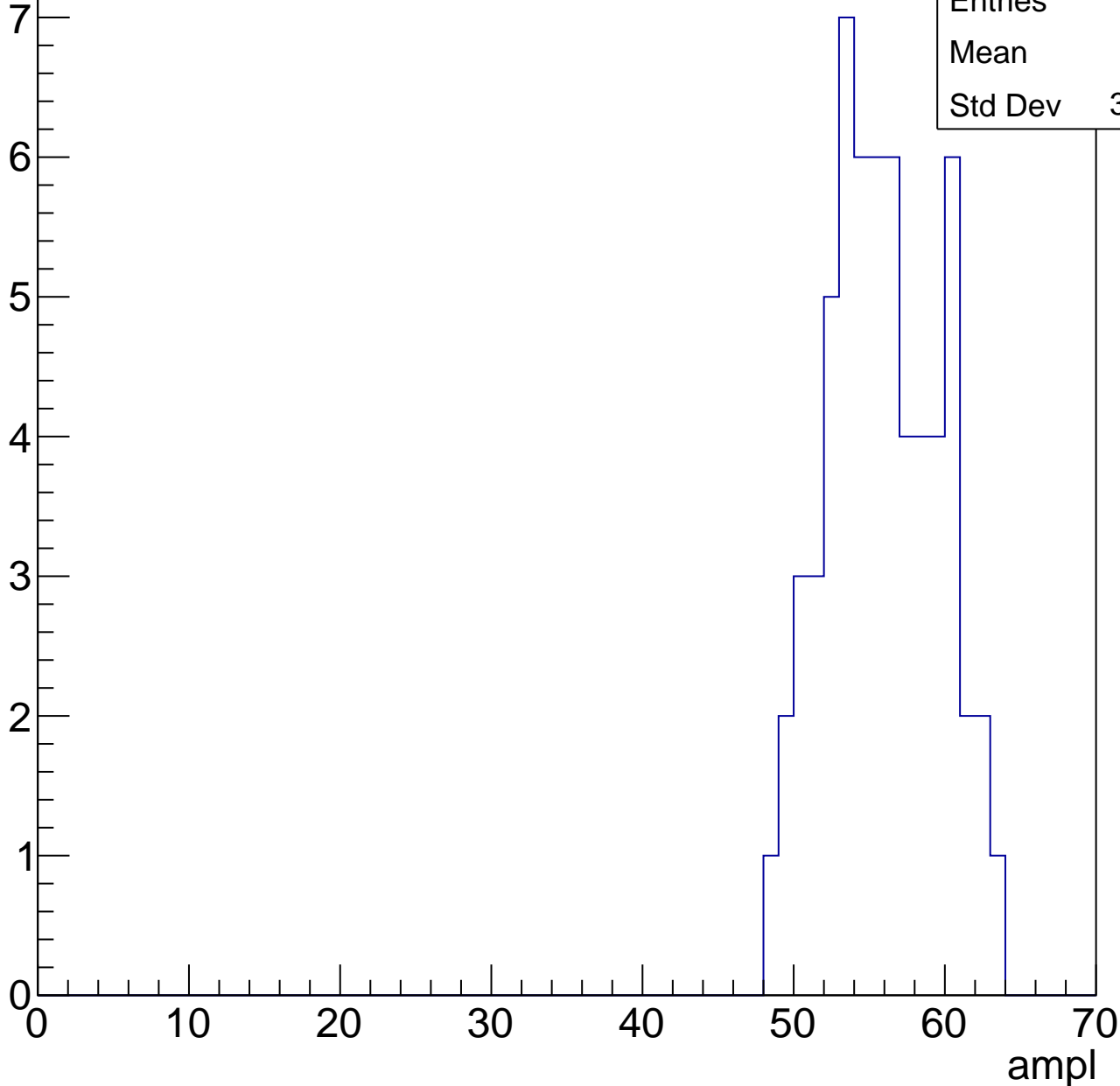


B1L101S, U17-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.4
Std Dev	3.634

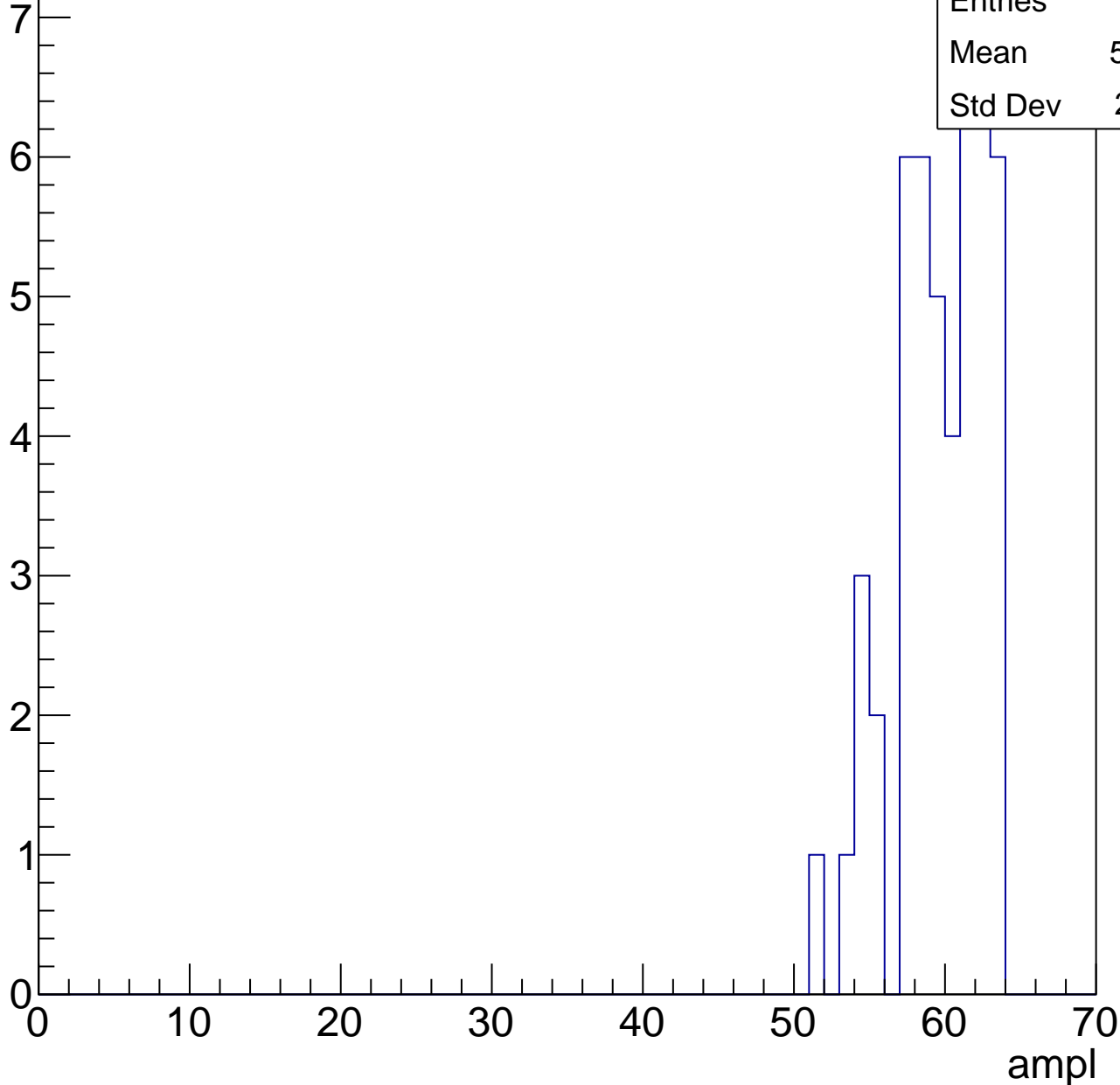


B1L101S, U17-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

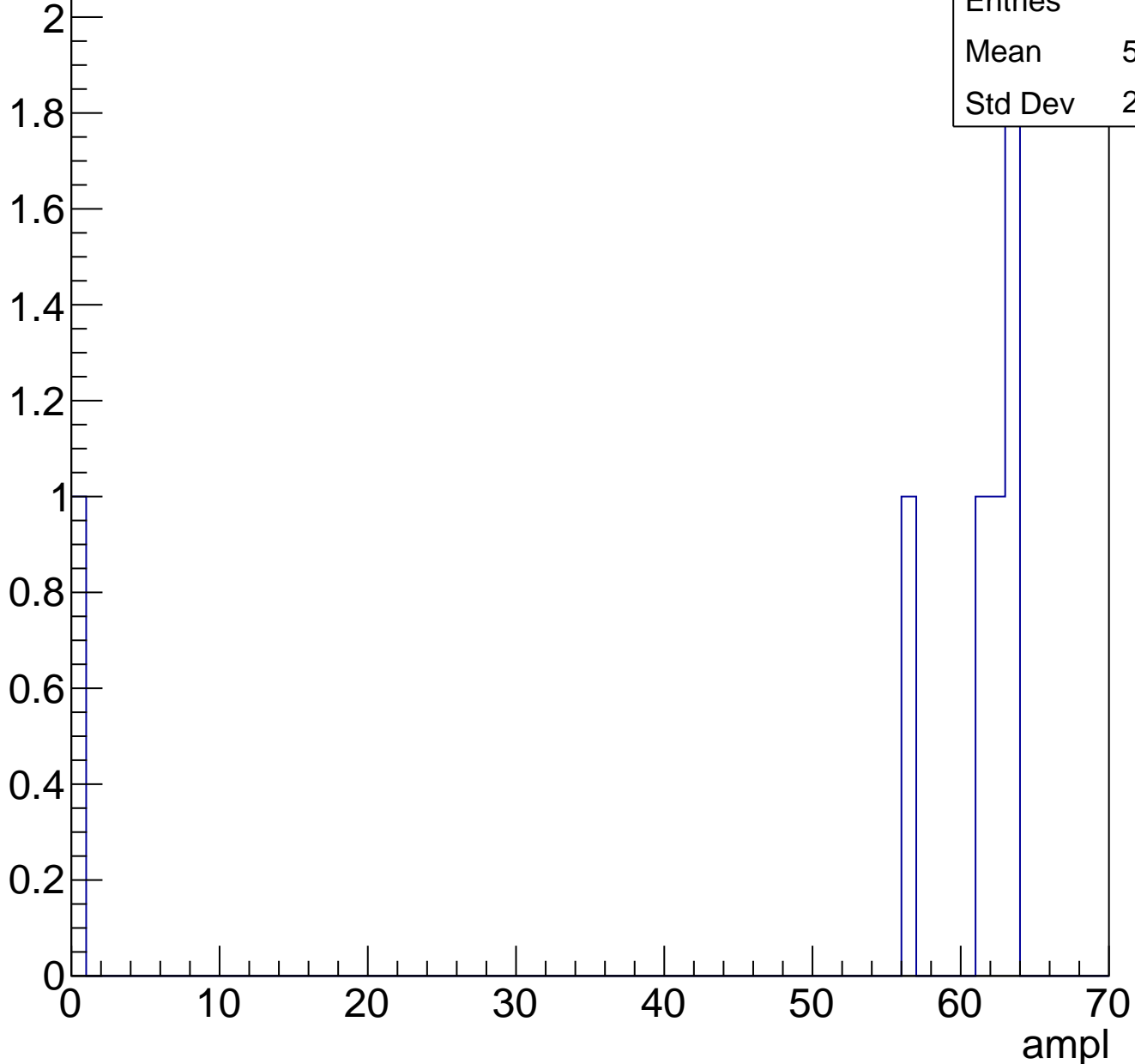
Entries	48
Mean	59.17
Std Dev	2.981



B1L101S, U17-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch39, adc0

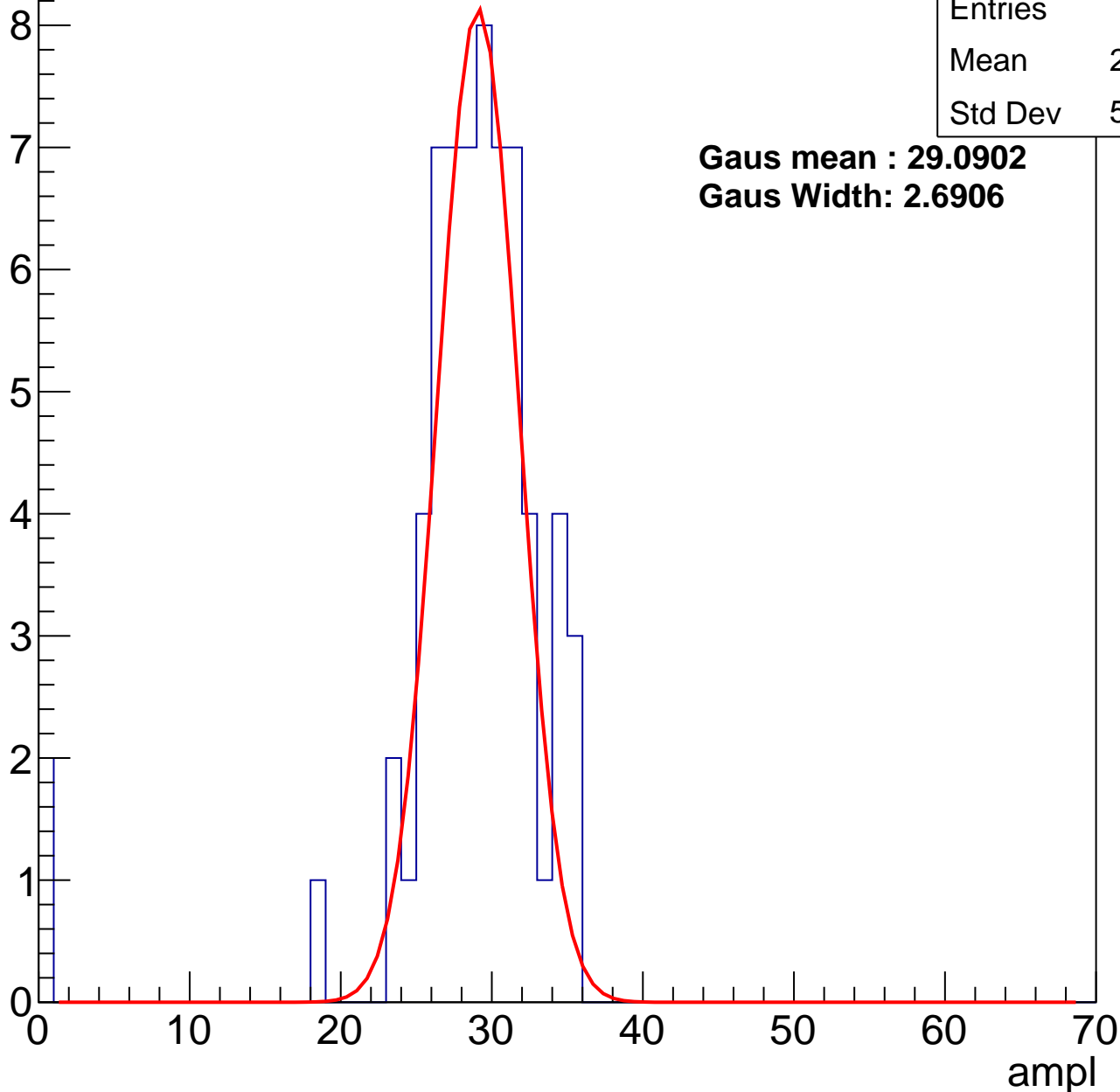
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.94
Std Dev	5.927

Gaus mean : 29.0902

Gaus Width: 2.6906



B1L101S, U17-ch39, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	98
Mean	36.51
Std Dev	4.173

Gaus mean : 36.9693

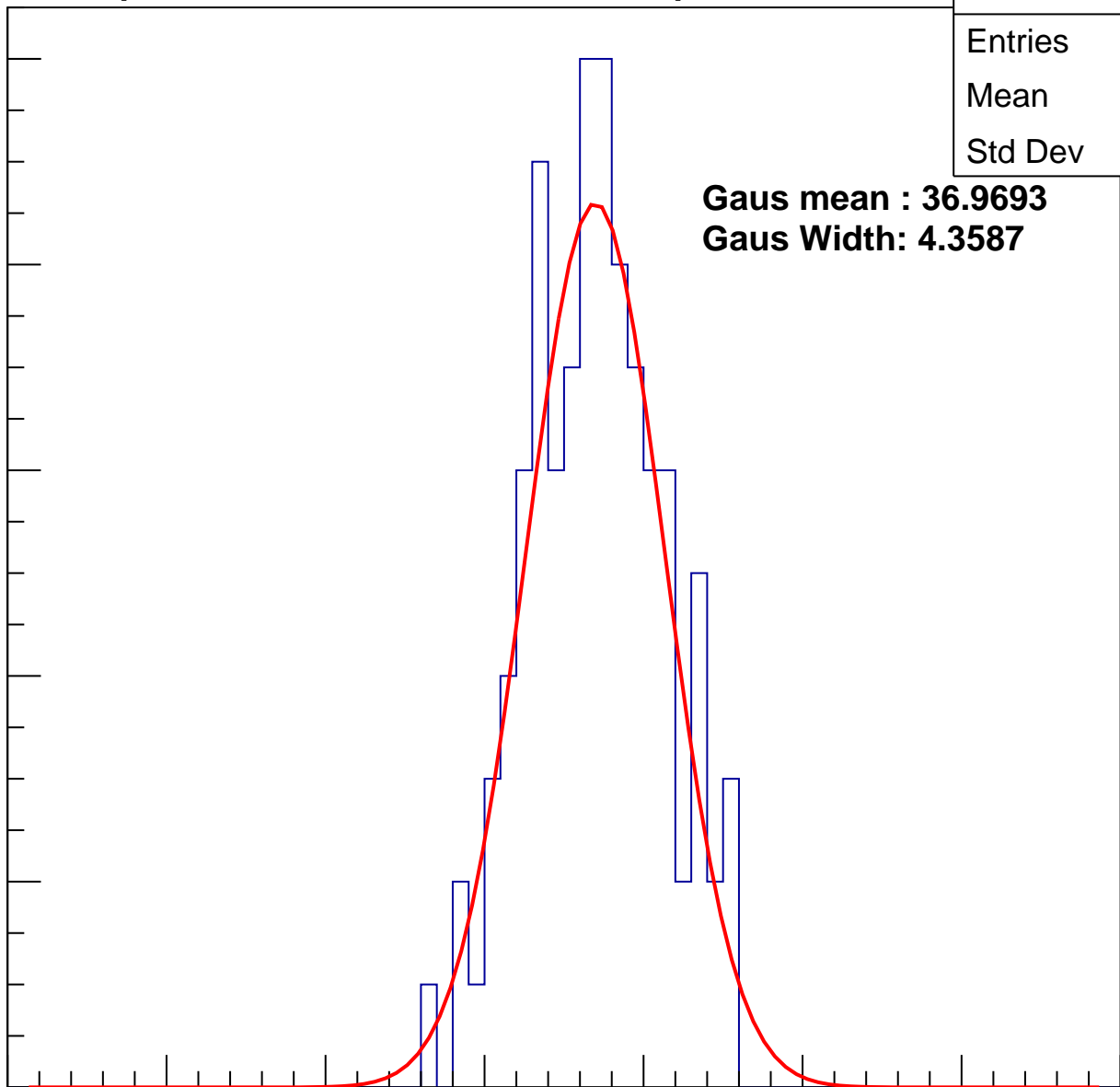
Gaus Width: 4.3587

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch39, adc2

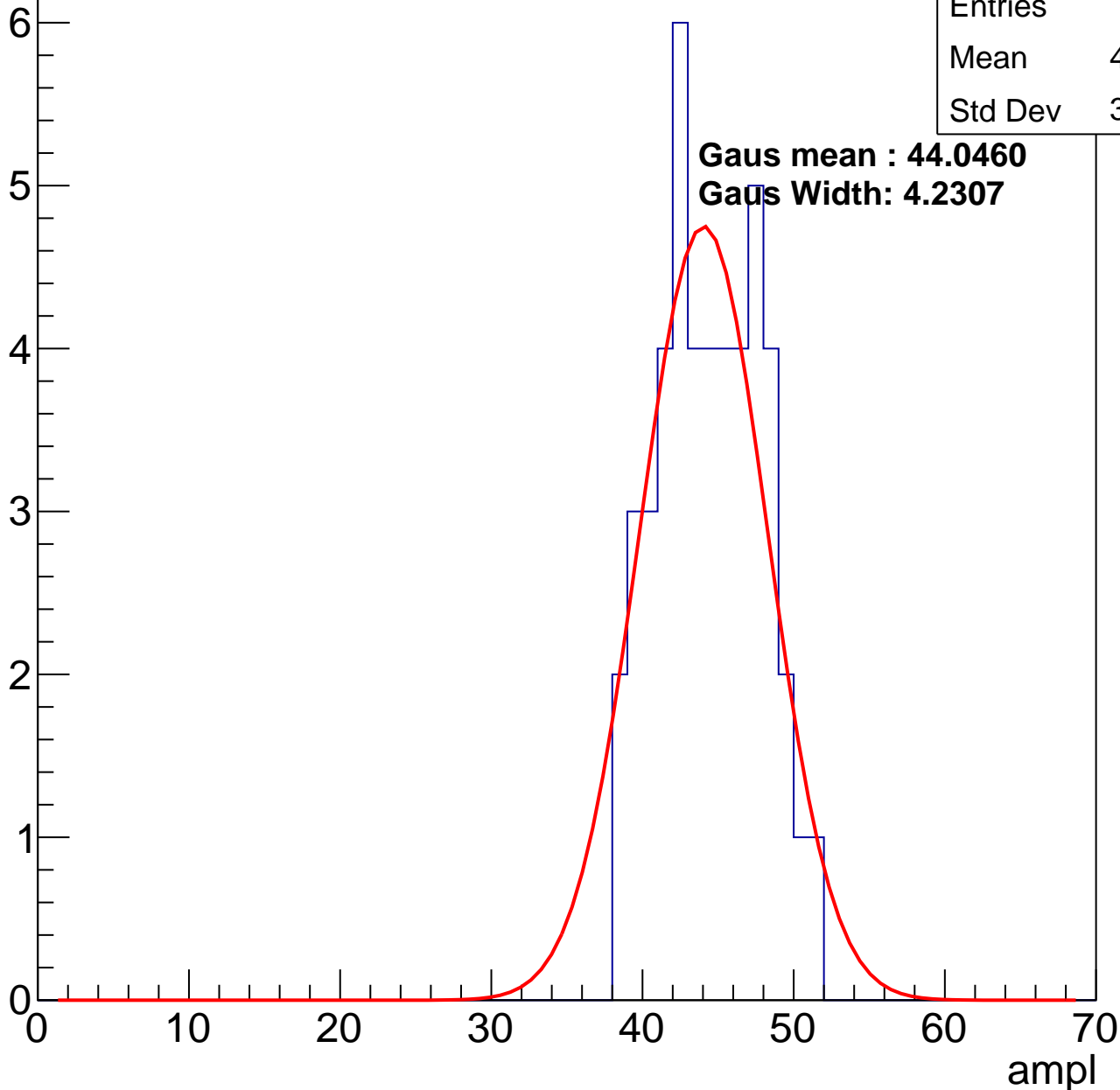
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	43.98
Std Dev	3.336

Gaus mean : 44.0460

Gaus Width: 4.2307

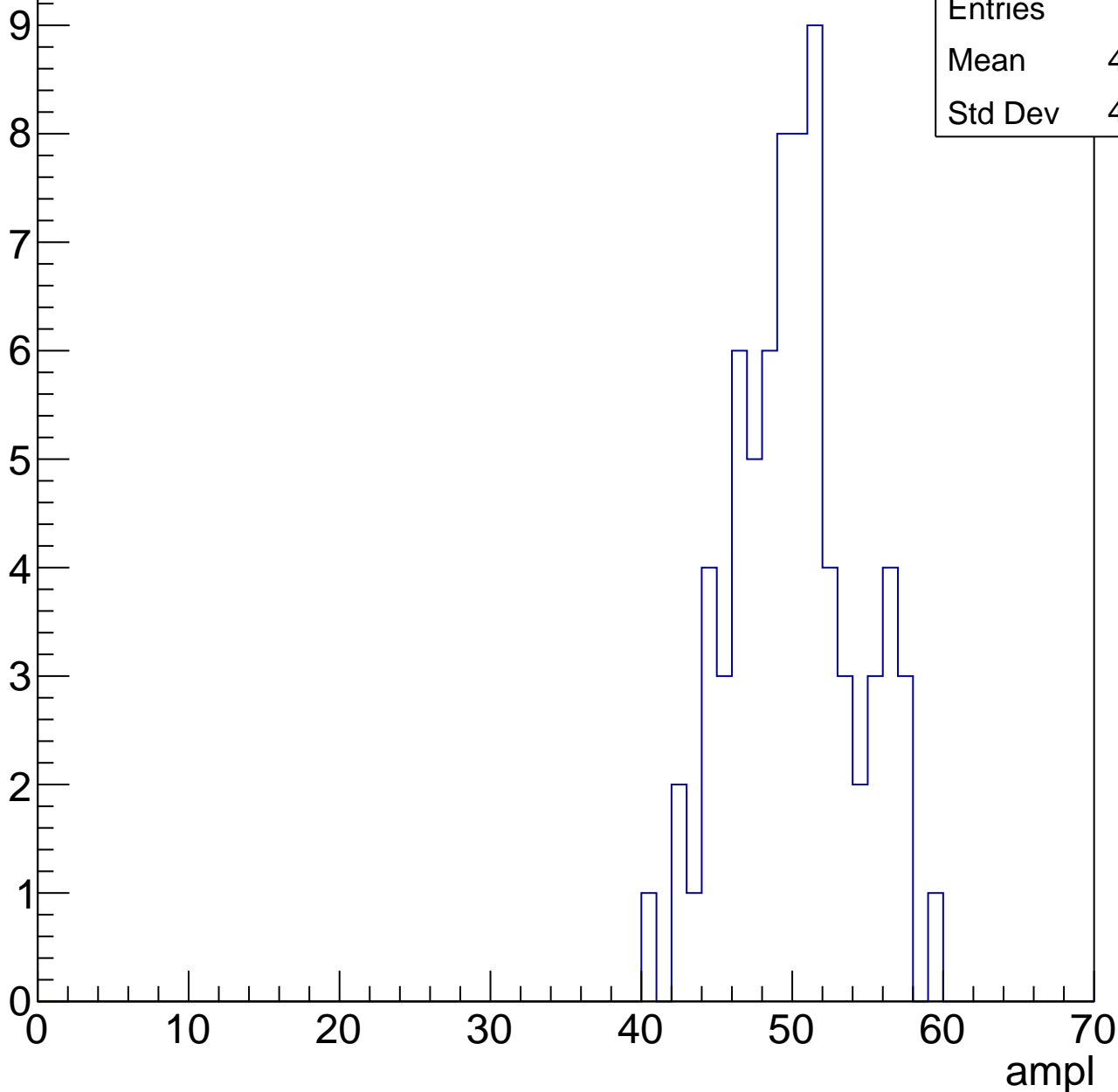


B1L101S, U17-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	49.62
Std Dev	4.036

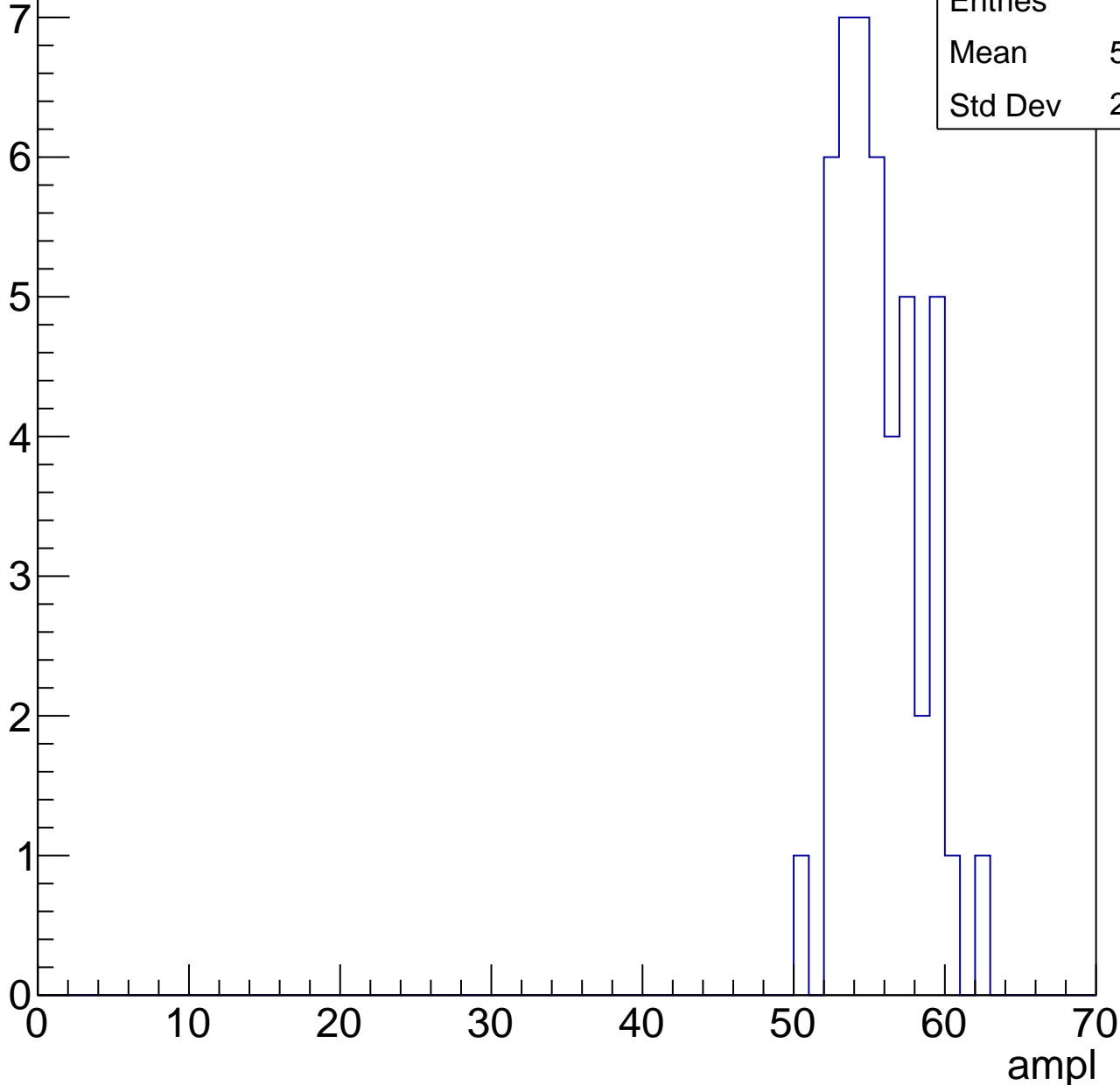


B1L101S, U17-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.18
Std Dev	2.619

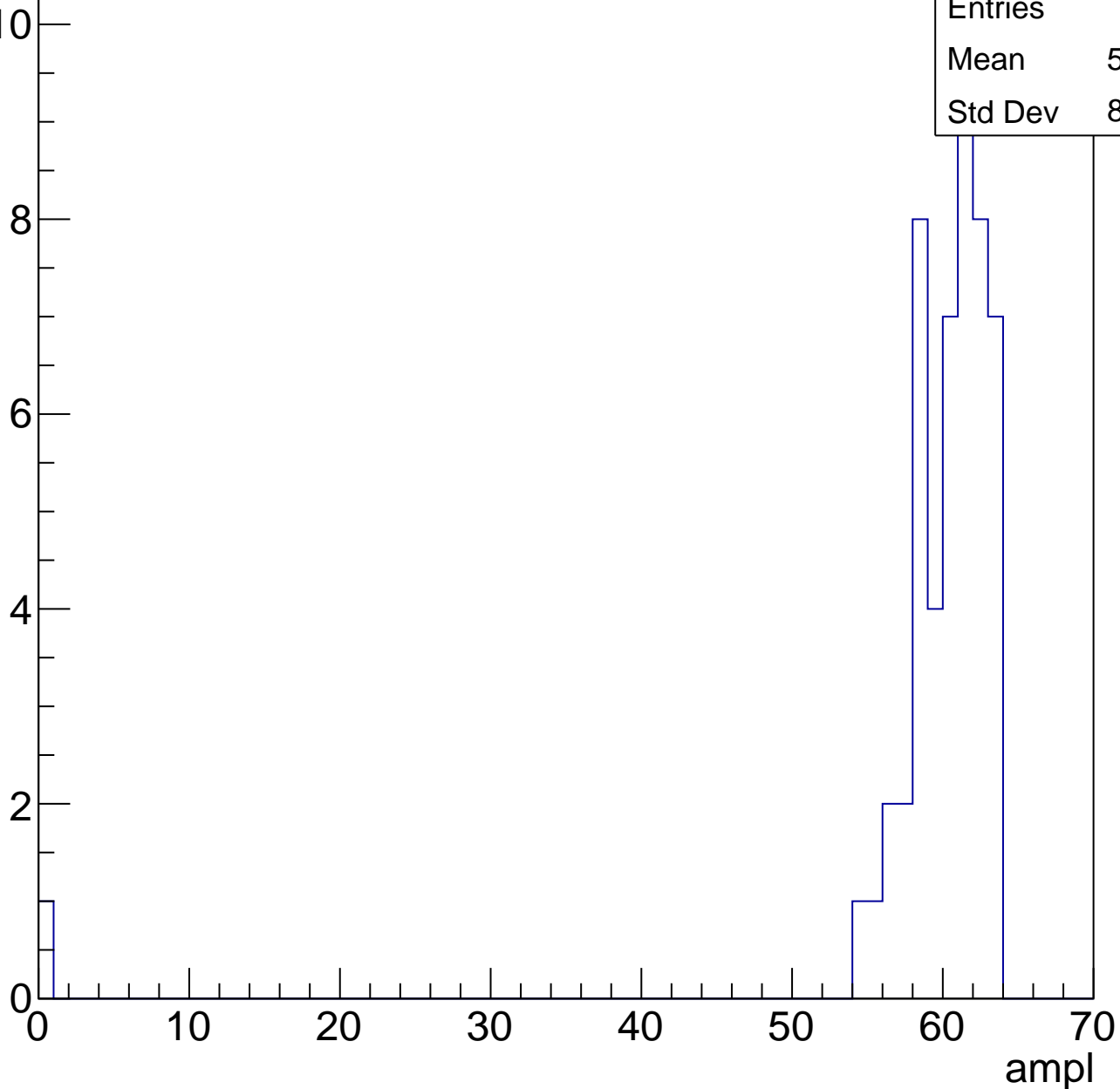


B1L101S, U17-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

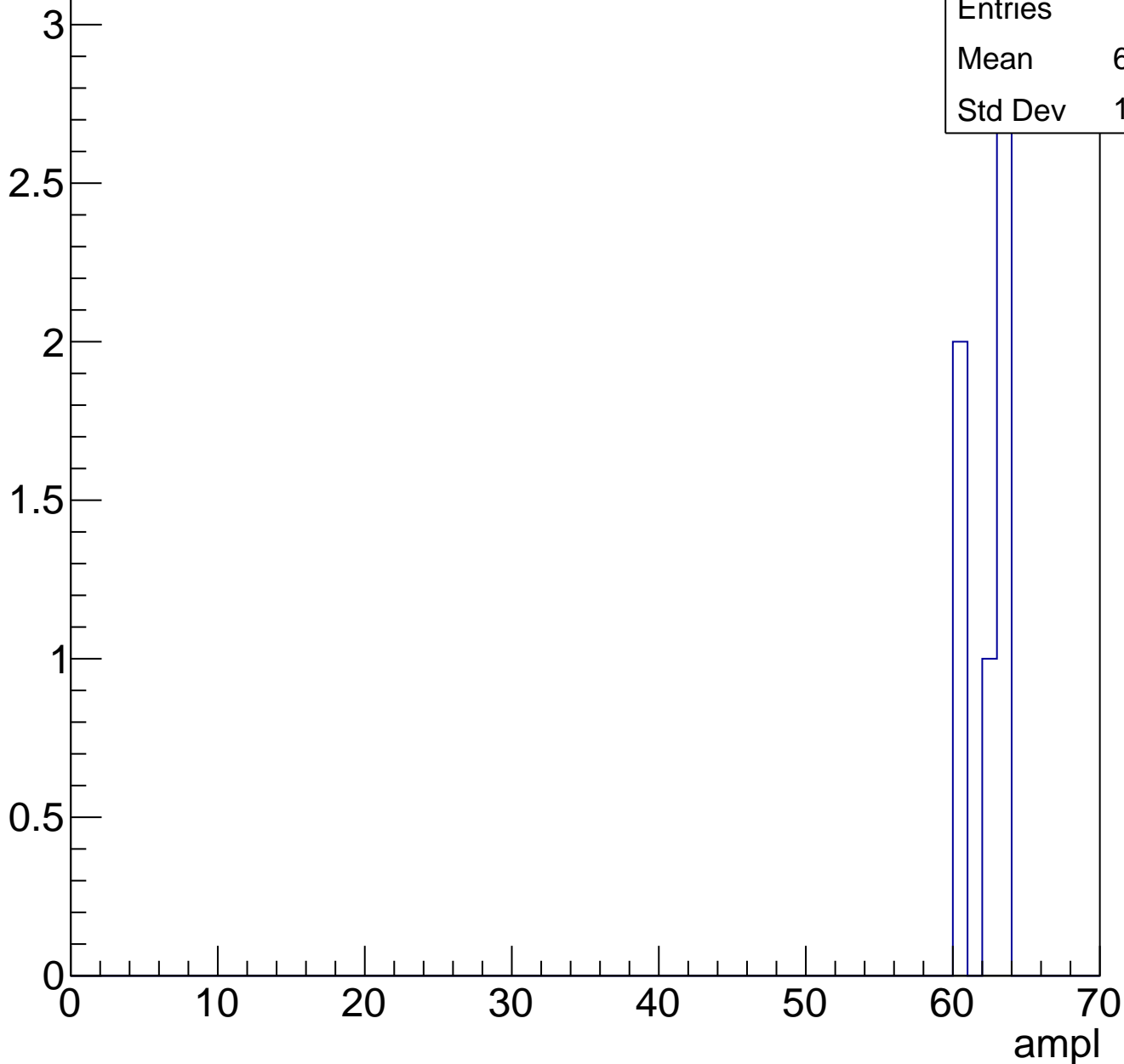
Entries	51
Mean	58.86
Std Dev	8.616



B1L101S, U17-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch40, adc0

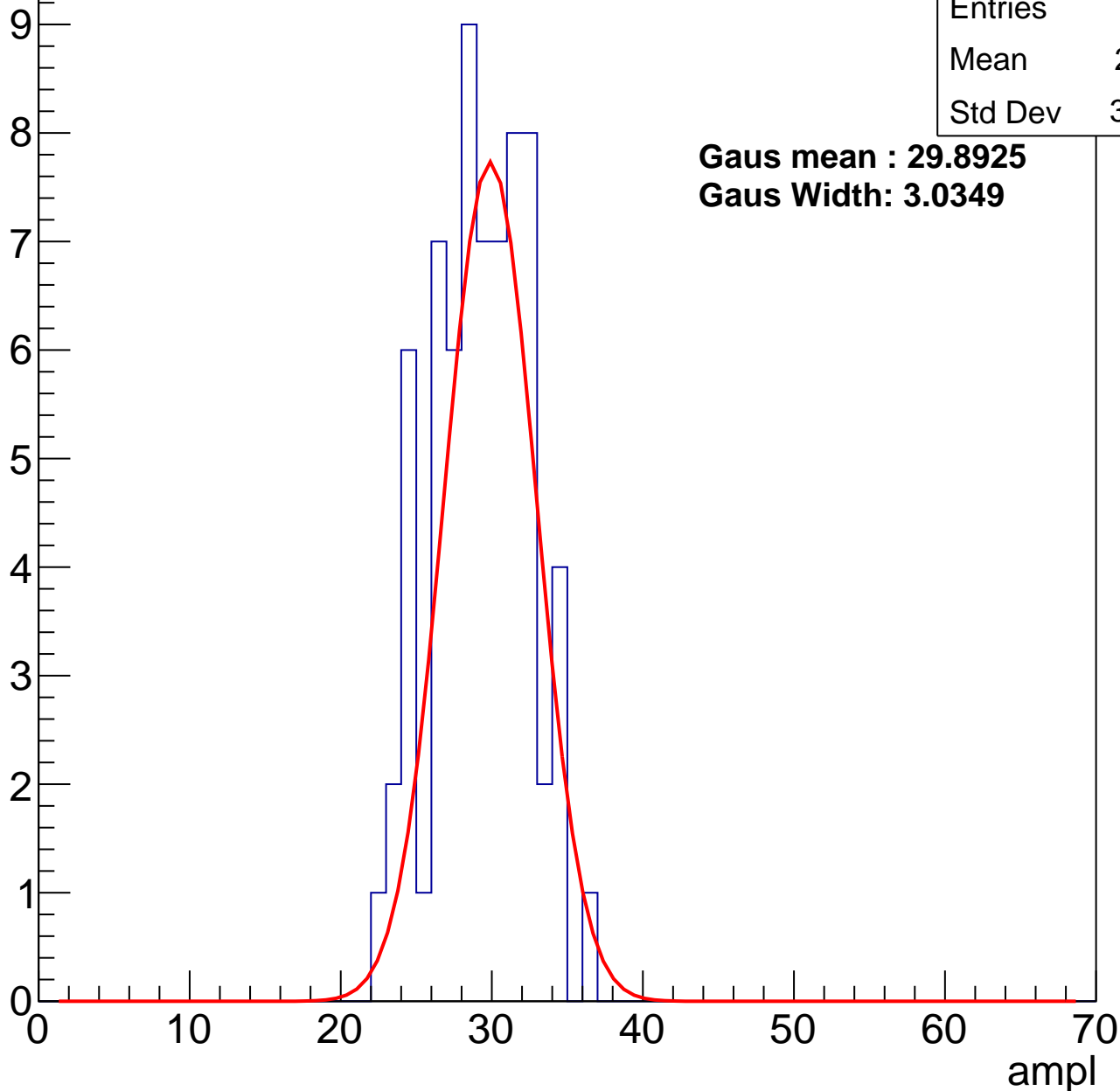
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.81
Std Dev	3.122

Gaus mean : 29.8925

Gaus Width: 3.0349



B1L101S, U17-ch40, adc1

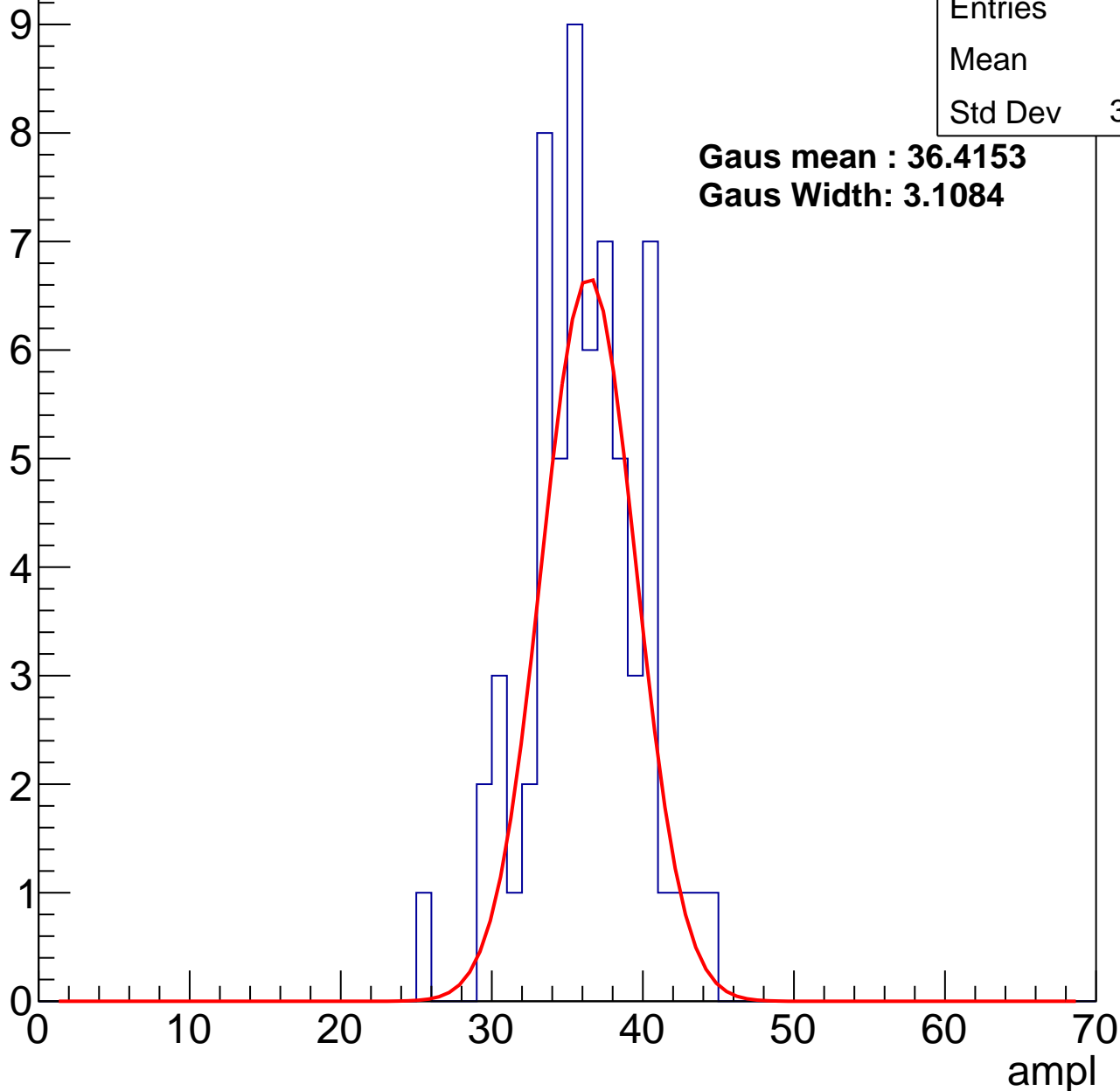
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.7
Std Dev	3.606

Gaus mean : 36.4153

Gaus Width: 3.1084



B1L101S, U17-ch40, adc2

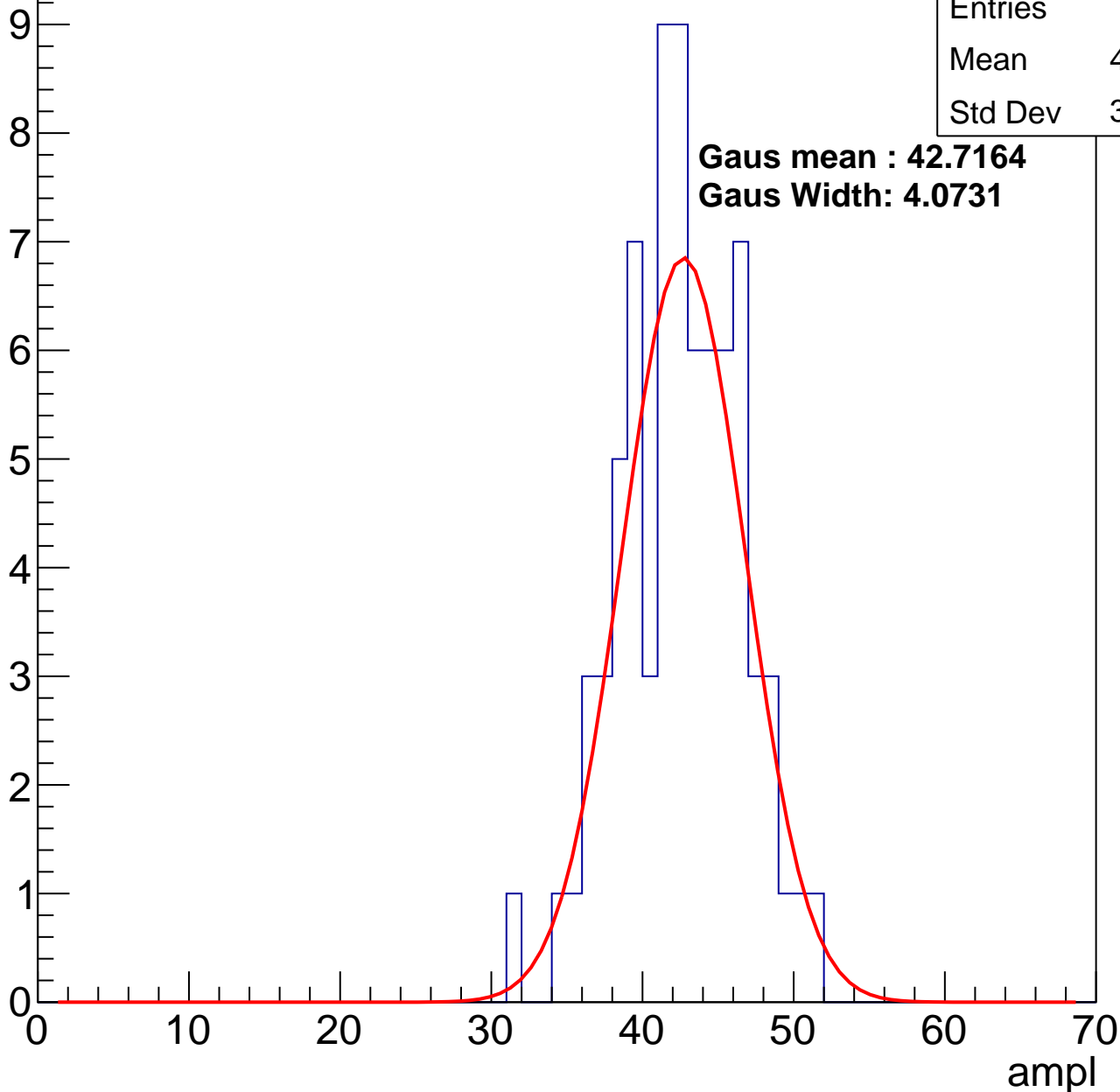
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	42.08
Std Dev	3.896

Gaus mean : 42.7164

Gaus Width: 4.0731

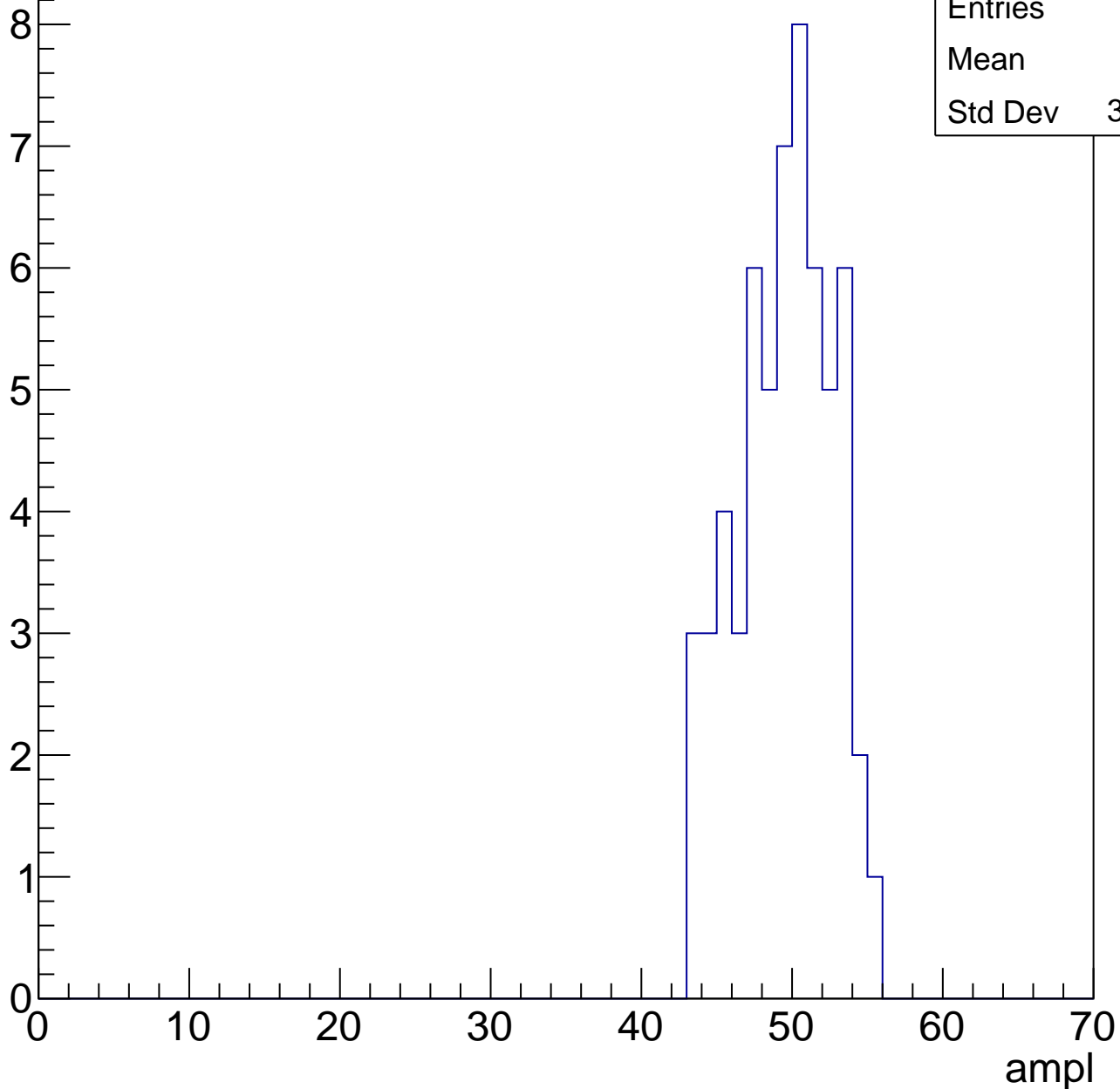


B1L101S, U17-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

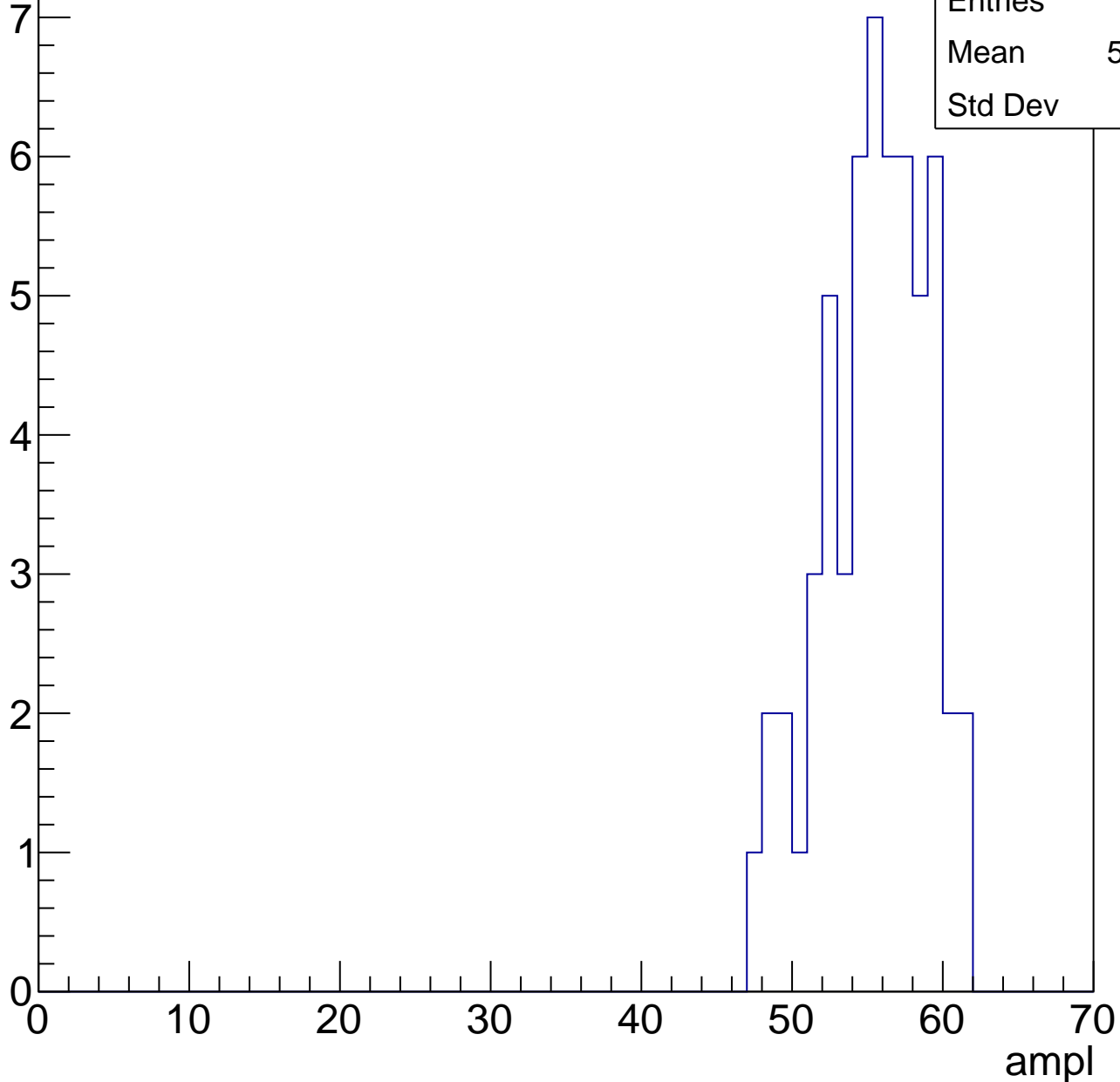
Entries	59
Mean	49
Std Dev	3.086



B1L101S, U17-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

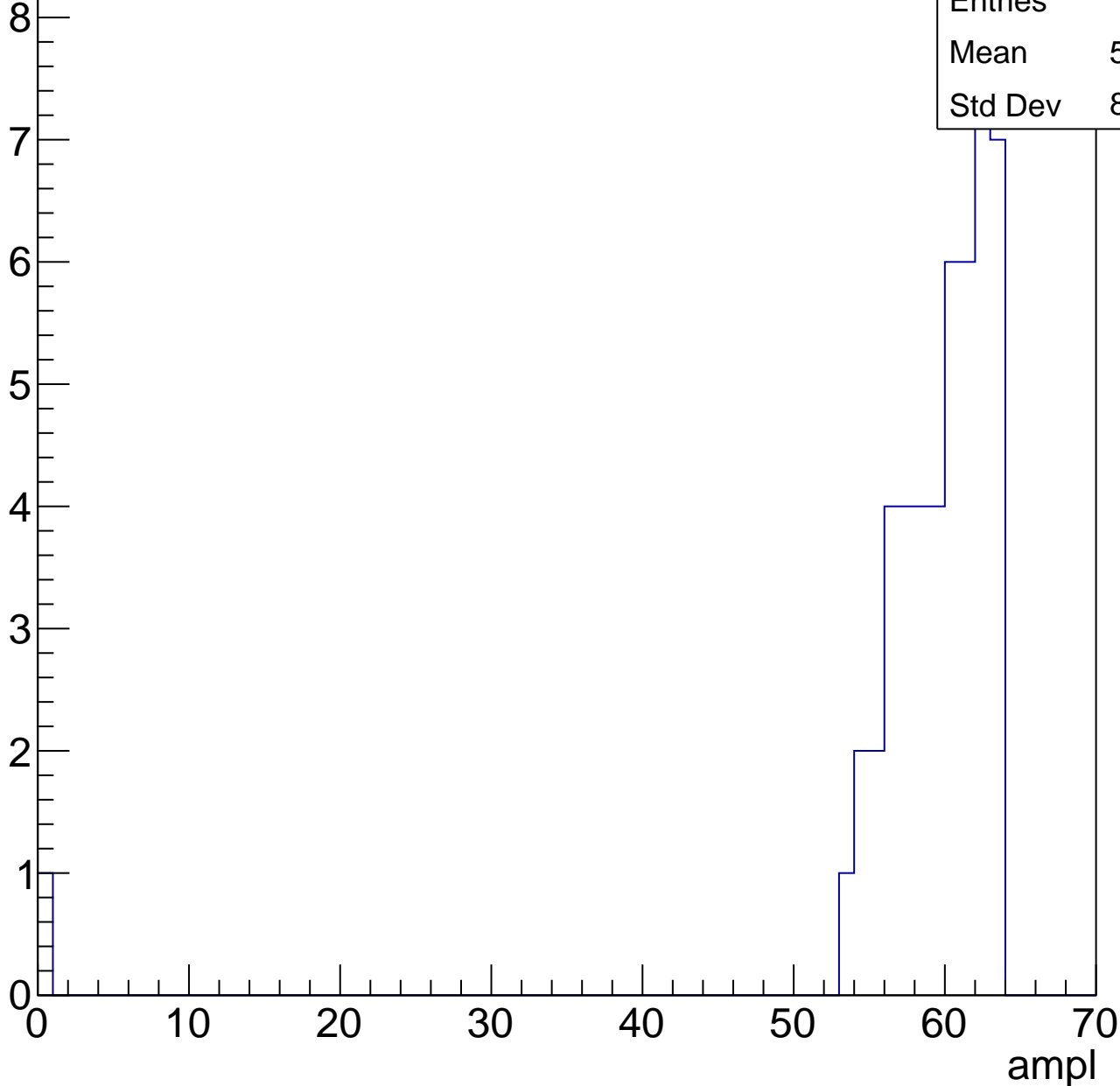


B1L101S, U17-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

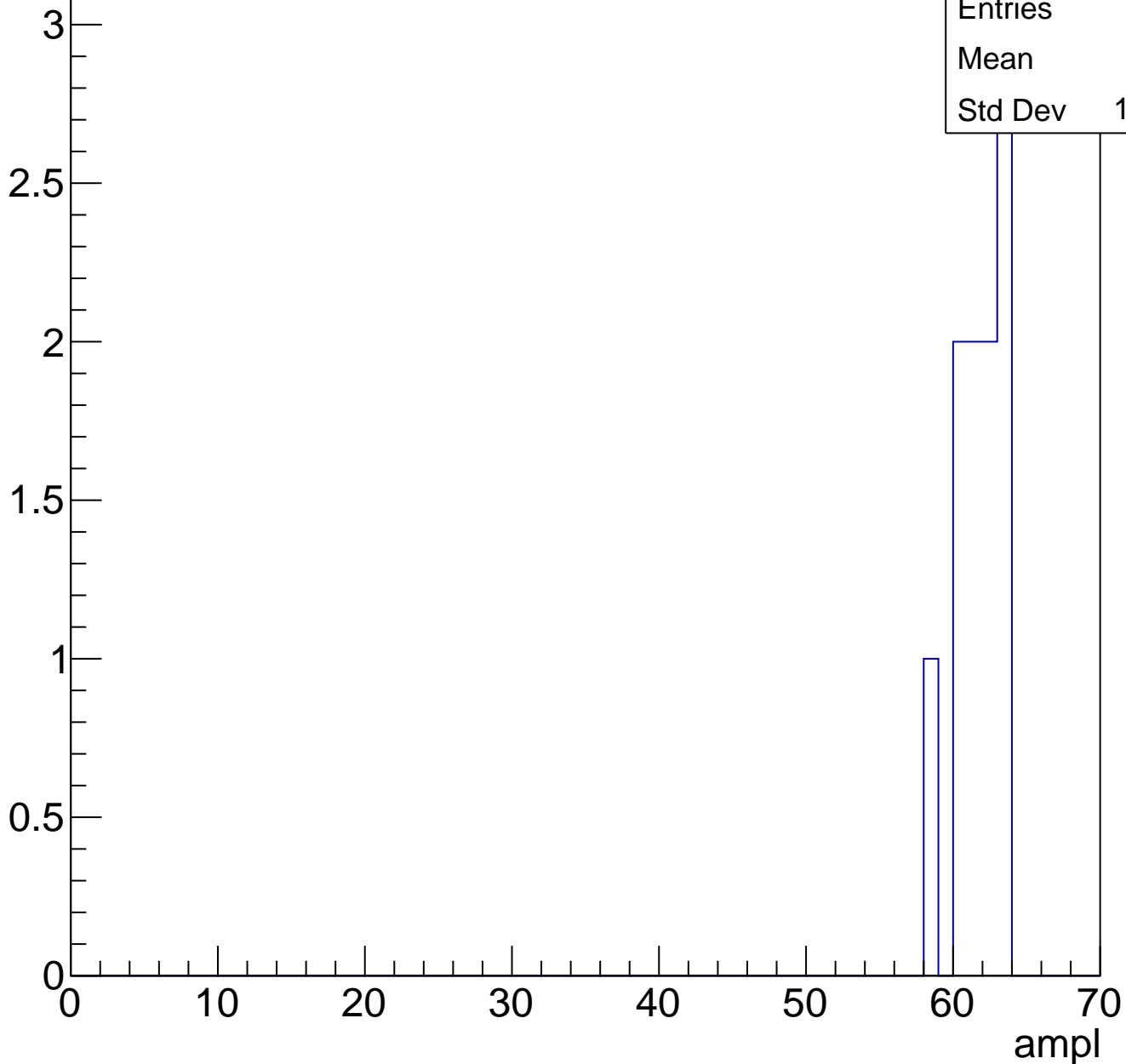
Entries	49
Mean	58.24
Std Dev	8.854



B1L101S, U17-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U17-ch41, adc0

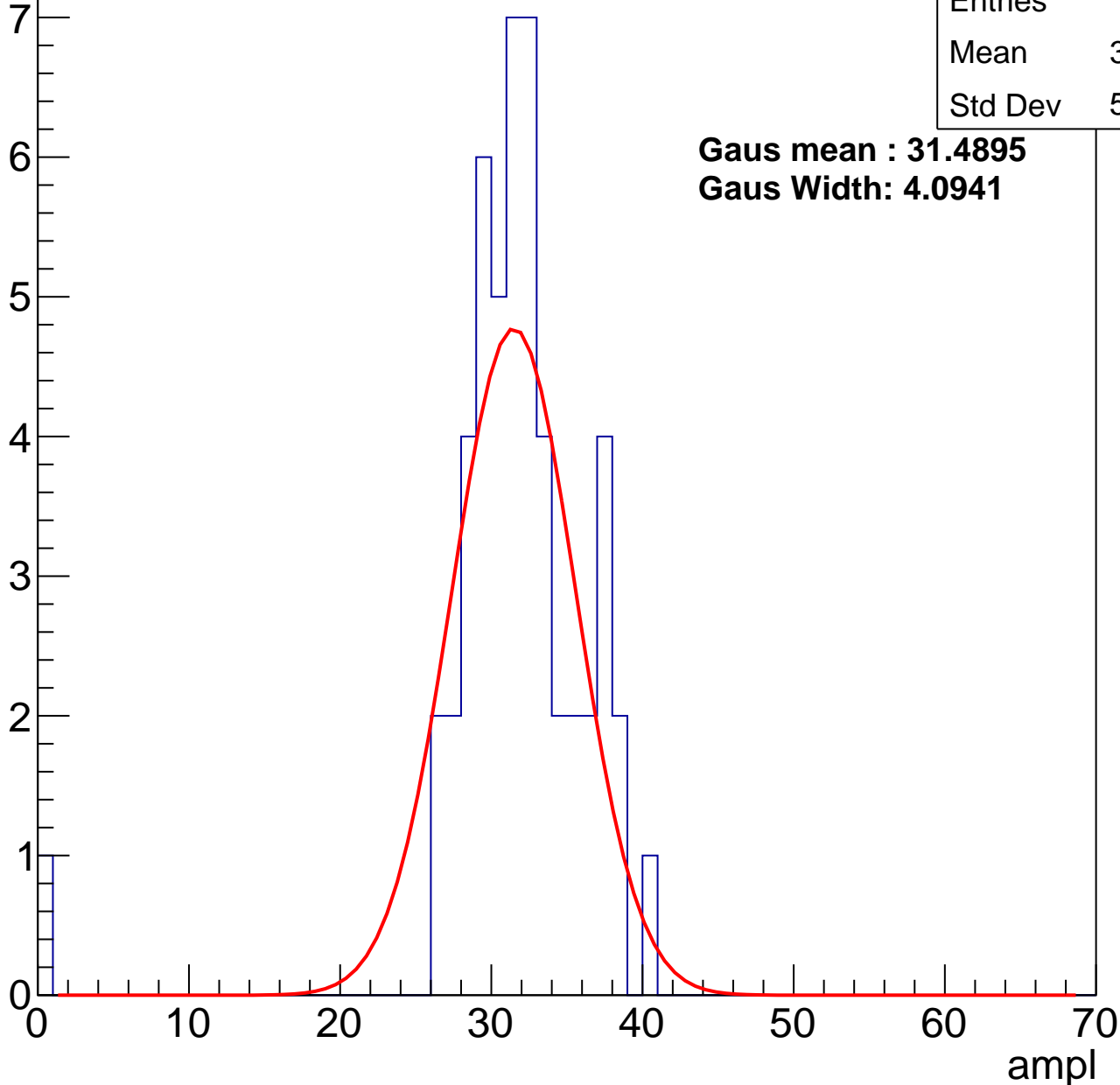
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	31.16
Std Dev	5.518

Gaus mean : 31.4895

Gaus Width: 4.0941



B1L101S, U17-ch41, adc1

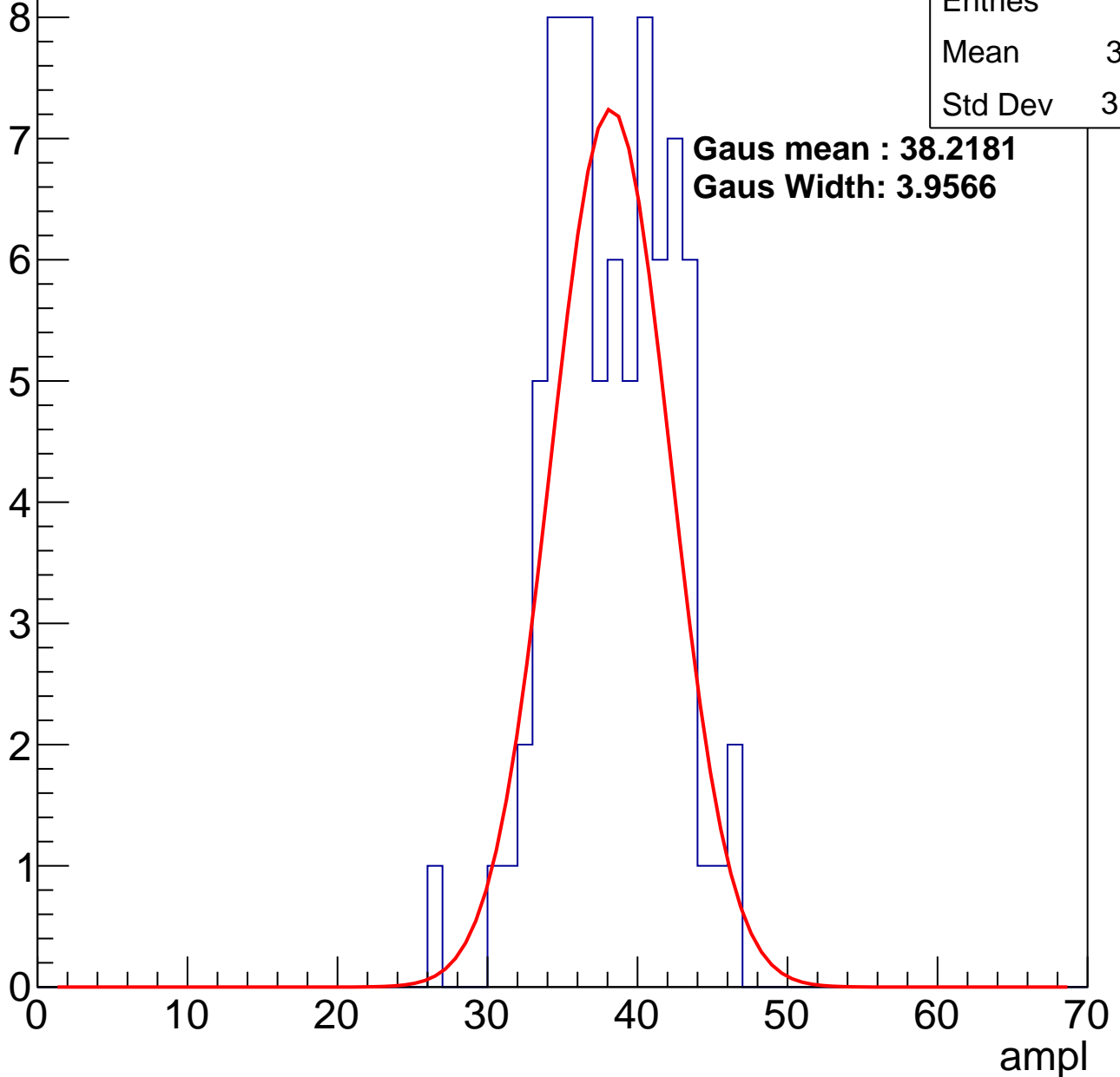
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.81
Std Dev	3.935

Gaus mean : 38.2181

Gaus Width: 3.9566



B1L101S, U17-ch41, adc2

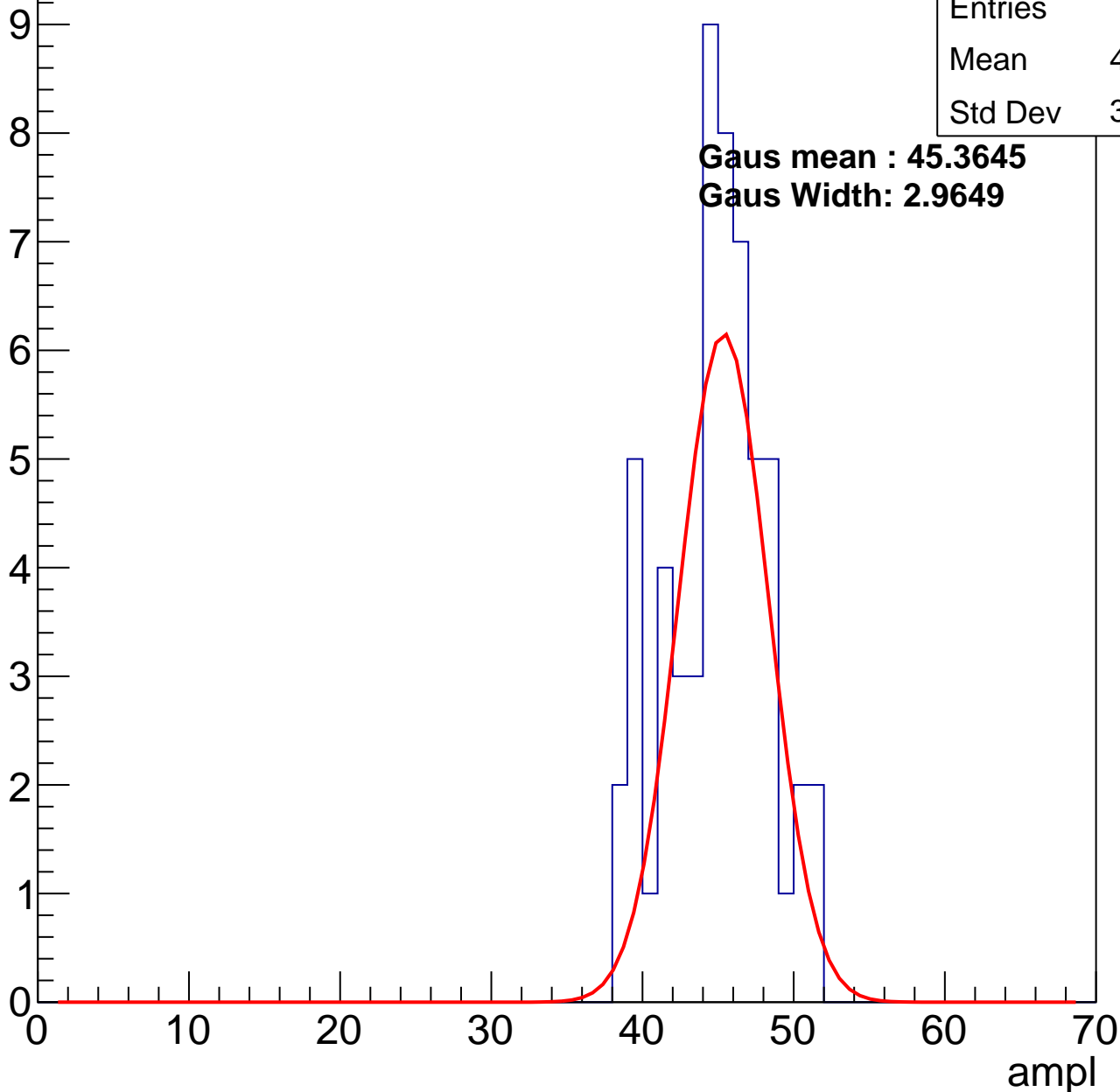
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.46
Std Dev	3.266

Gaus mean : 45.3645

Gaus Width: 2.9649

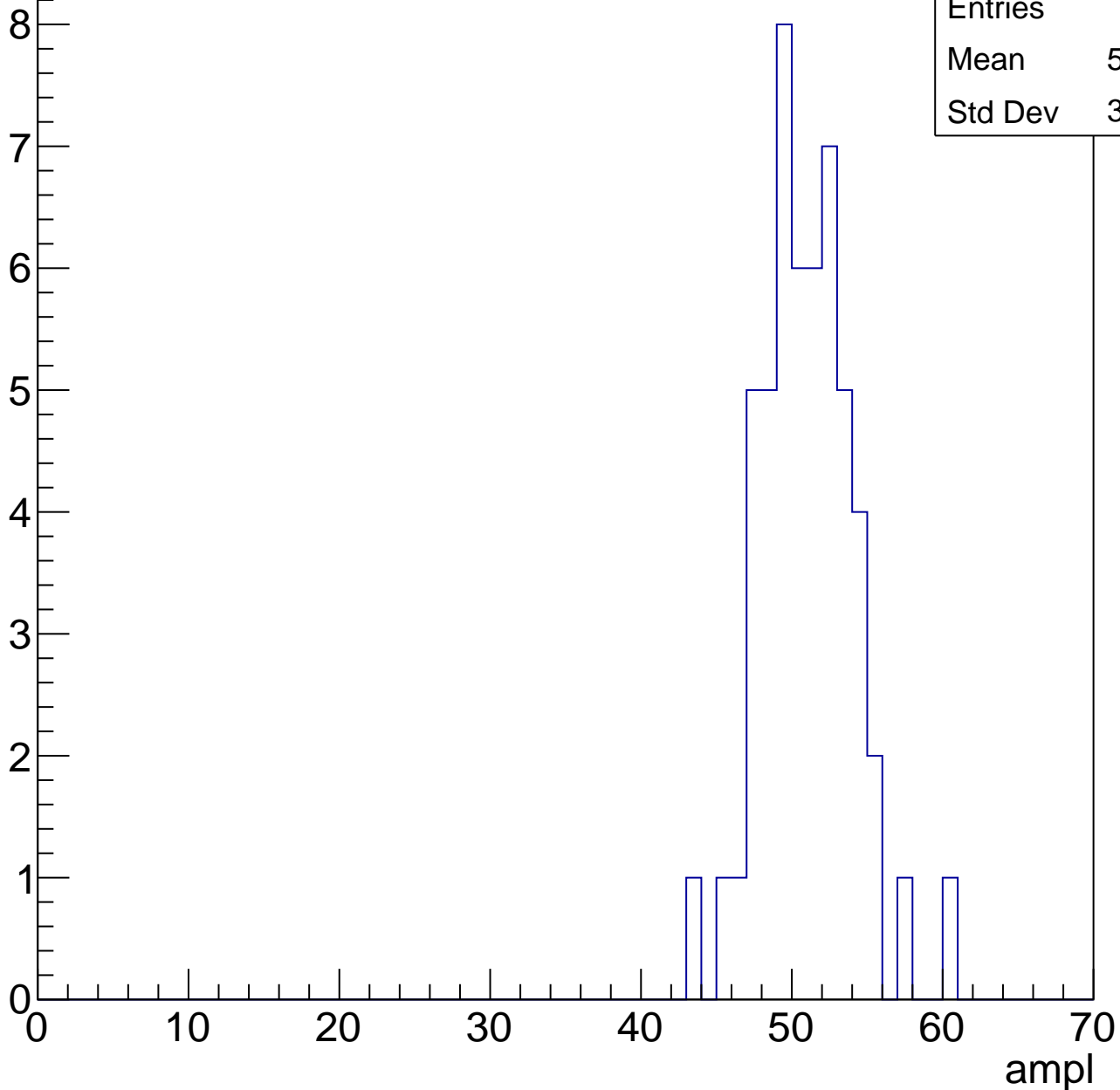


B1L101S, U17-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

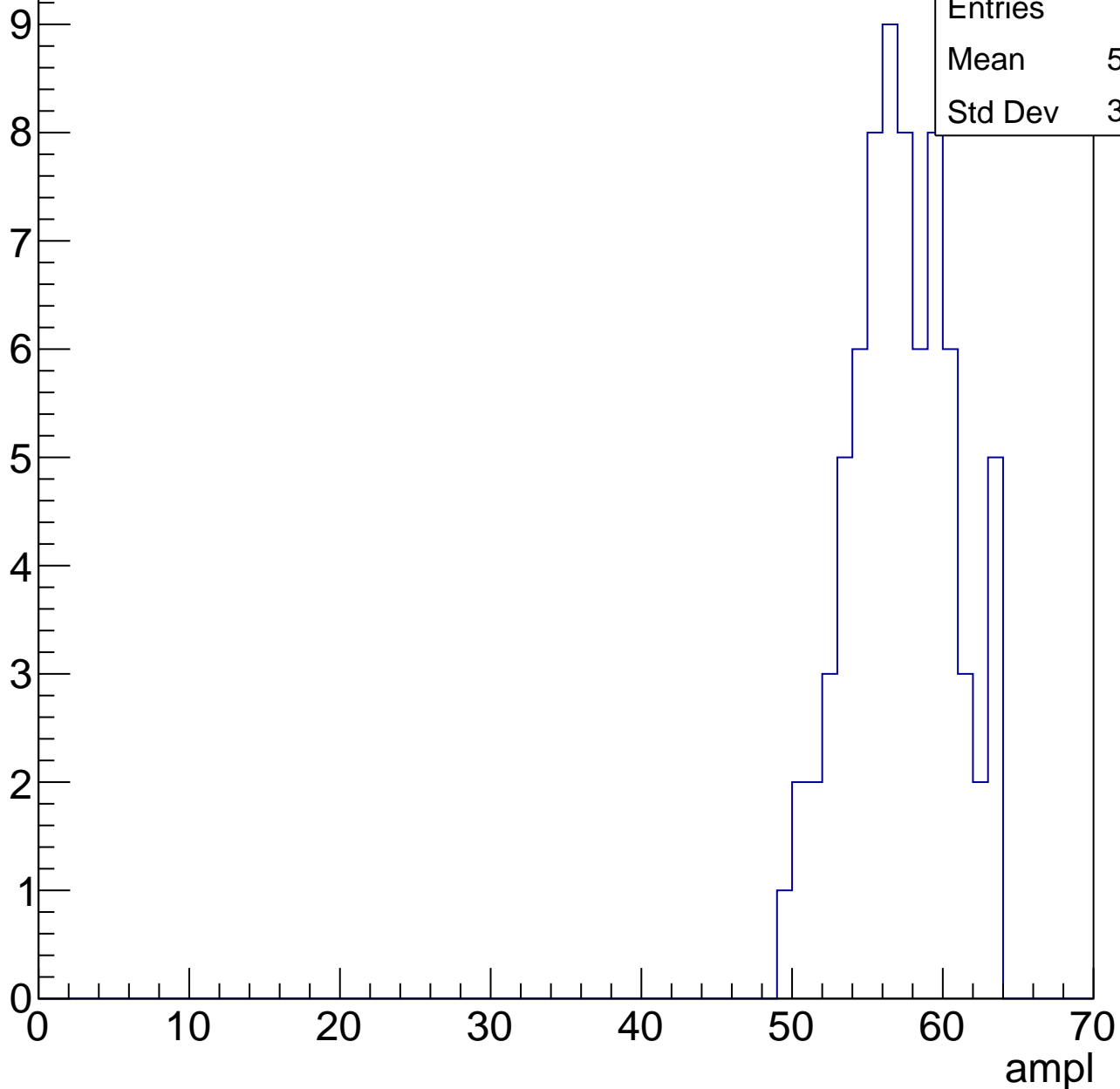
Entries	53
Mean	50.55
Std Dev	3.038



B1L101S, U17-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



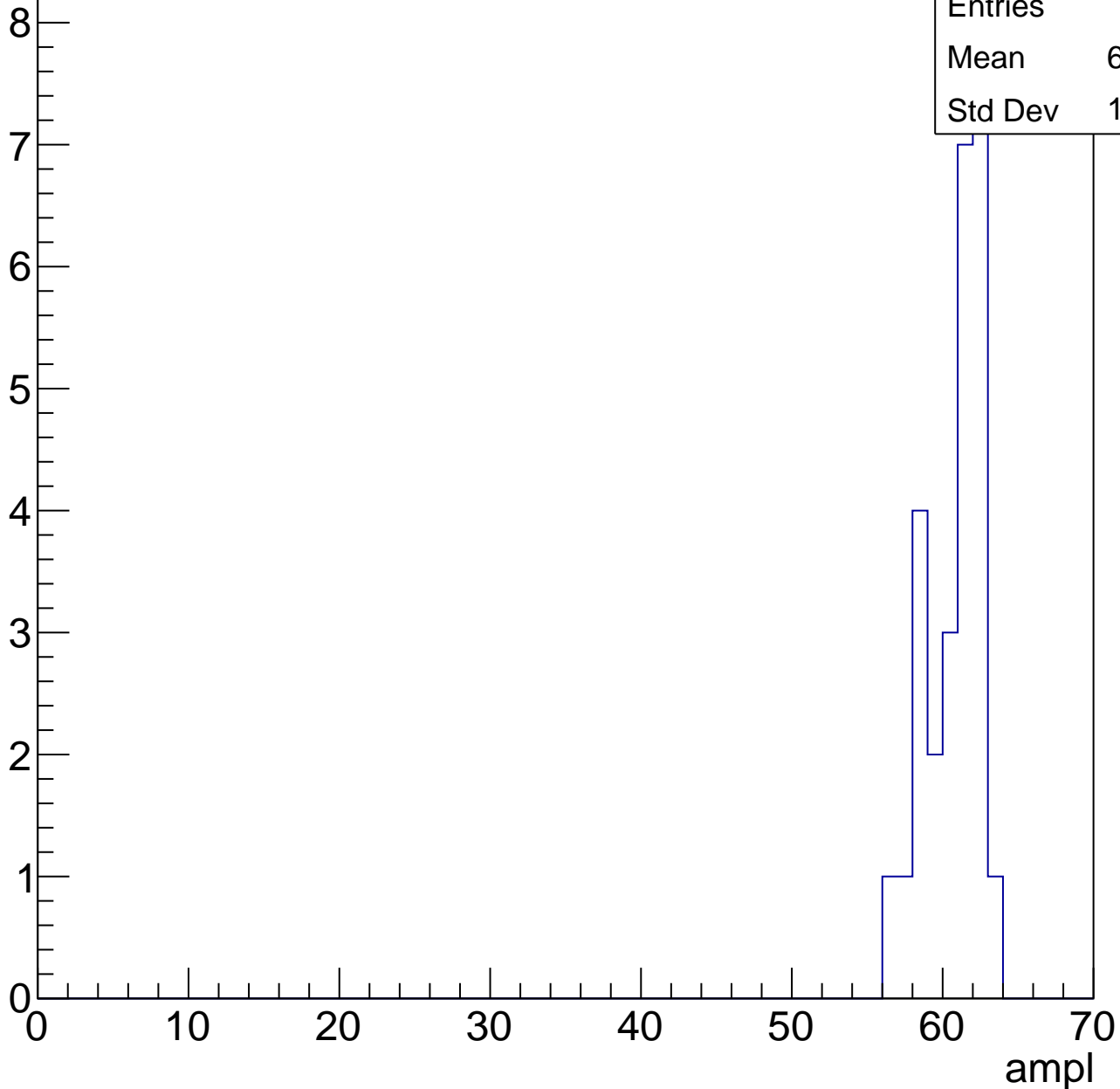
Entries	74
Mean	56.73
Std Dev	3.386

B1L101S, U17-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

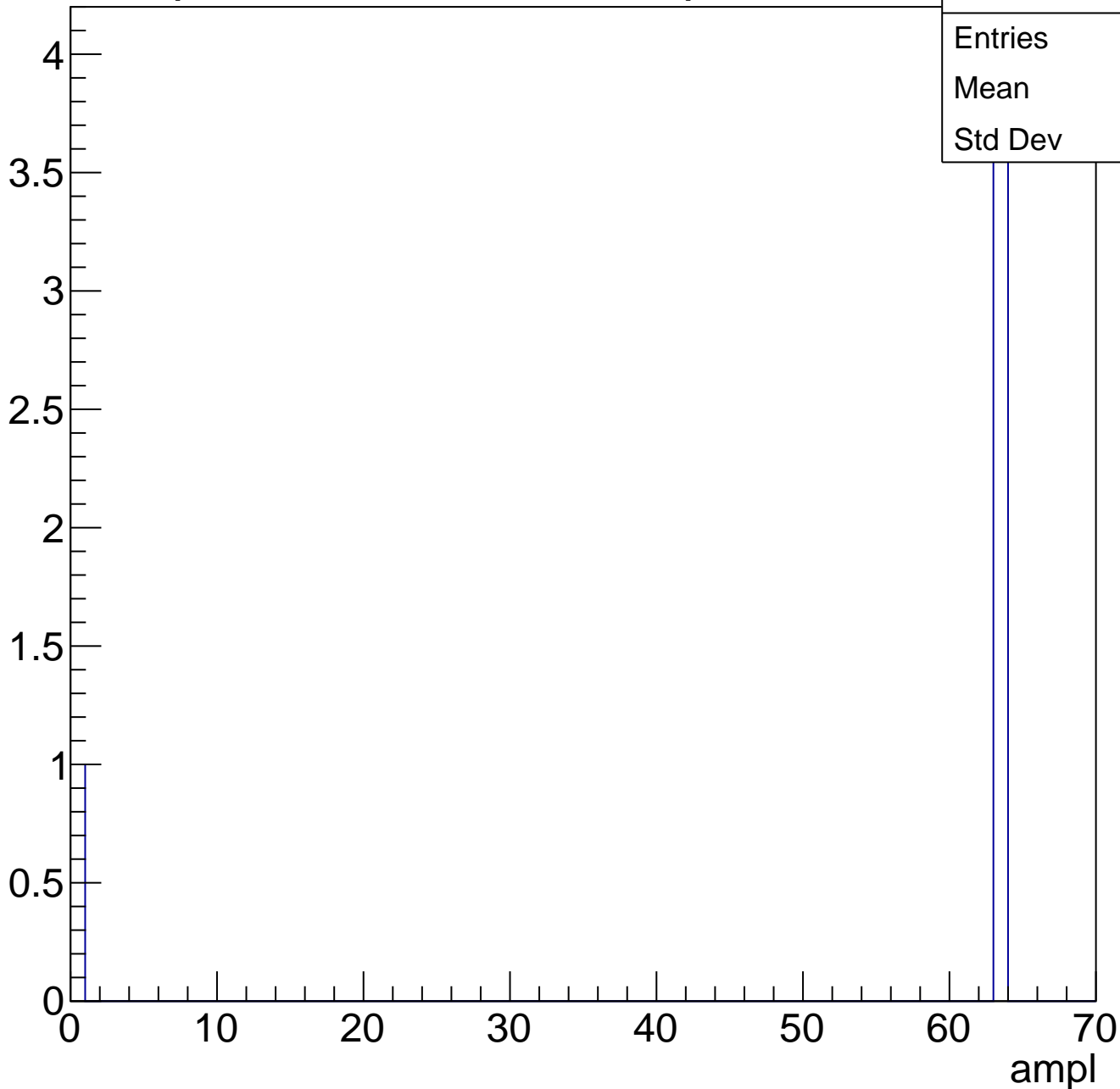
Entries	27
Mean	60.33
Std Dev	1.805



B1L101S, U17-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch42, adc0

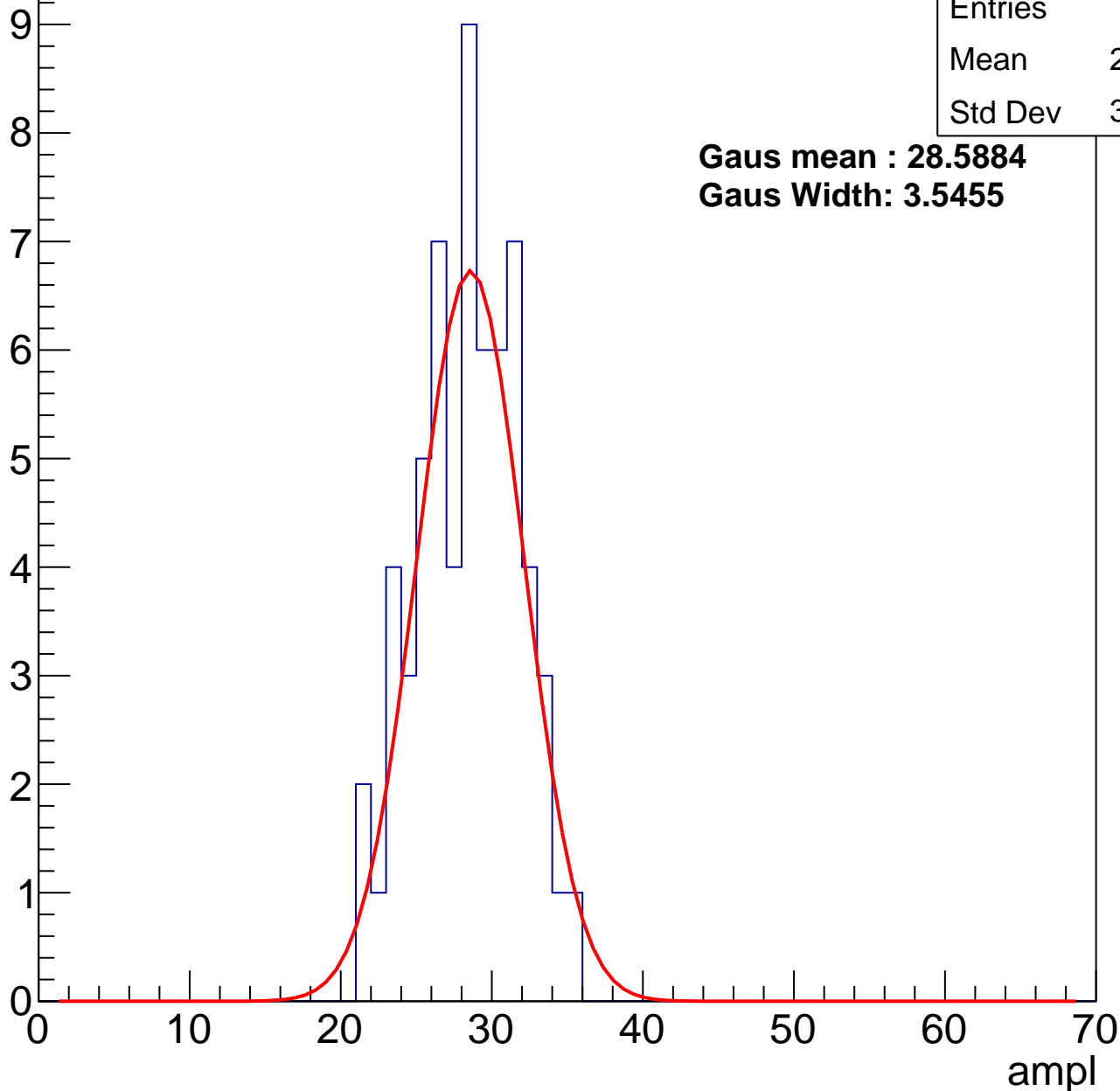
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.97
Std Dev	3.276

Gaus mean : 28.5884

Gaus Width: 3.5455



B1L101S, U17-ch42, adc1

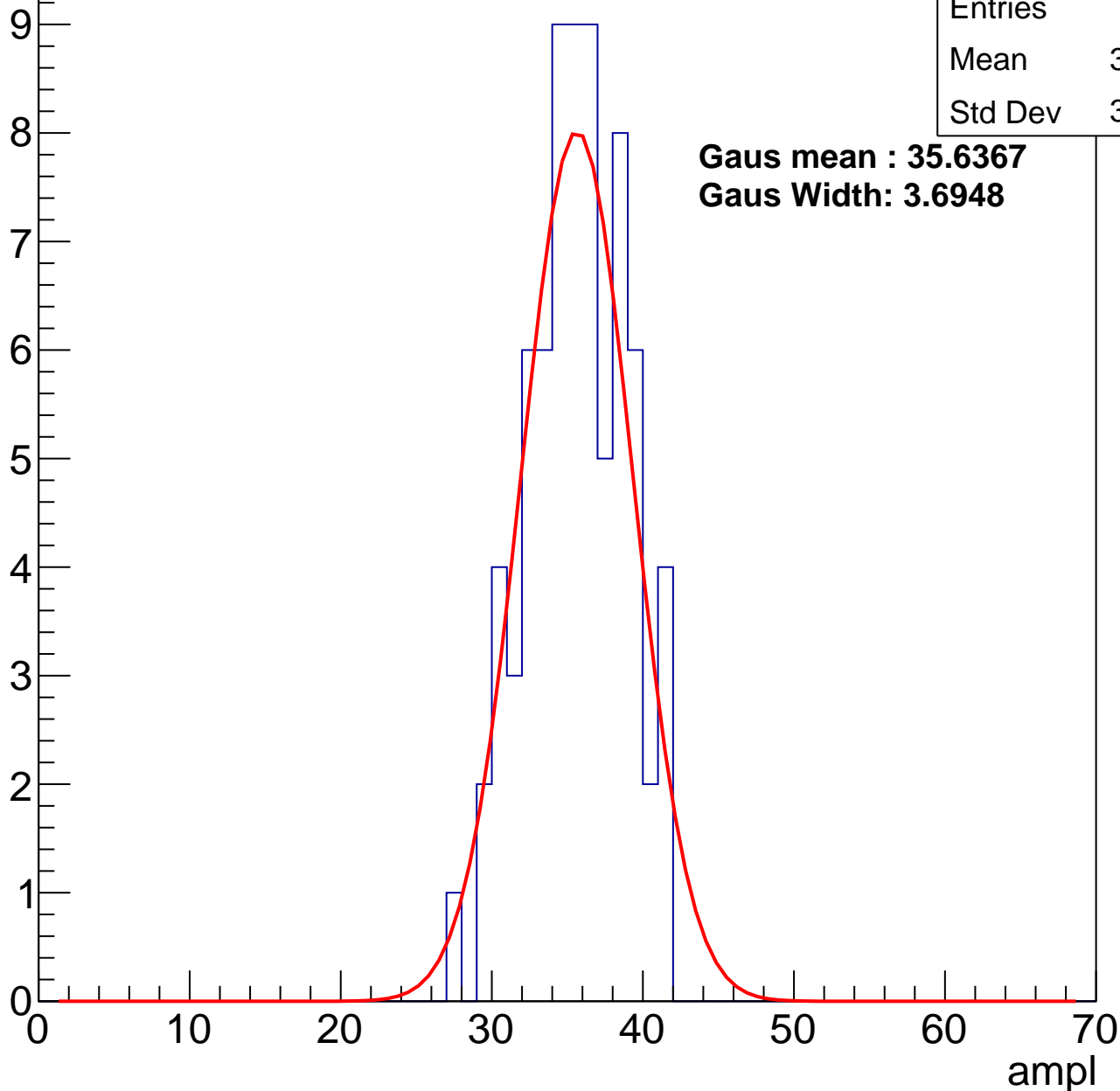
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.14
Std Dev	3.206

Gaus mean : 35.6367

Gaus Width: 3.6948



B1L101S, U17-ch42, adc2

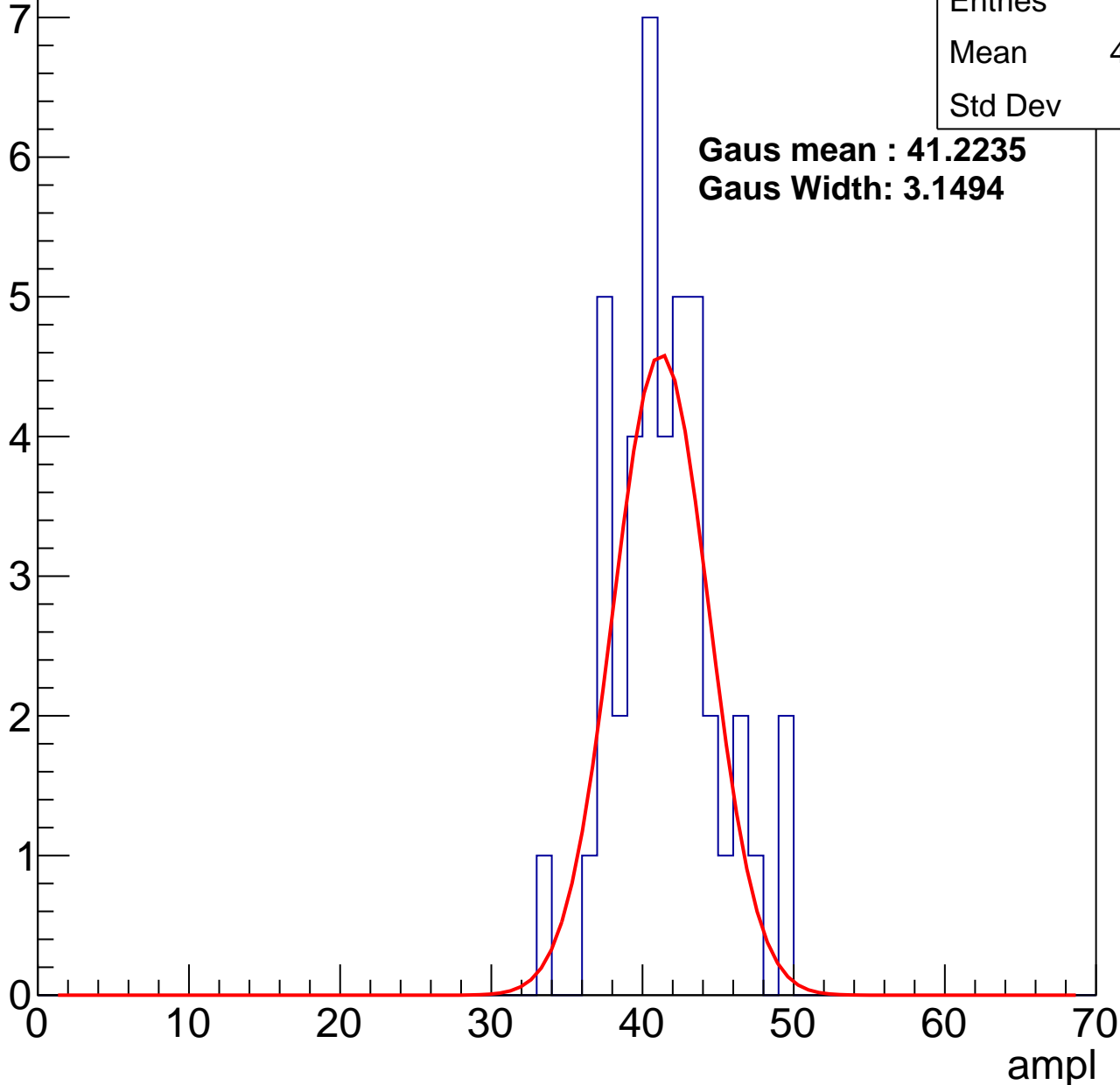
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	41.07
Std Dev	3.39

Gaus mean : 41.2235

Gaus Width: 3.1494

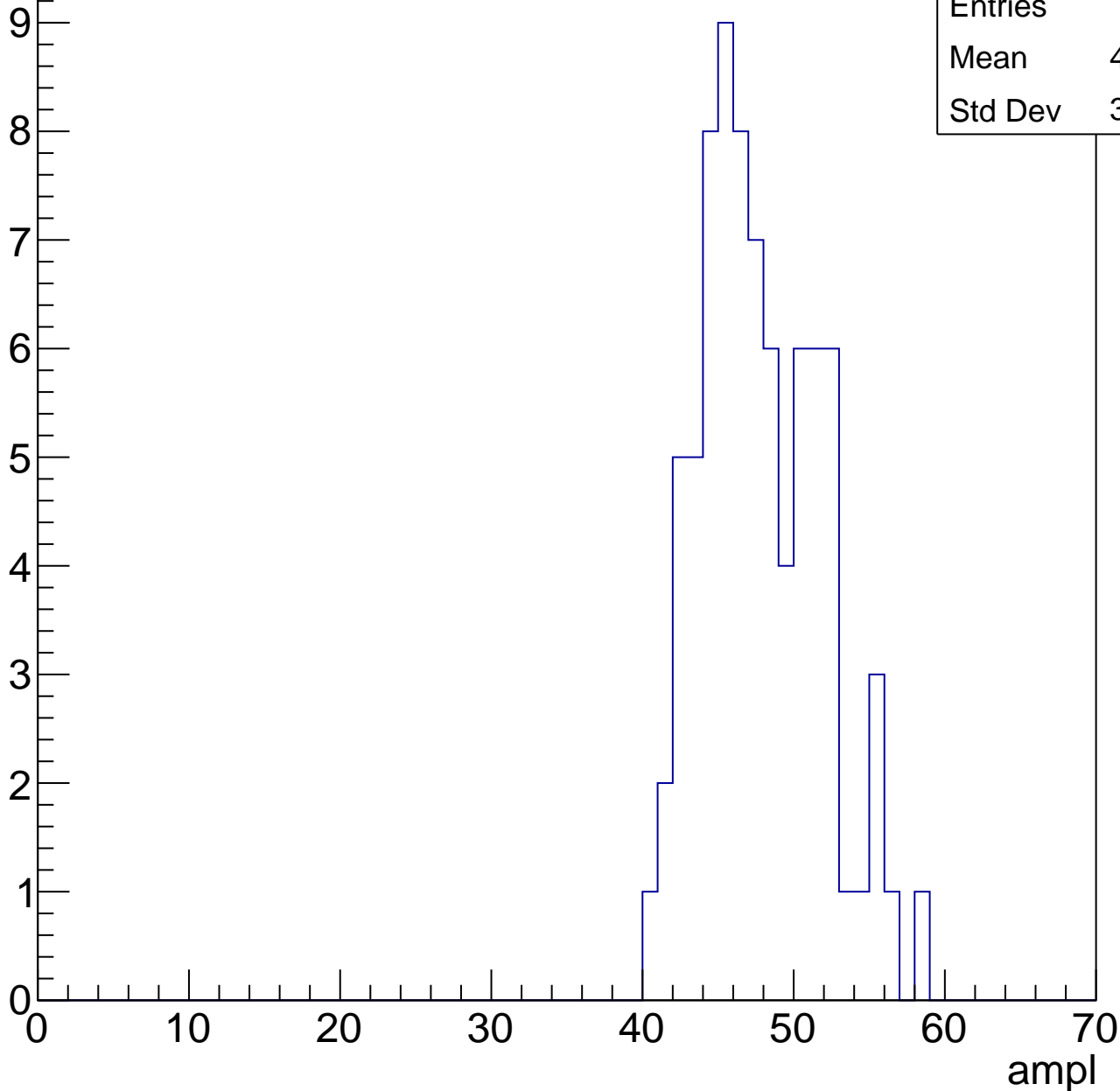


B1L101S, U17-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	47.36
Std Dev	3.925

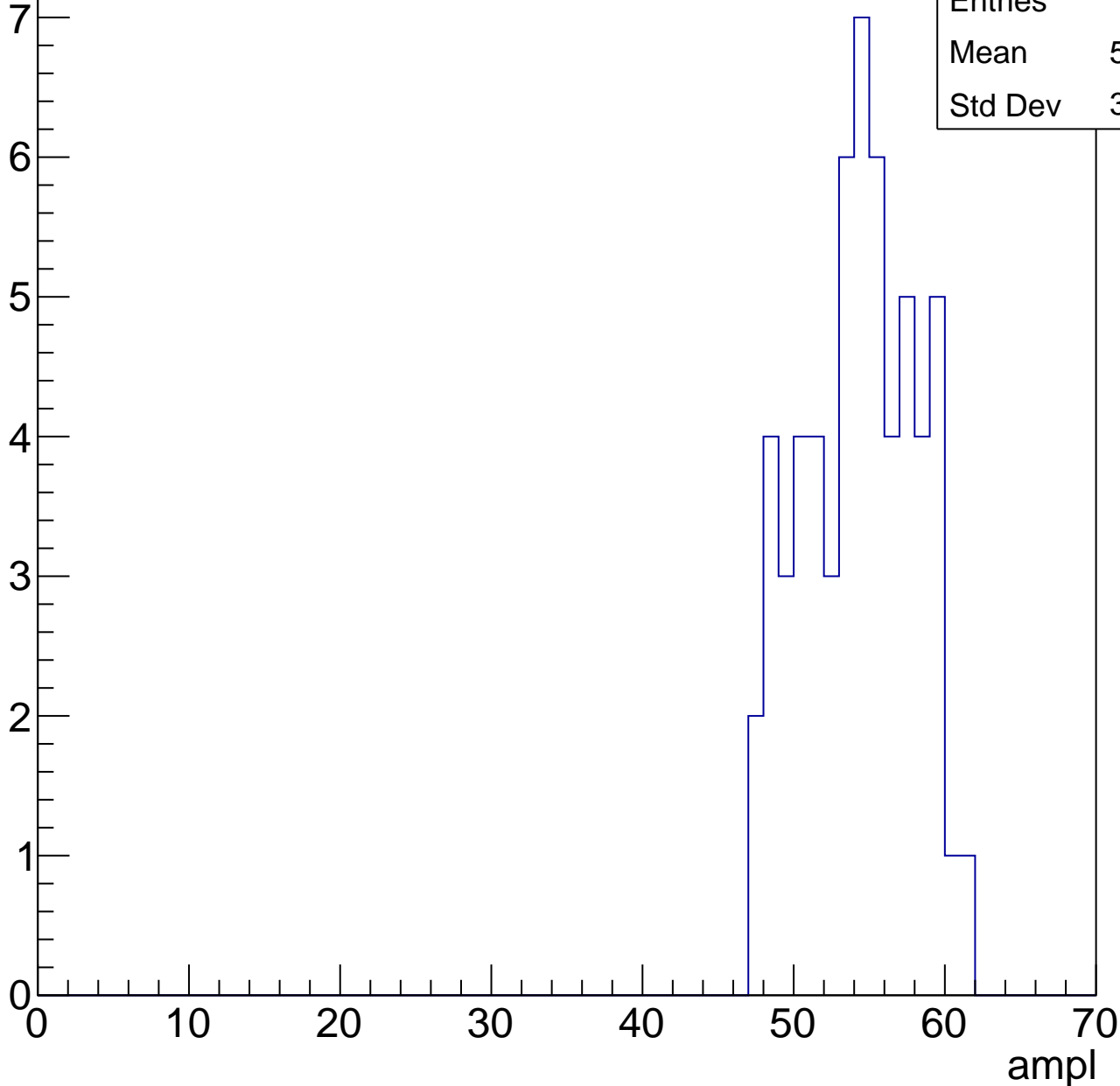


B1L101S, U17-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	53.83
Std Dev	3.623

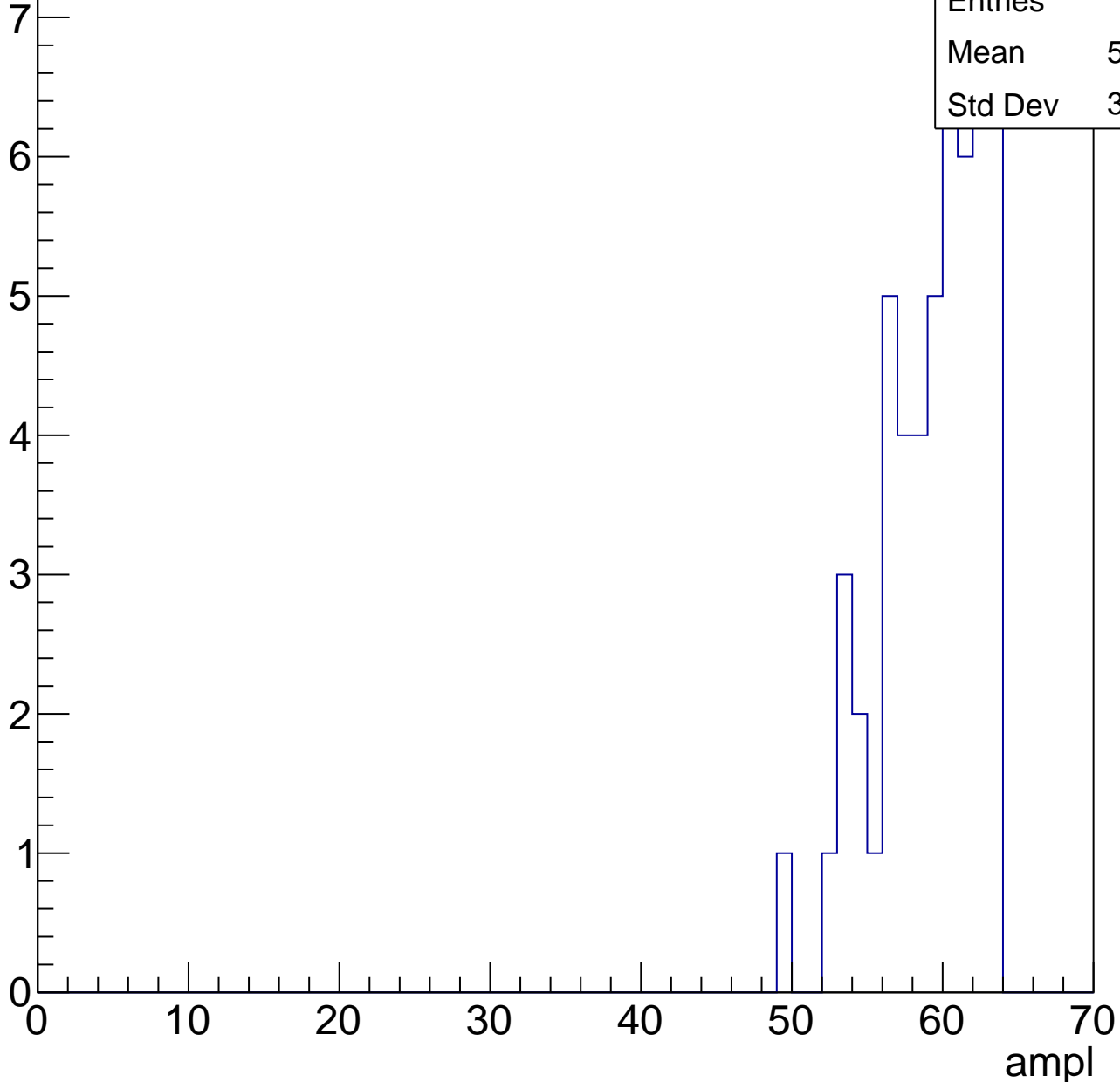


B1L101S, U17-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

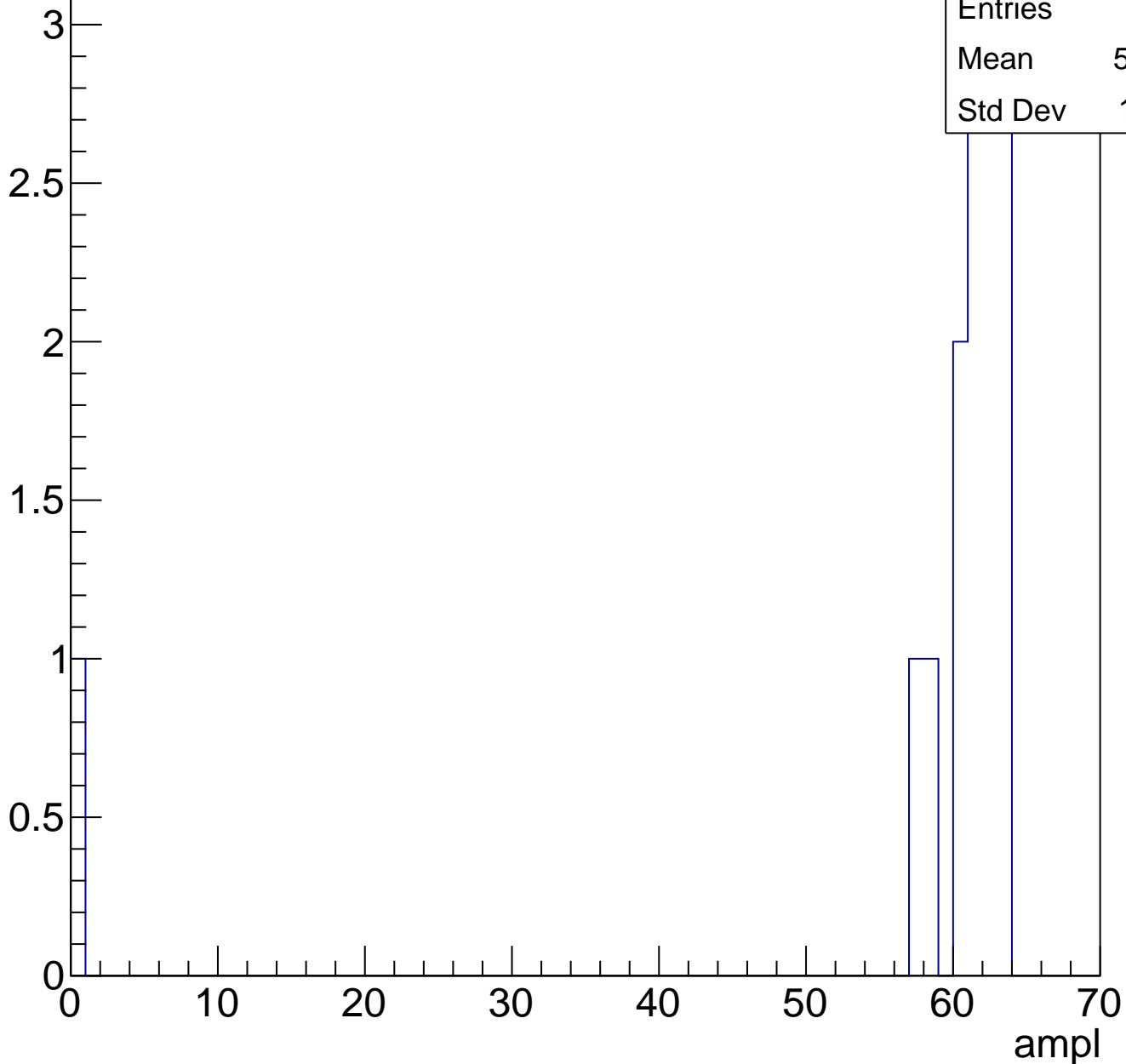
Entries	53
Mean	58.85
Std Dev	3.344



B1L101S, U17-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L101S, U17-ch43, adc0

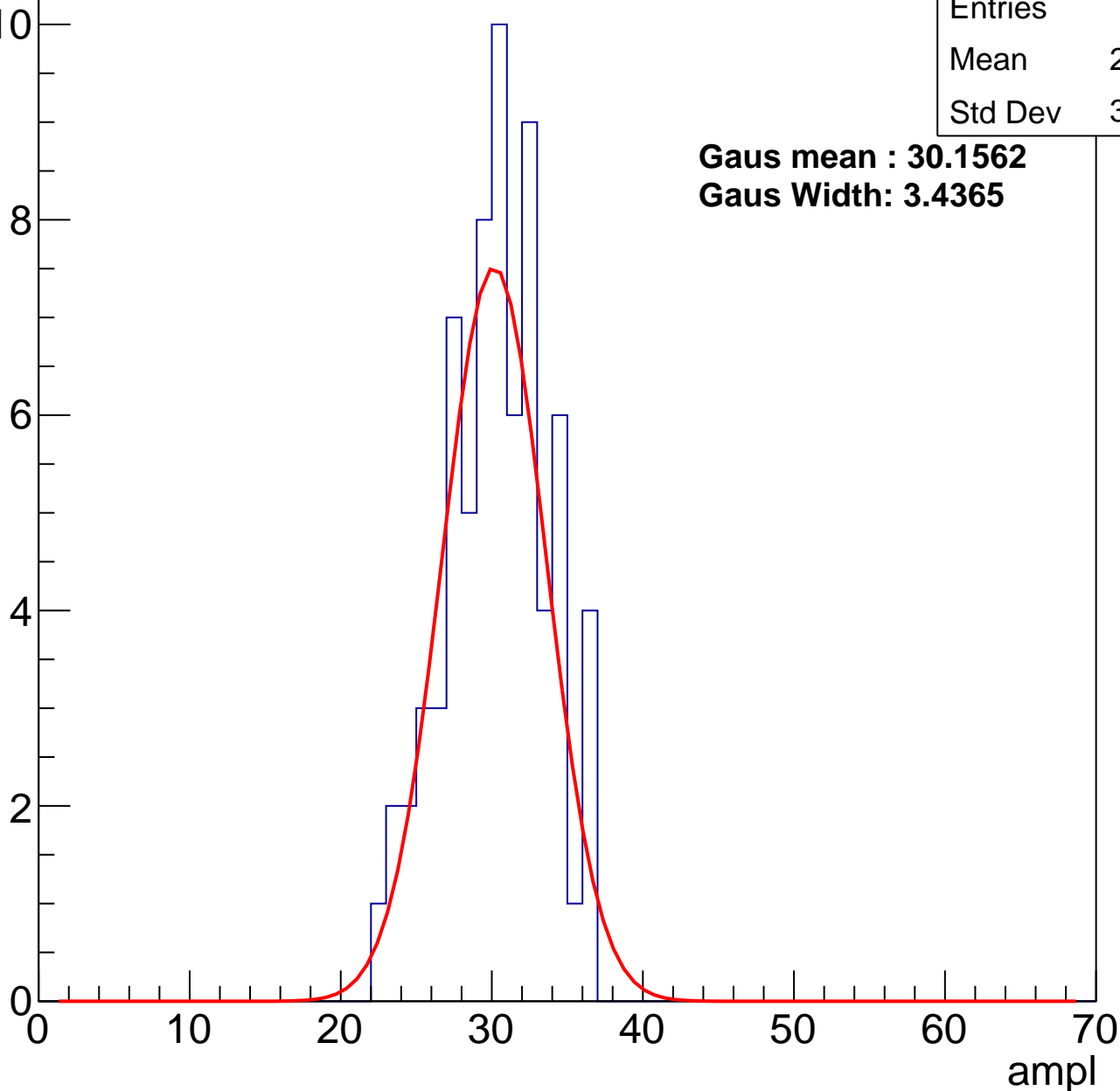
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.85
Std Dev	3.334

Gaus mean : 30.1562

Gaus Width: 3.4365



B1L101S, U17-ch43, adc1

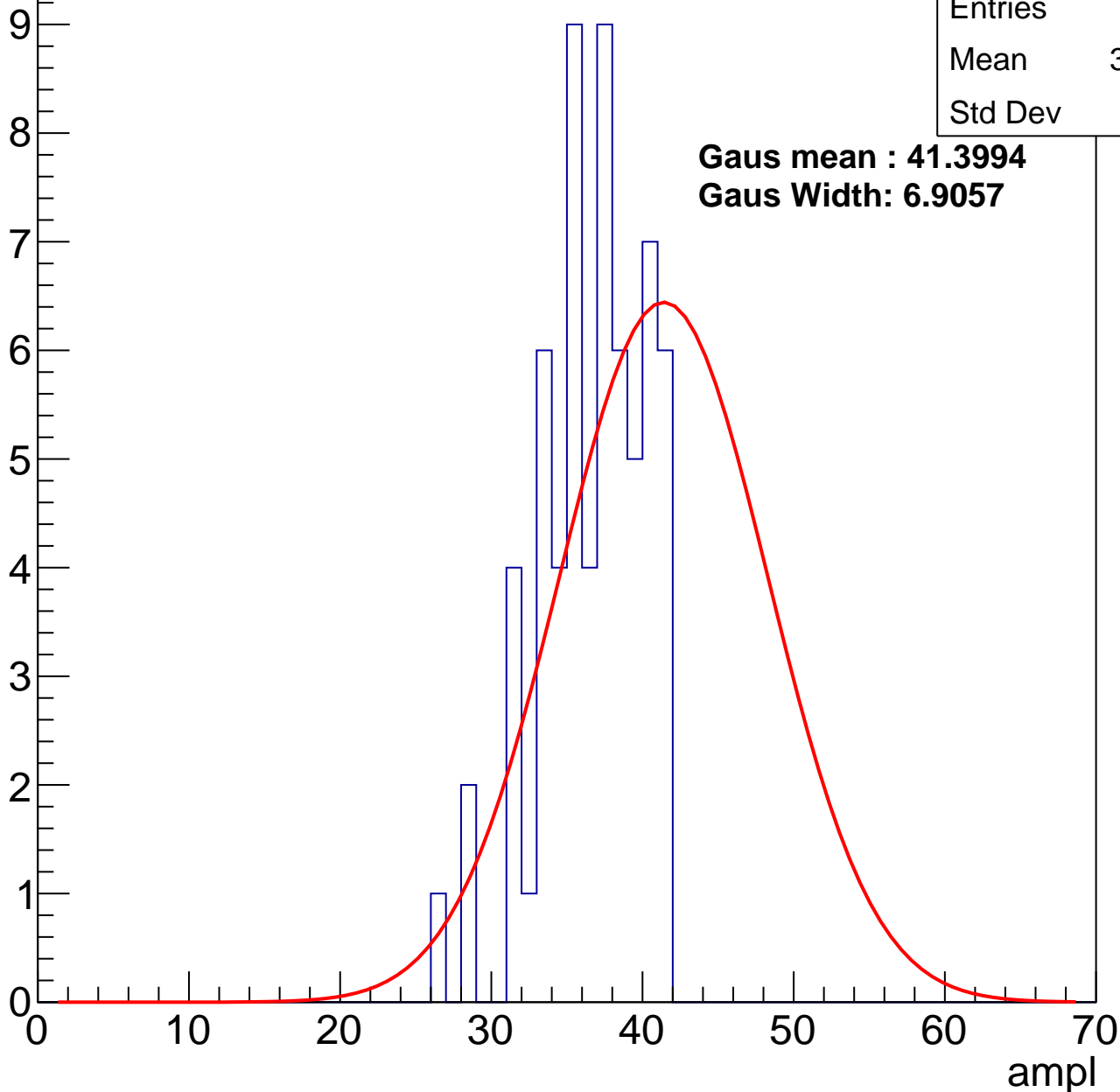
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.14
Std Dev	3.45

Gaus mean : 41.3994

Gaus Width: 6.9057



B1L101S, U17-ch43, adc2

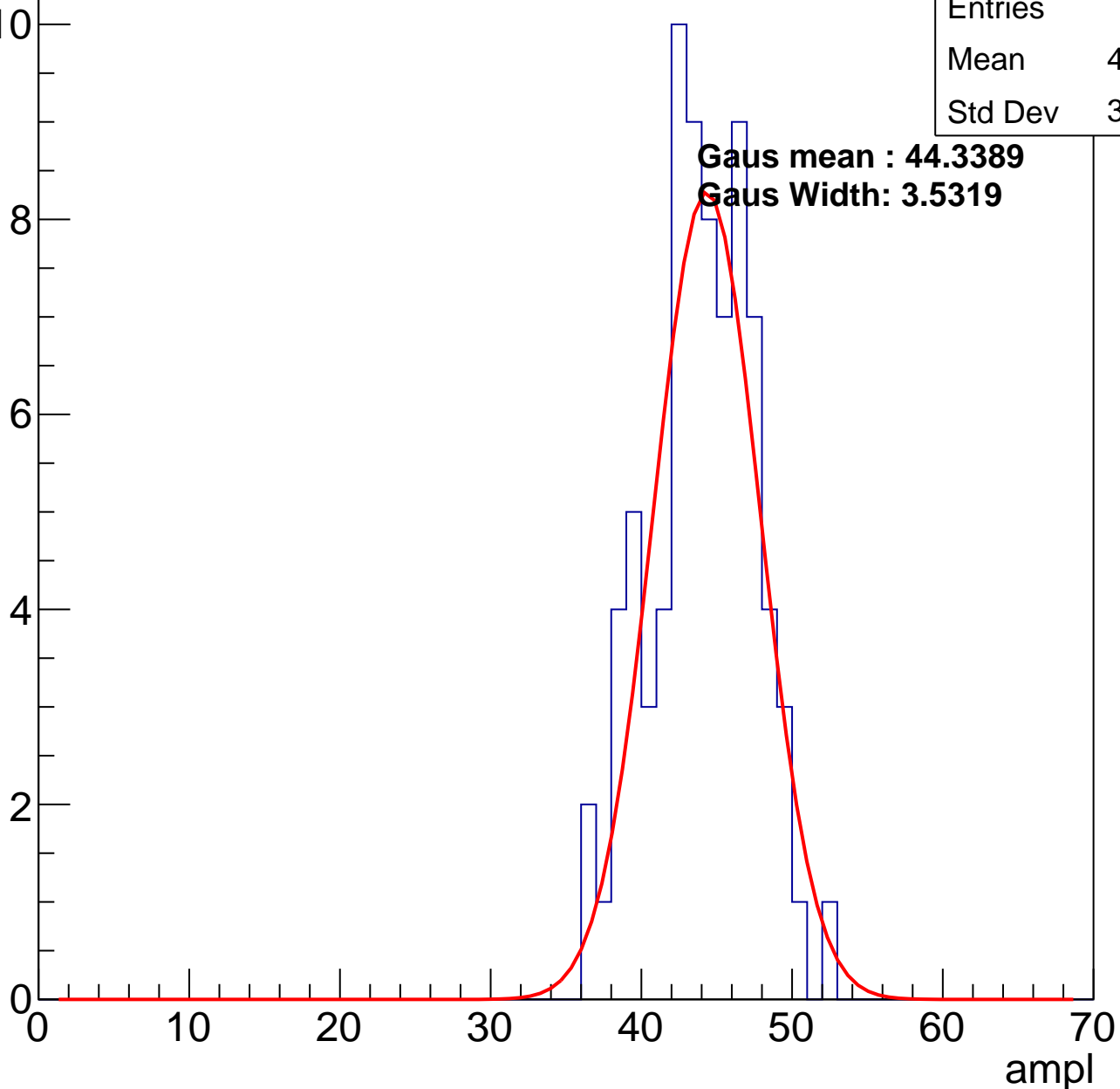
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.56
Std Dev	3.414

Gaus mean : 44.3389

Gaus Width: 3.5319

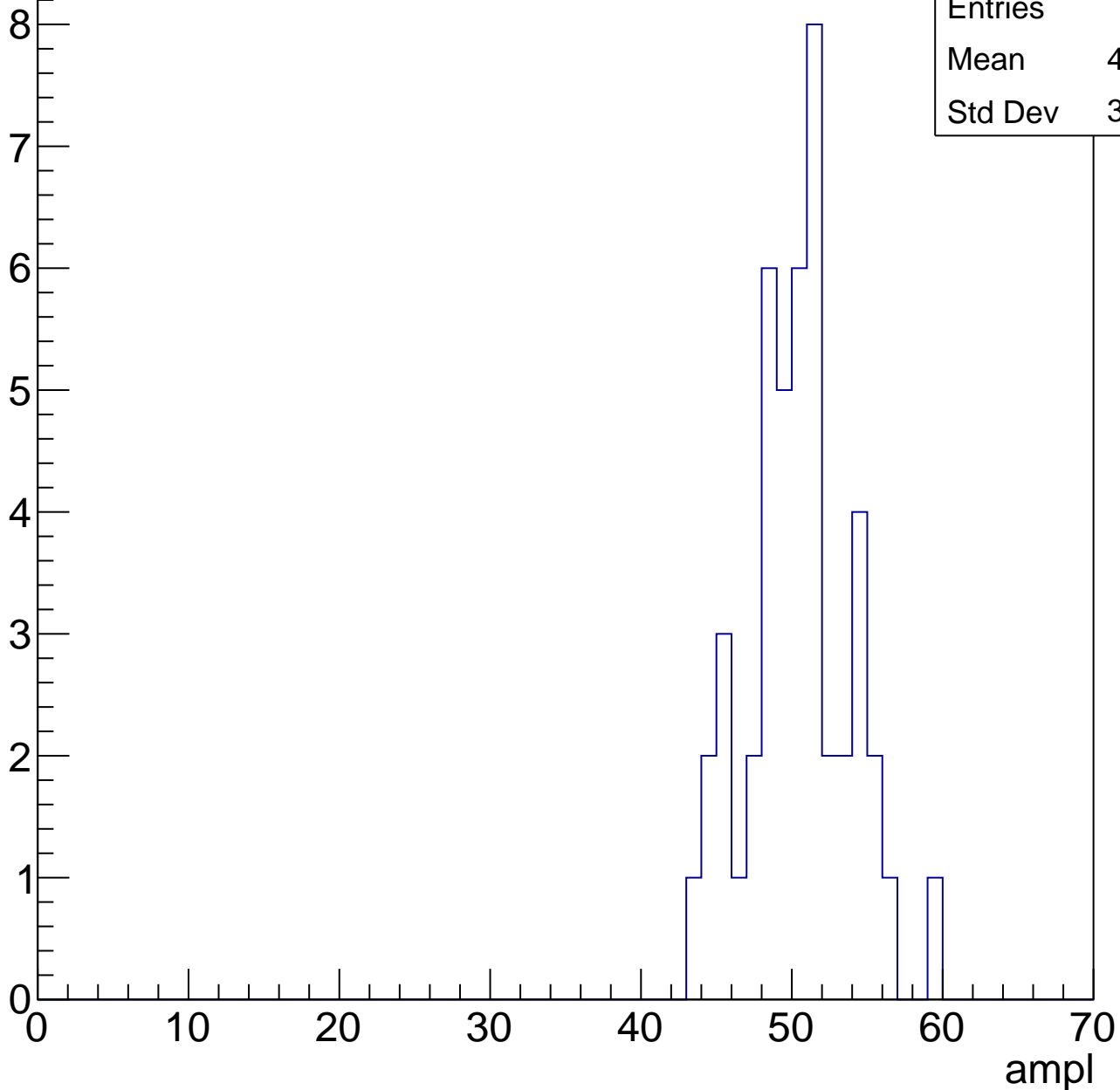


B1L101S, U17-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	49.96
Std Dev	3.375

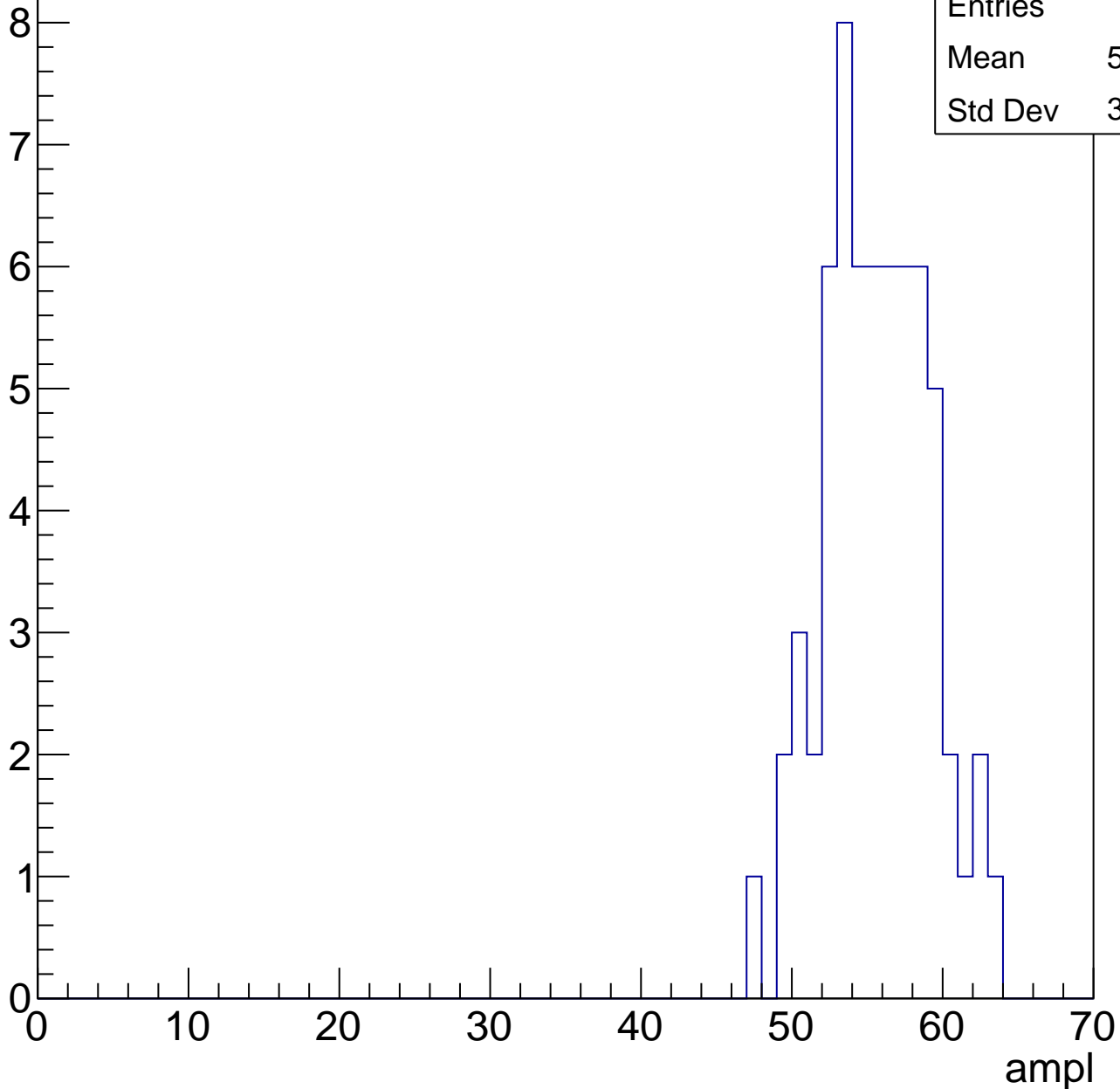


B1L101S, U17-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.17
Std Dev	3.439



B1L101S, U17-ch43, adc5

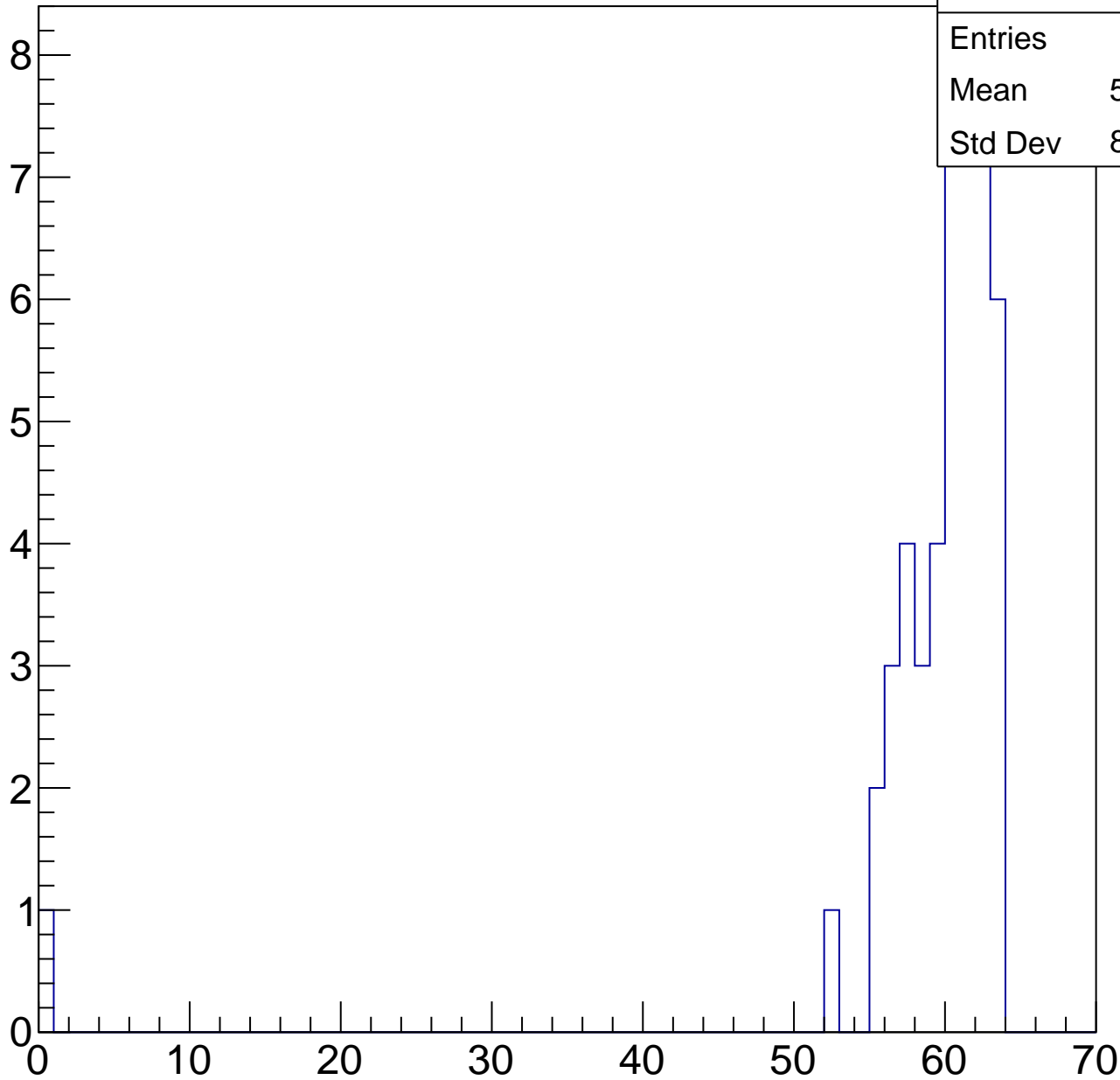
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.54
Std Dev	8.904

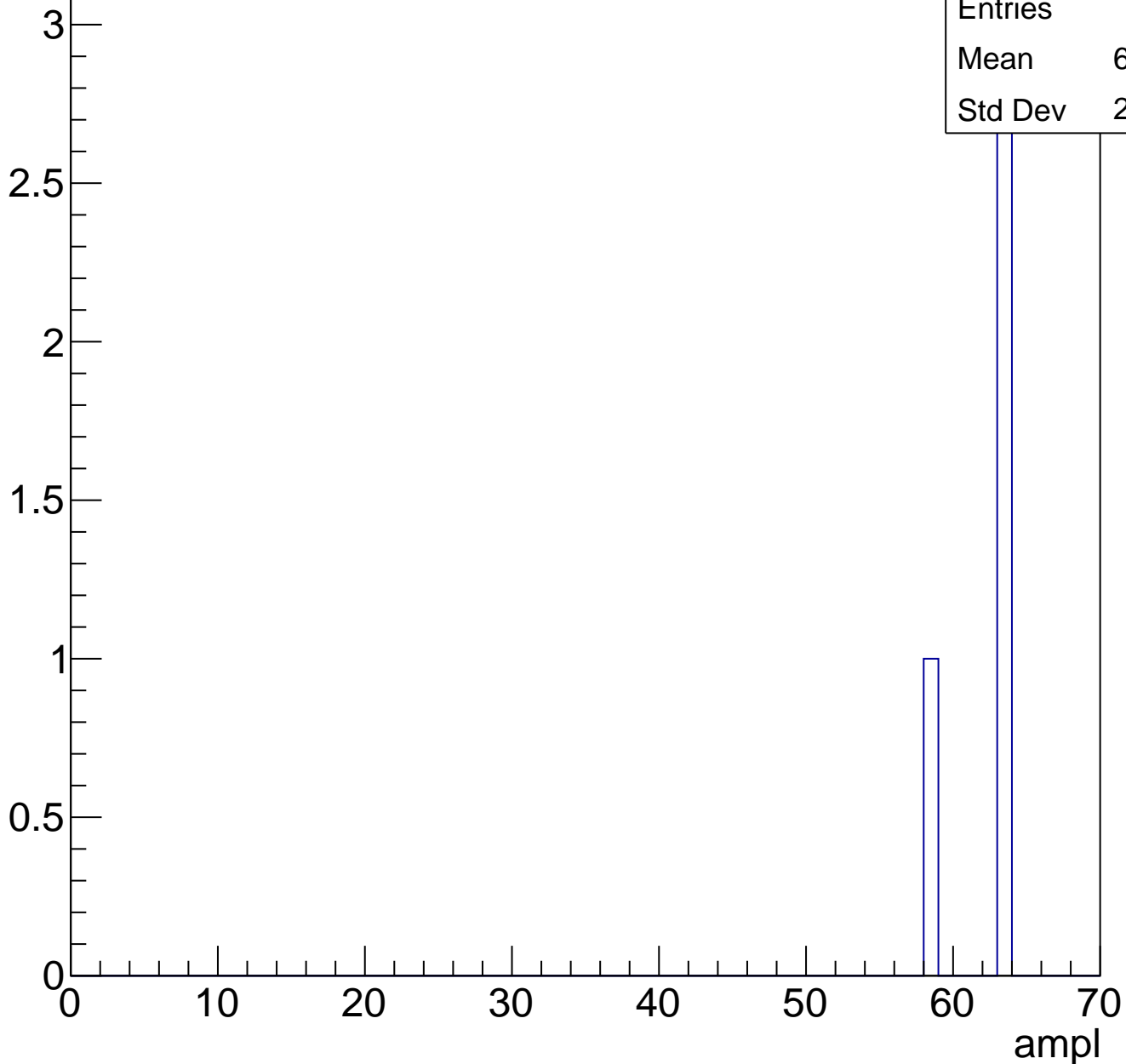
ampl



B1L101S, U17-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch44, adc0

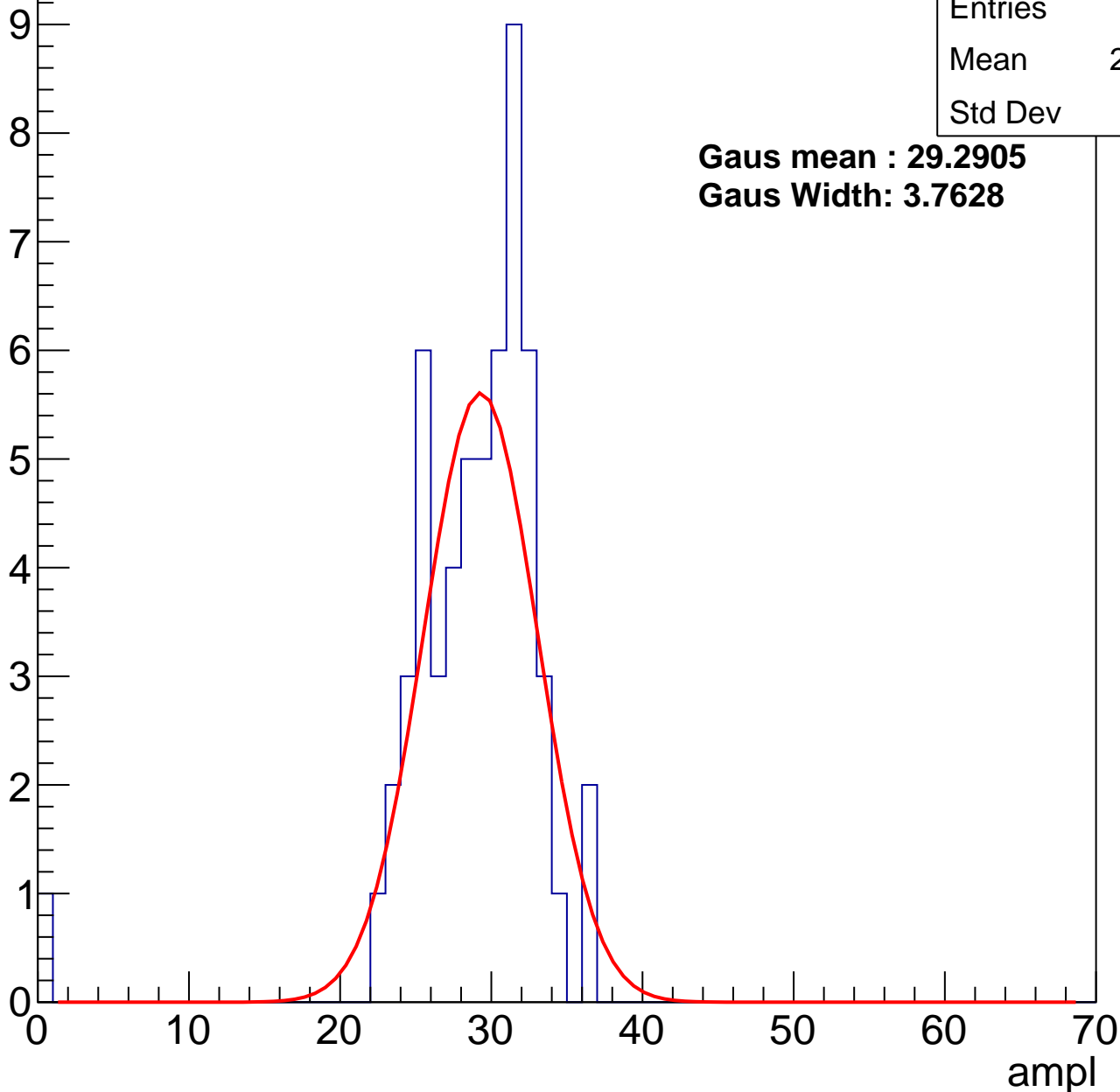
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.37
Std Dev	5

Gaus mean : 29.2905

Gaus Width: 3.7628



B1L101S, U17-ch44, adc1

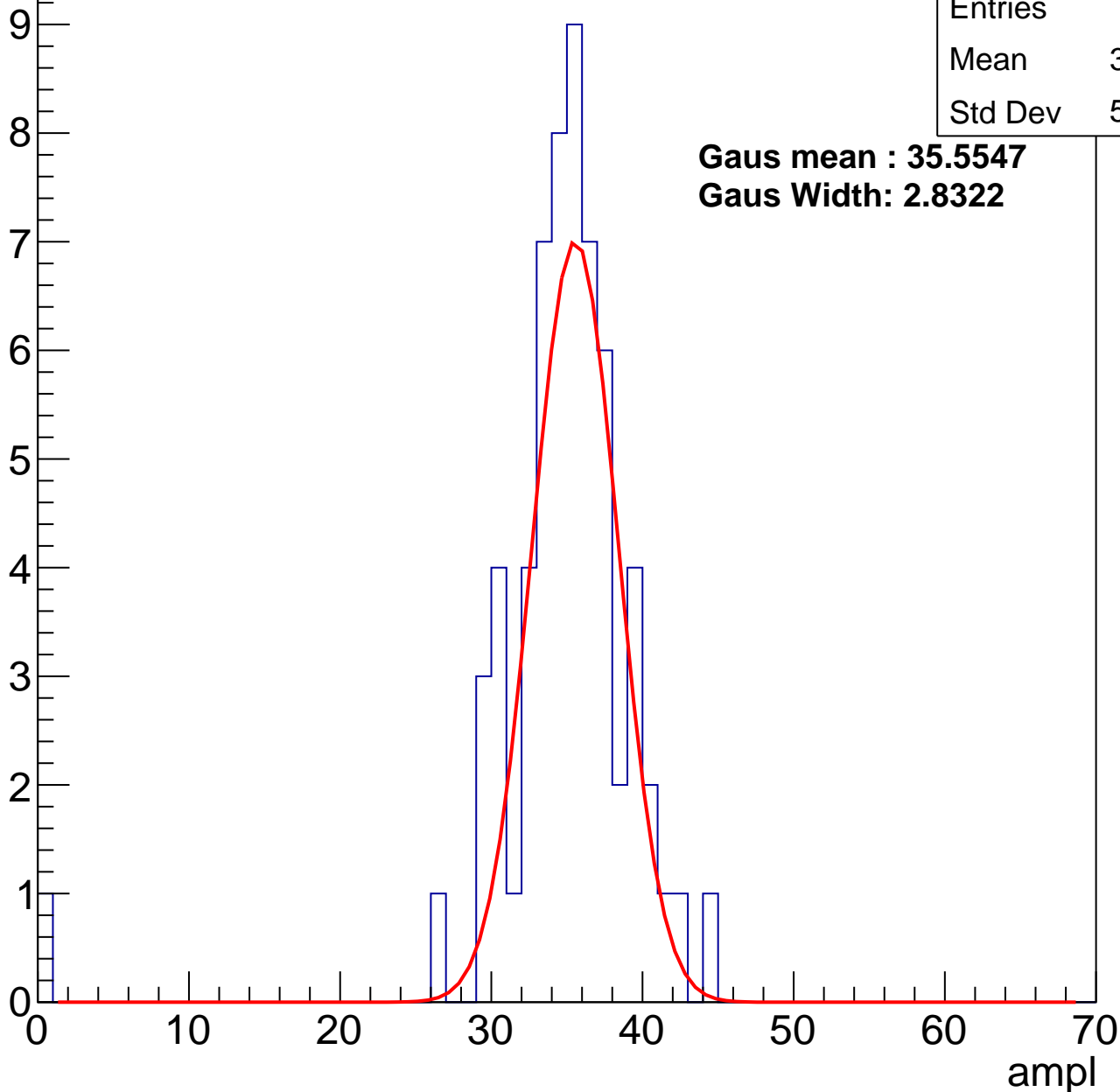
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	34.24
Std Dev	5.529

Gaus mean : 35.5547

Gaus Width: 2.8322



B1L101S, U17-ch44, adc2

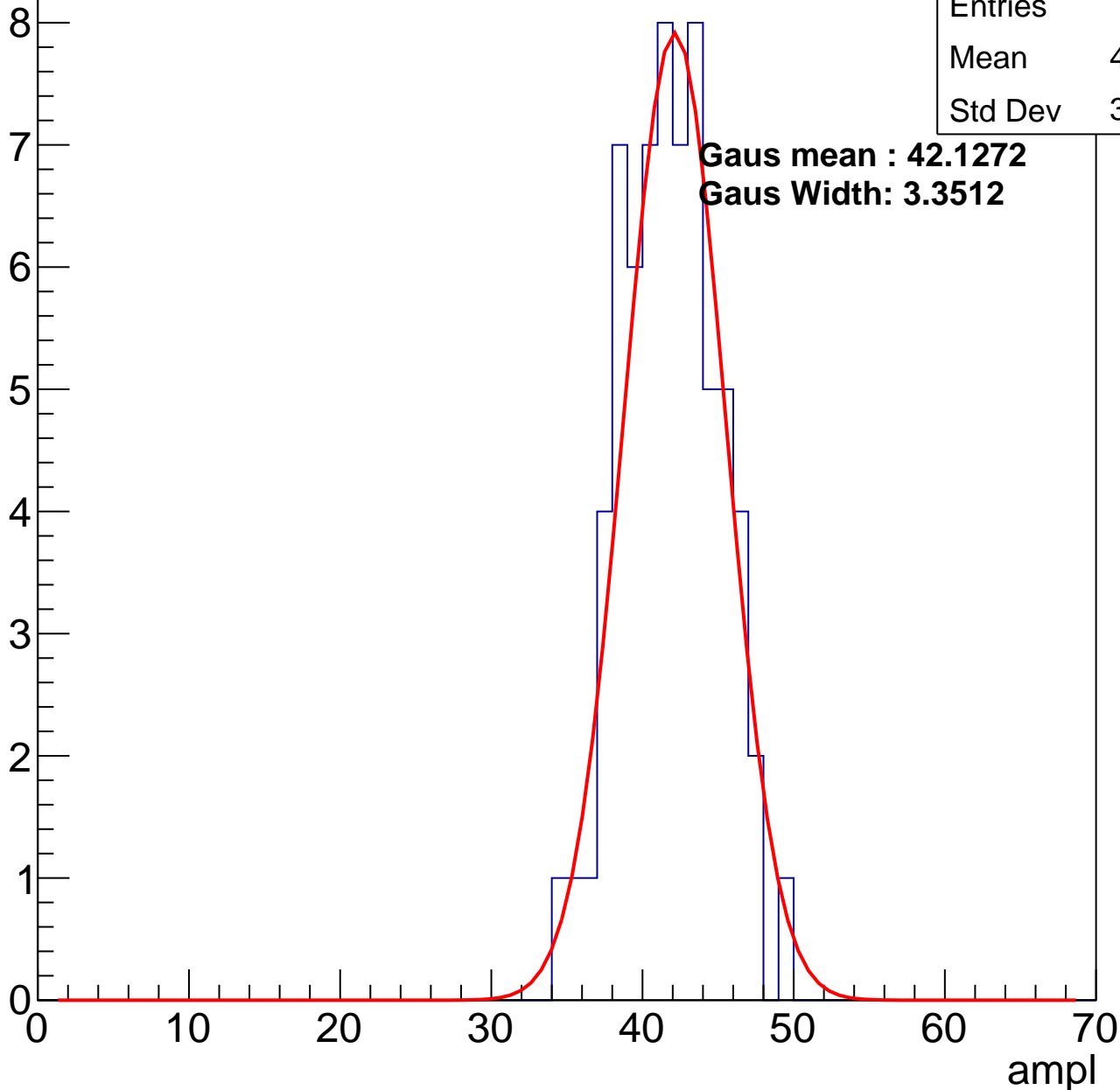
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	41.36
Std Dev	3.137

Gaus mean : 42.1272

Gaus Width: 3.3512

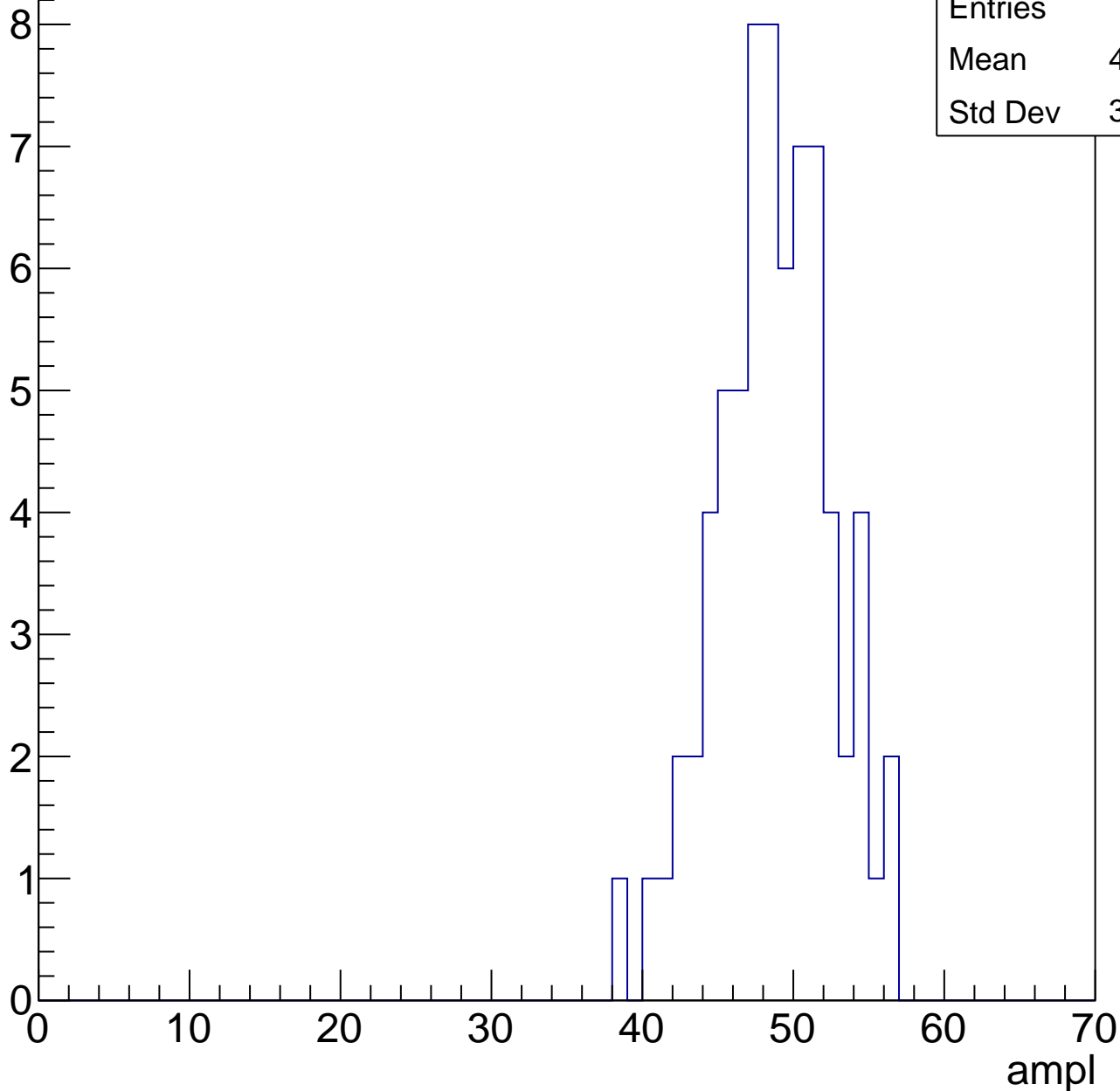


B1L101S, U17-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.26
Std Dev	3.778

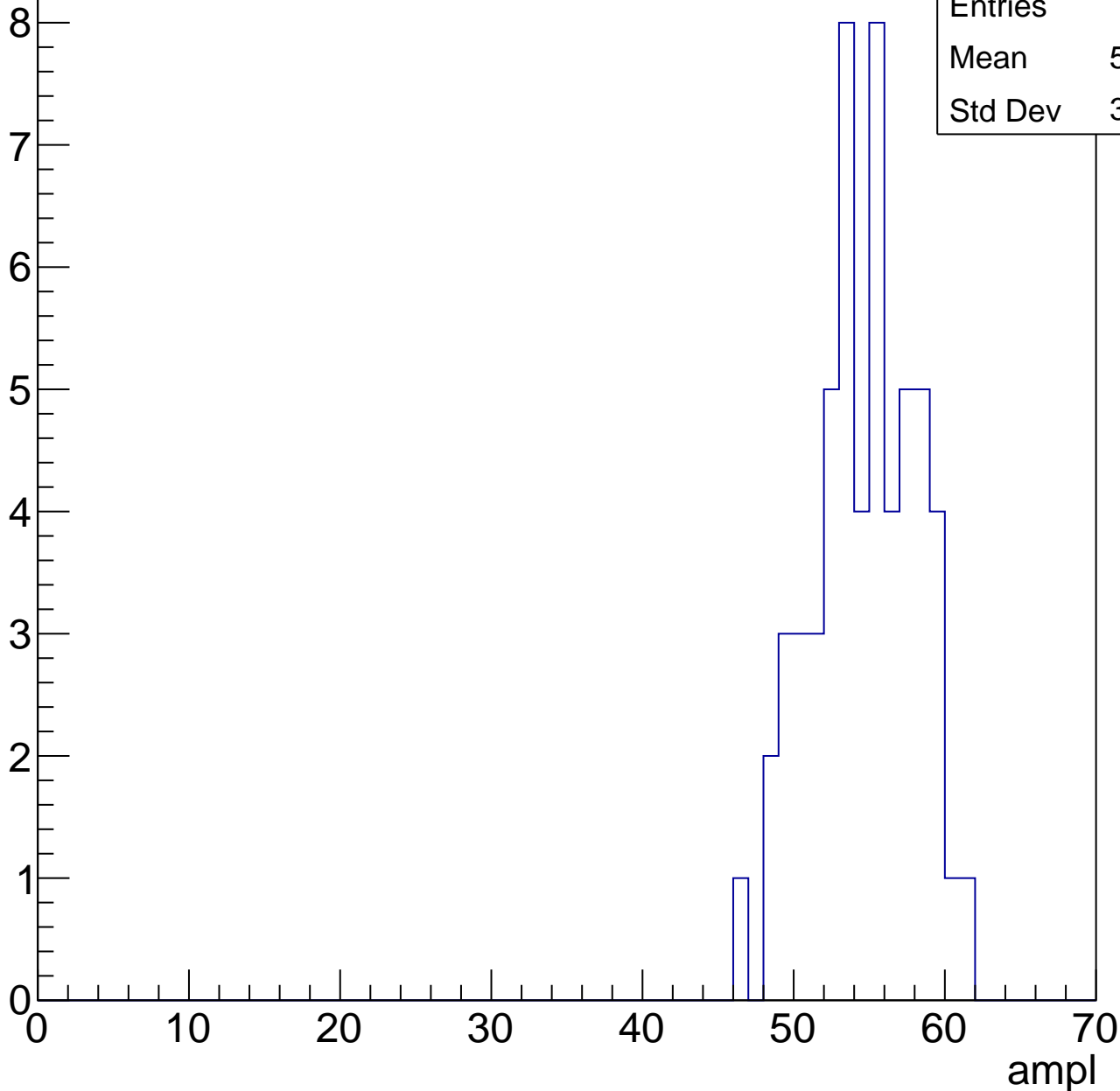


B1L101S, U17-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

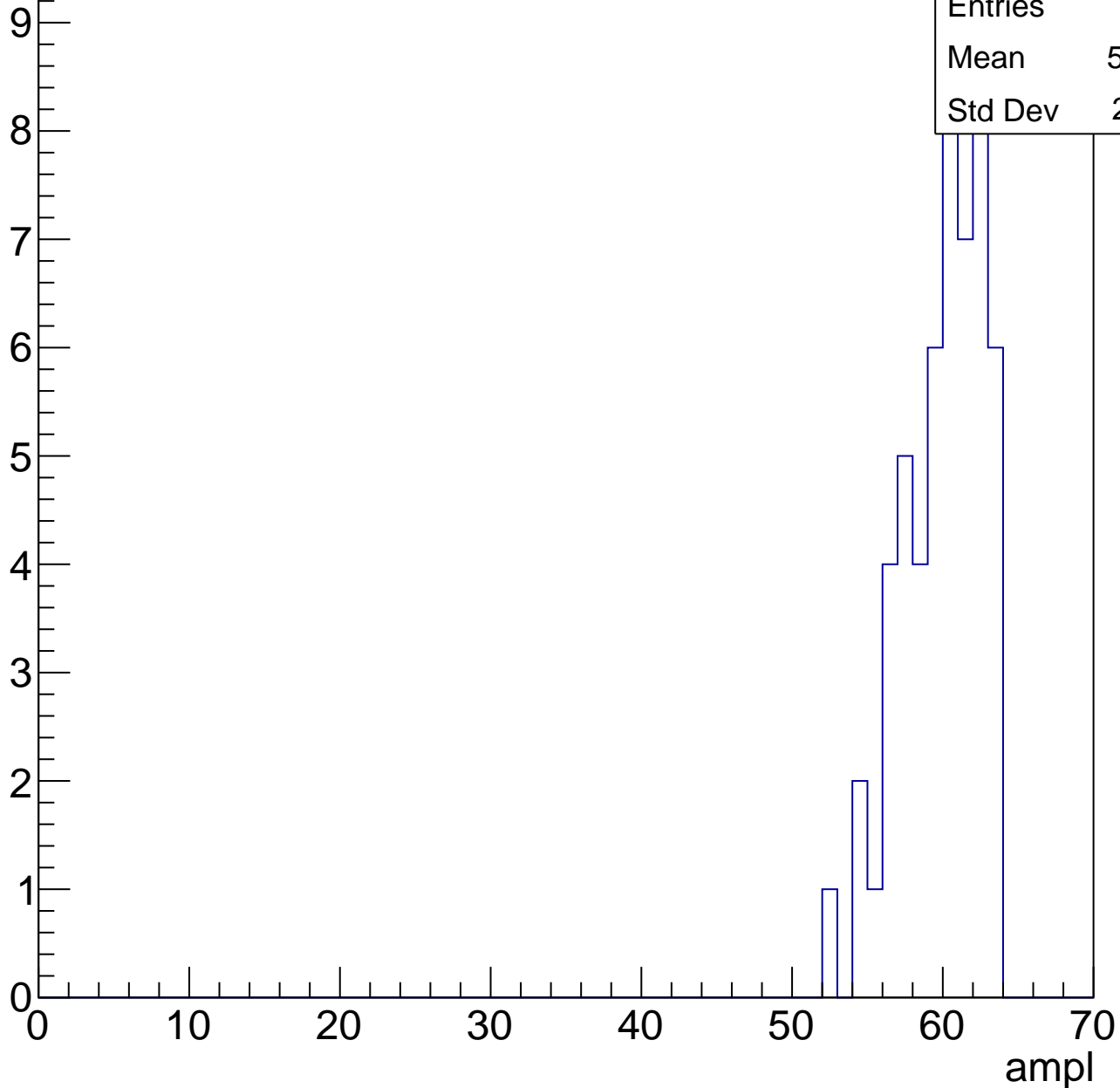
Entries	57
Mean	54.18
Std Dev	3.367



B1L101S, U17-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

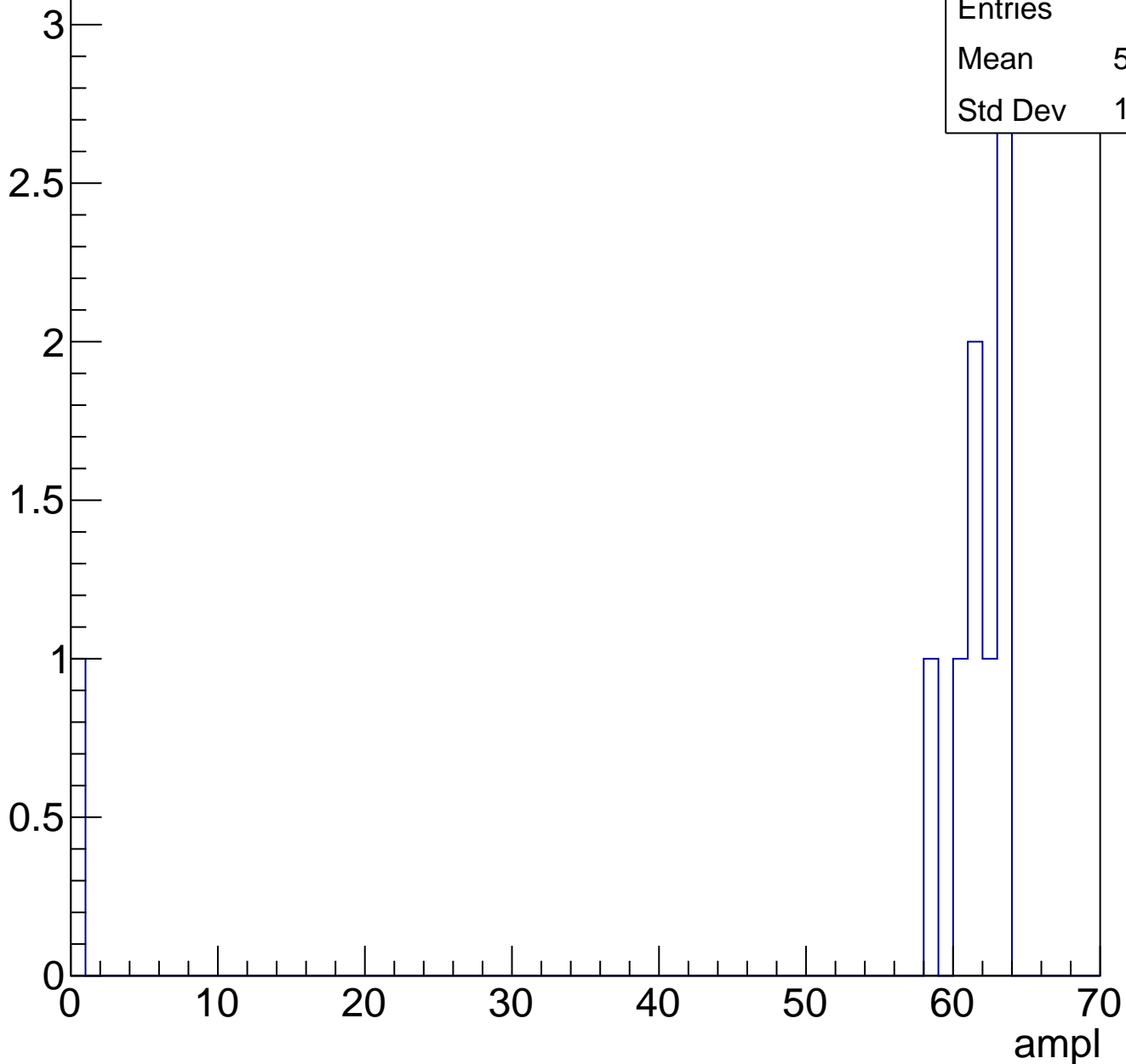
Entry



B1L101S, U17-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

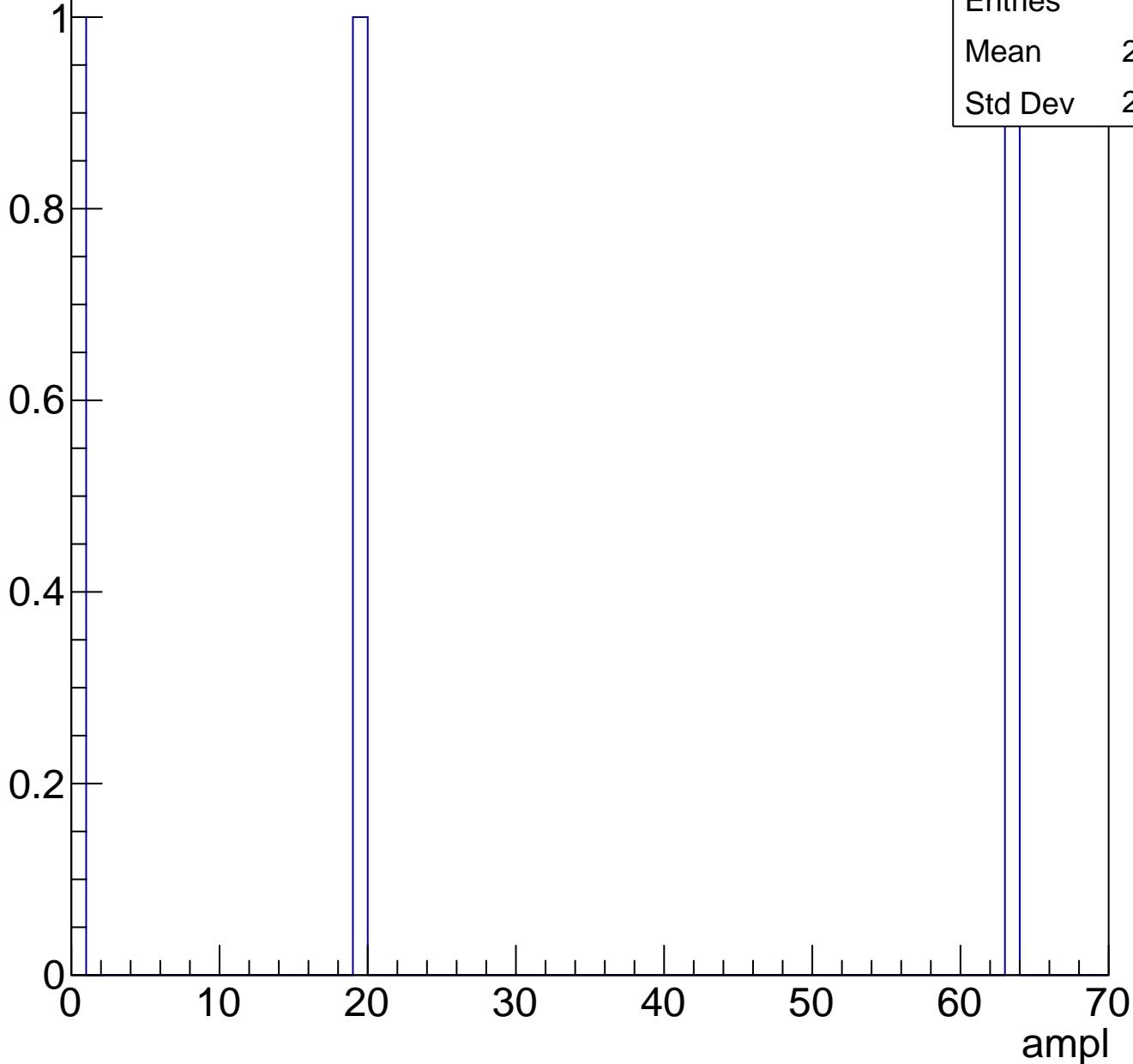
Entry



B1L101S, U17-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L101S, U17-ch45, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	29.19
Std Dev	4.872

Gaus mean : 29.8818

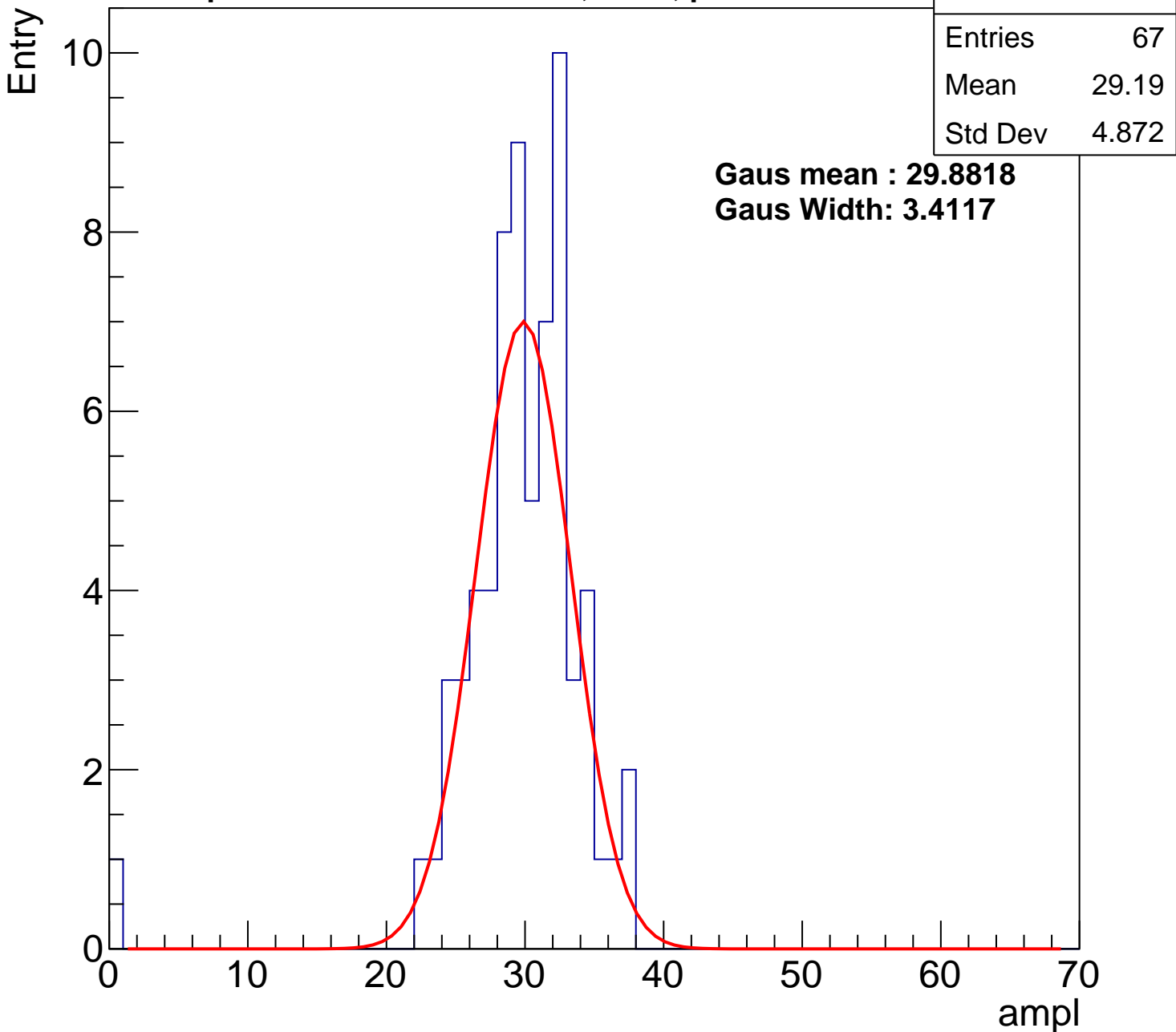
Gaus Width: 3.4117

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch45, adc1

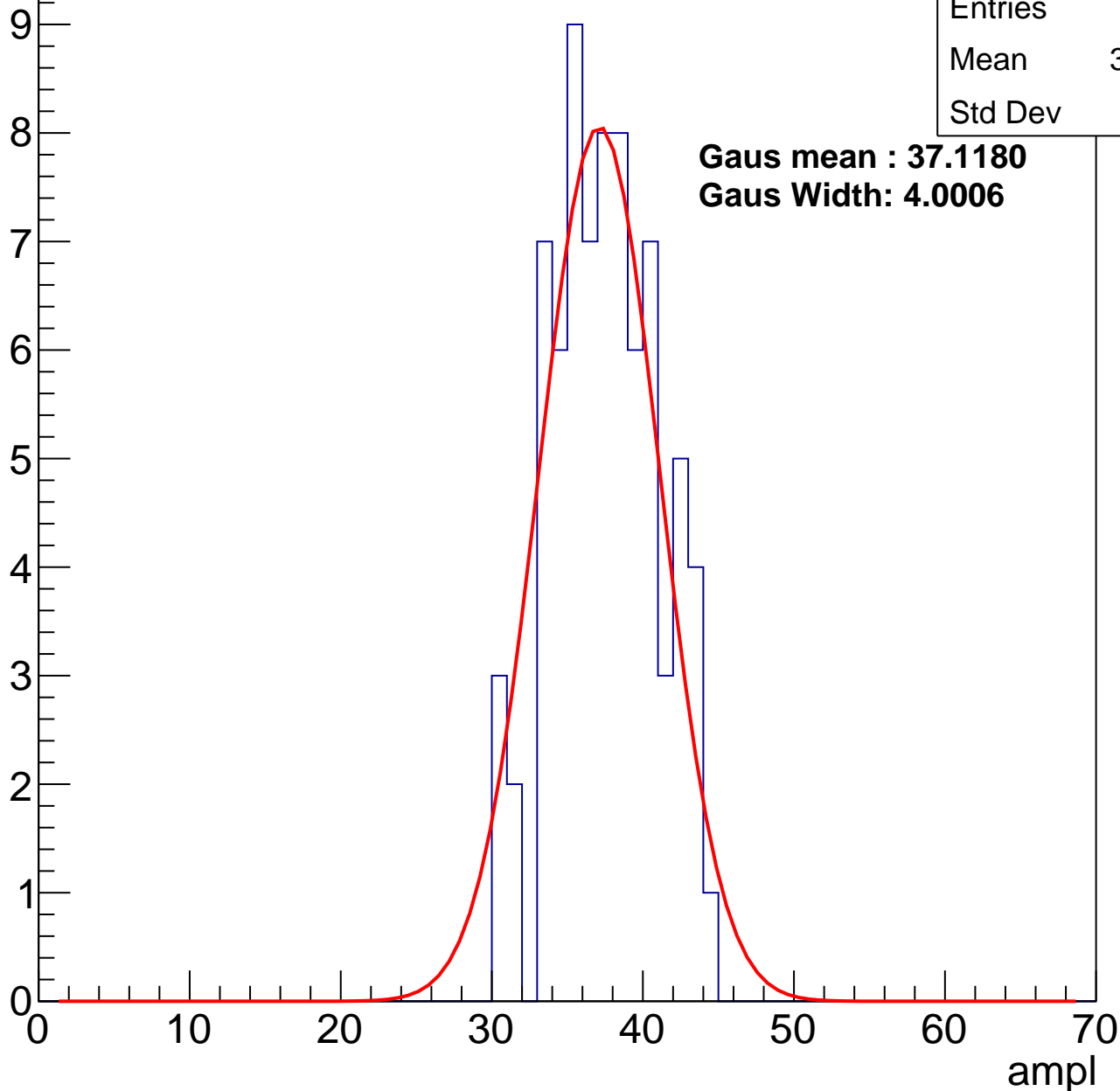
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.07
Std Dev	3.42

Gaus mean : 37.1180

Gaus Width: 4.0006



B1L101S, U17-ch45, adc2

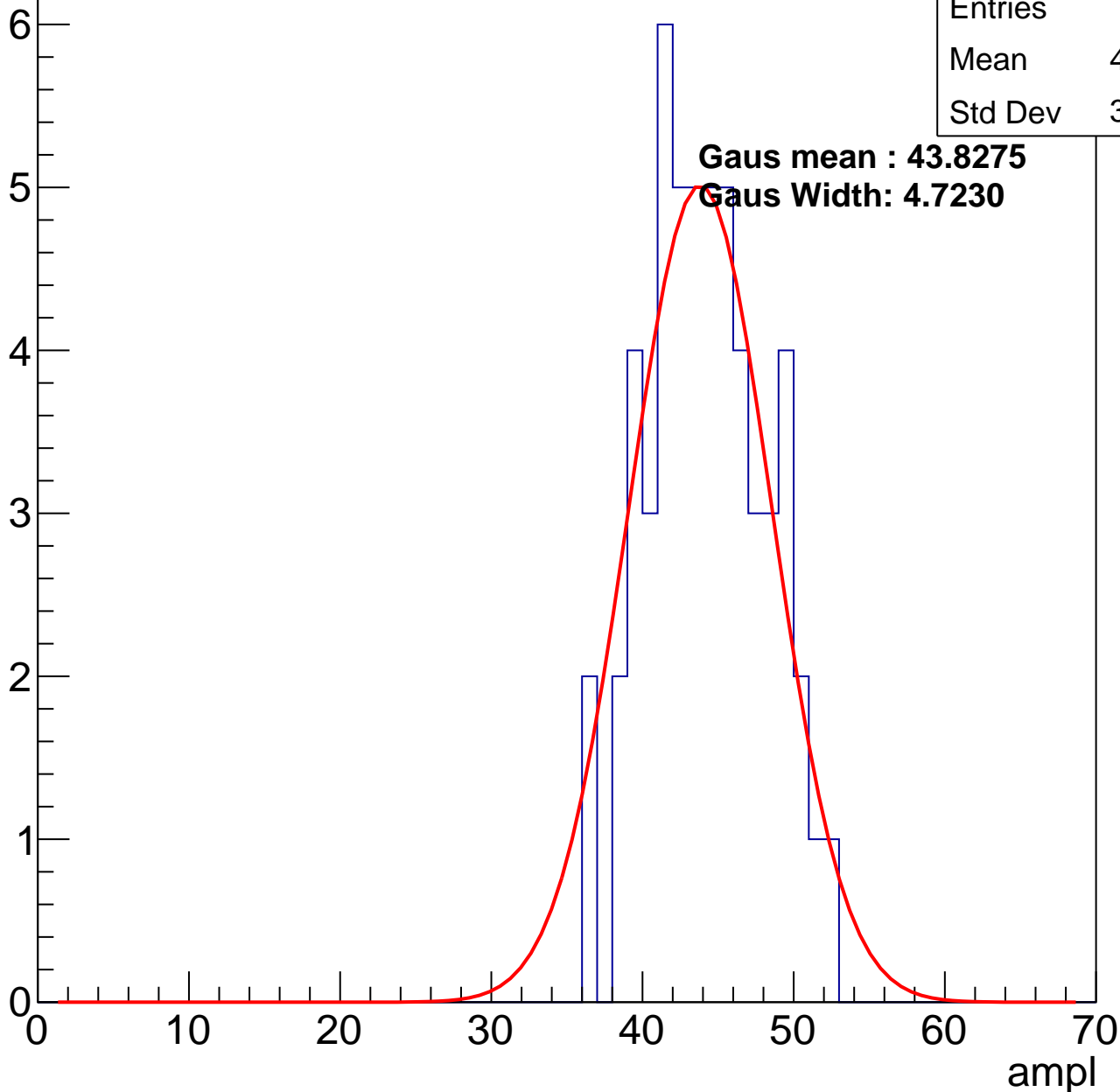
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.78
Std Dev	3.827

Gaus mean : 43.8275

Gaus Width: 4.7230

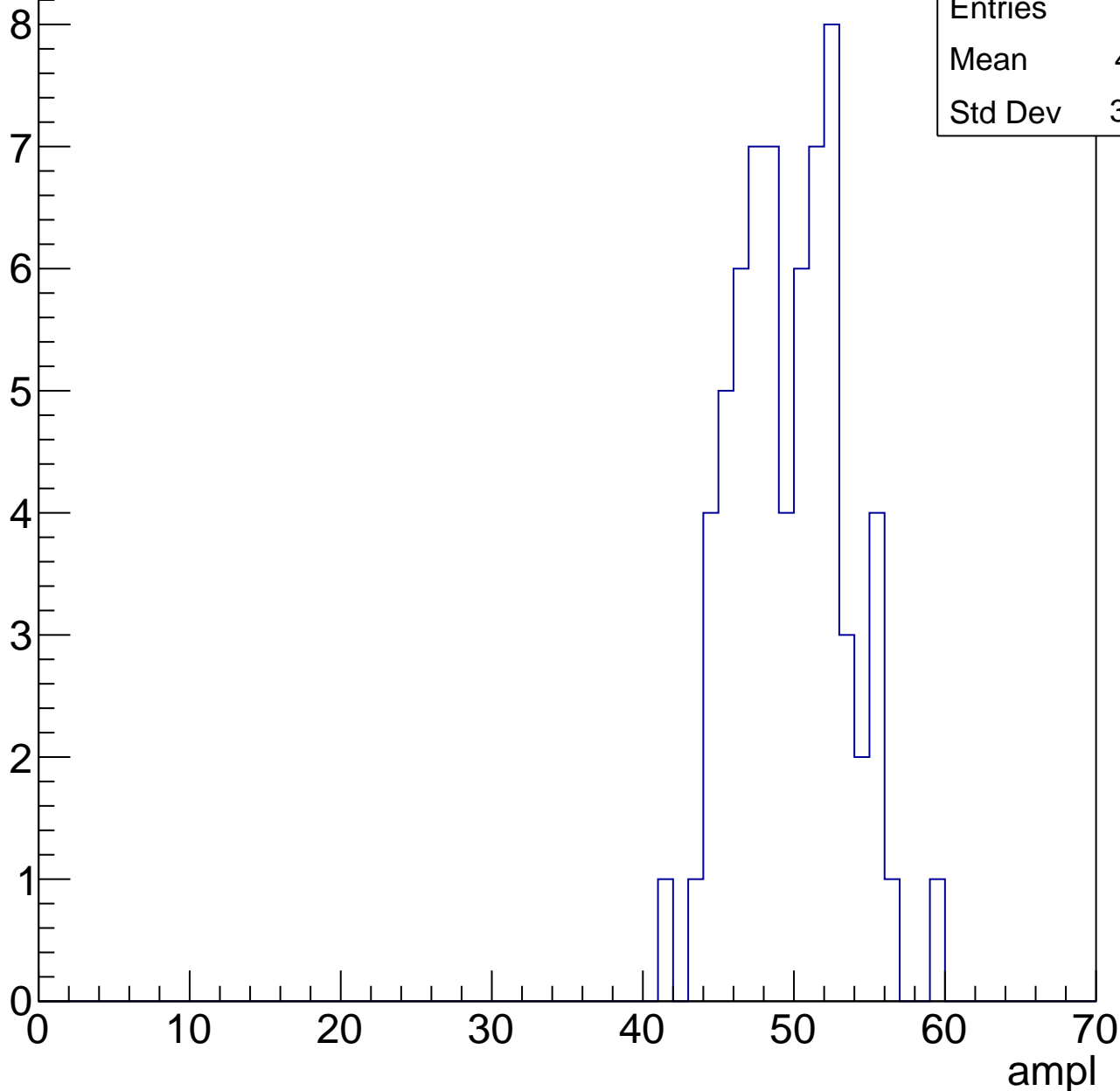


B1L101S, U17-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.21
Std Dev	3.589

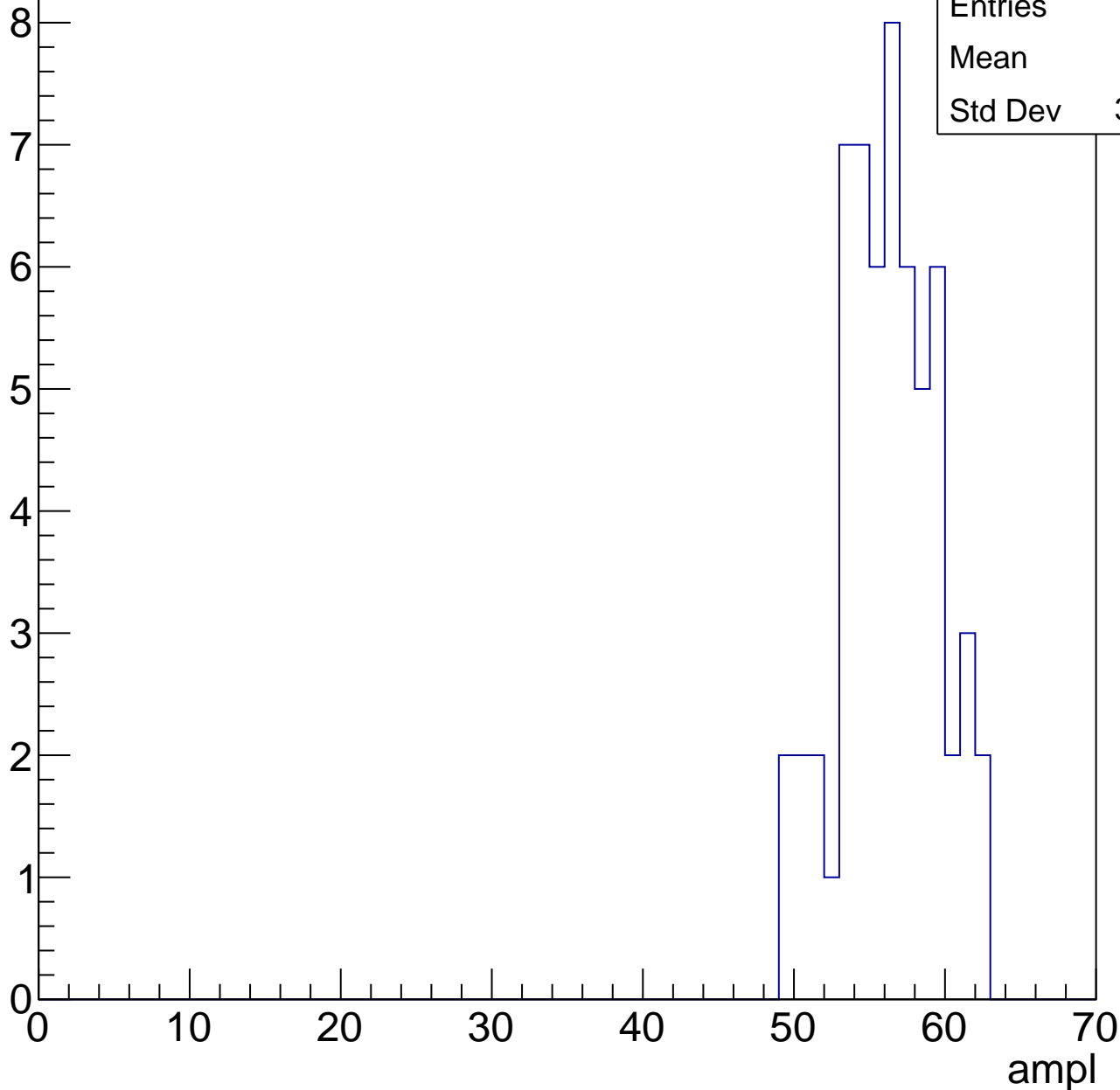


B1L101S, U17-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

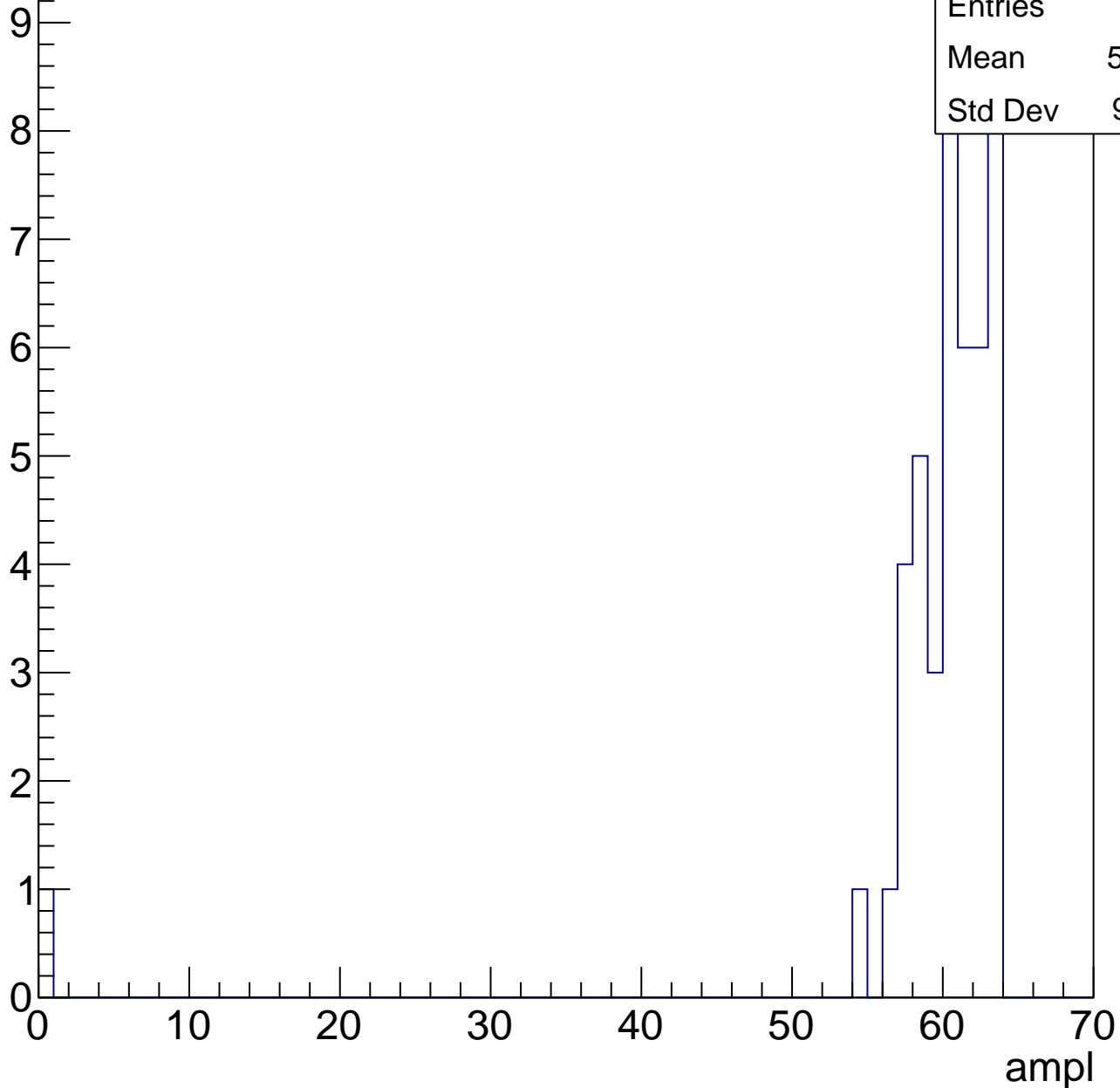
Entries	59
Mean	55.8
Std Dev	3.161



B1L101S, U17-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch46, adc0

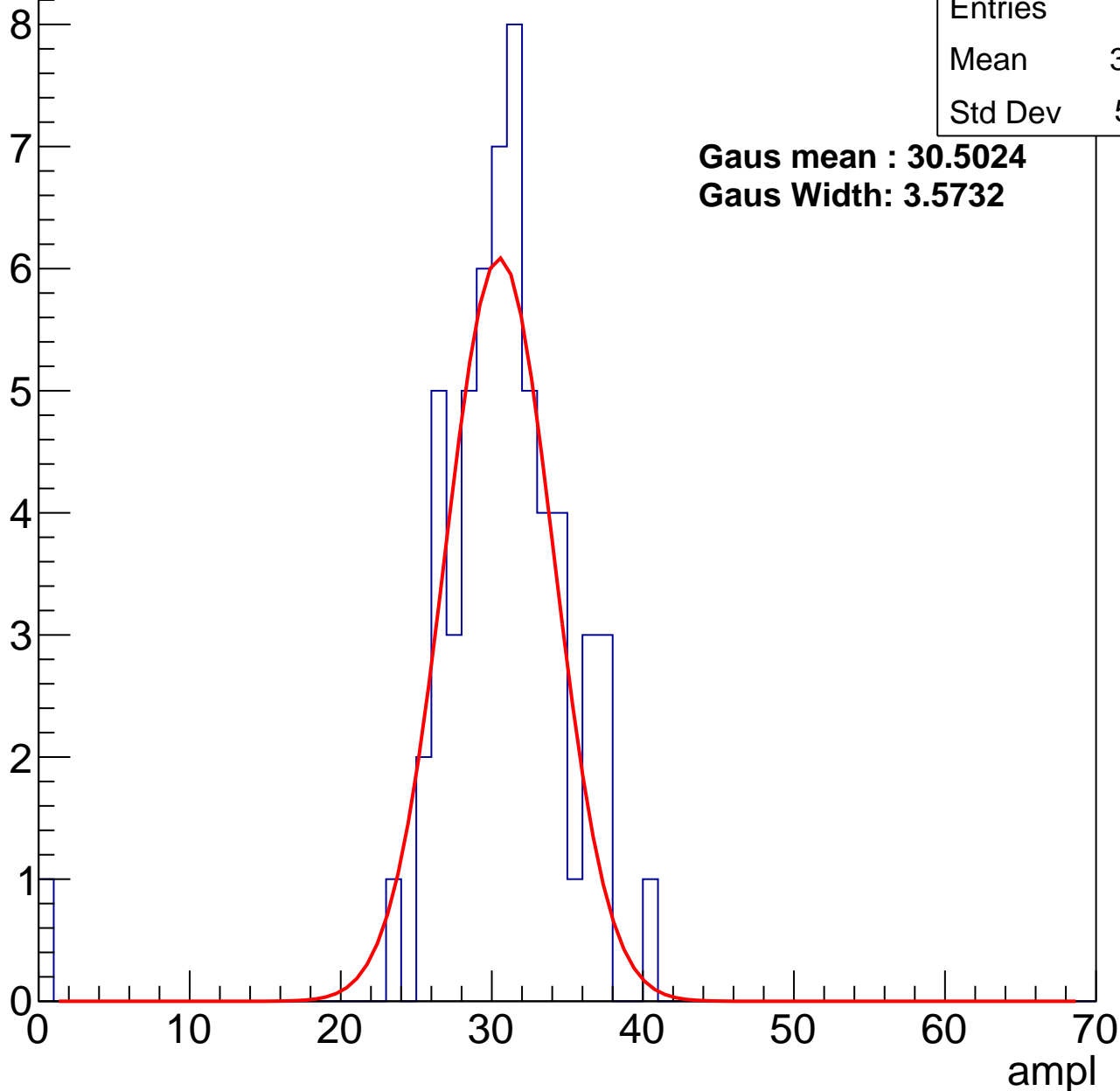
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	30.14
Std Dev	5.271

Gaus mean : 30.5024

Gaus Width: 3.5732



B1L101S, U17-ch46, adc1

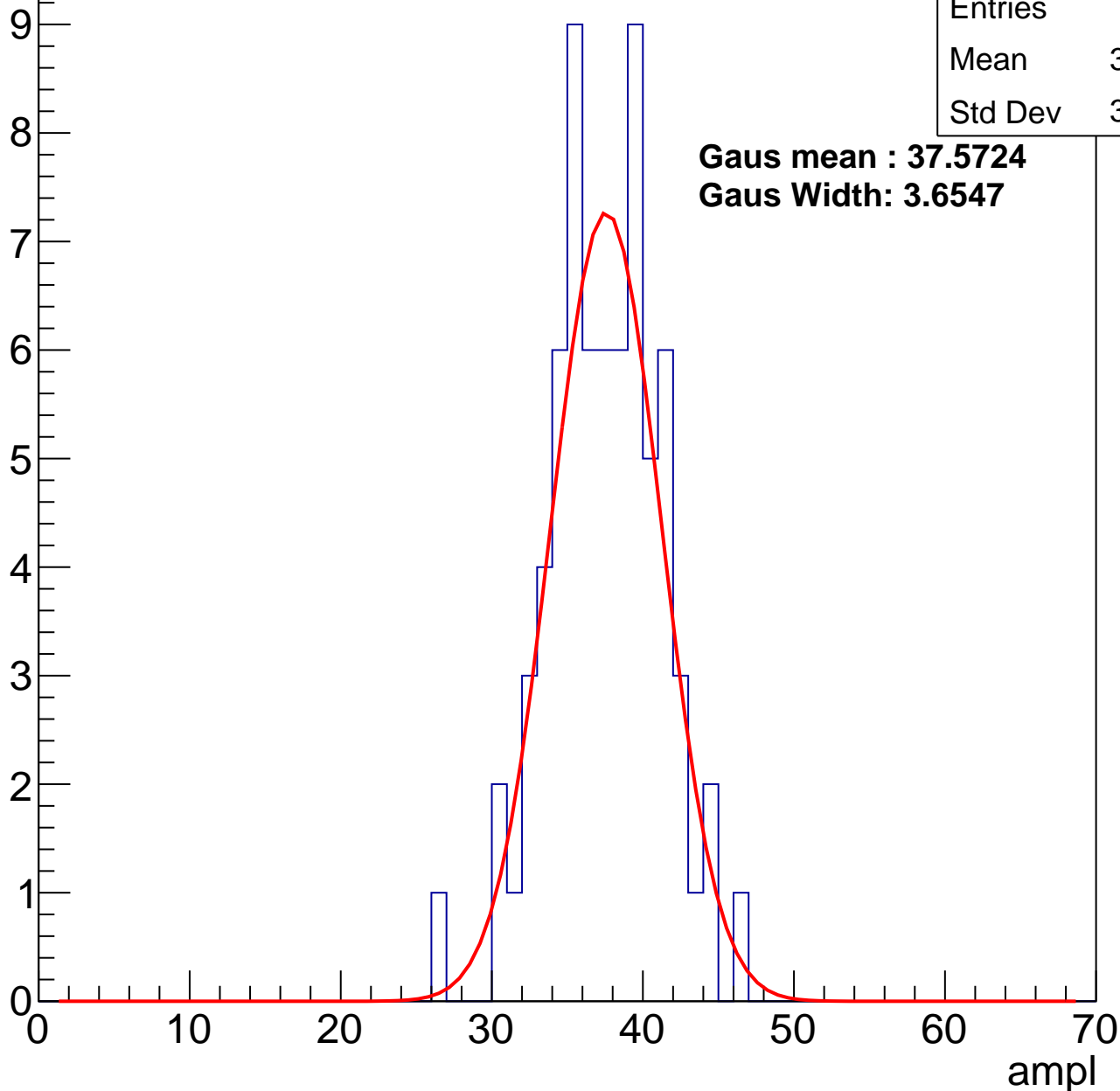
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.04
Std Dev	3.683

Gaus mean : 37.5724

Gaus Width: 3.6547



B1L101S, U17-ch46, adc2

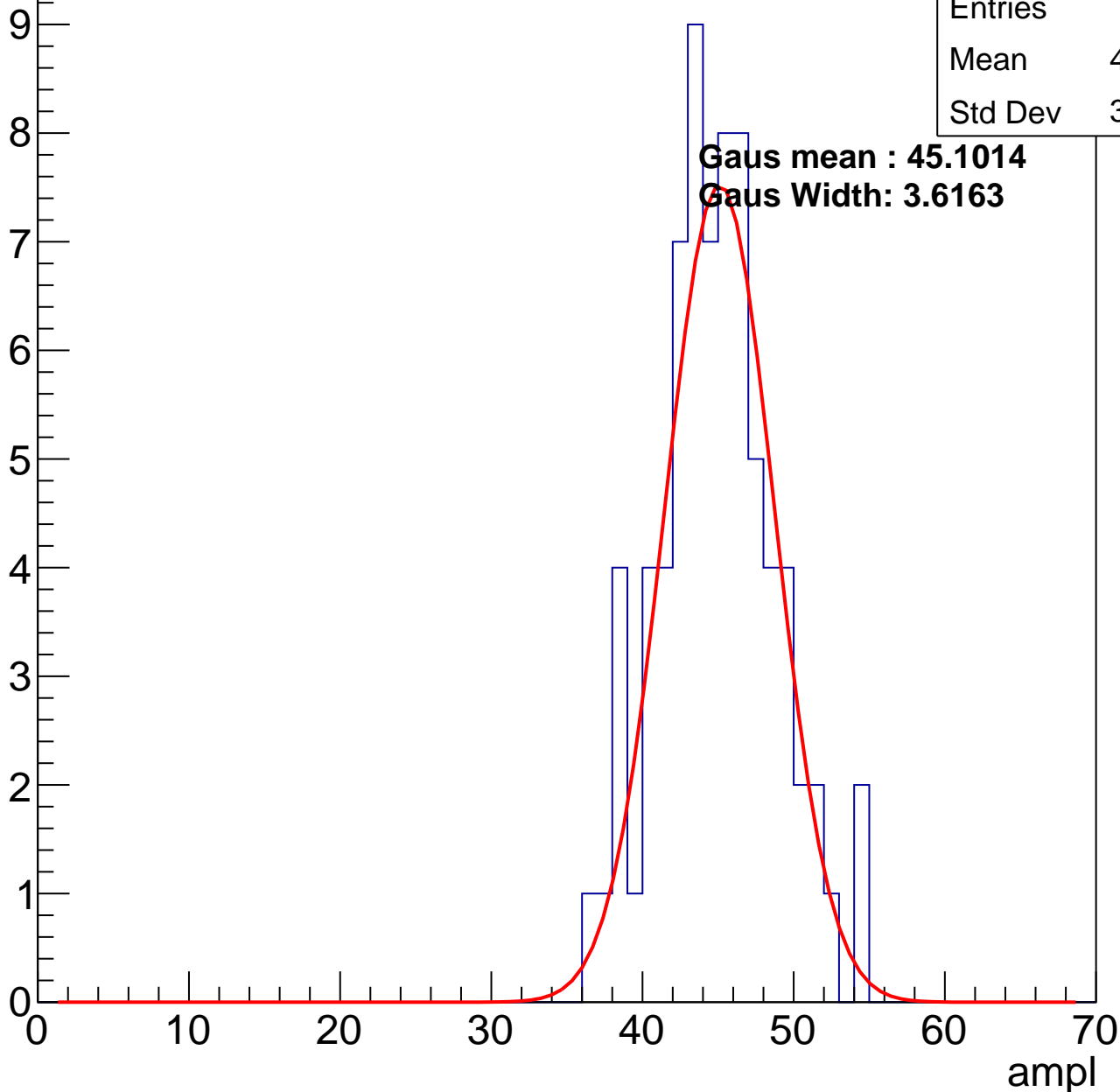
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.46
Std Dev	3.832

Gaus mean : 45.1014

Gaus Width: 3.6163

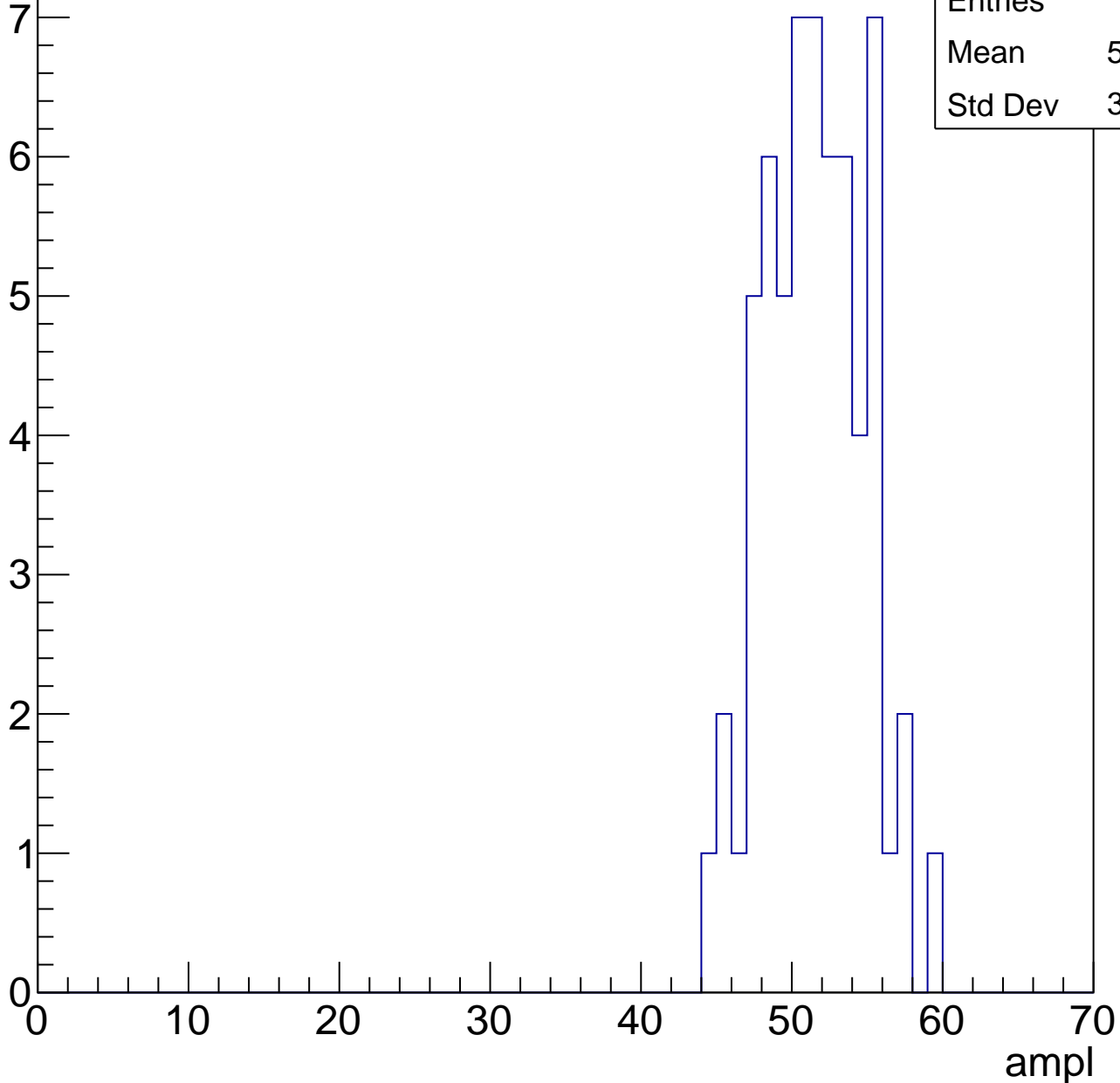


B1L101S, U17-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	51.07
Std Dev	3.254

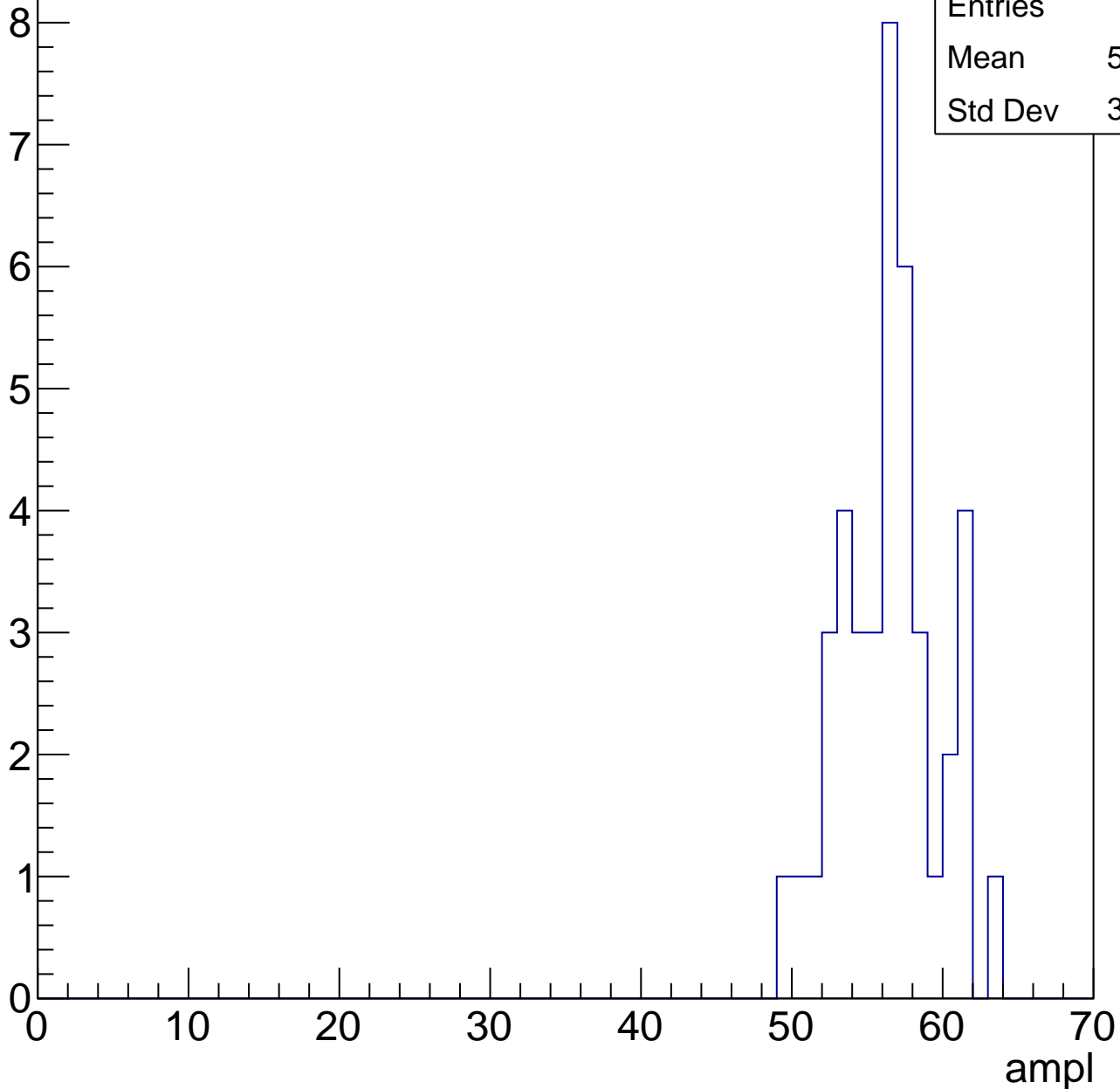


B1L101S, U17-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	55.98
Std Dev	3.189

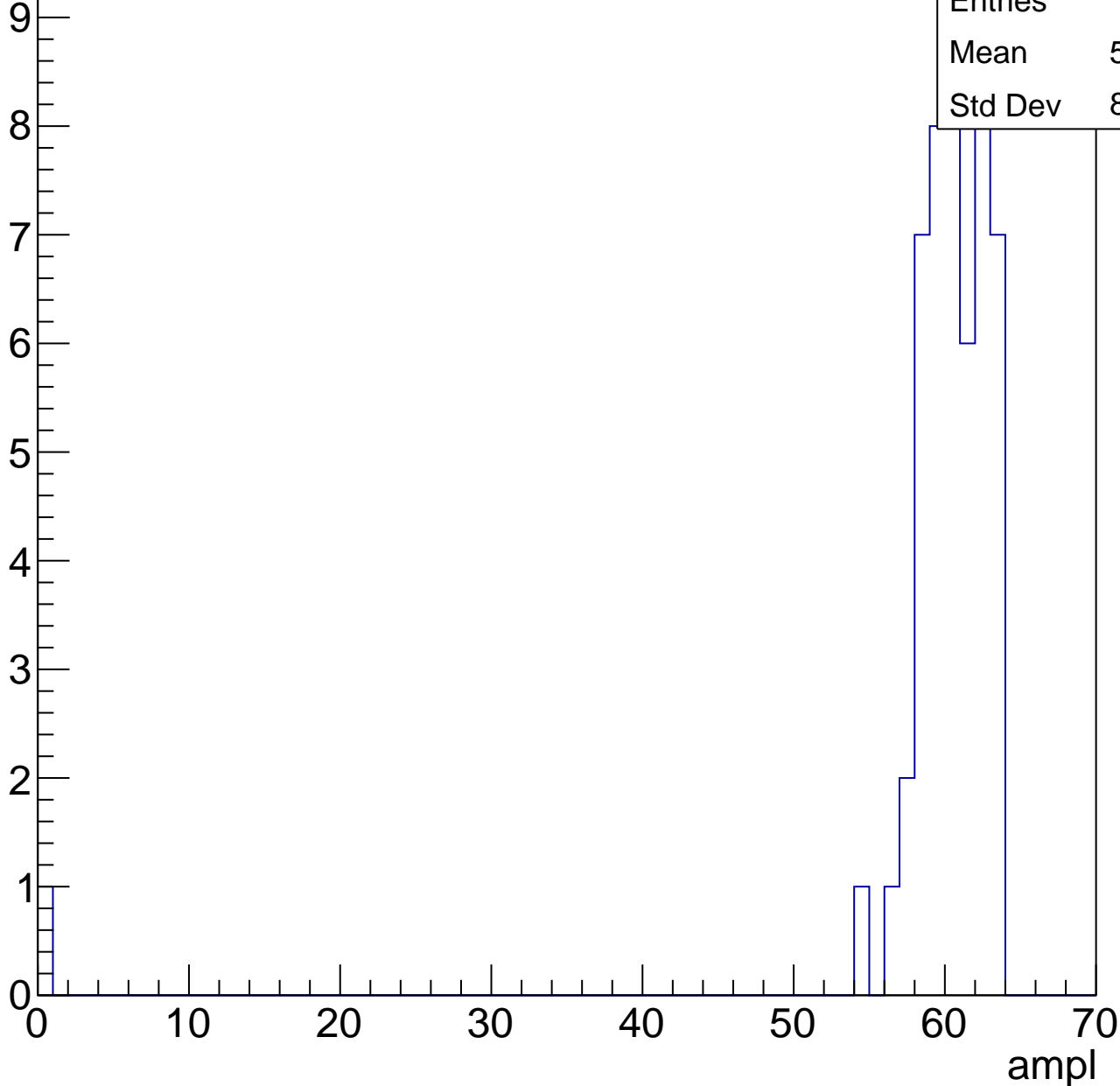


B1L101S, U17-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.94
Std Dev	8.666



B1L101S, U17-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U17-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch47, adc0

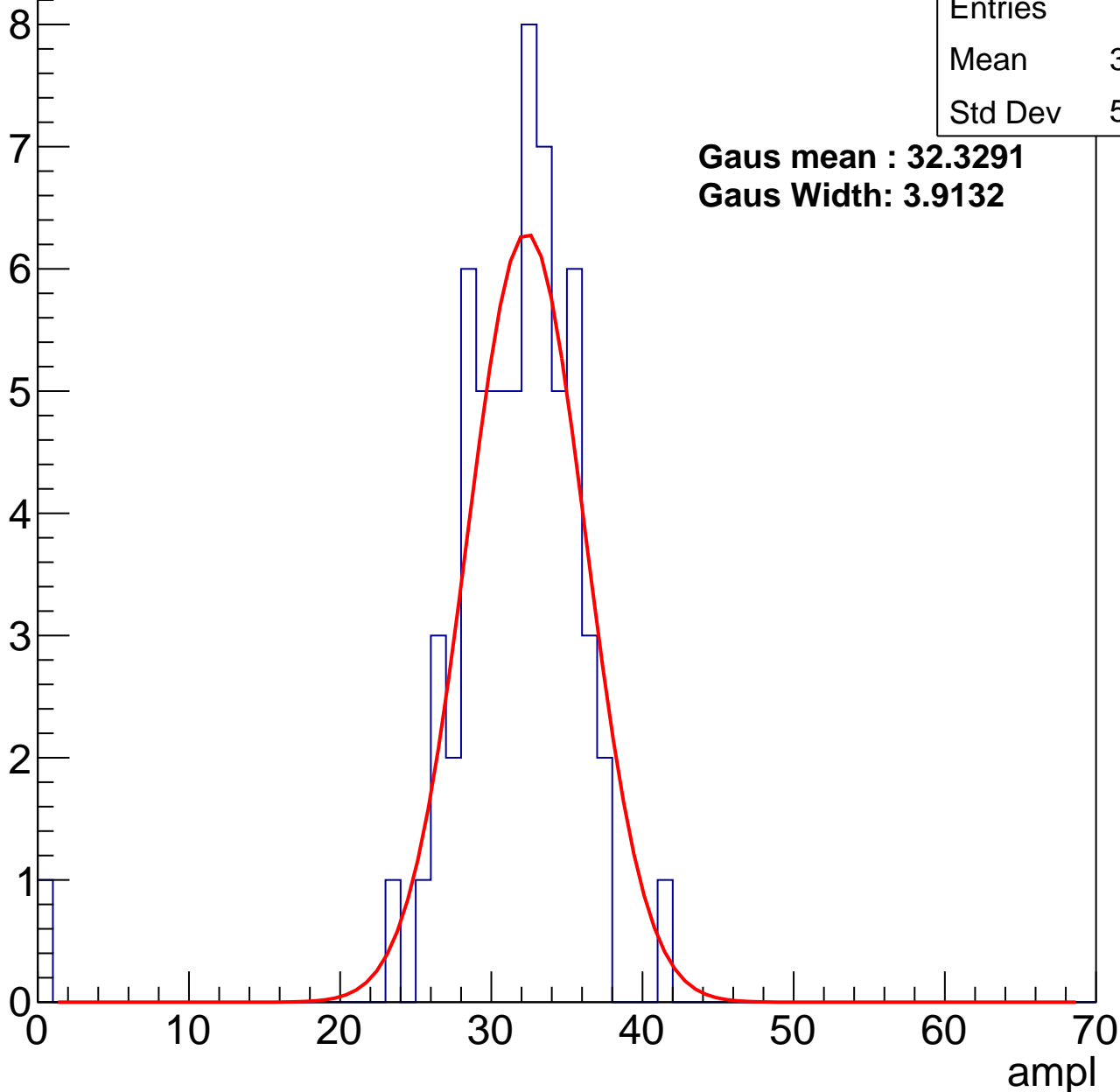
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	30.95
Std Dev	5.234

Gaus mean : 32.3291

Gaus Width: 3.9132



B1L101S, U17-ch47, adc1

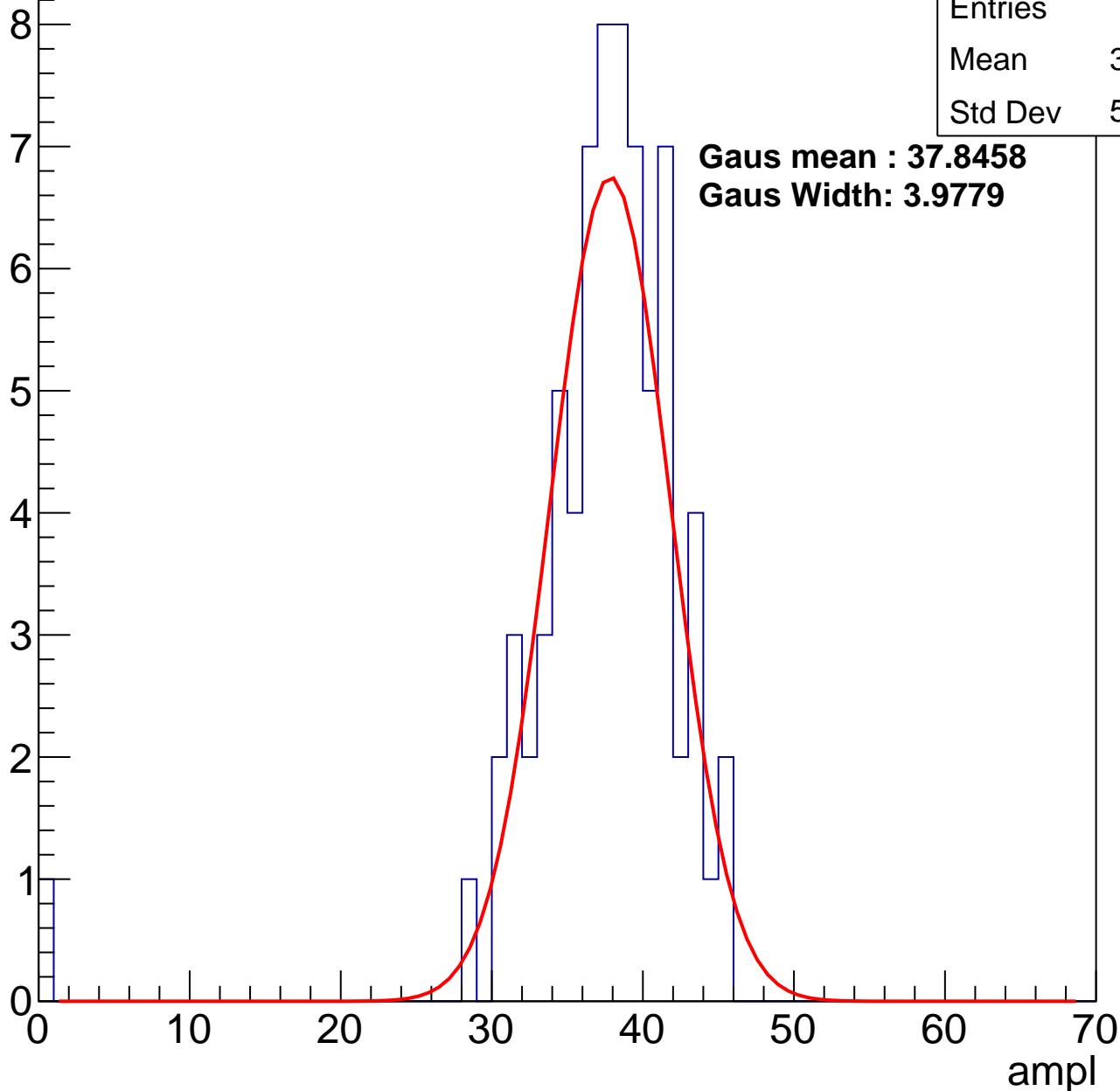
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.89
Std Dev	5.756

Gaus mean : 37.8458

Gaus Width: 3.9779



B1L101S, U17-ch47, adc2

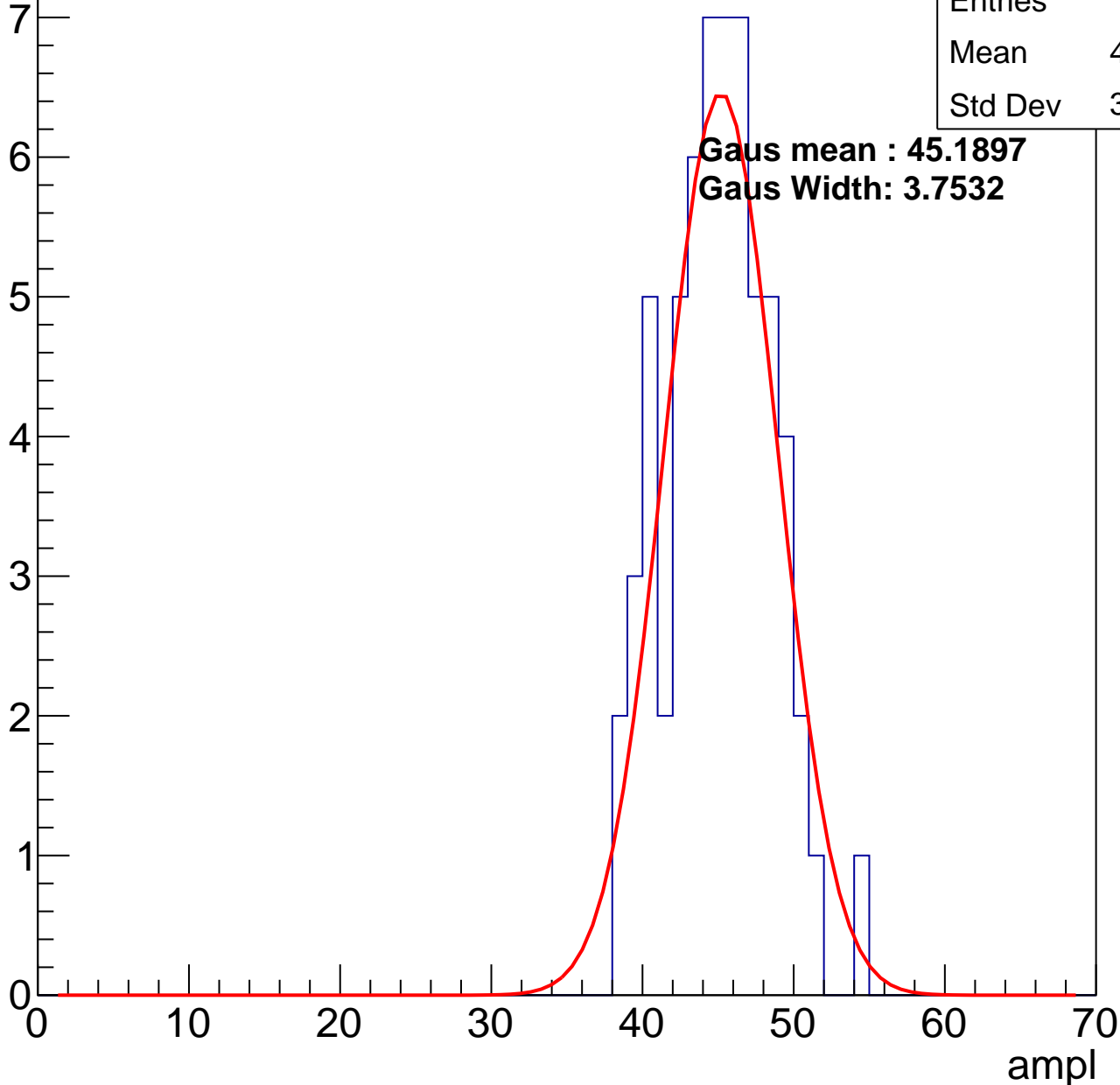
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.58
Std Dev	3.434

Gaus mean : 45.1897

Gaus Width: 3.7532

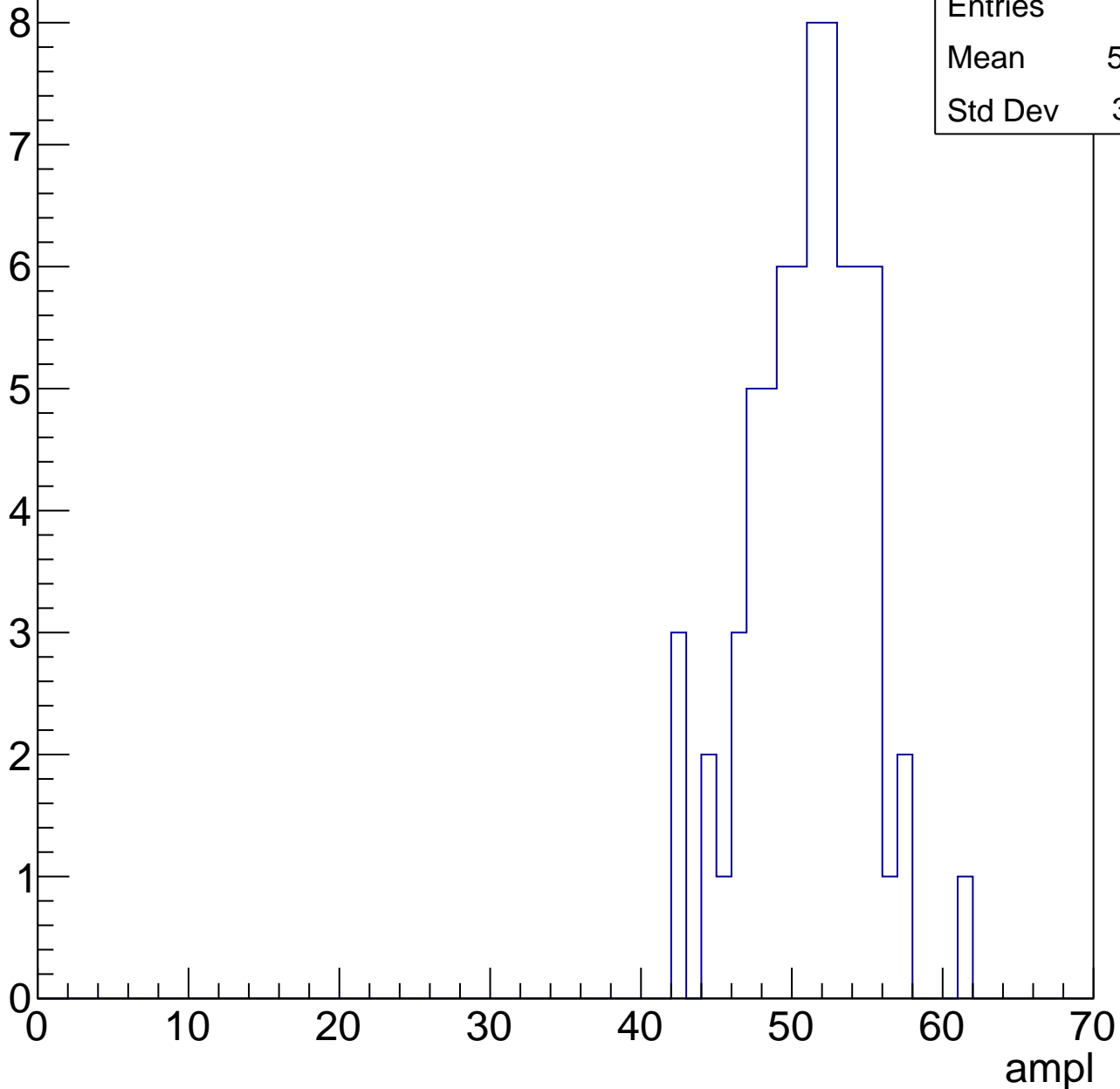


B1L101S, U17-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.62
Std Dev	3.761

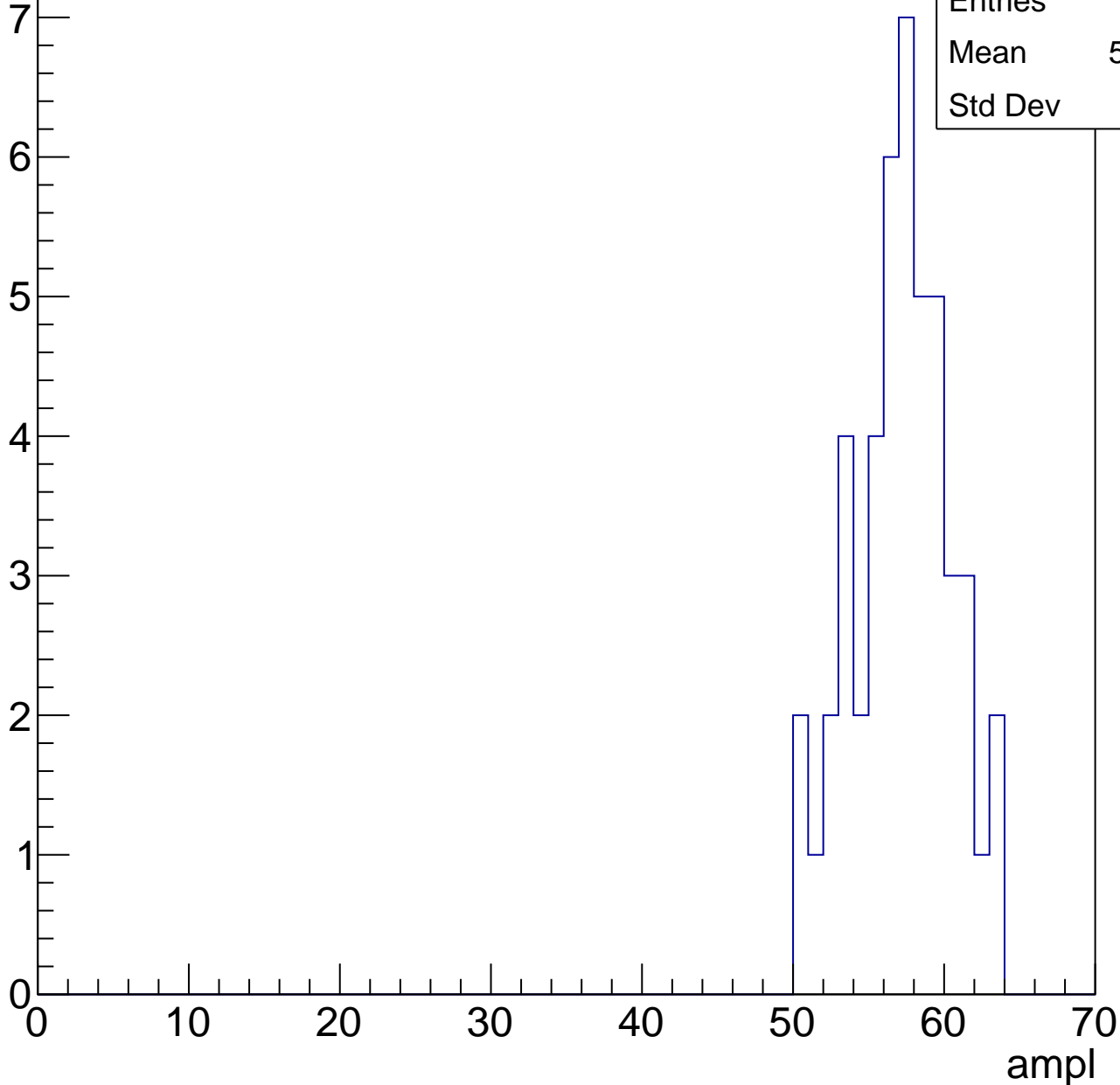


B1L101S, U17-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56.72
Std Dev	3.2

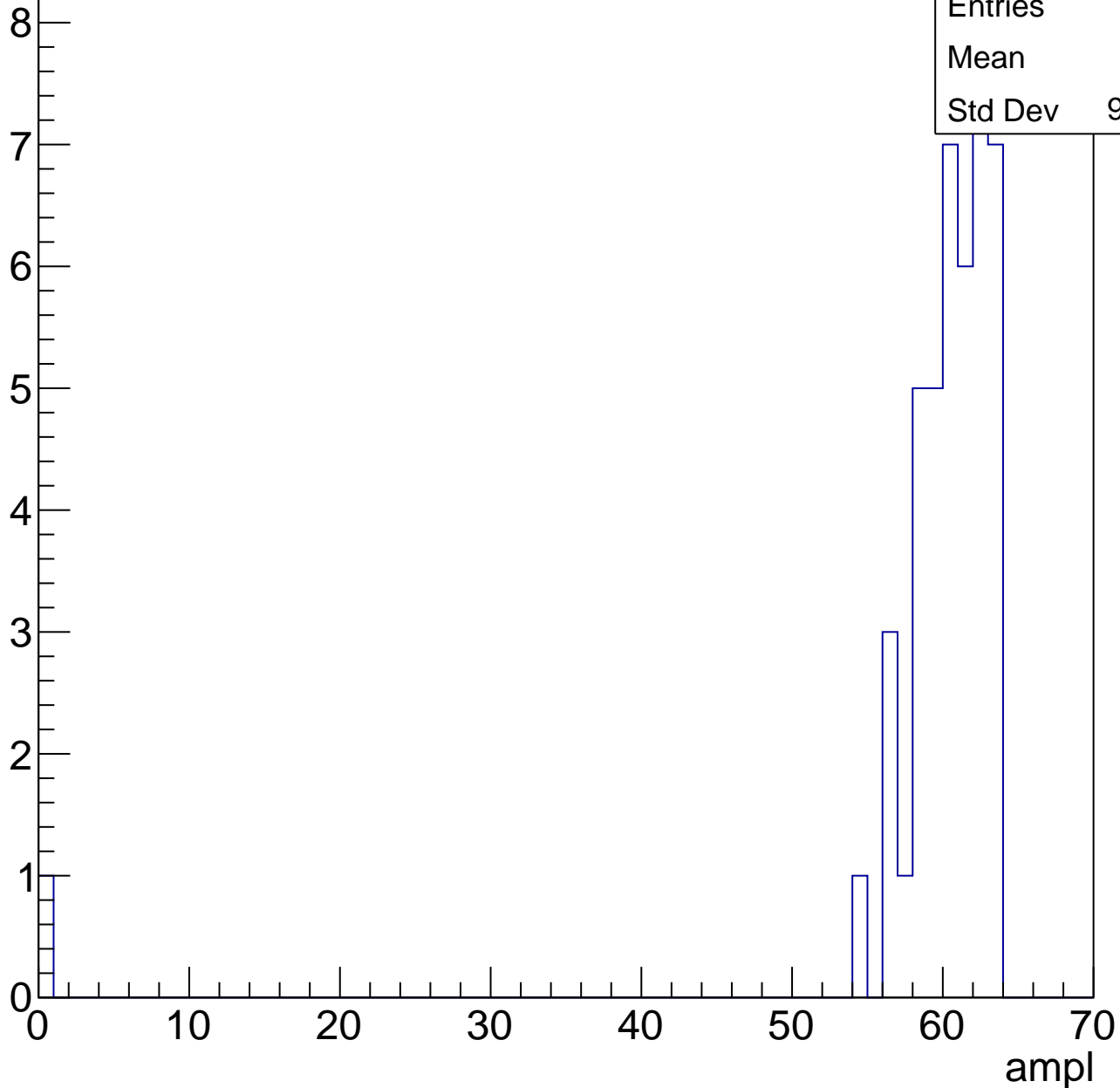


B1L101S, U17-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.8
Std Dev	9.238



B1L101S, U17-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch48, adc0

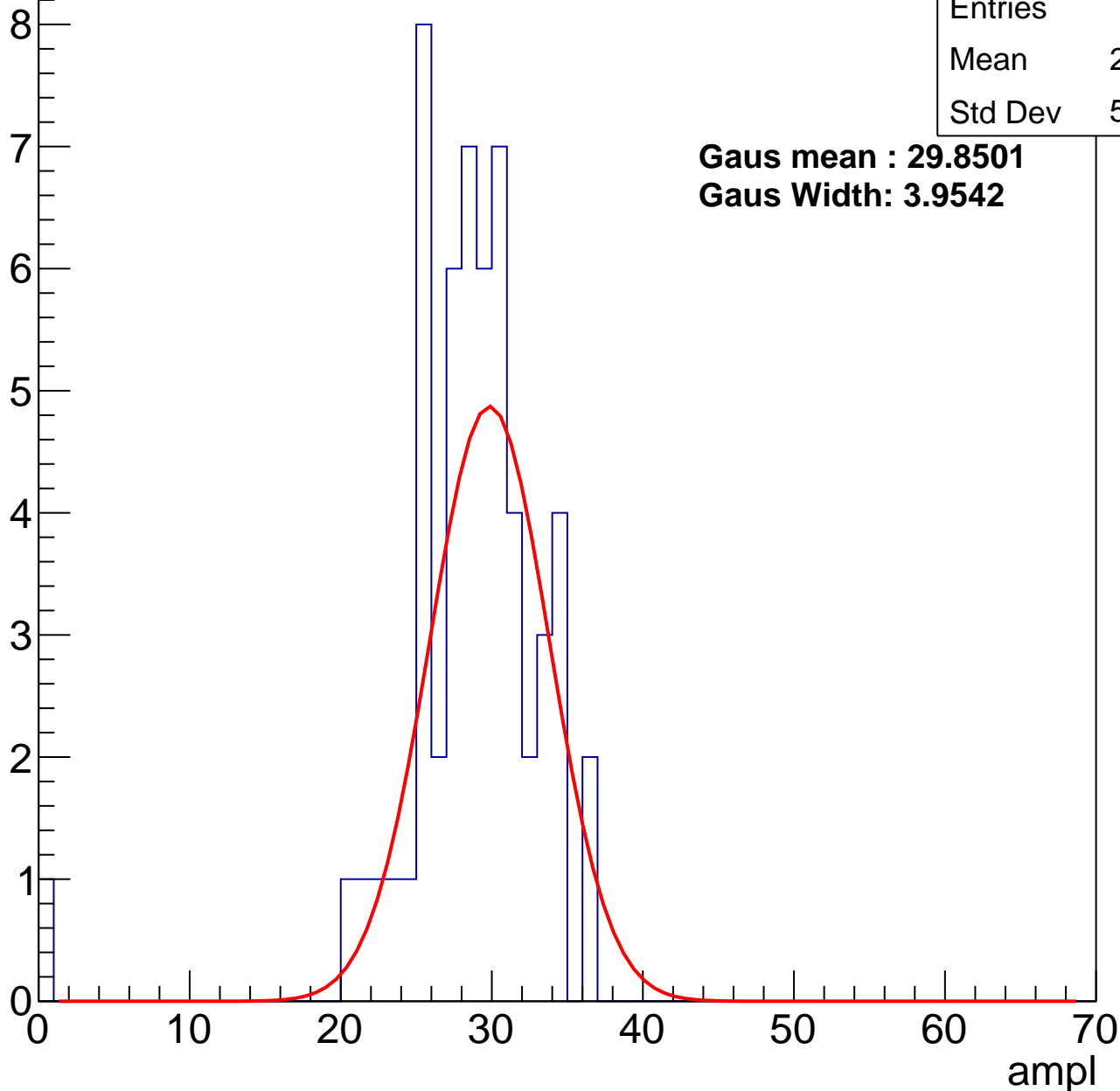
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.05
Std Dev	5.145

Gaus mean : 29.8501

Gaus Width: 3.9542



B1L101S, U17-ch48, adc1

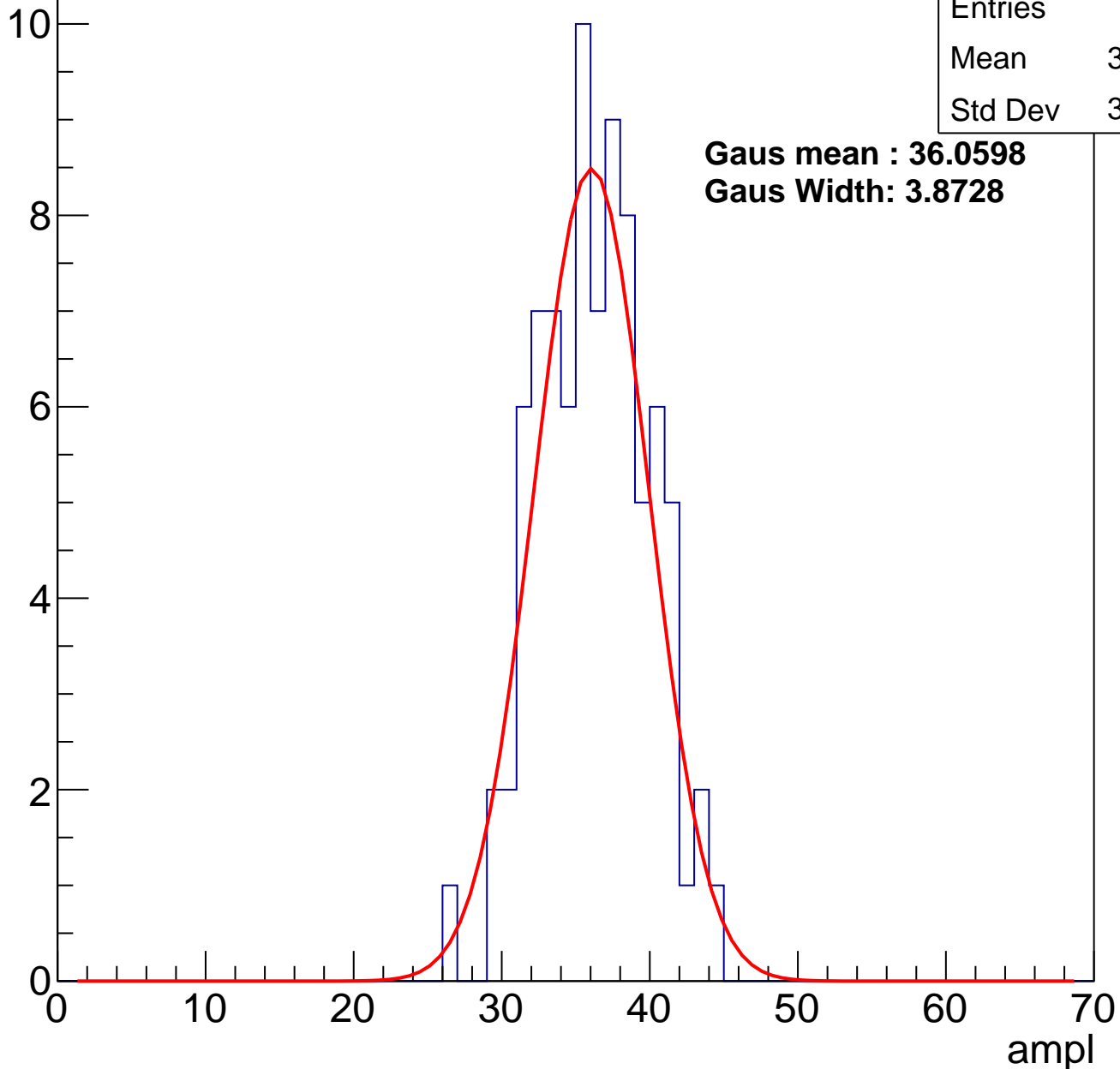
calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	35.76
Std Dev	3.645

Gaus mean : 36.0598

Gaus Width: 3.8728

Entry



B1L101S, U17-ch48, adc2

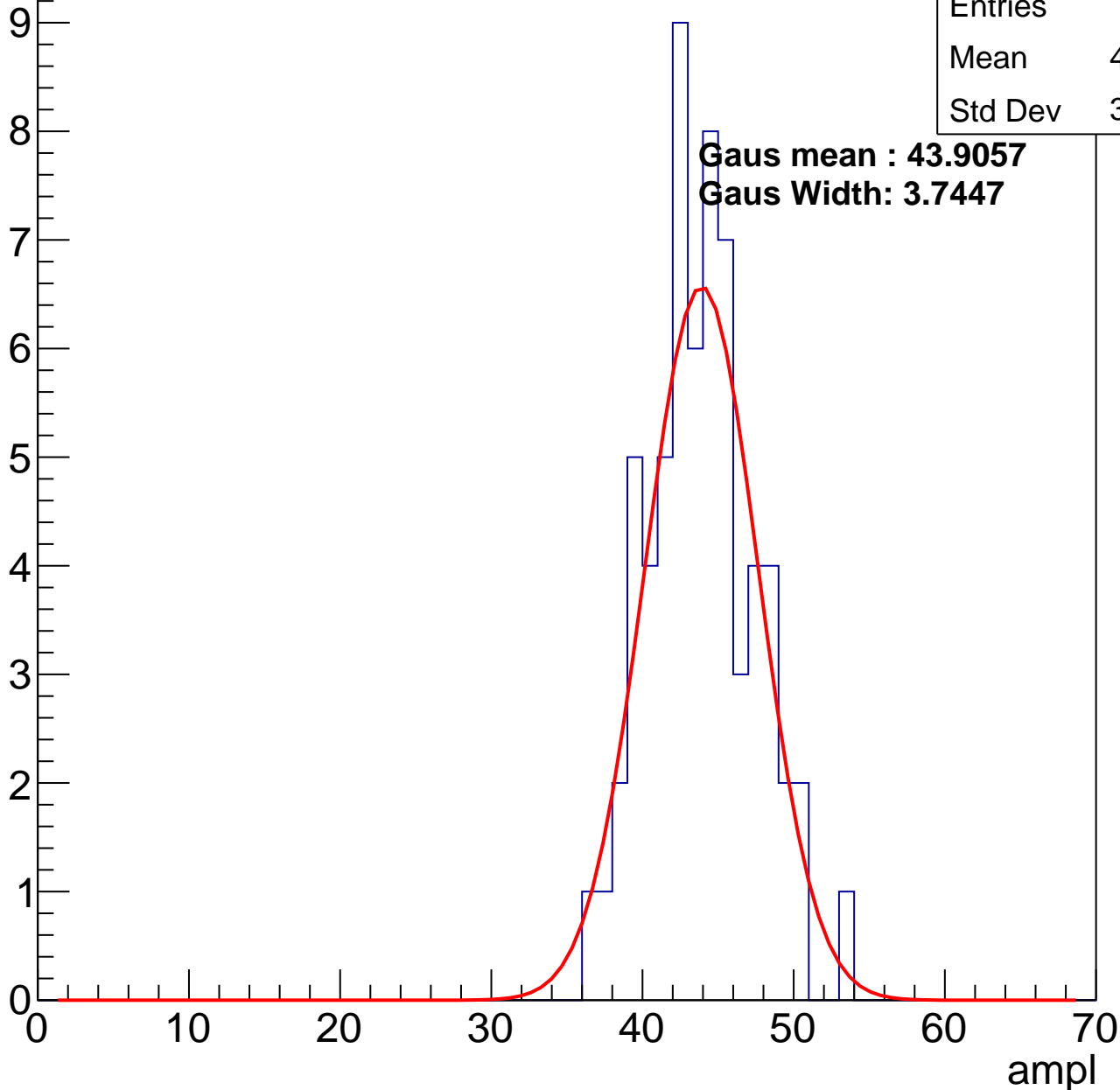
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.45
Std Dev	3.455

Gaus mean : 43.9057

Gaus Width: 3.7447

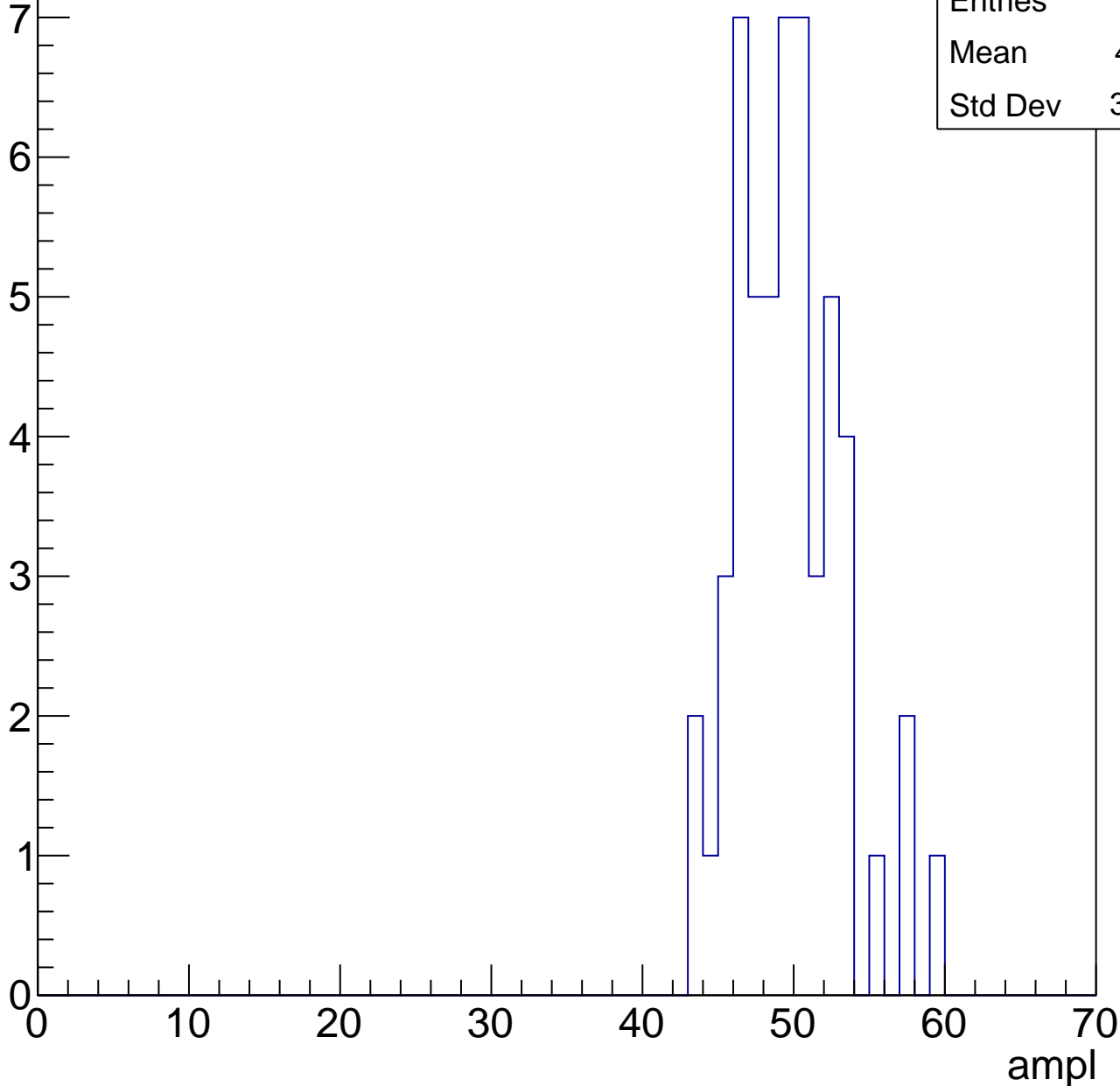


B1L101S, U17-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	49.21
Std Dev	3.428

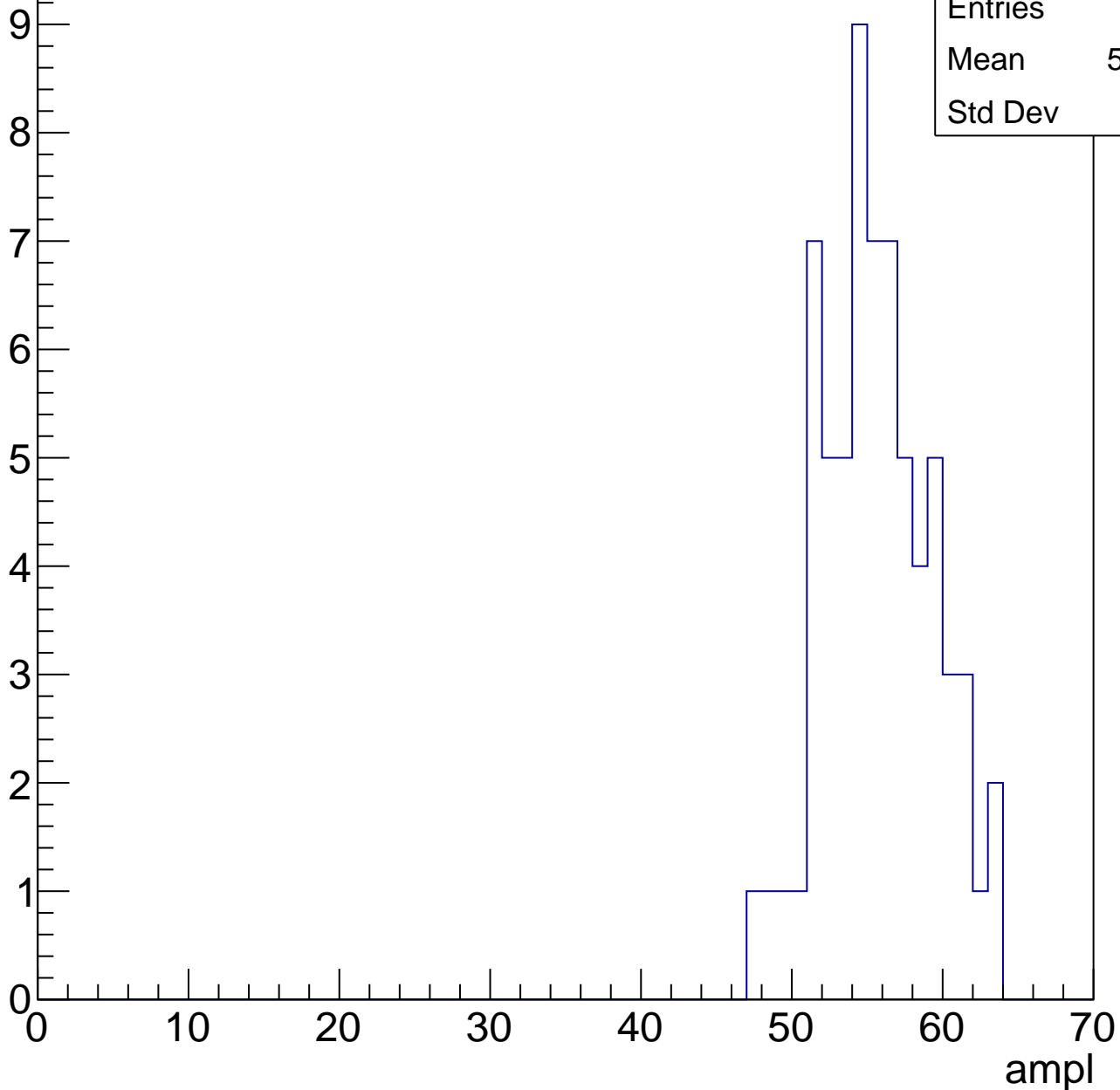


B1L101S, U17-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	55.25
Std Dev	3.58



B1L101S, U17-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries 43

Mean 59.6

Std Dev 2.488

0

0

10

20

30

40

50

60

70

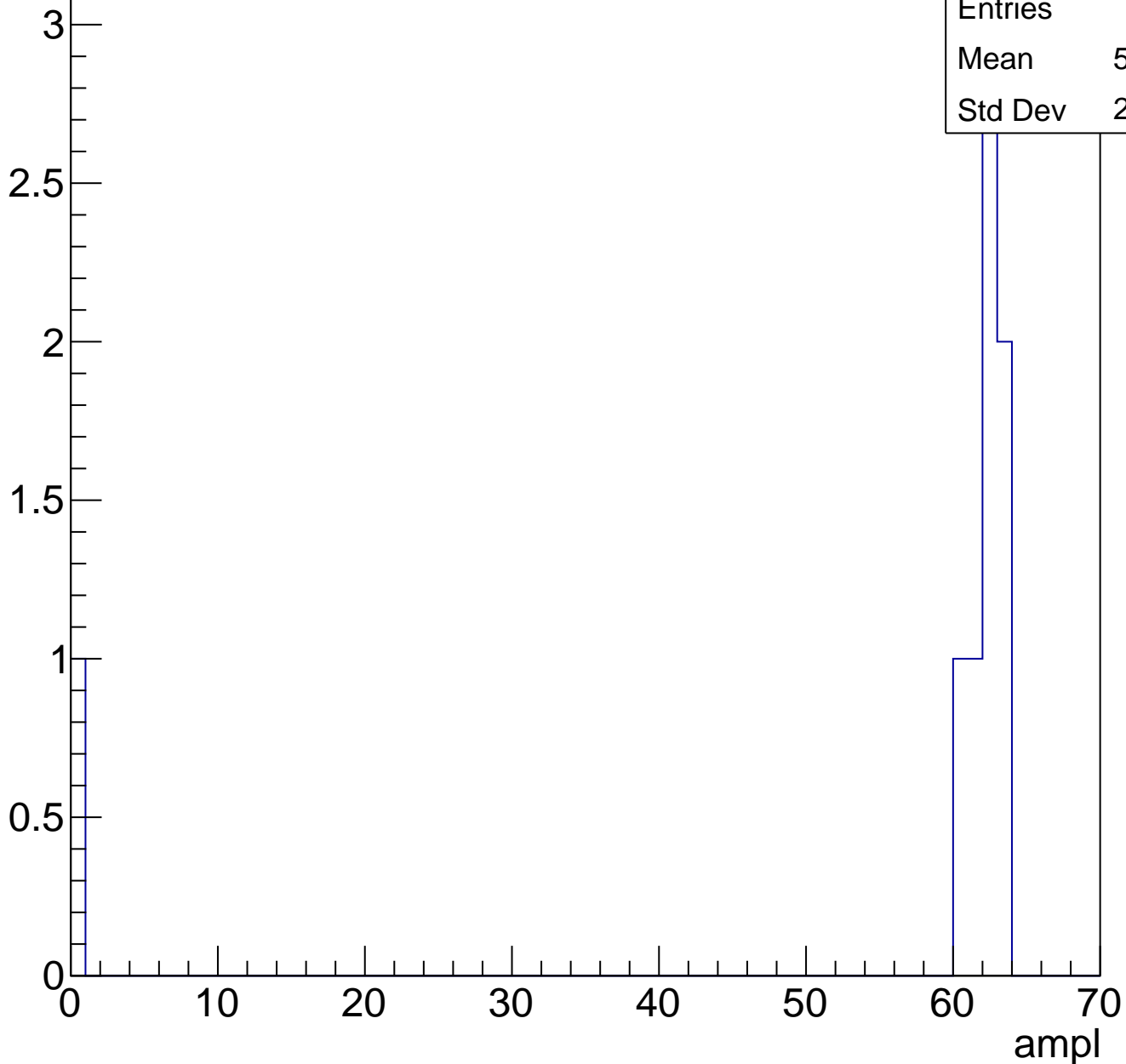
ampl

0

B1L101S, U17-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch49, adc0

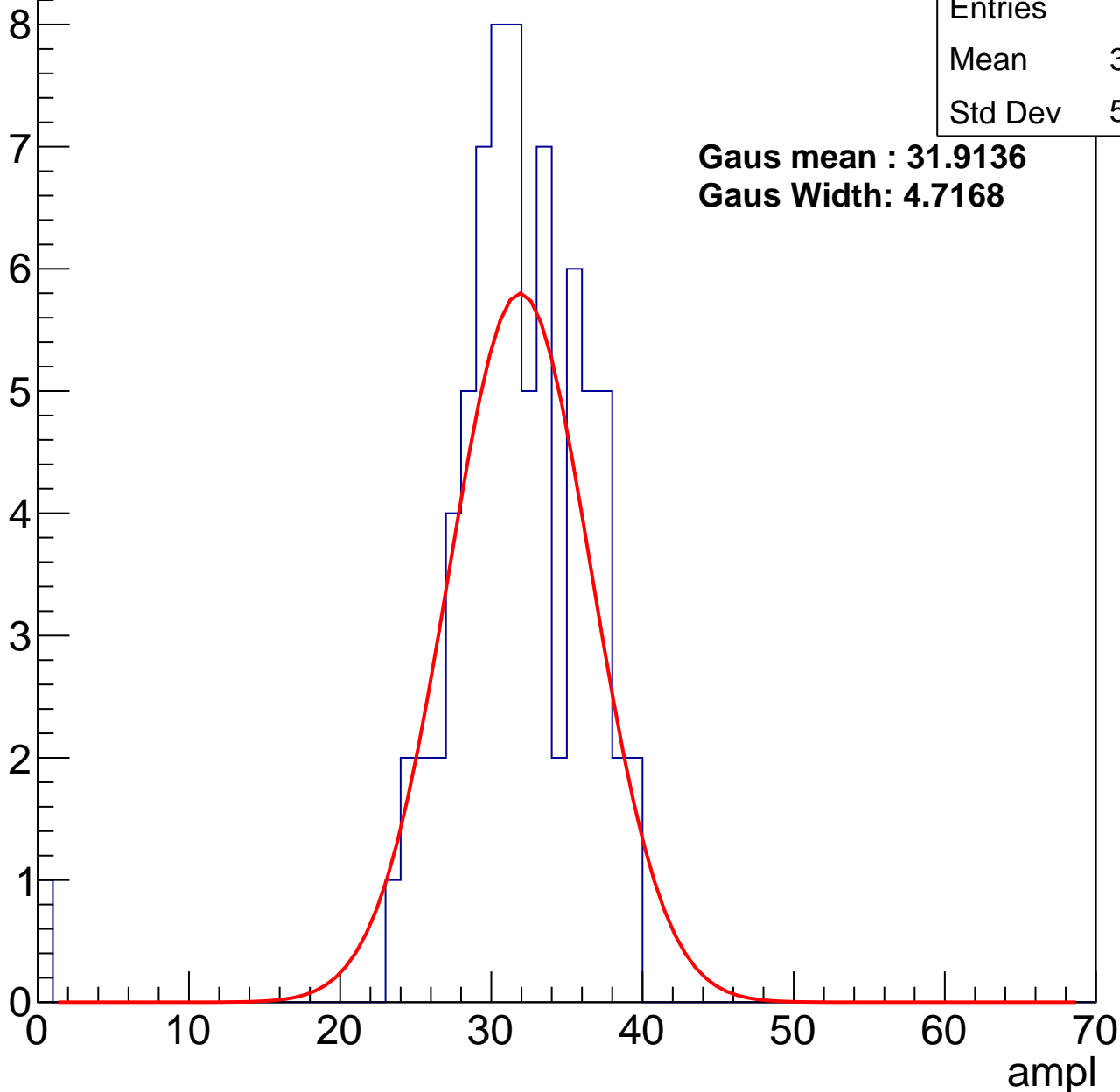
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	31.08
Std Dev	5.286

Gaus mean : 31.9136

Gaus Width: 4.7168



B1L101S, U17-ch49, adc1

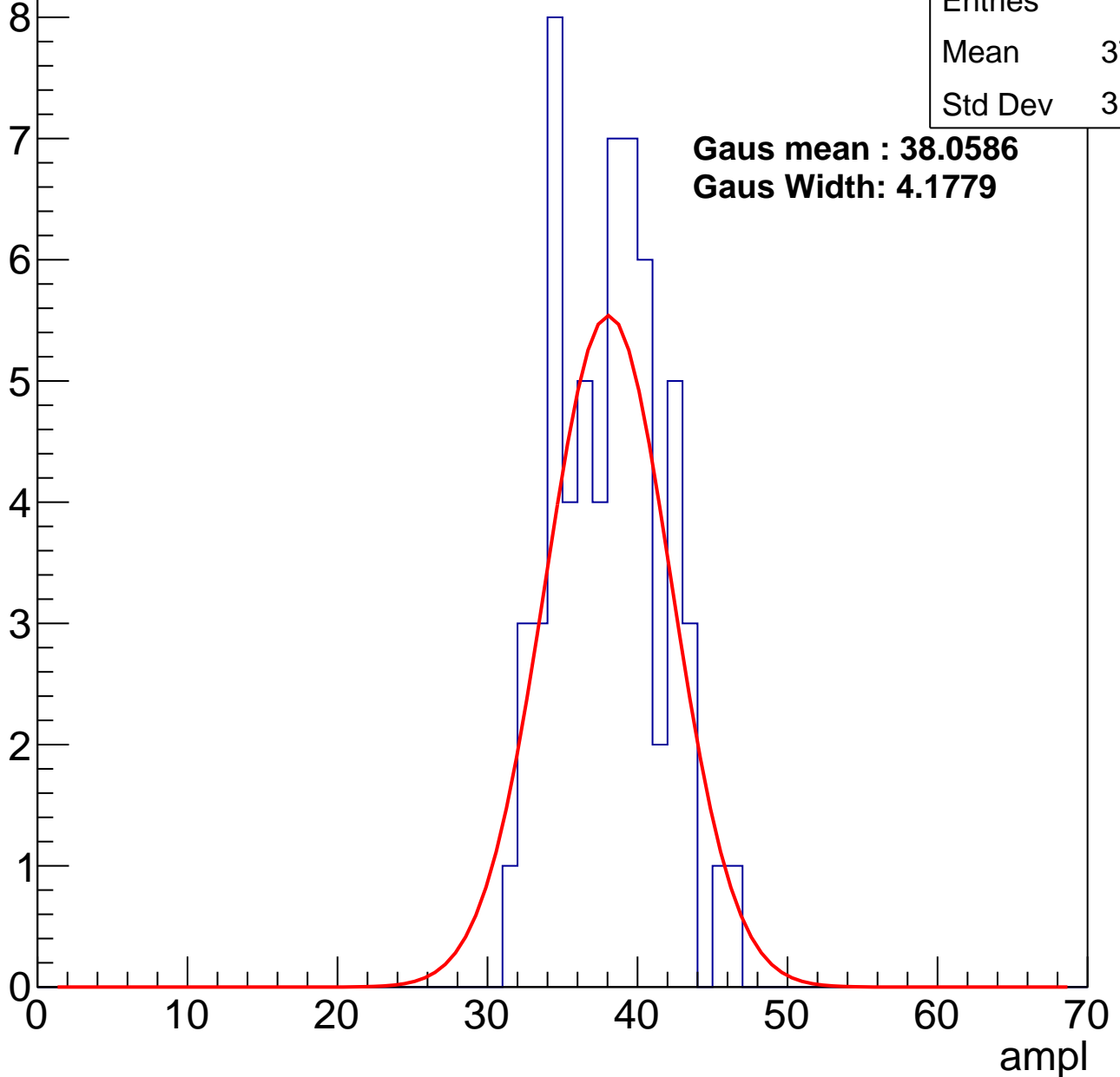
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.62
Std Dev	3.484

Gaus mean : 38.0586

Gaus Width: 4.1779



B1L101S, U17-ch49, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	44.62
Std Dev	3.385

Gaus mean : 45.1713

Gaus Width: 3.3557

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

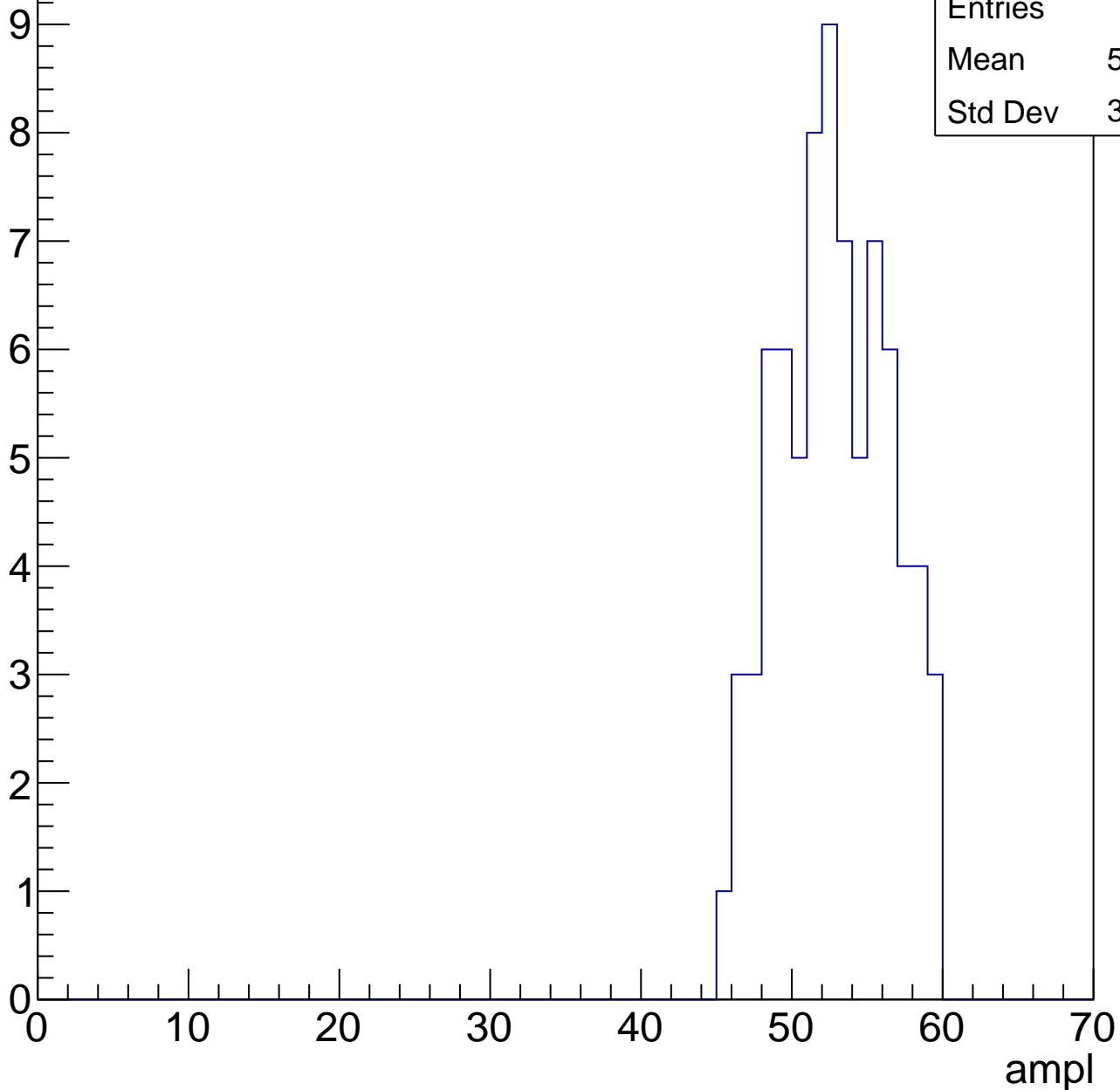
70

B1L101S, U17-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	52.35
Std Dev	3.563

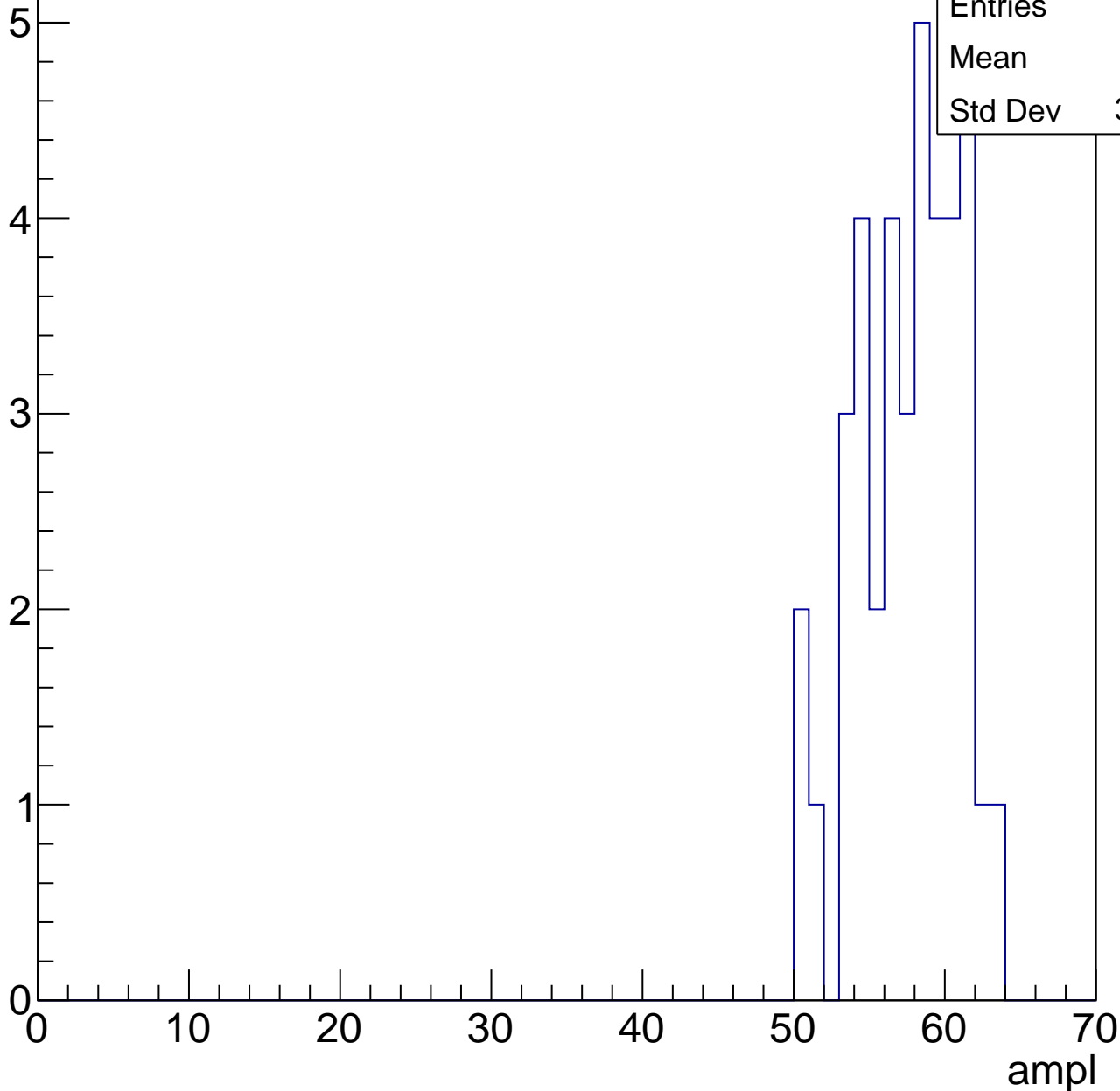


B1L101S, U17-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	57.1
Std Dev	3.311

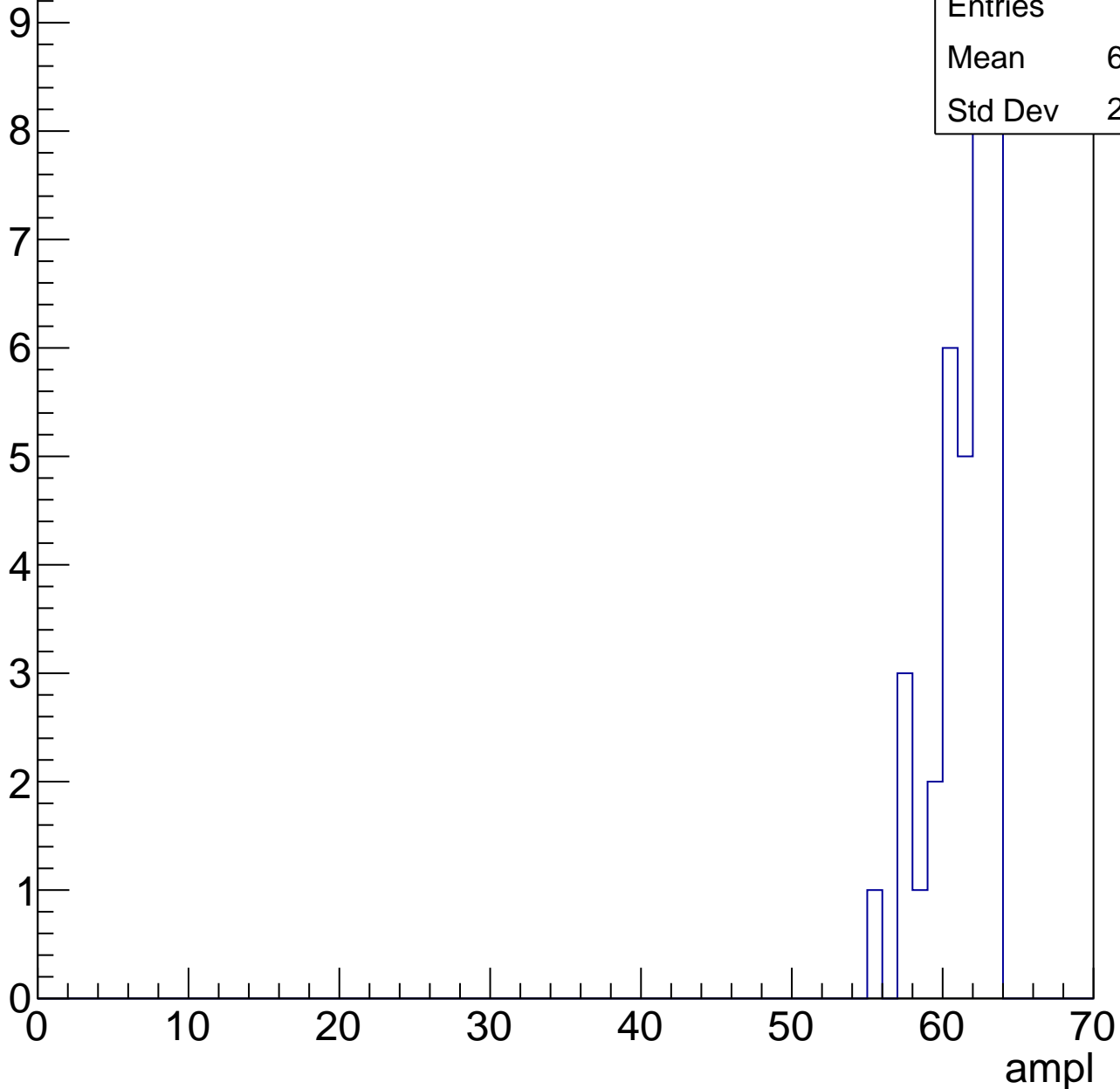


B1L101S, U17-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

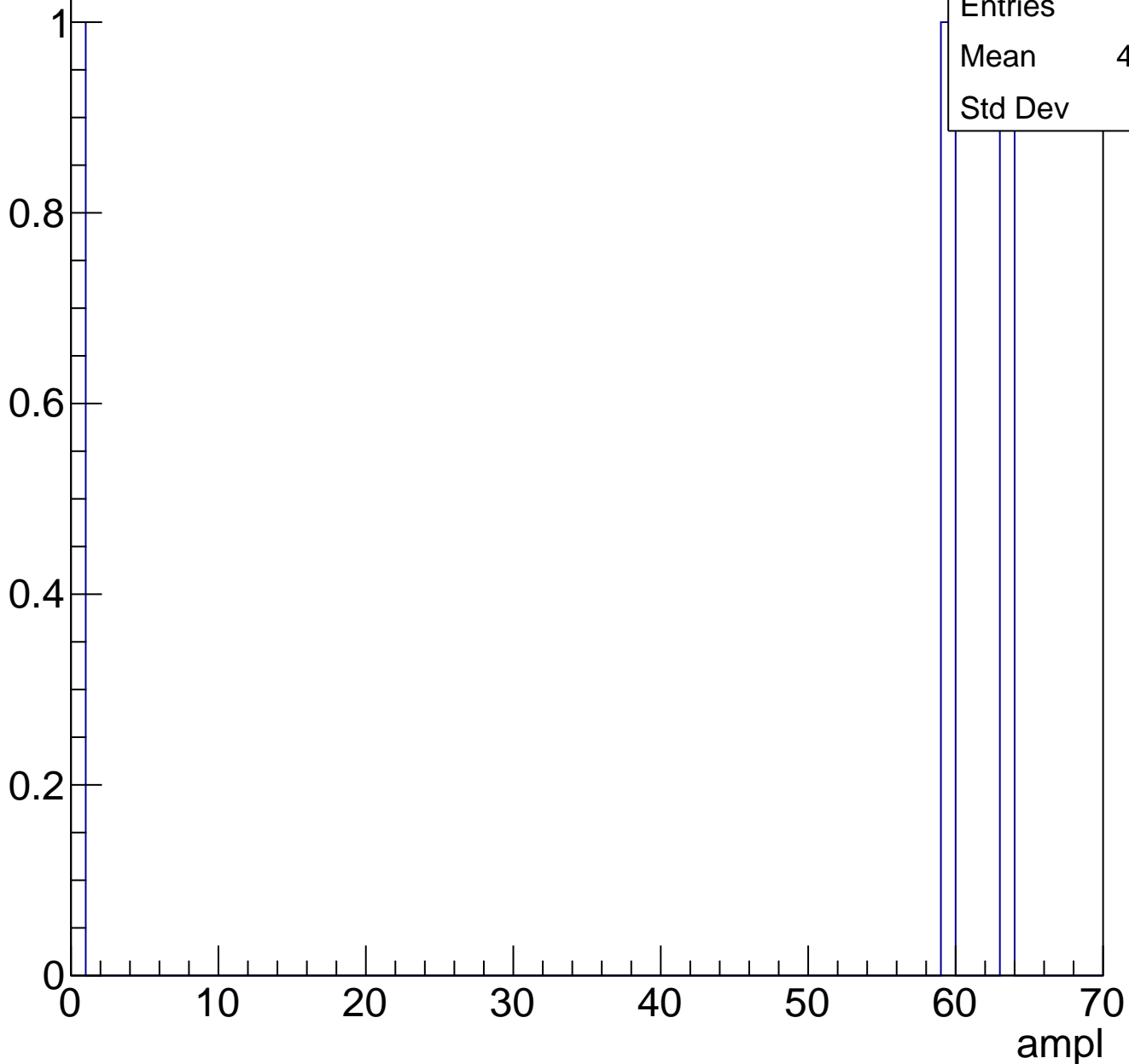
Entries	35
Mean	60.83
Std Dev	2.049



B1L101S, U17-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch50, adc0

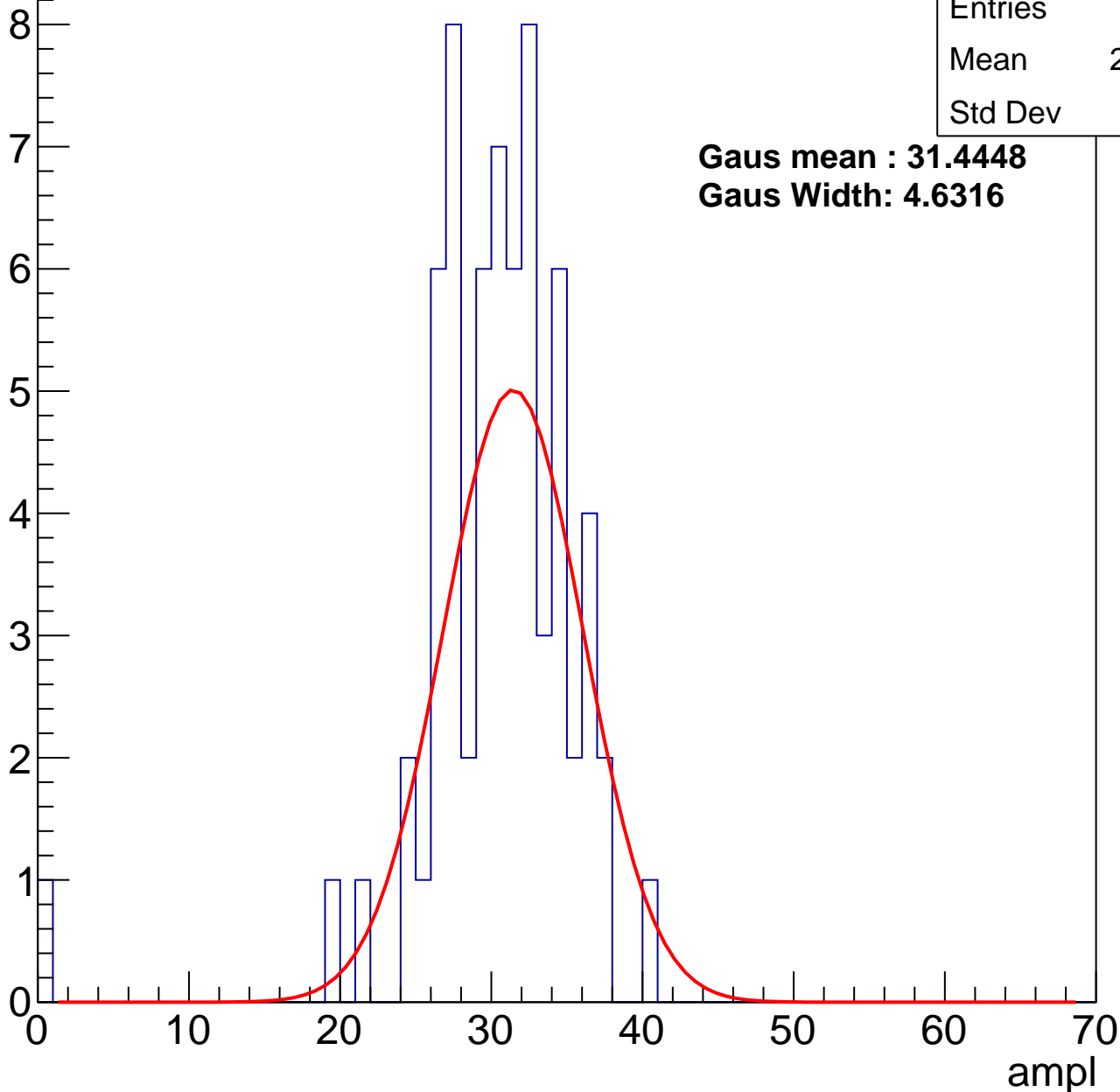
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.82
Std Dev	5.37

Gaus mean : 31.4448

Gaus Width: 4.6316



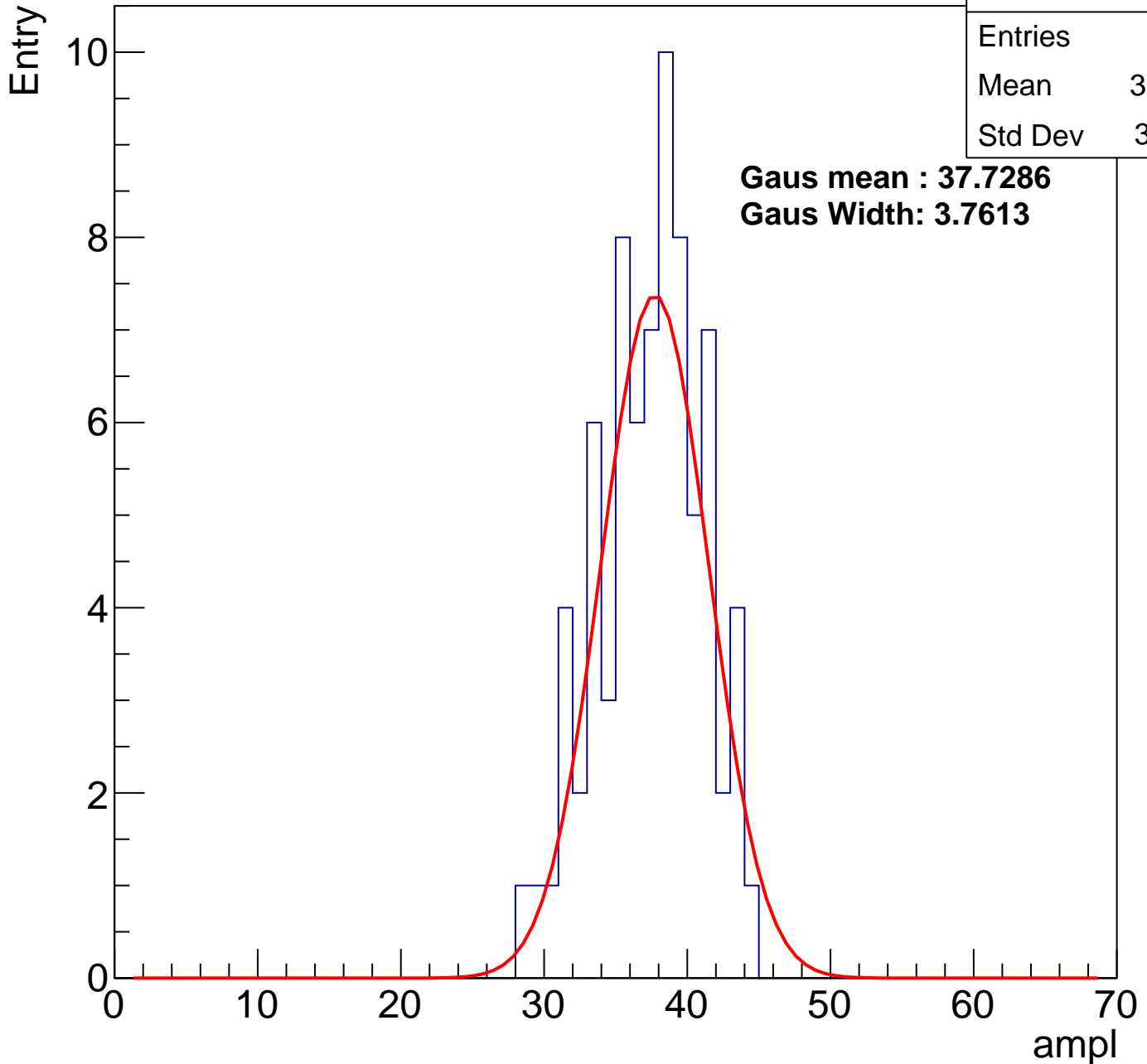
B1L101S, U17-ch50, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.96
Std Dev	3.611

Gaus mean : 37.7286

Gaus Width: 3.7613



B1L101S, U17-ch50, adc2

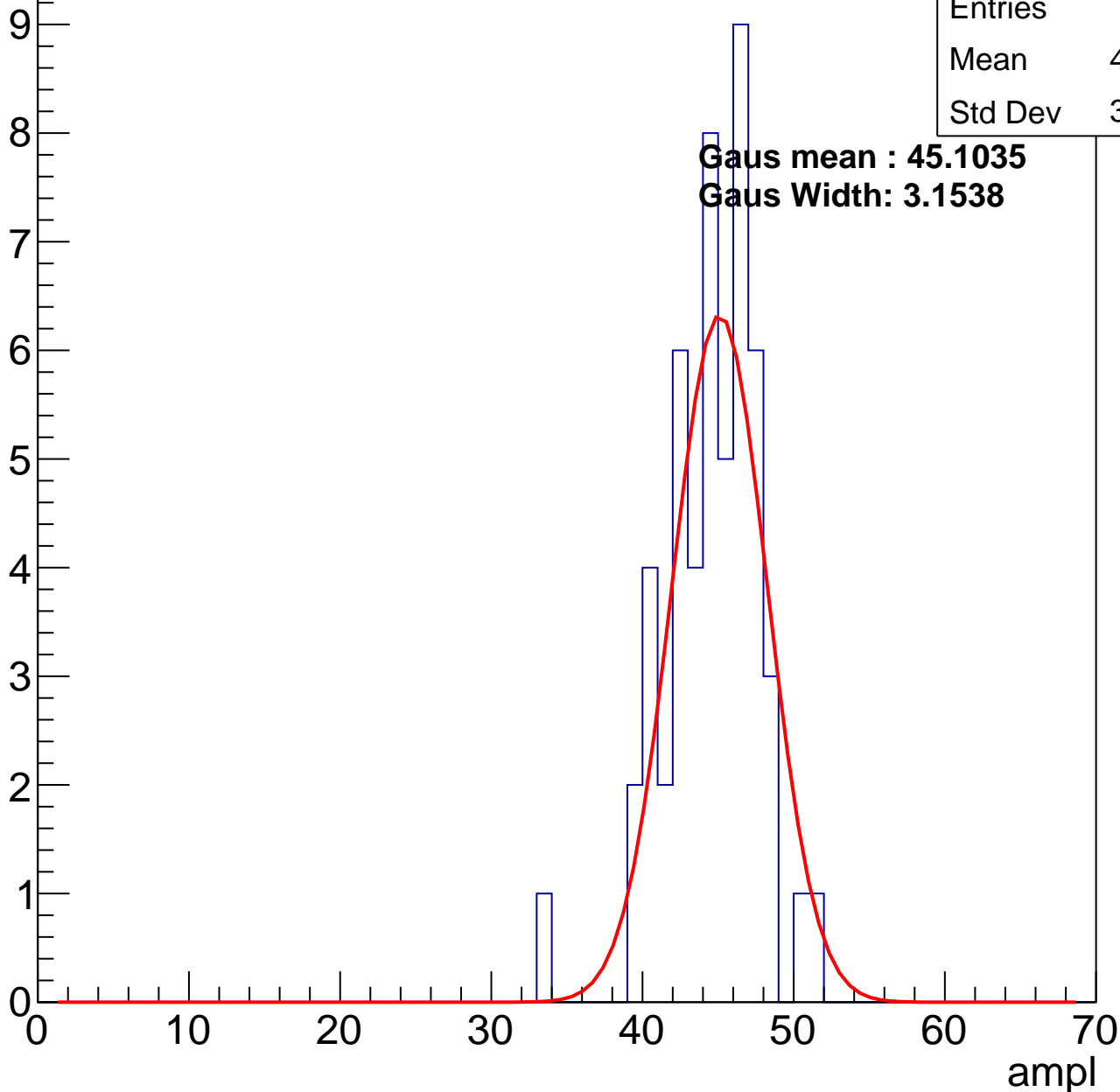
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.13
Std Dev	3.126

Gaus mean : 45.1035

Gaus Width: 3.1538

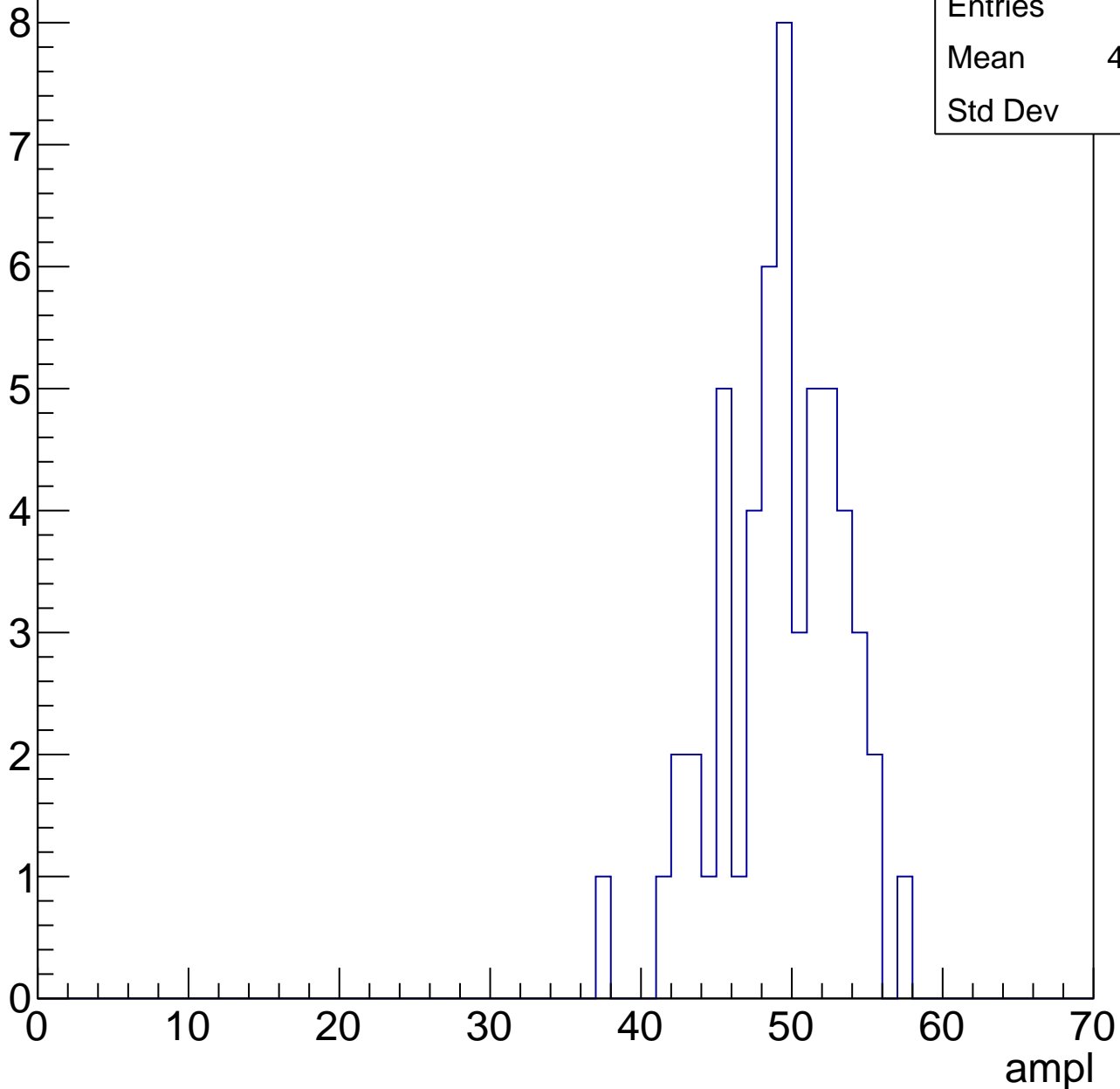


B1L101S, U17-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	48.83
Std Dev	3.98

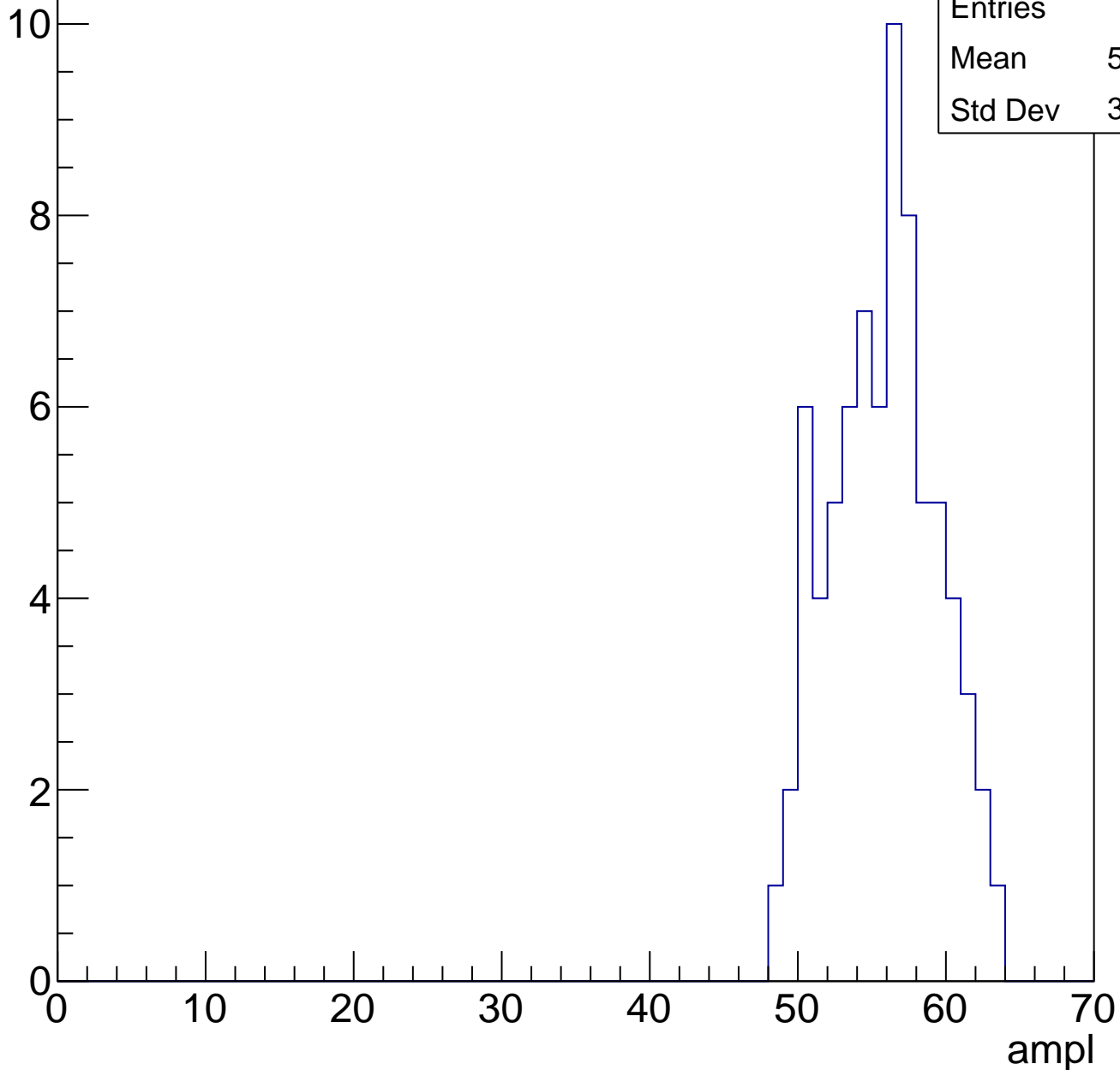


B1L101S, U17-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	55.29
Std Dev	3.543

Entry

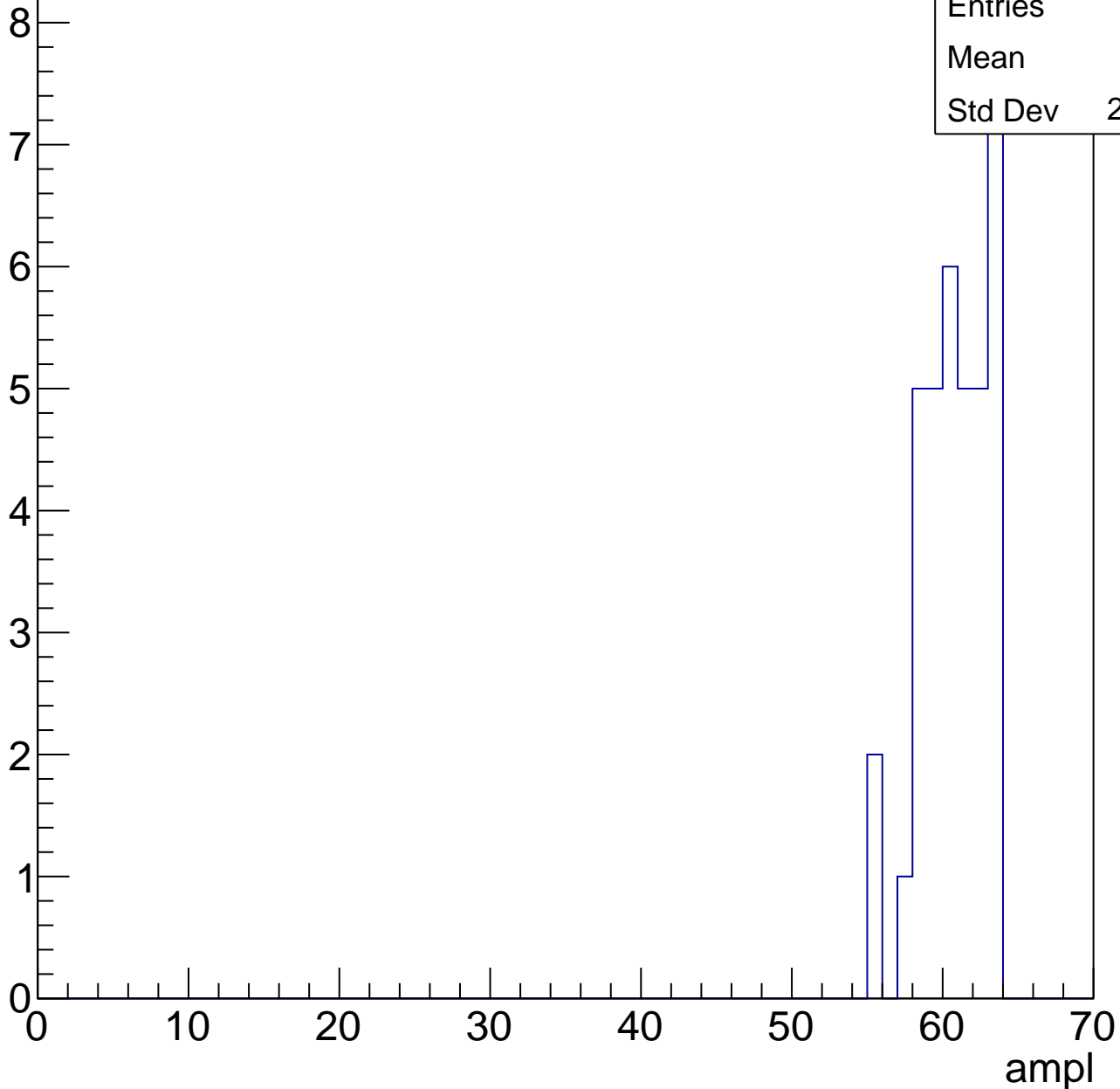


B1L101S, U17-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

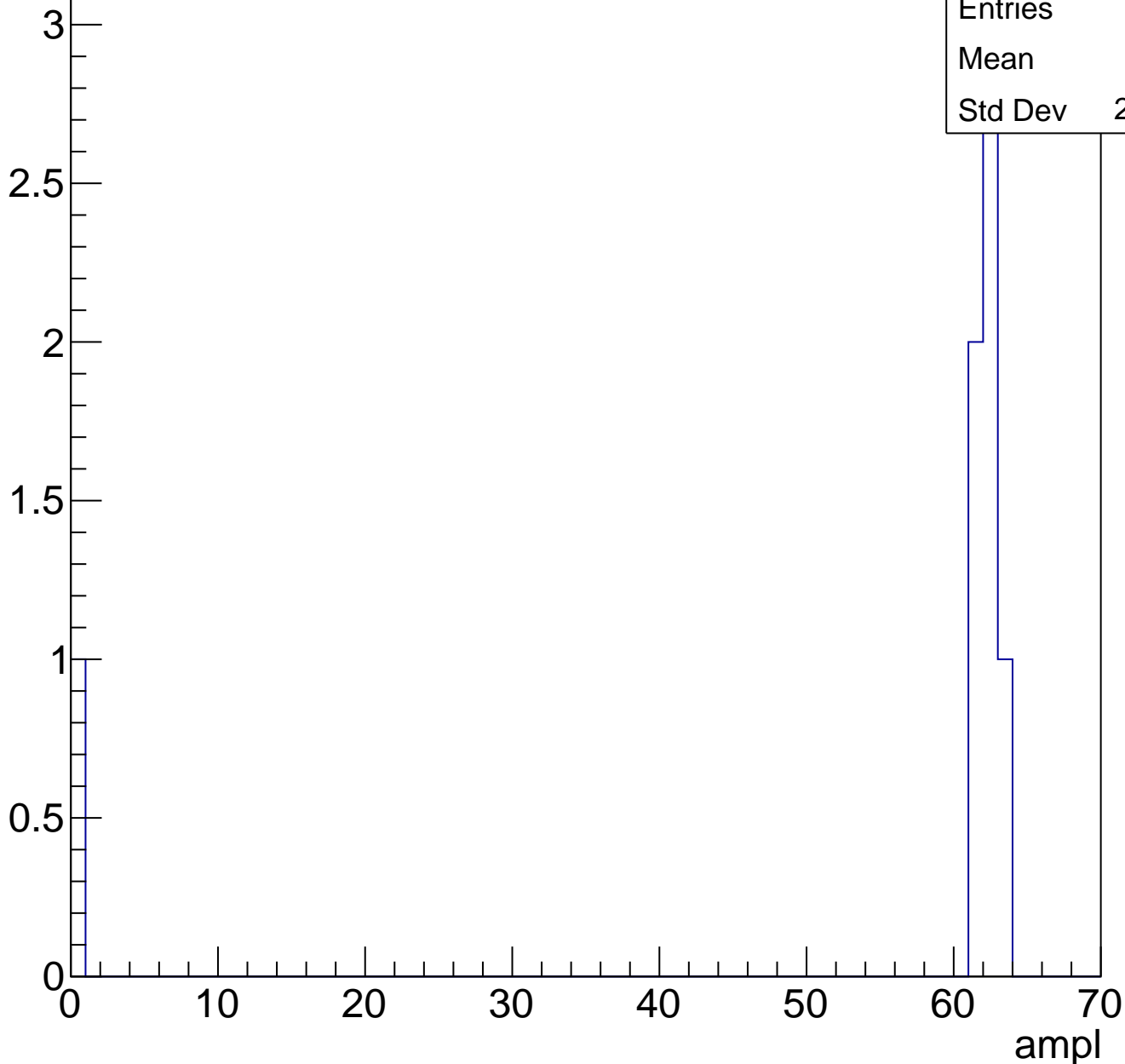
Entries	37
Mean	60.3
Std Dev	2.192



B1L101S, U17-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch51, adc0

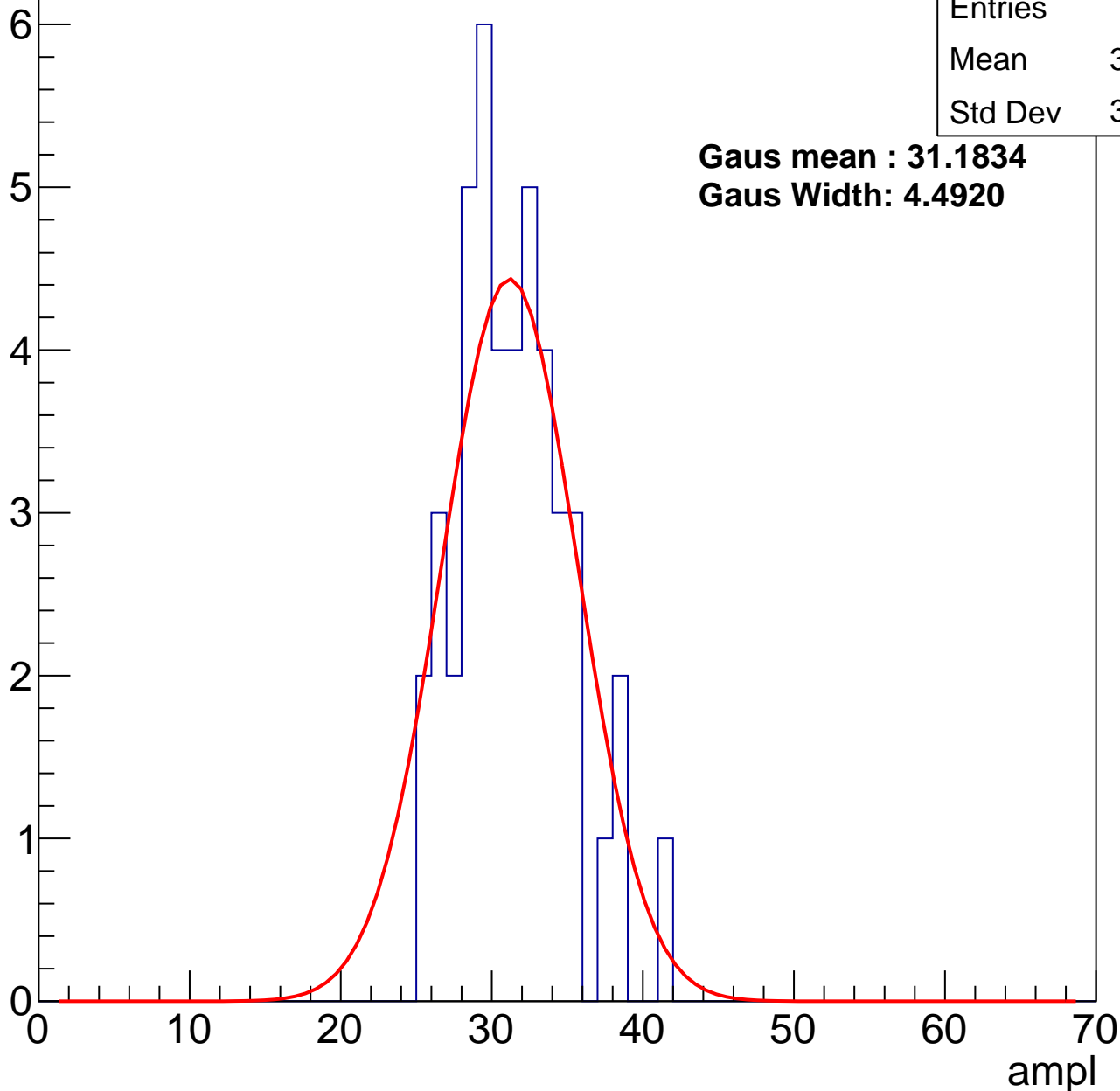
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	30.96
Std Dev	3.596

Gaus mean : 31.1834

Gaus Width: 4.4920



B1L101S, U17-ch51, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	36.52
Std Dev	4.201

Gaus mean : 36.6779

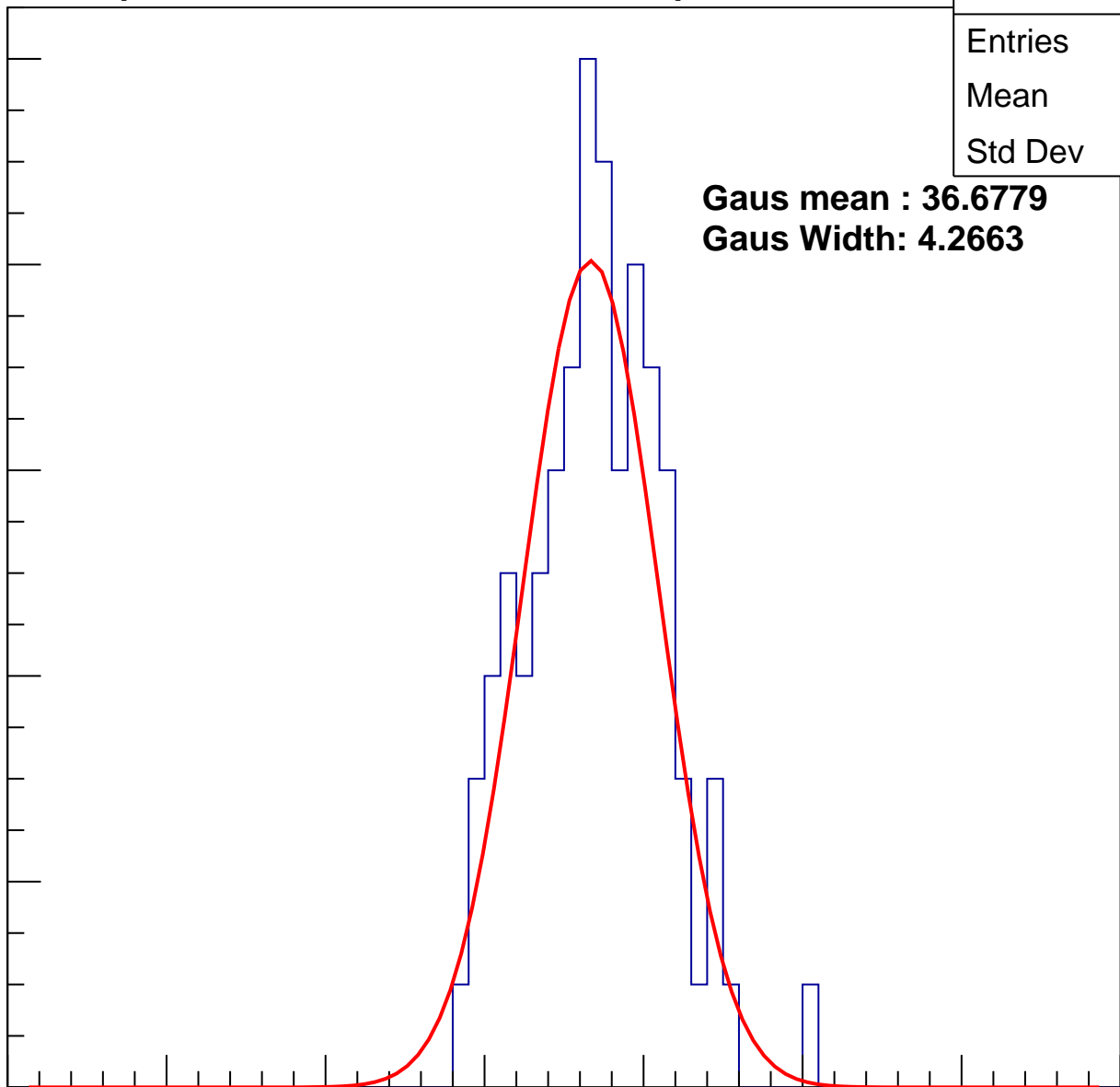
Gaus Width: 4.2663

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch51, adc2

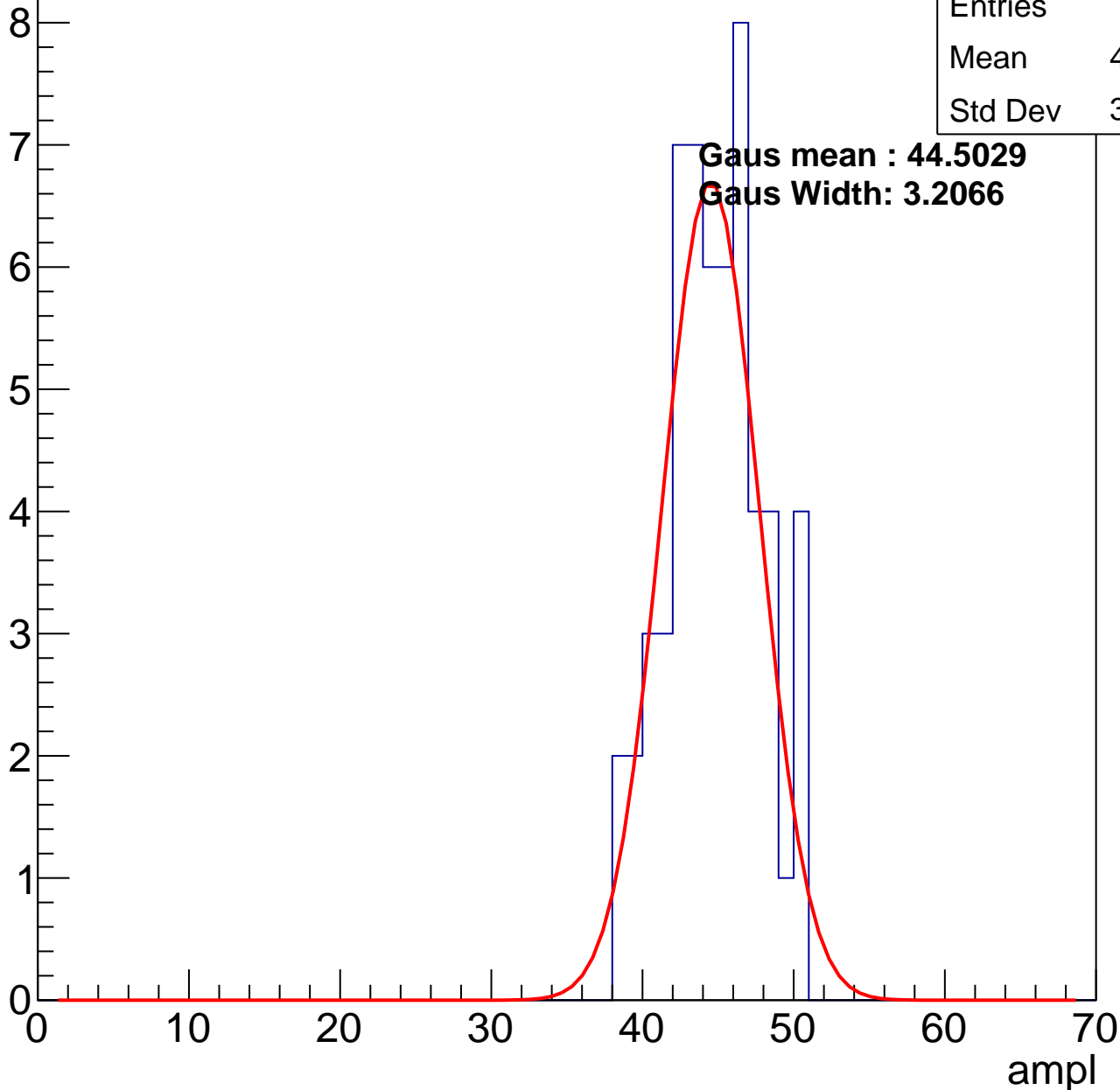
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.26
Std Dev	3.064

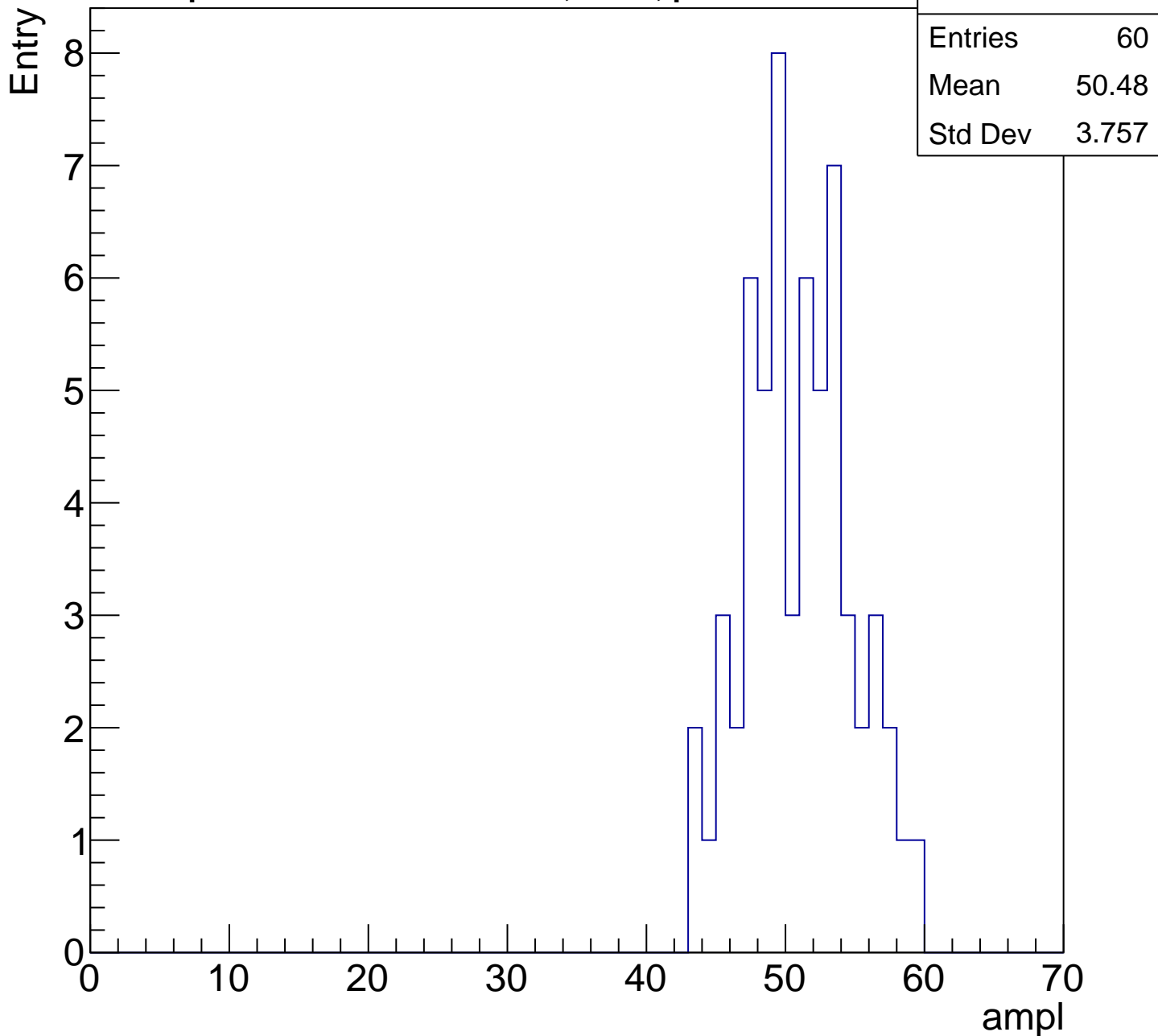
Gaus mean : 44.5029

Gaus Width: 3.2066



B1L101S, U17-ch51, adc3

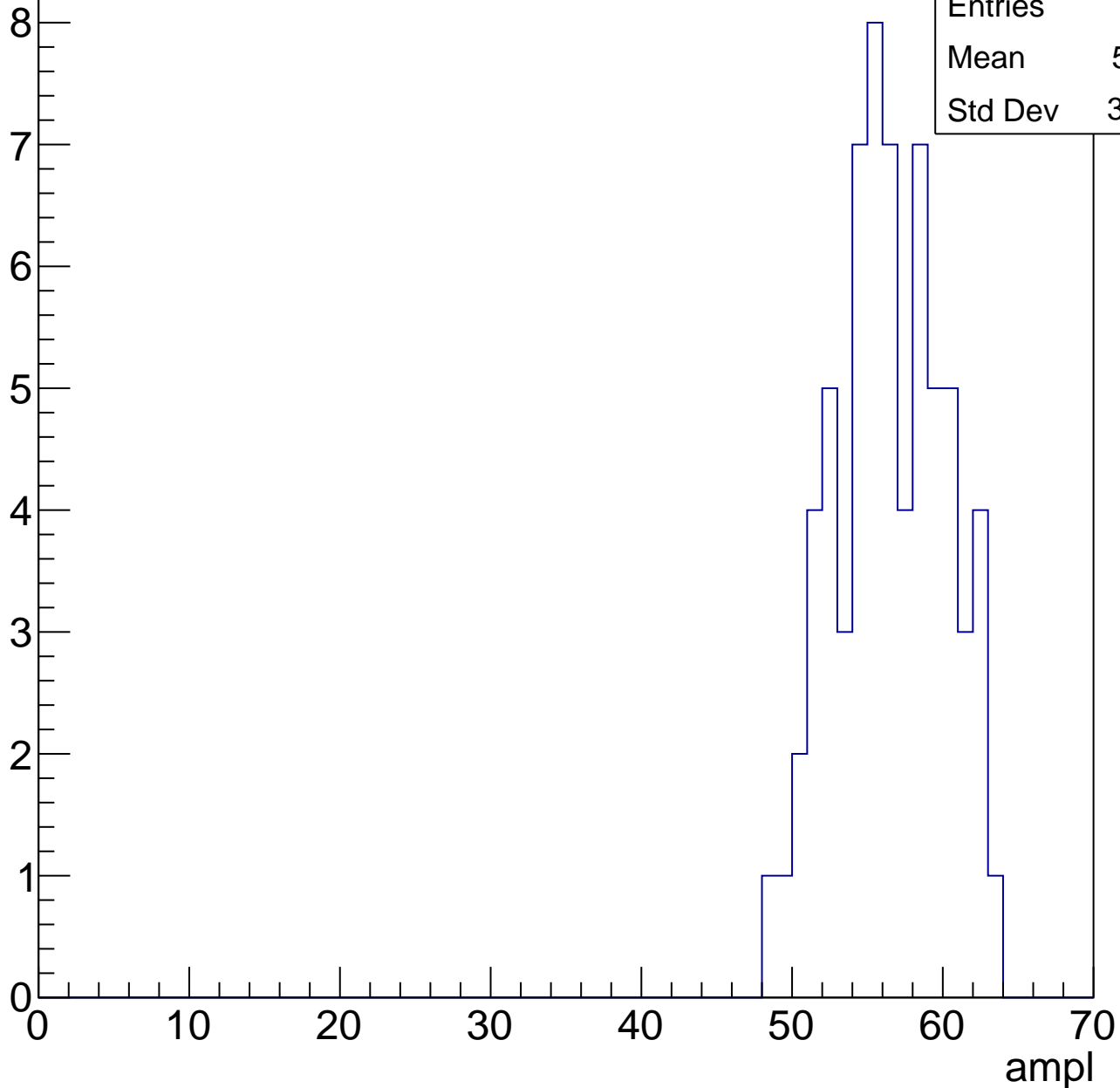
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

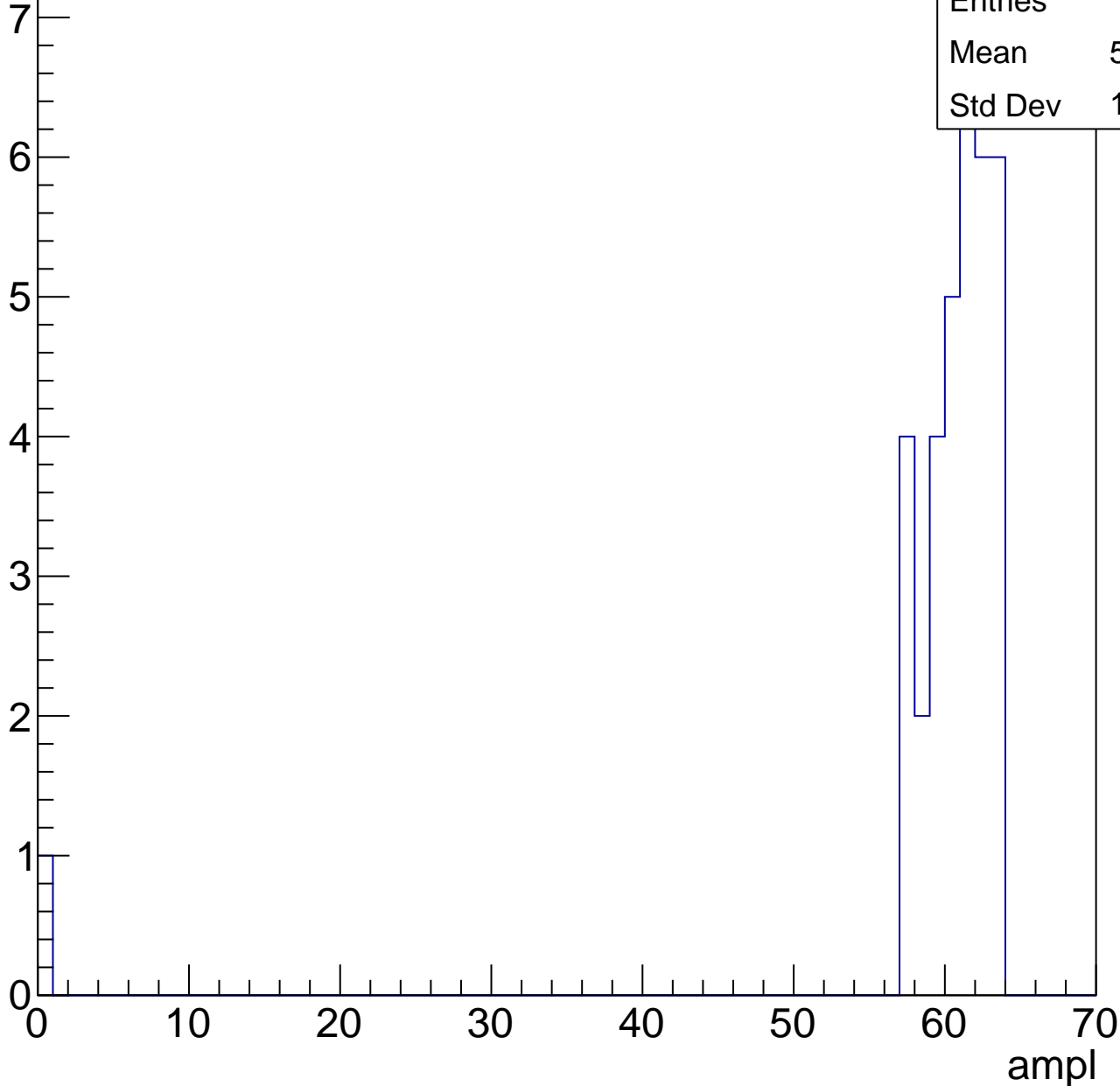


B1L101S, U17-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.77
Std Dev	10.25



B1L101S, U17-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L101S, U17-ch52, adc0

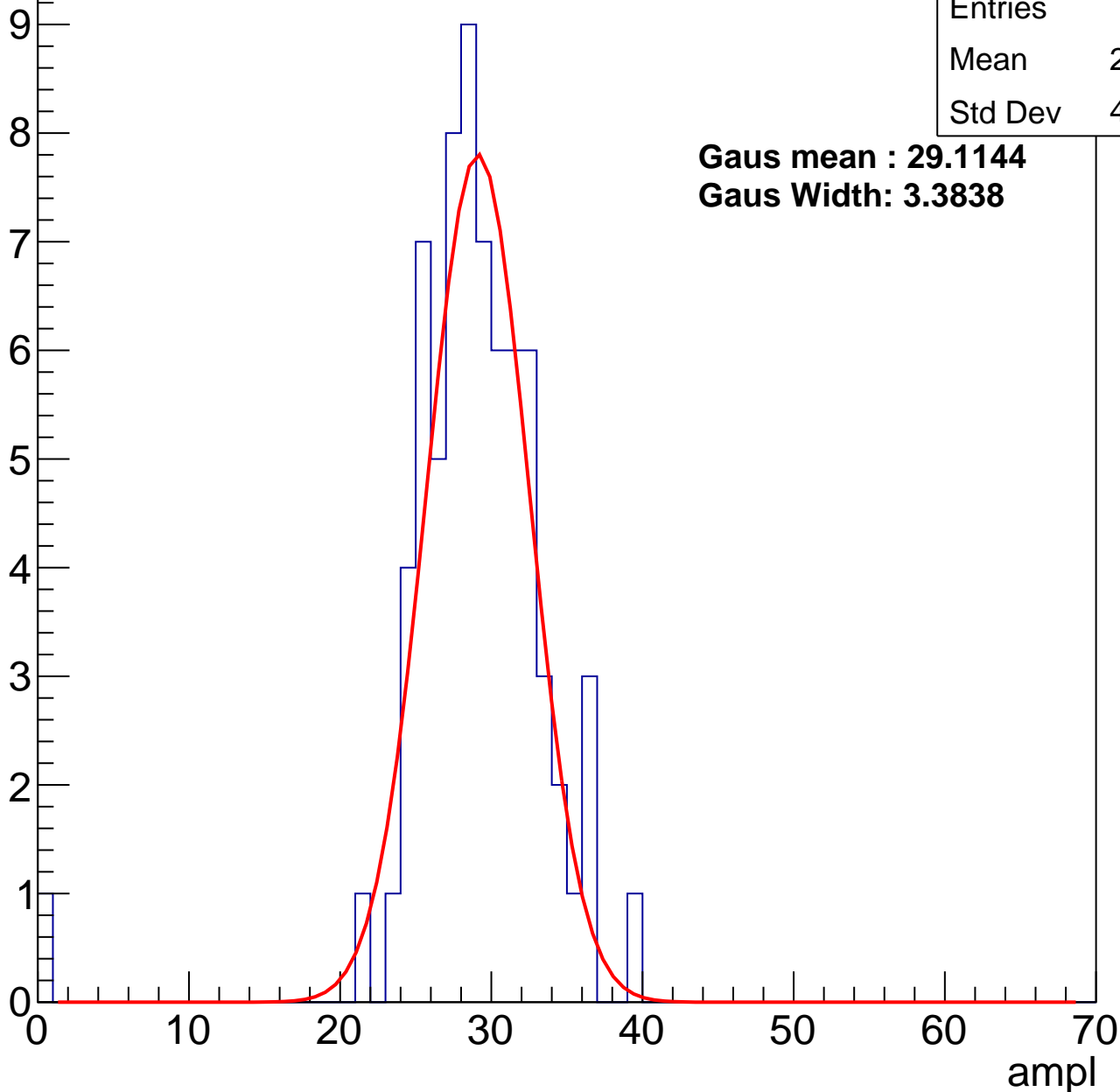
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.49
Std Dev	4.873

Gaus mean : 29.1144

Gaus Width: 3.3838



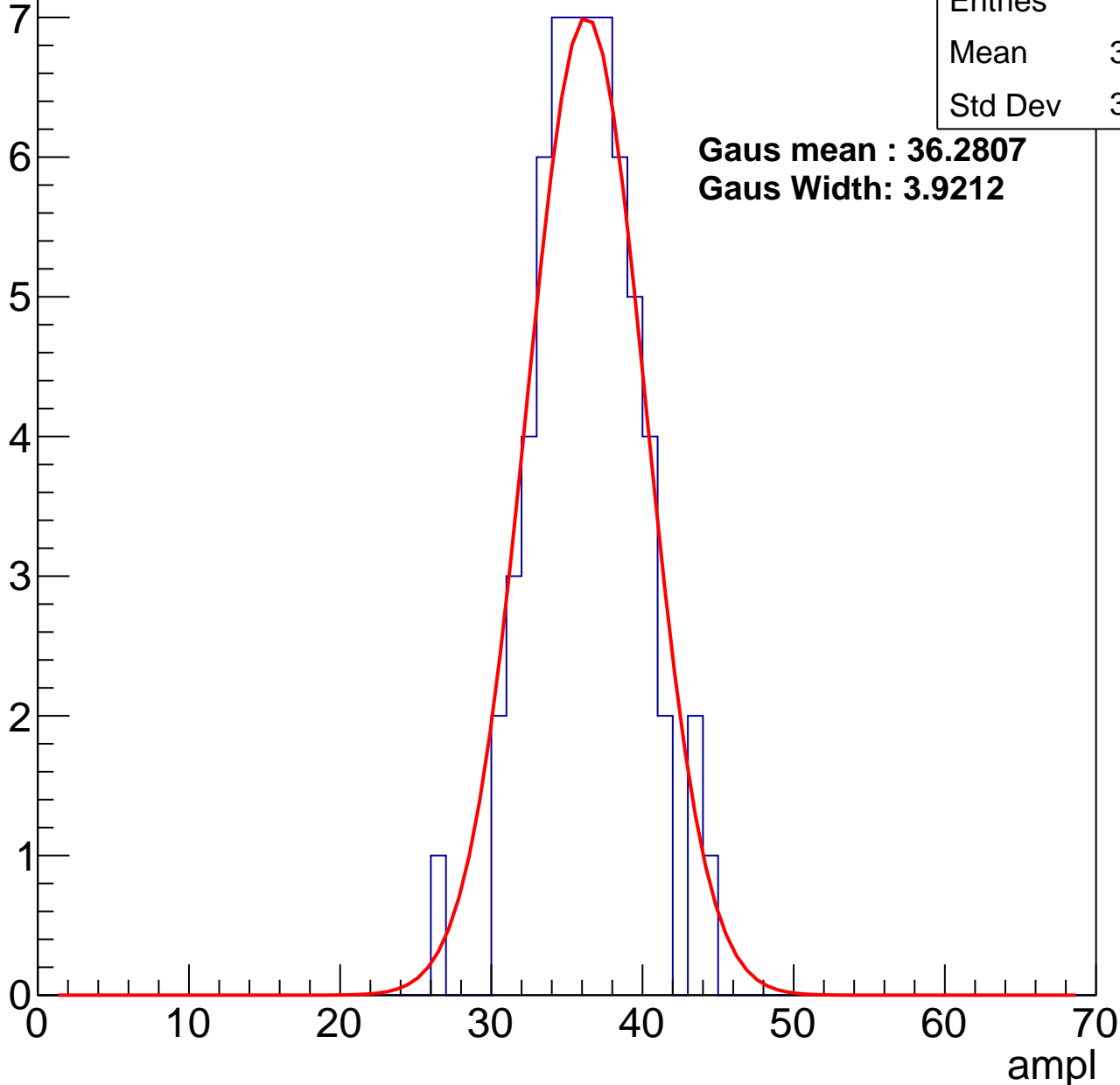
B1L101S, U17-ch52, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.84
Std Dev	3.424

Gaus mean : 36.2807
Gaus Width: 3.9212



B1L101S, U17-ch52, adc2

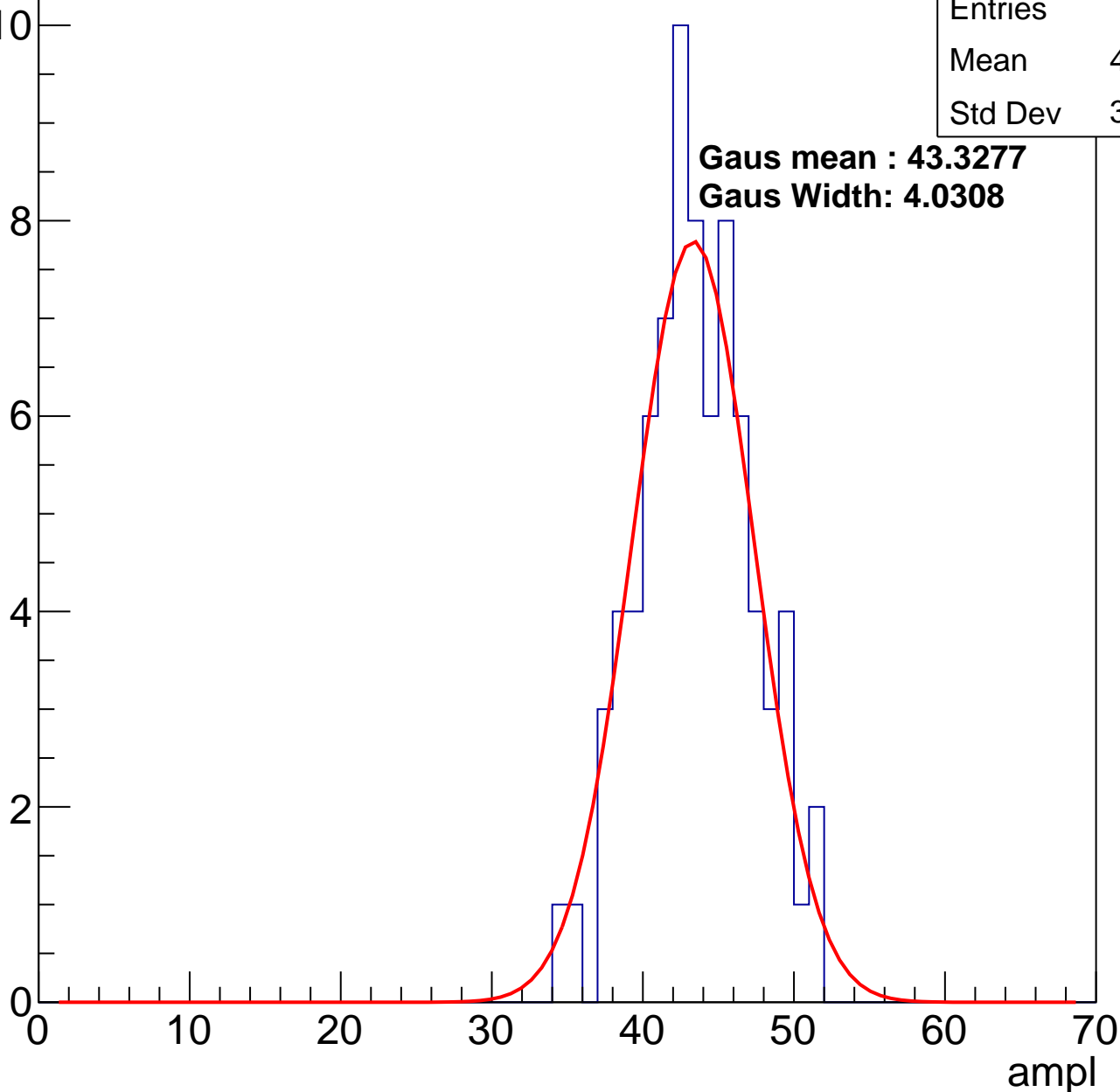
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.06
Std Dev	3.688

Gaus mean : 43.3277

Gaus Width: 4.0308

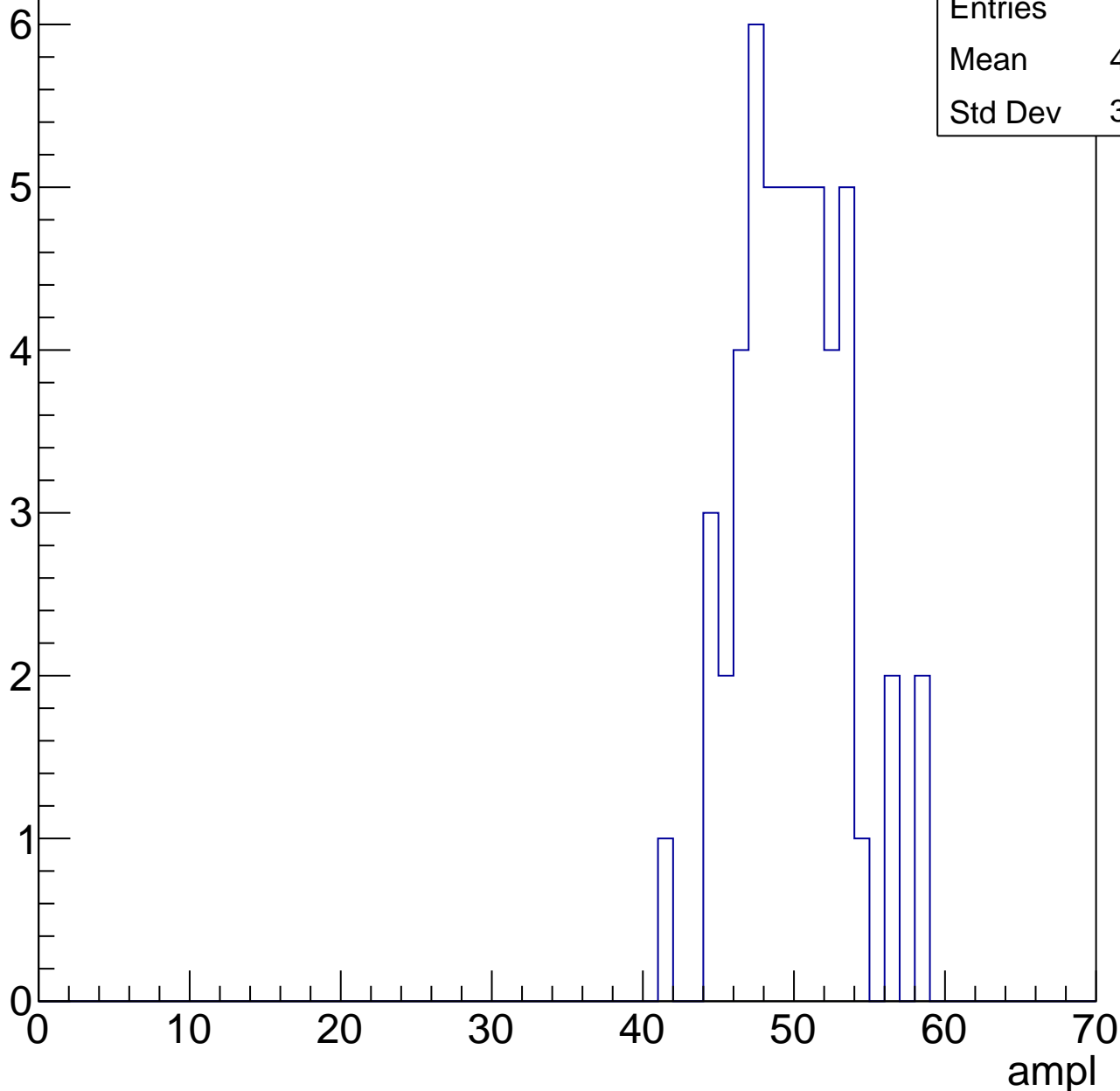


B1L101S, U17-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

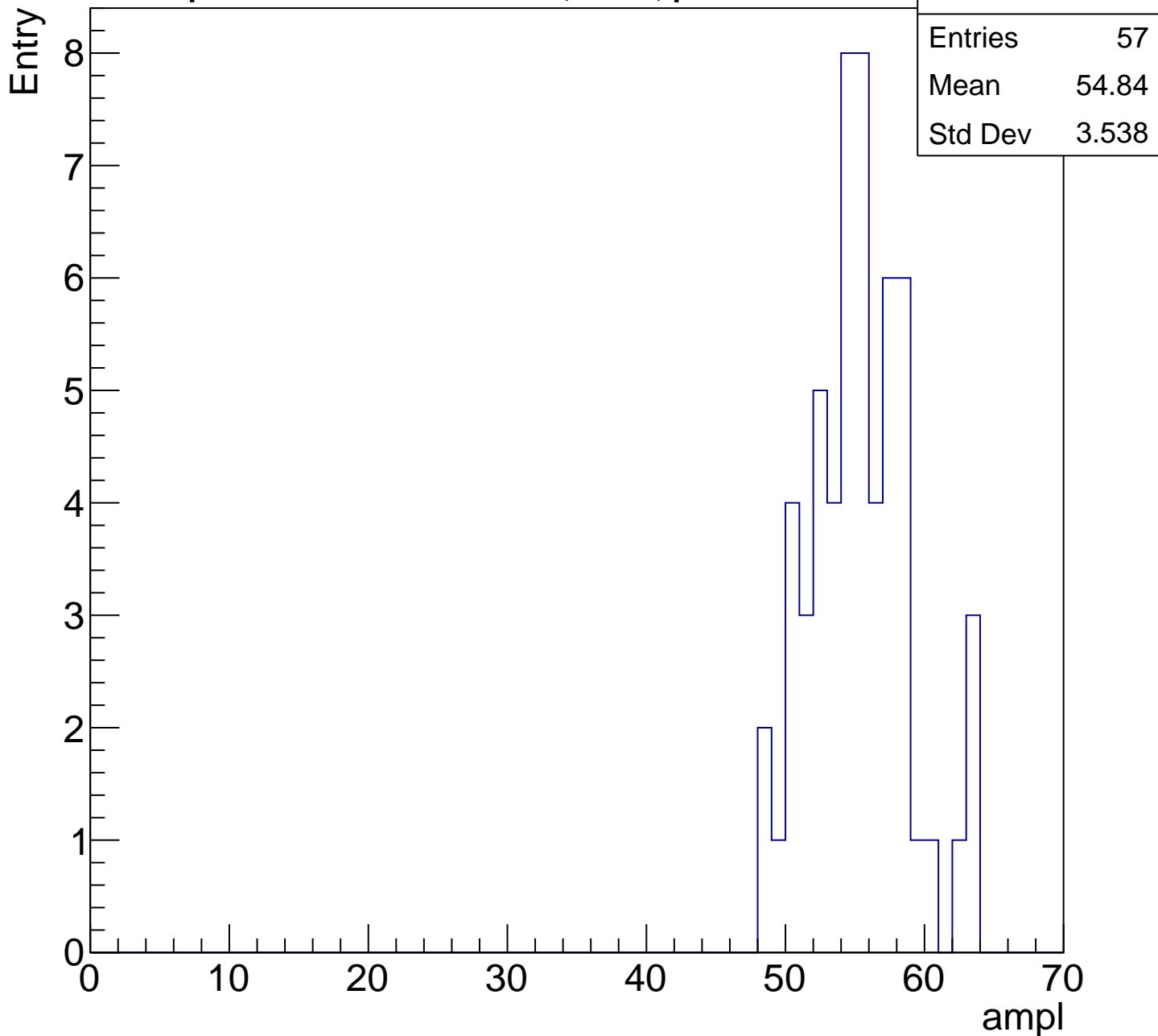
Entry

Entries	50
Mean	49.48
Std Dev	3.607



B1L101S, U17-ch52, adc4

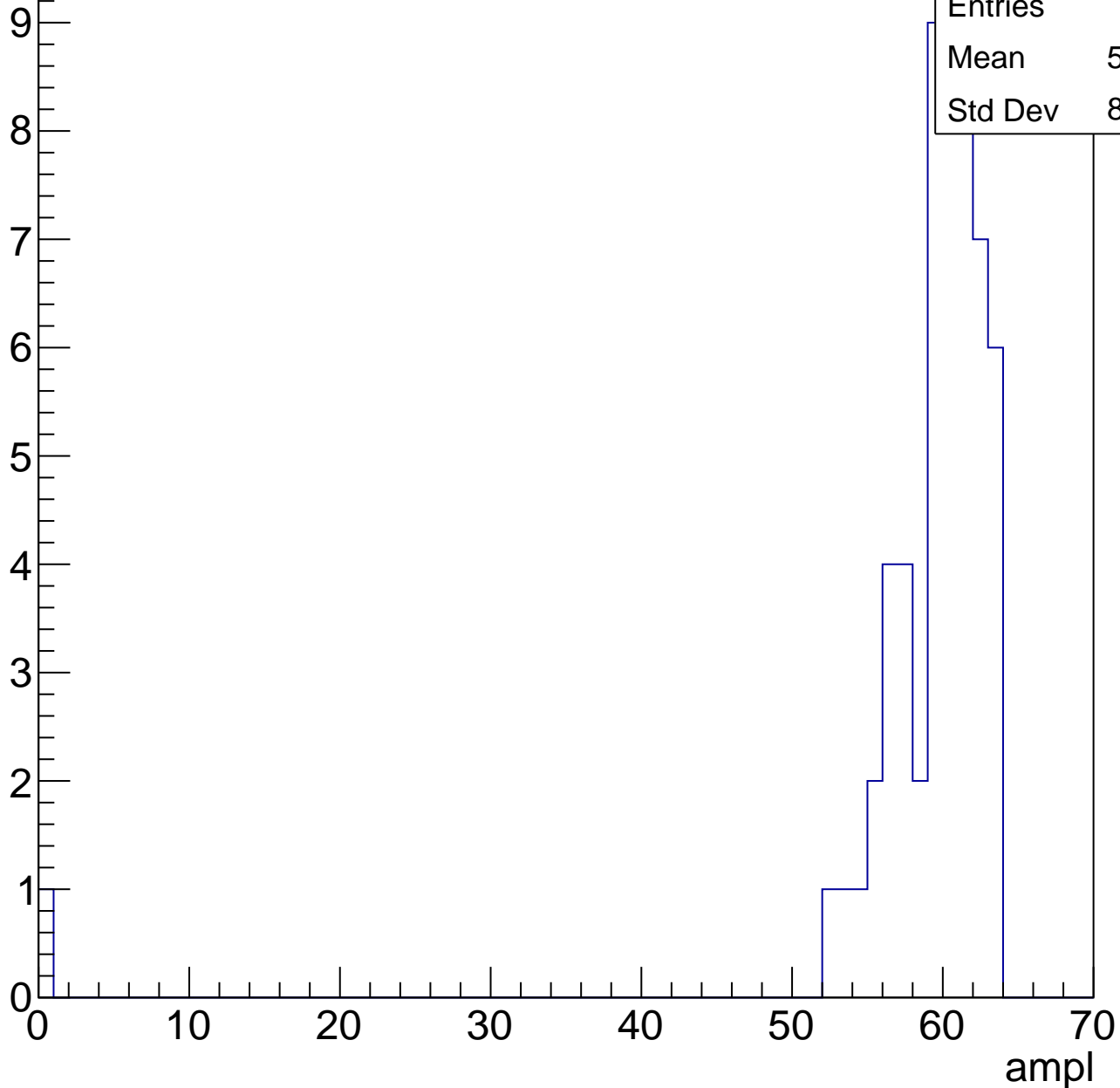
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

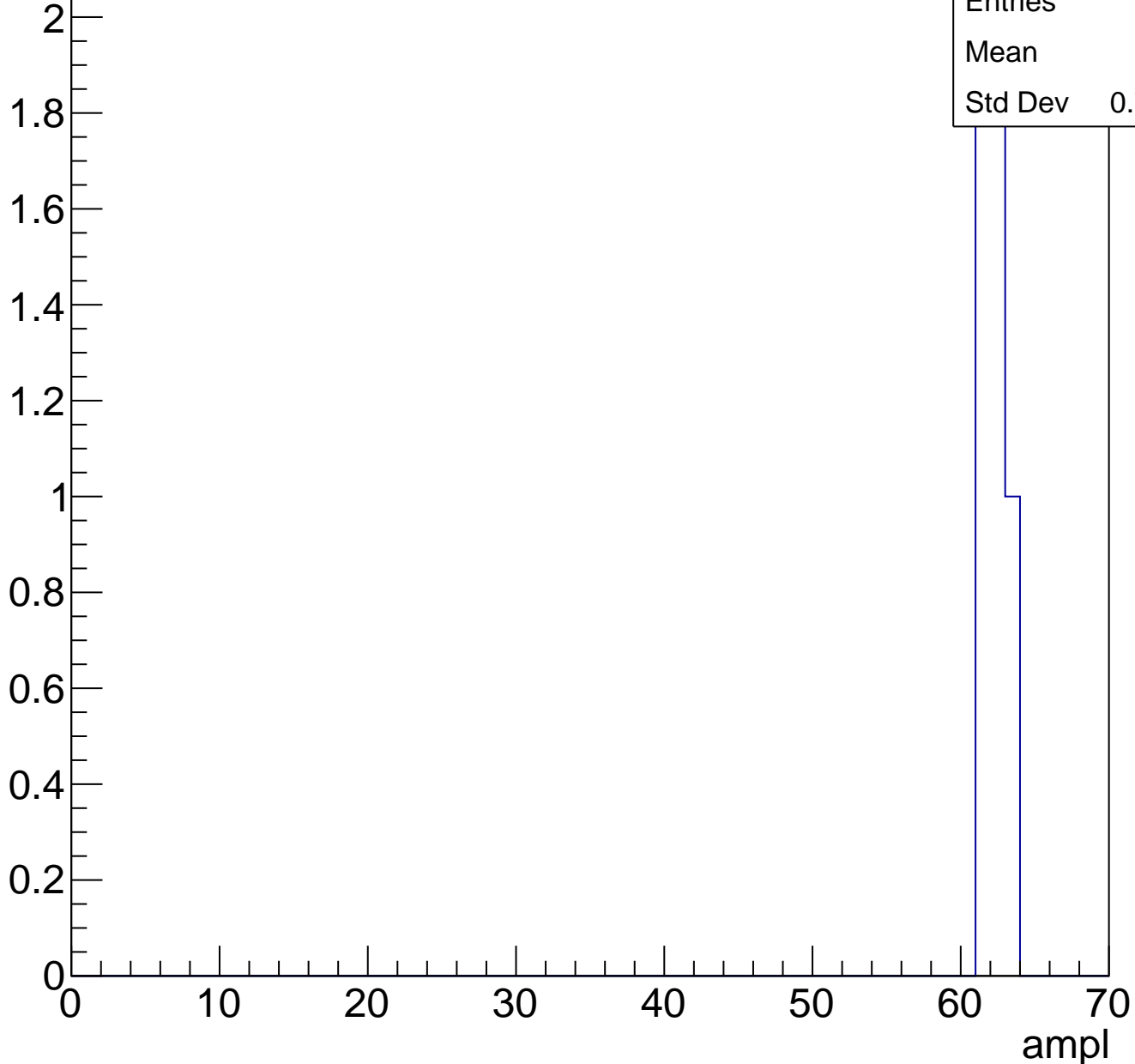
Entry



B1L101S, U17-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch53, adc0

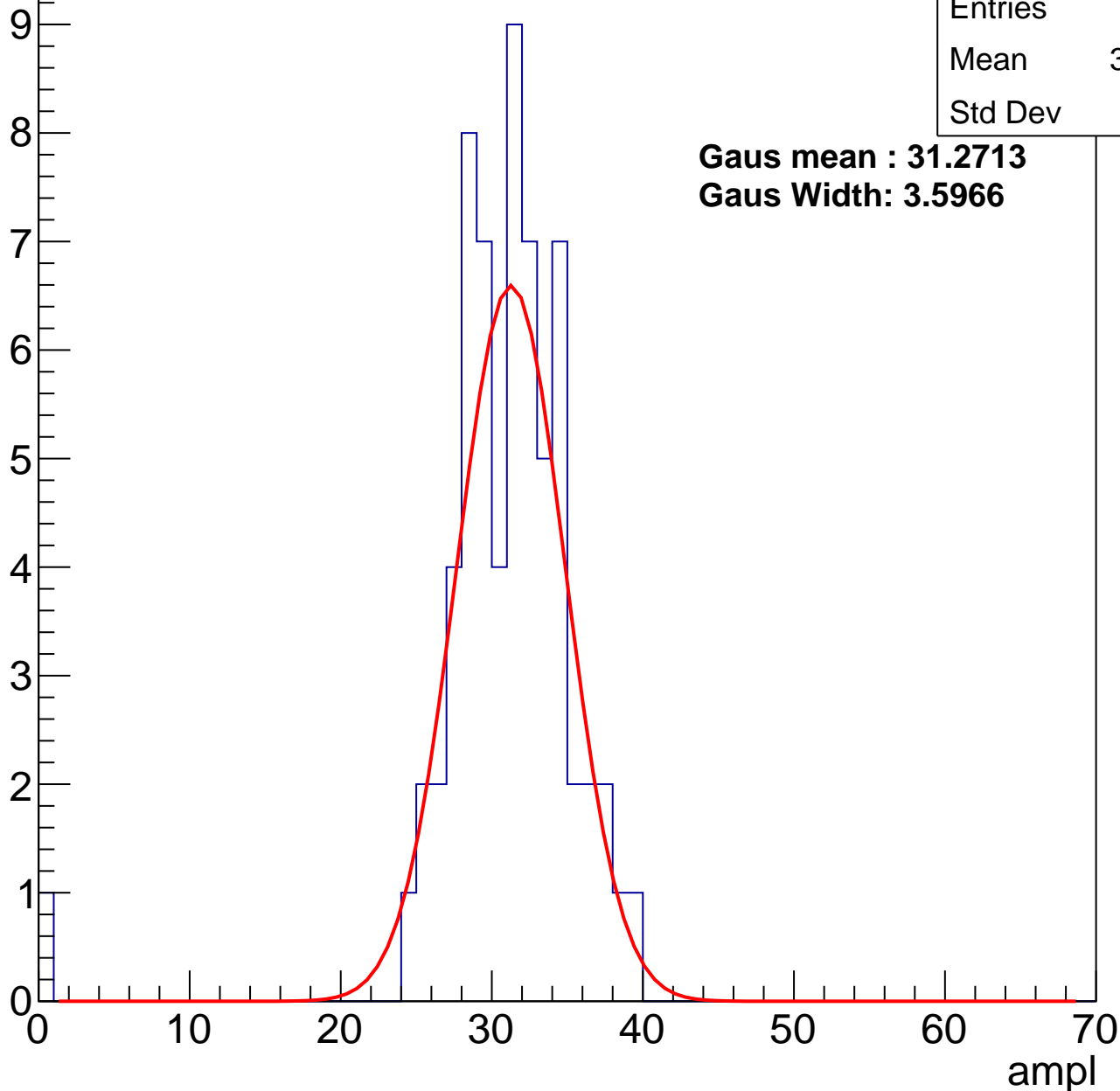
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.46
Std Dev	5.02

Gaus mean : 31.2713

Gaus Width: 3.5966



B1L101S, U17-ch53, adc1

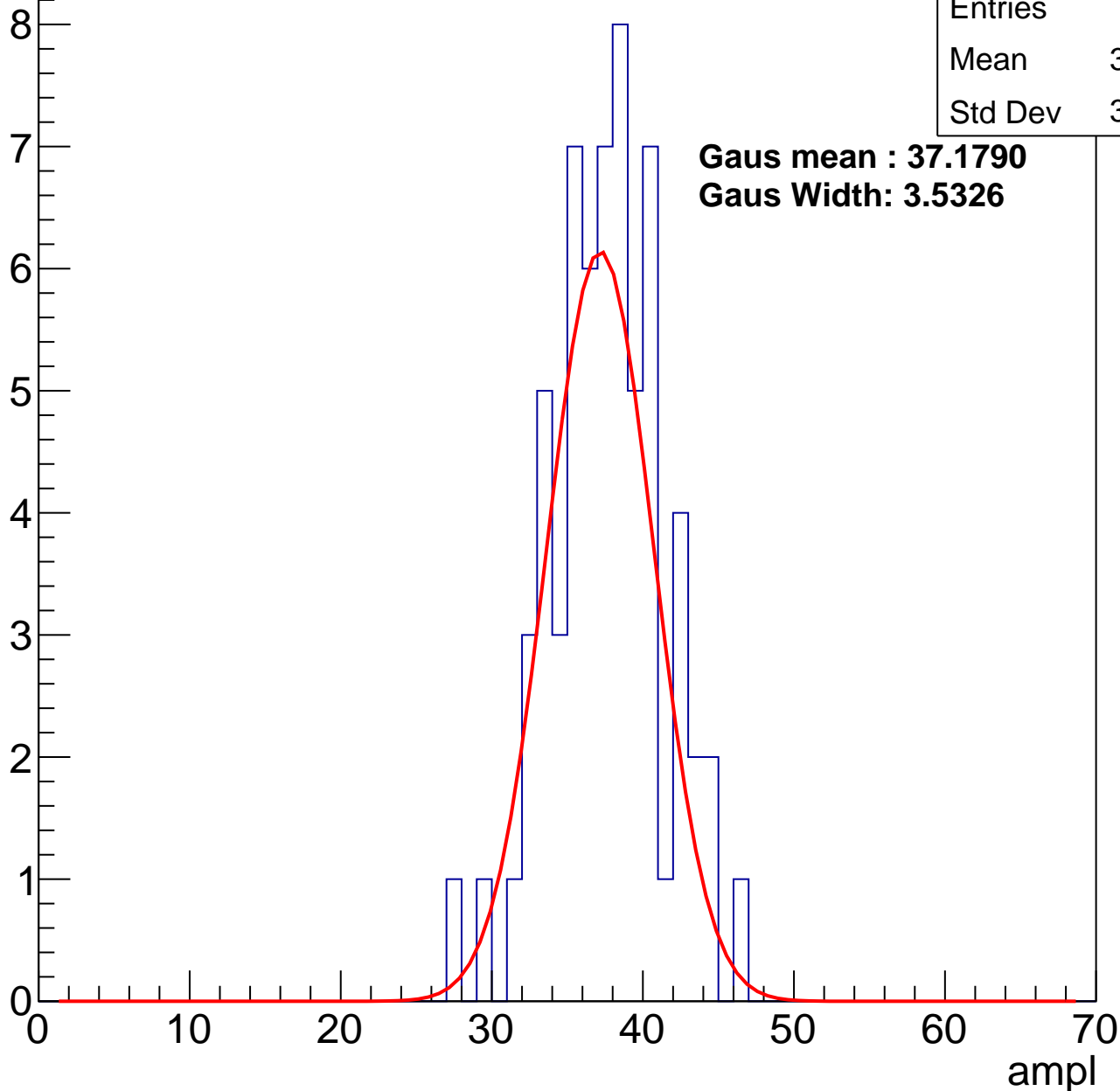
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.16
Std Dev	3.692

Gaus mean : 37.1790

Gaus Width: 3.5326



B1L101S, U17-ch53, adc2

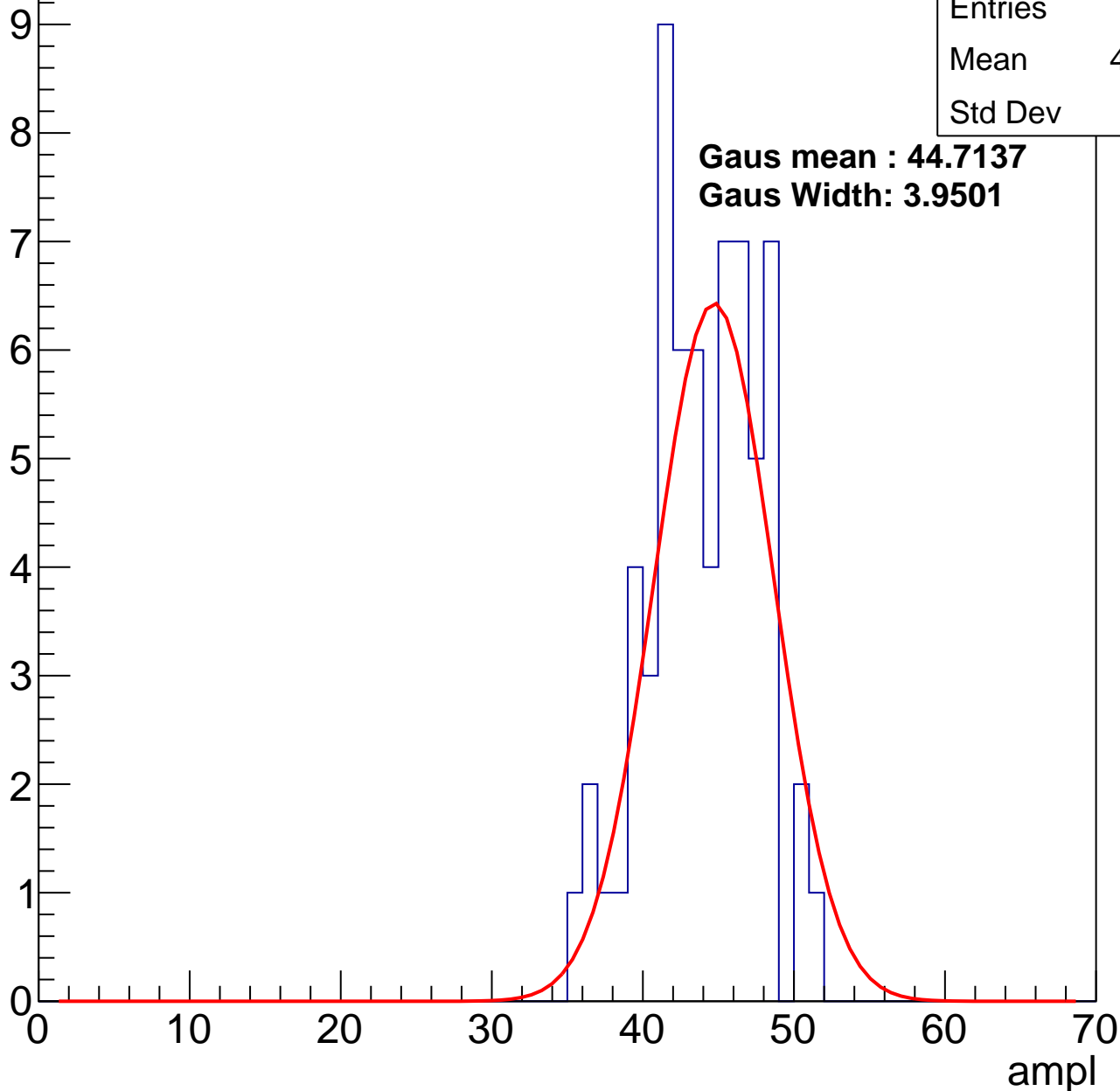
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.52
Std Dev	3.59

Gaus mean : 44.7137

Gaus Width: 3.9501

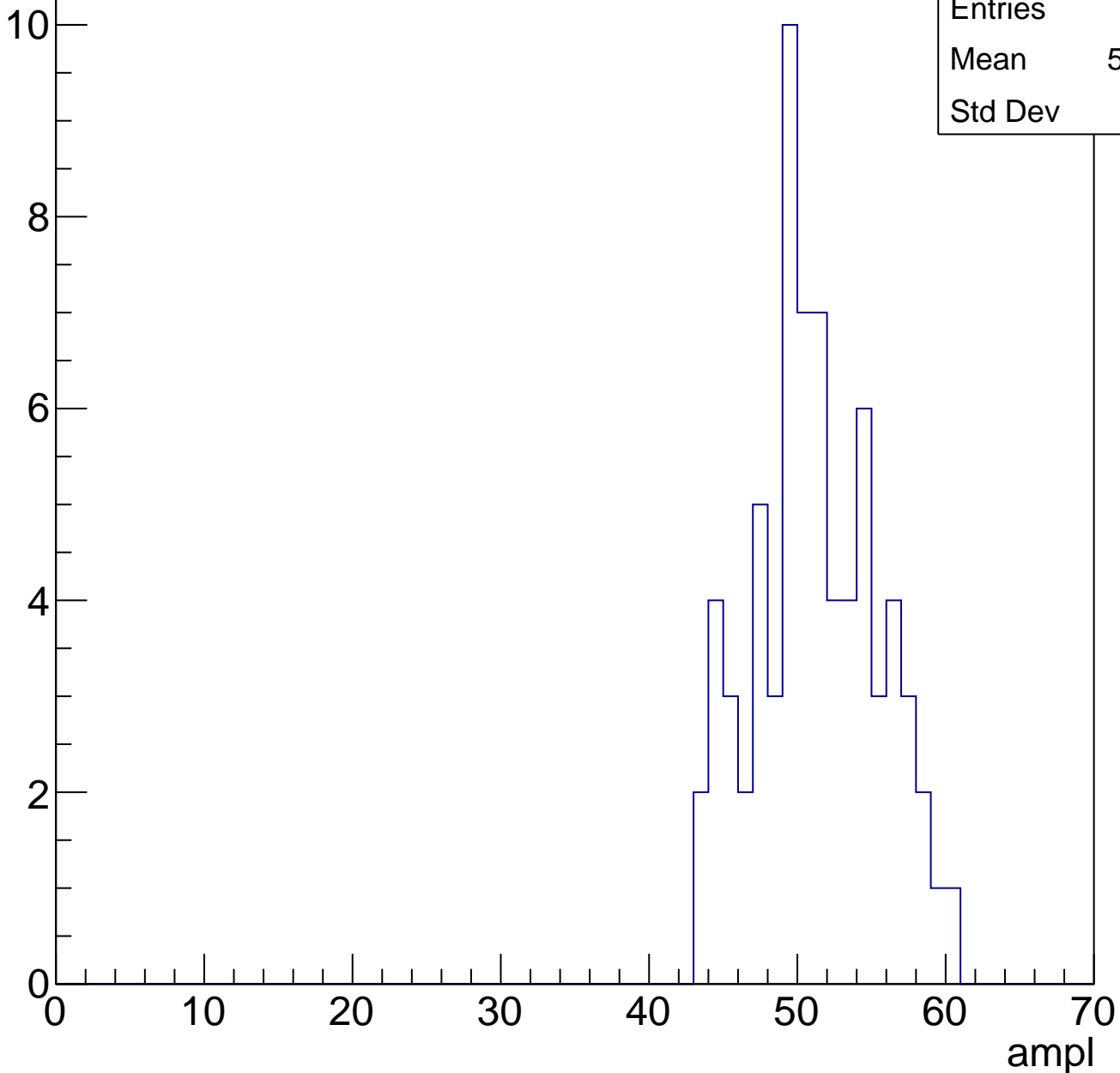


B1L101S, U17-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.76
Std Dev	4.12

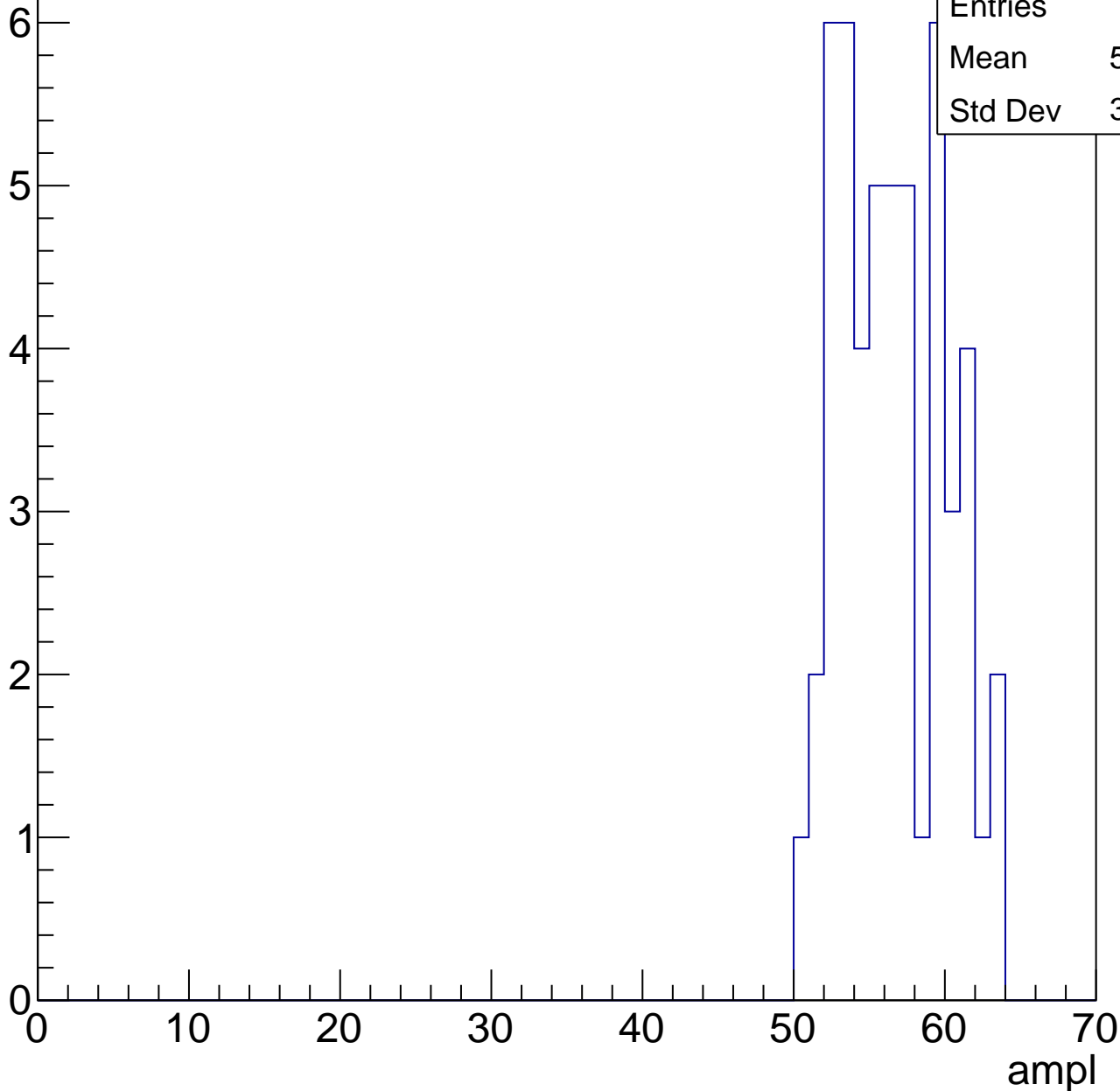


B1L101S, U17-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.12
Std Dev	3.434



B1L101S, U17-ch53, adc5

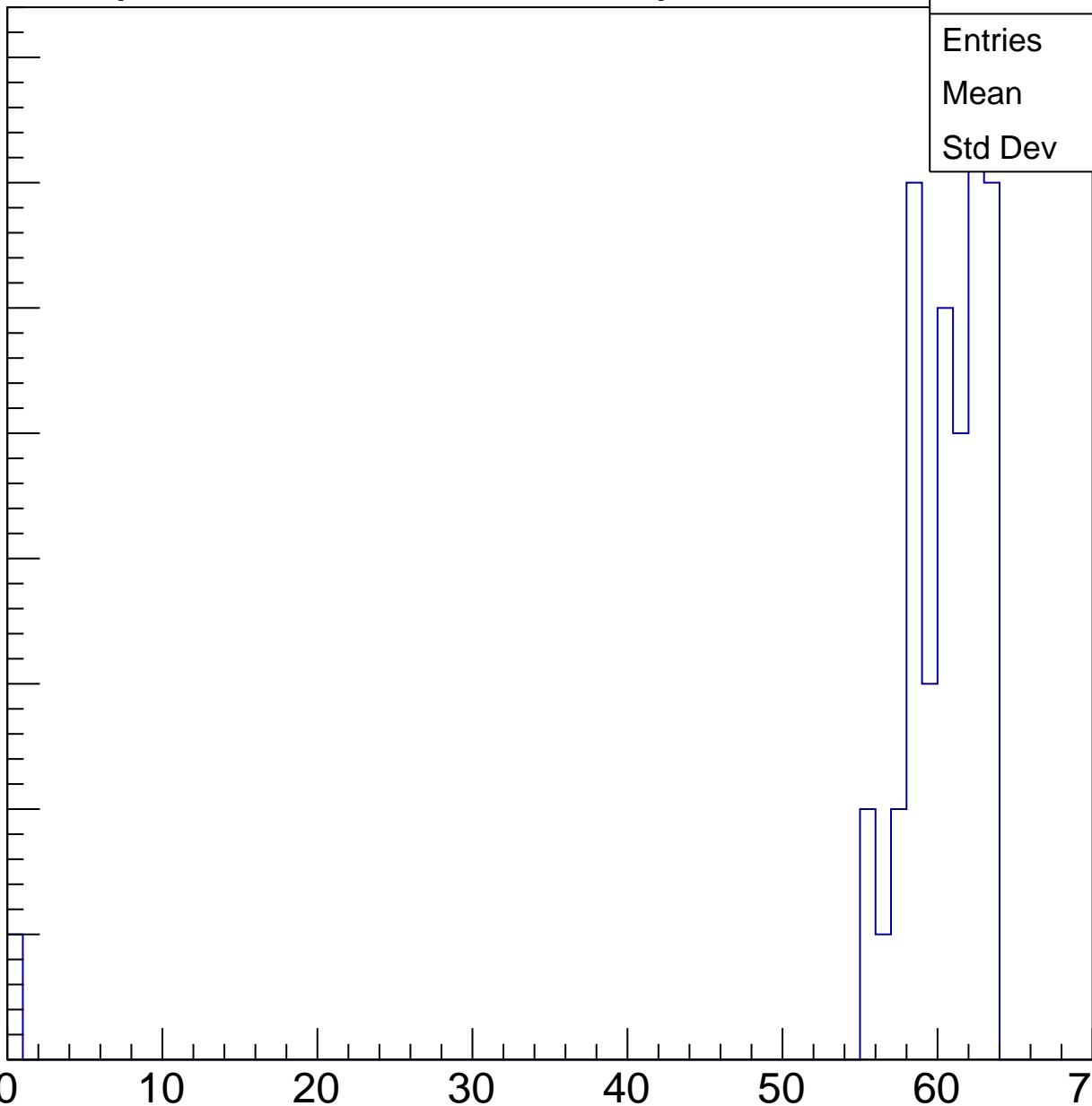
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.69
Std Dev	9.44

ampl



B1L101S, U17-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch54, adc0

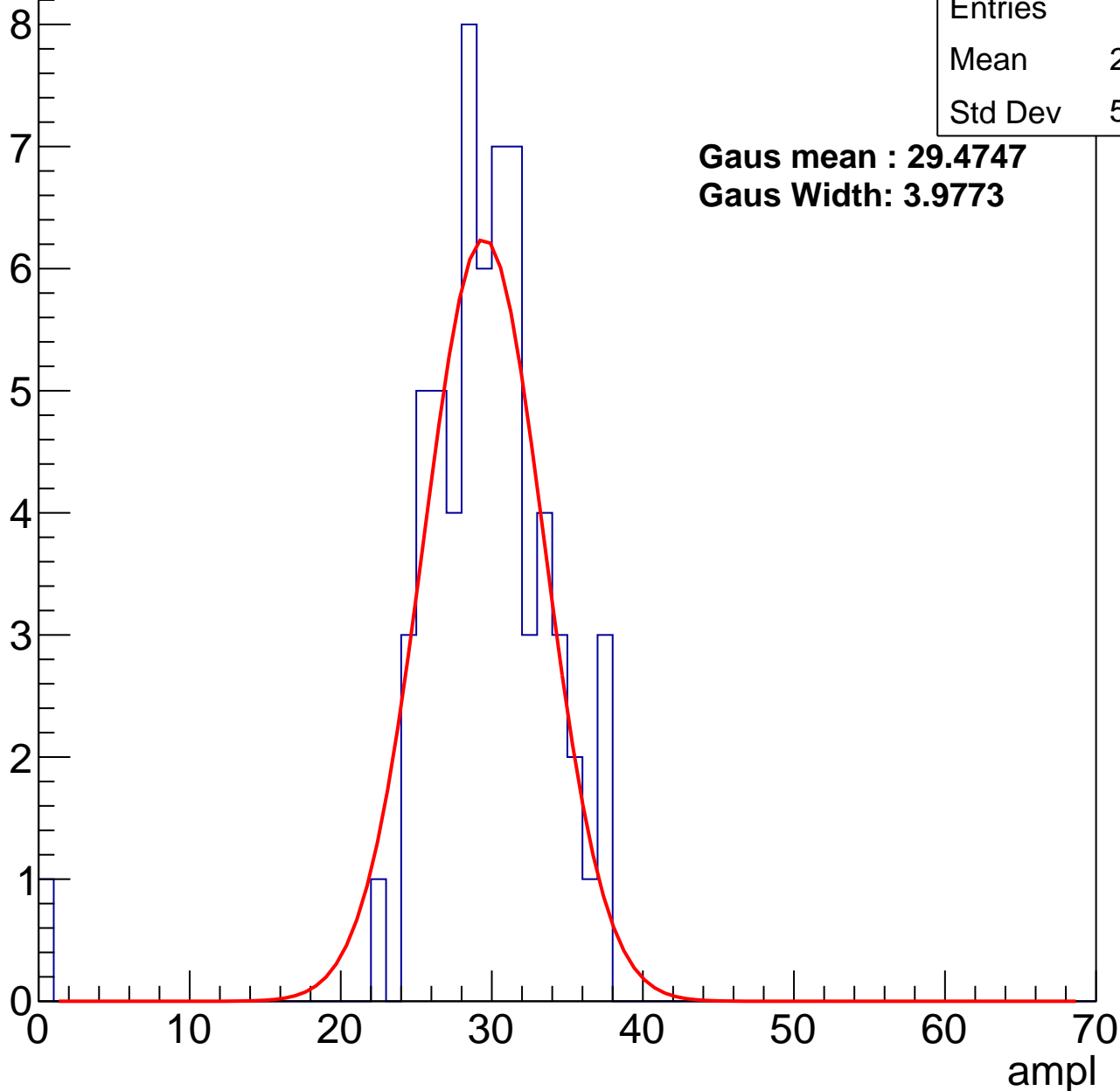
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.03
Std Dev	5.083

Gaus mean : 29.4747

Gaus Width: 3.9773



B1L101S, U17-ch54, adc1

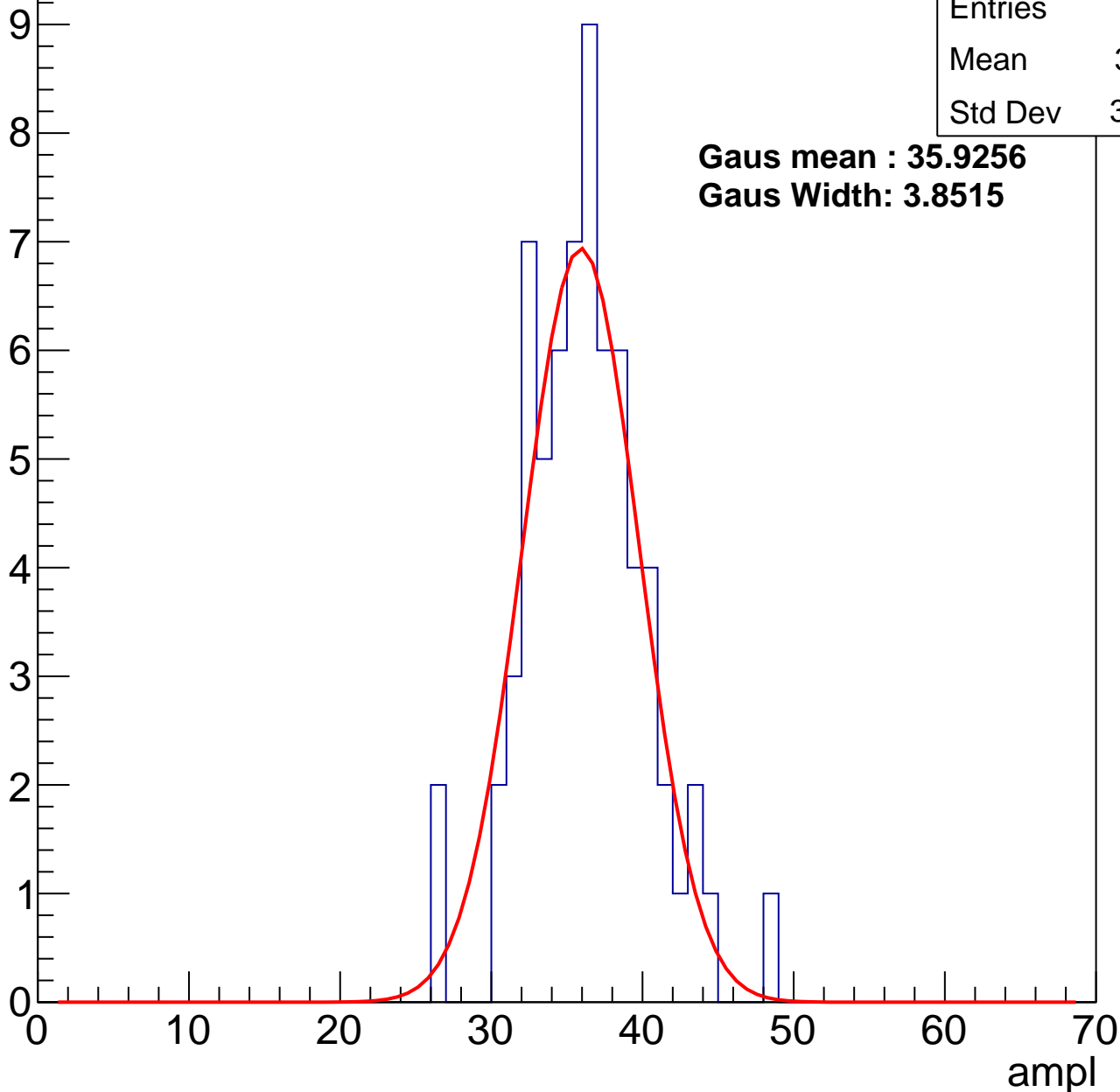
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.81
Std Dev	3.938

Gaus mean : 35.9256

Gaus Width: 3.8515



B1L101S, U17-ch54, adc2

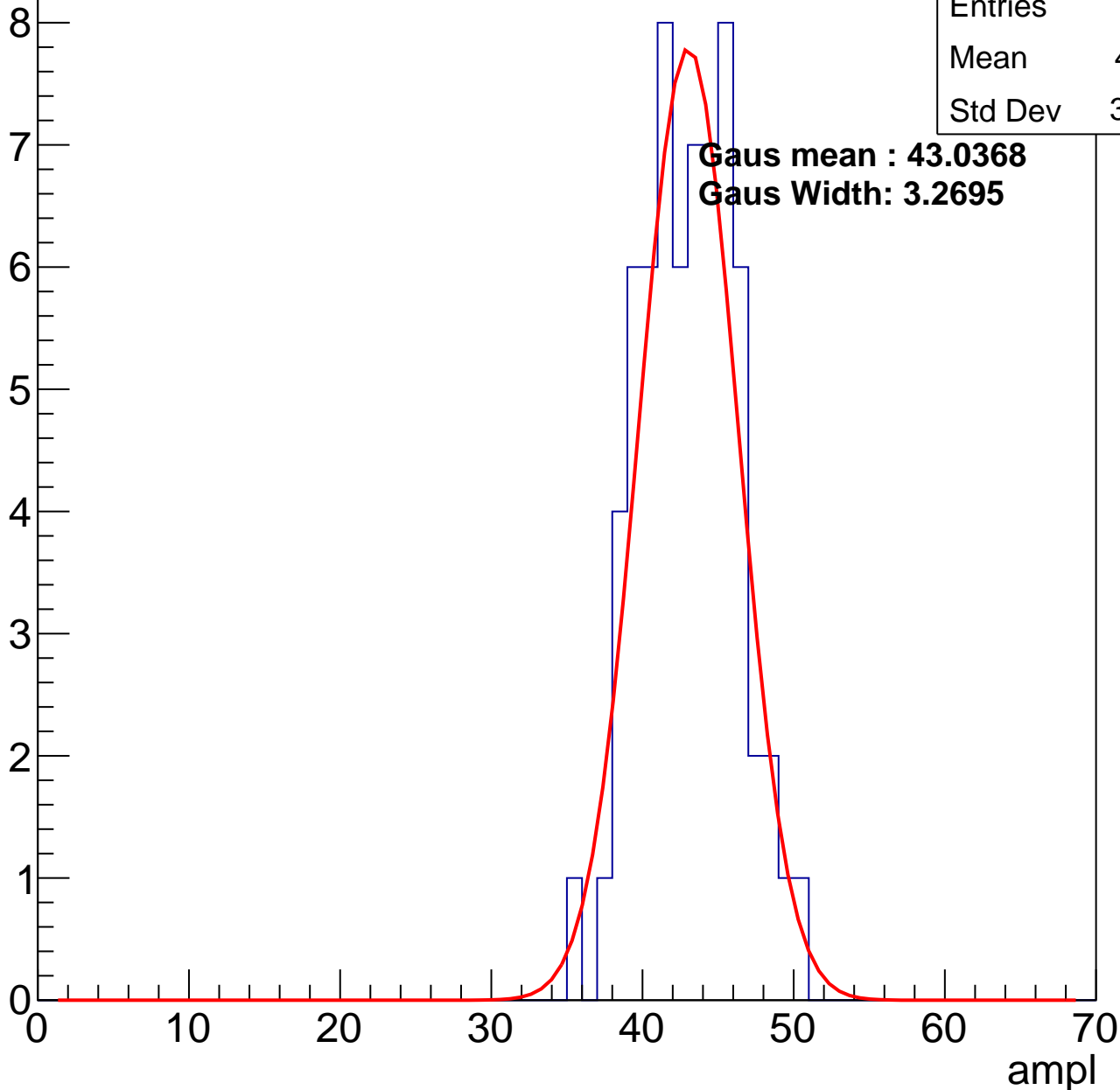
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.61
Std Dev	3.109

Gaus mean : 43.0368

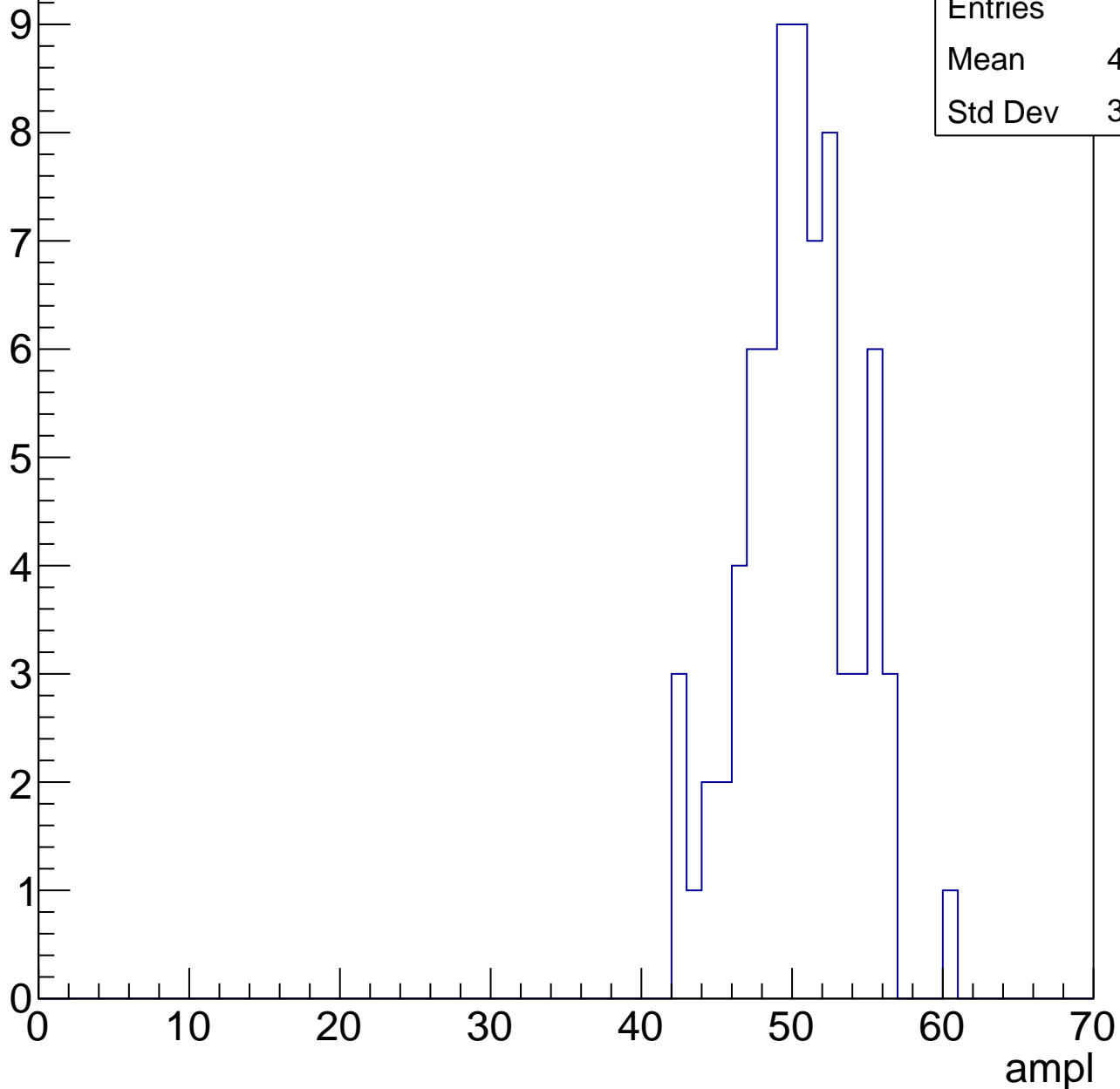
Gaus Width: 3.2695



B1L101S, U17-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

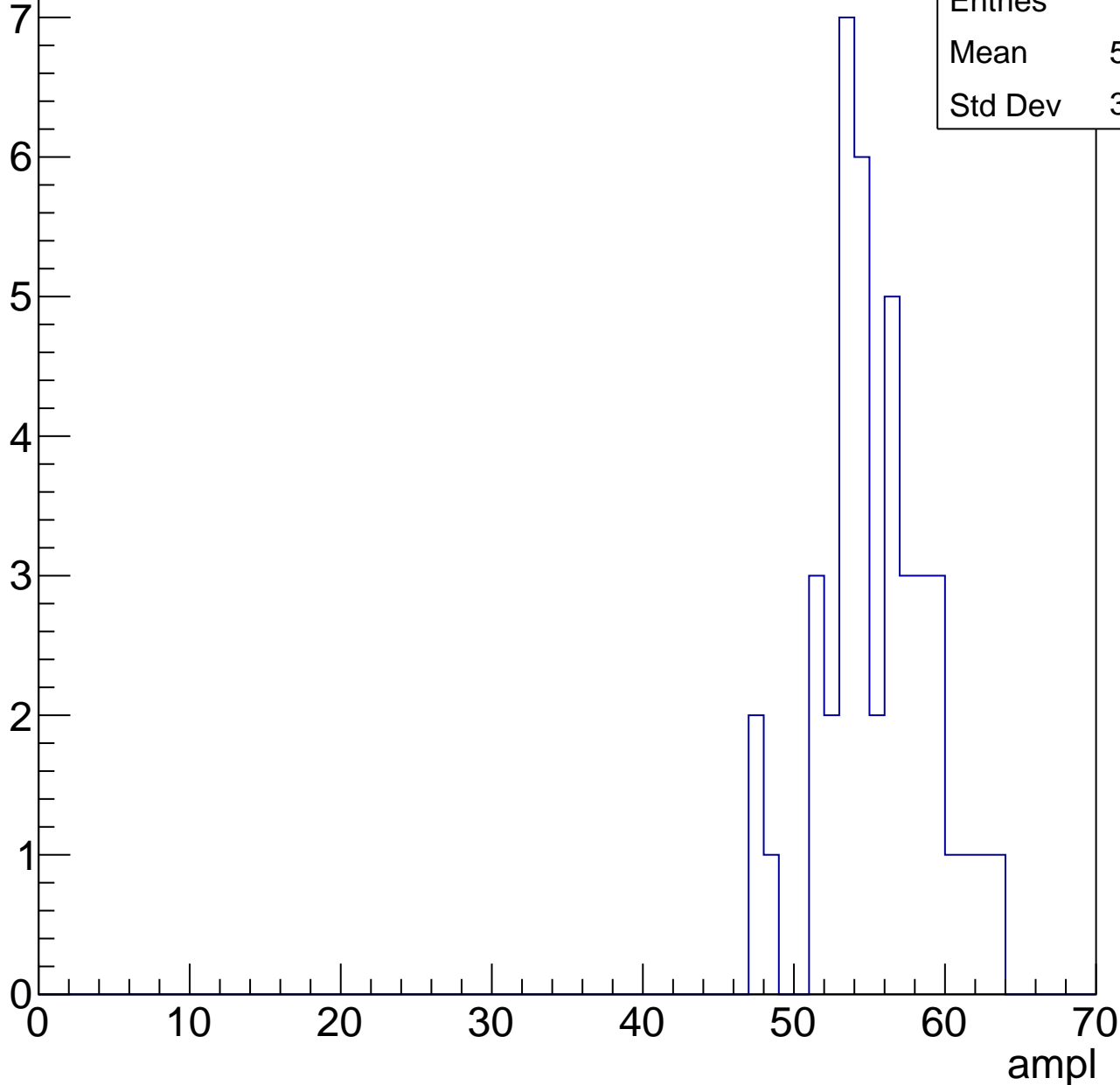


B1L101S, U17-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	54.93
Std Dev	3.638

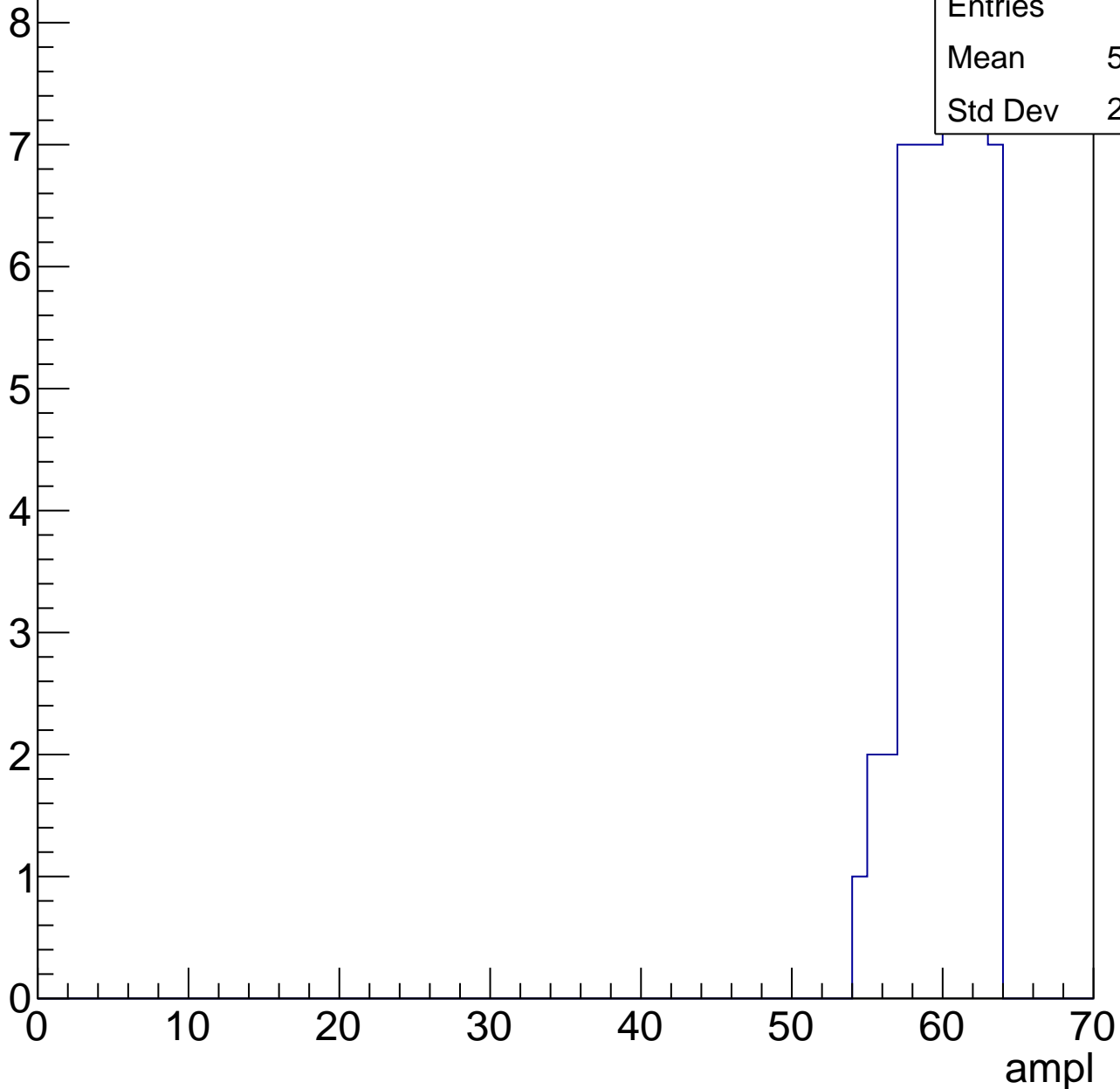


B1L101S, U17-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	59.63
Std Dev	2.337



B1L101S, U17-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U17-ch55, adc0

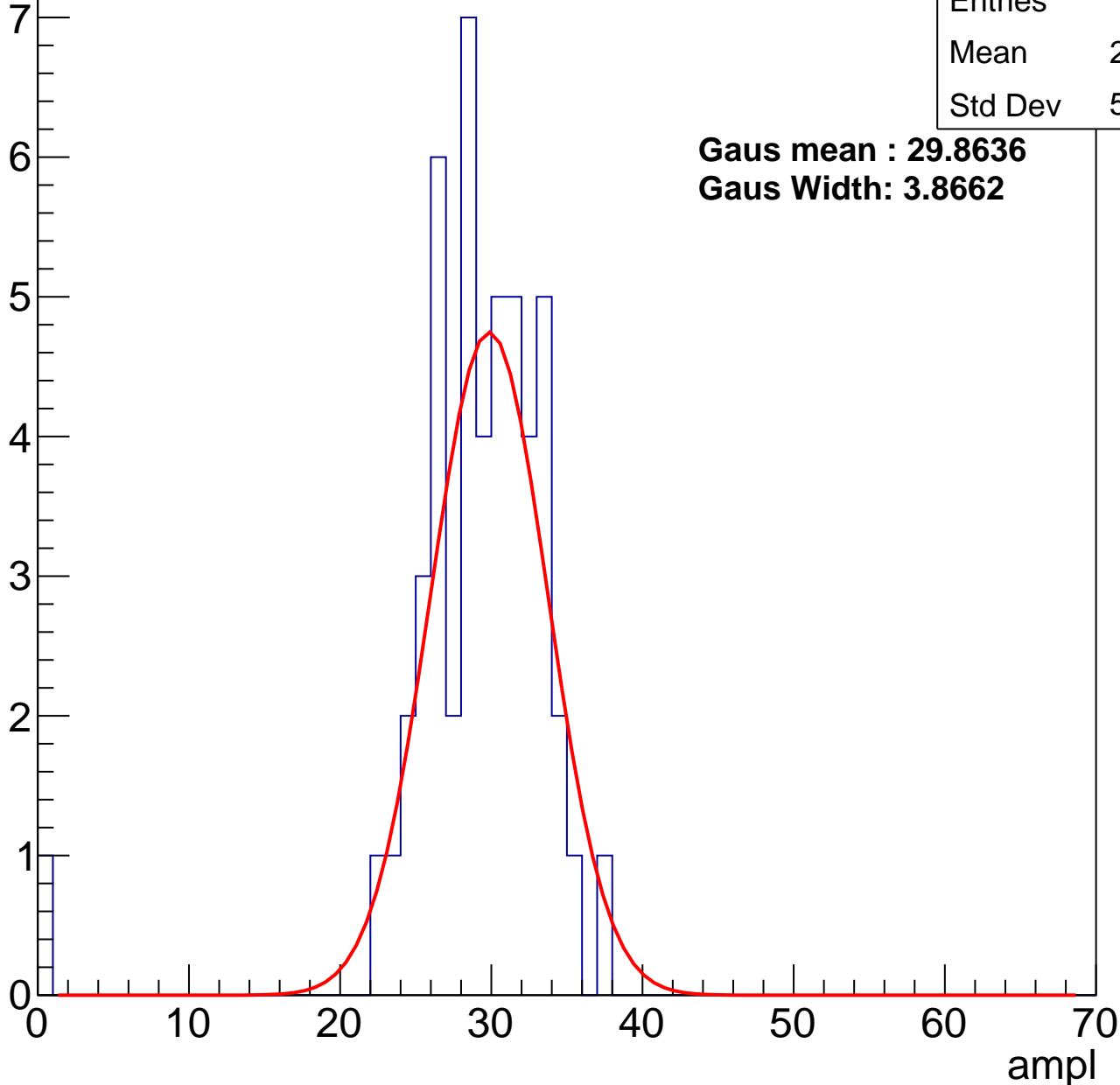
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	28.56
Std Dev	5.243

Gaus mean : 29.8636

Gaus Width: 3.8662



B1L101S, U17-ch55, adc1

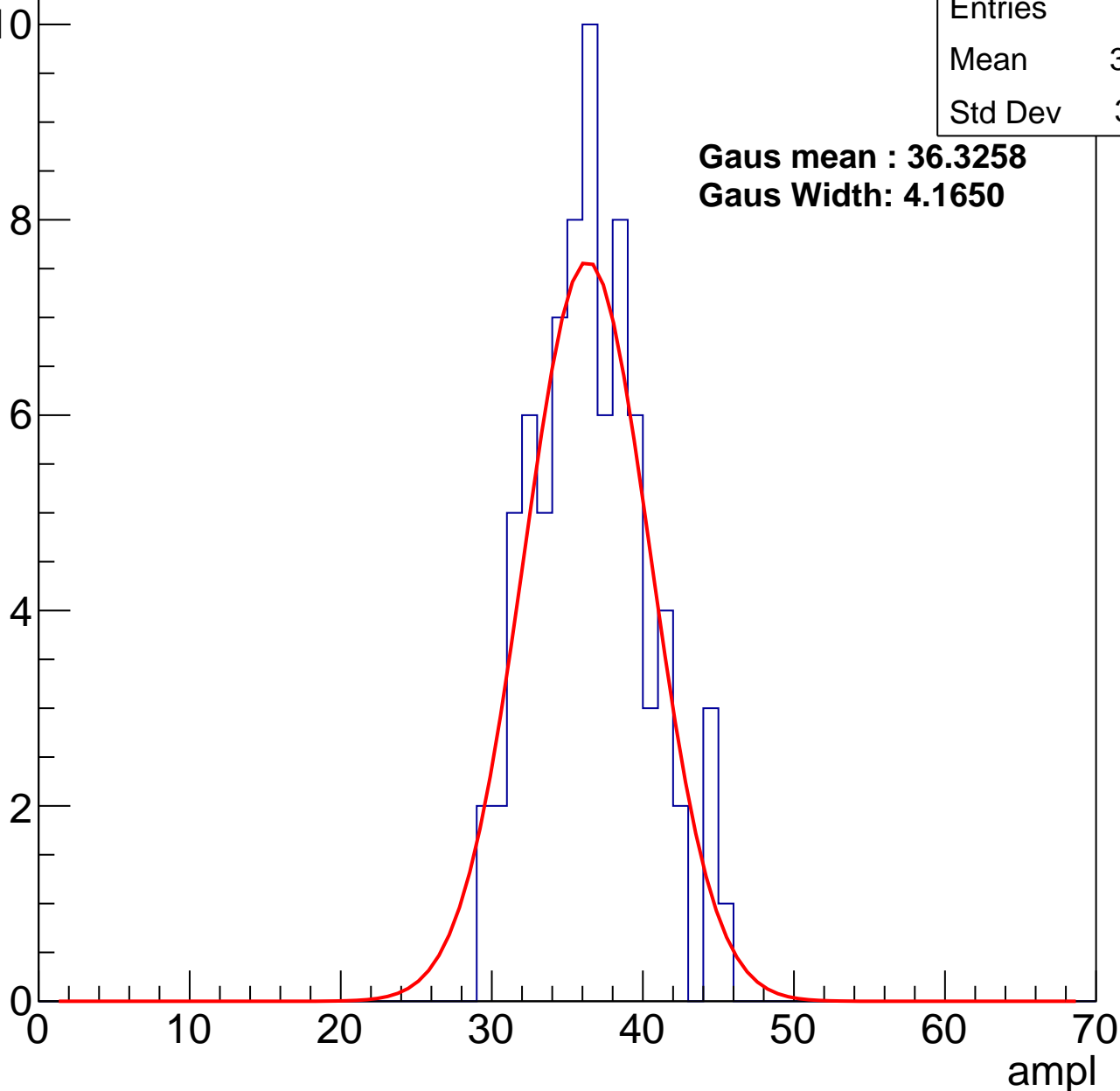
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.06
Std Dev	3.681

Gaus mean : 36.3258

Gaus Width: 4.1650



B1L101S, U17-ch55, adc2

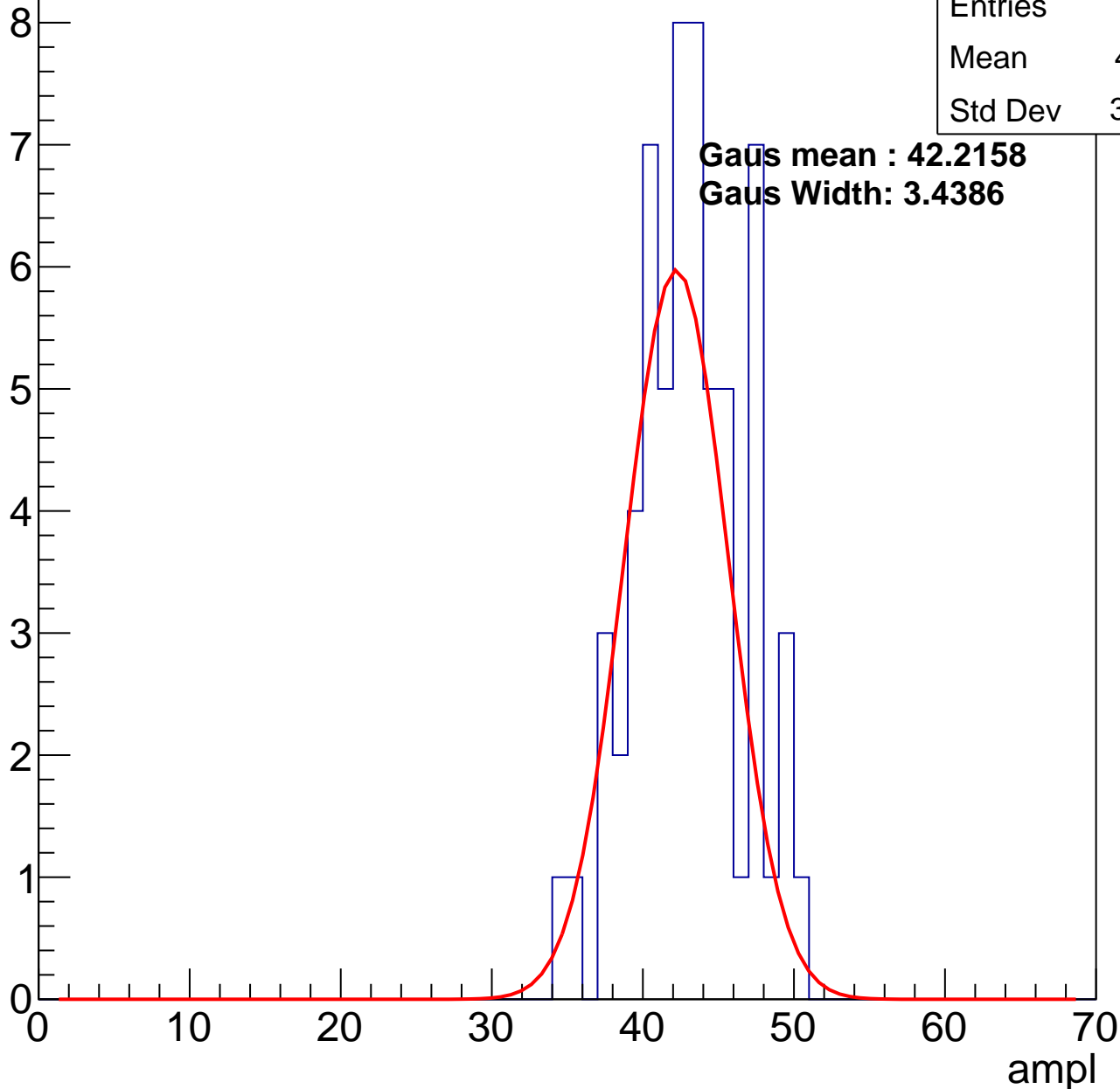
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.61
Std Dev	3.539

Gaus mean : 42.2158

Gaus Width: 3.4386

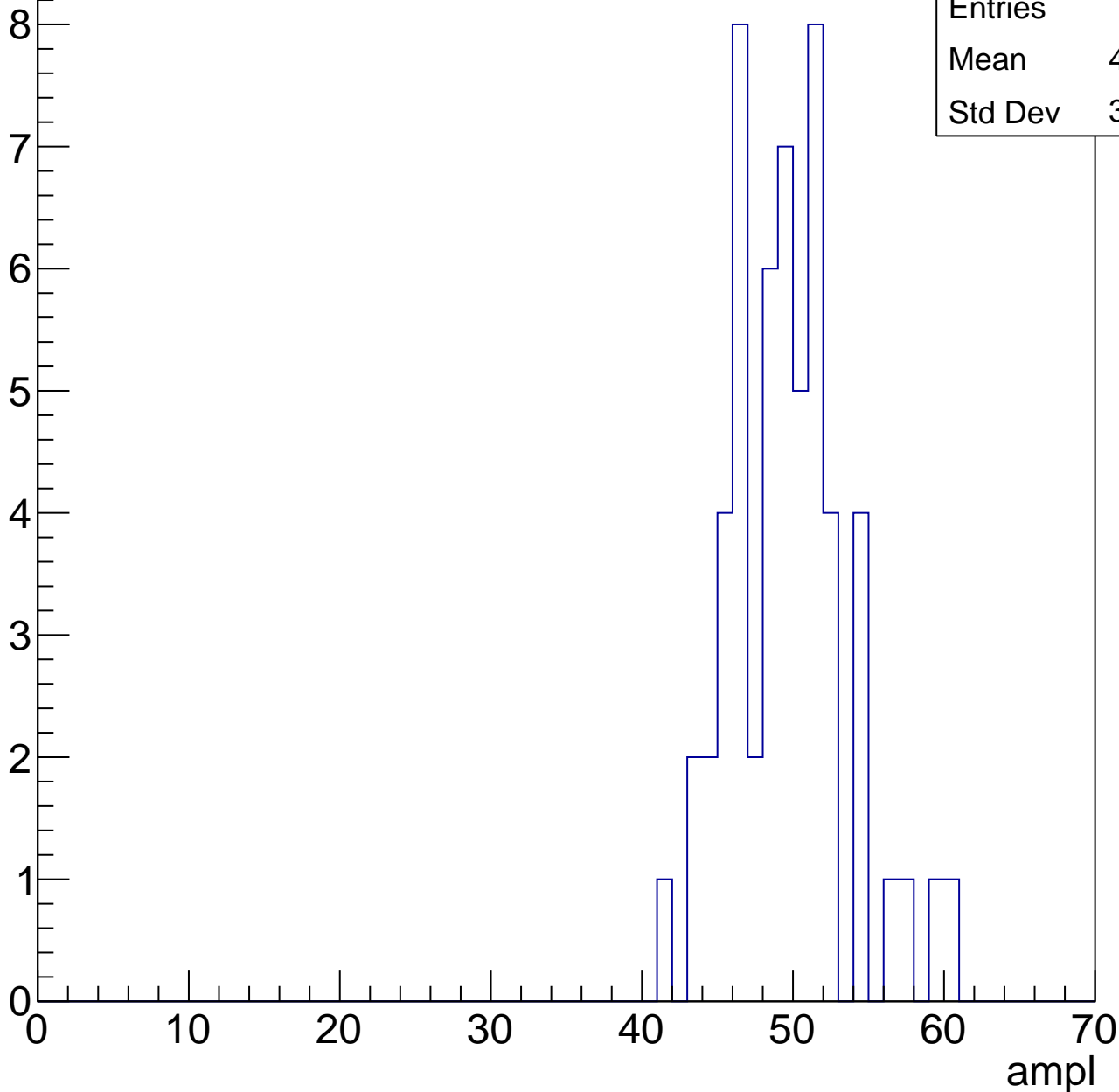


B1L101S, U17-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	49.16
Std Dev	3.842



B1L101S, U17-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	54.9
Std Dev	3.697

Entry

10

8

6

4

2

0

0

10

20

30

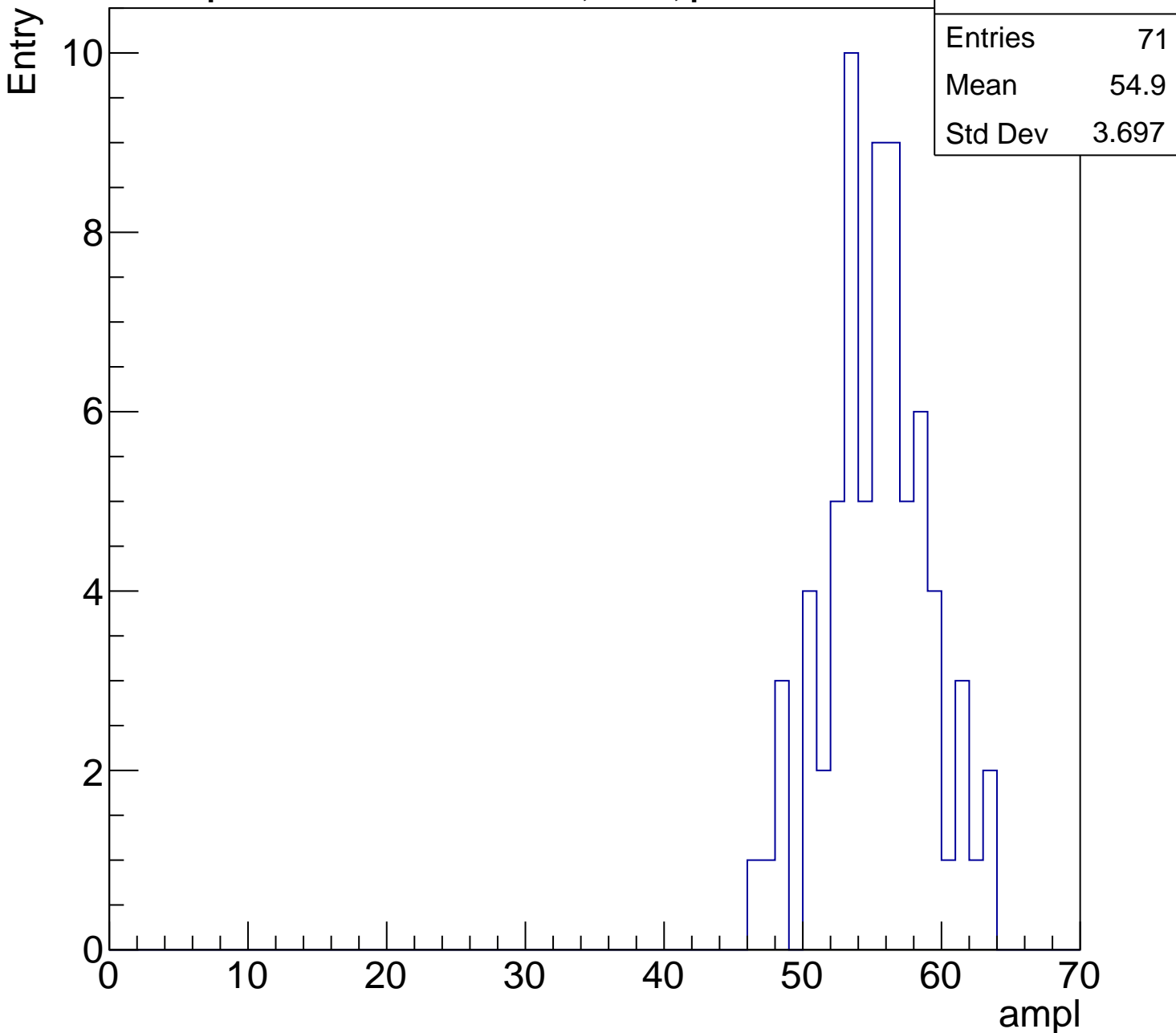
40

50

60

70

ampl



B1L101S, U17-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.47
Std Dev	9.357

ampl

0

10

20

30

40

50

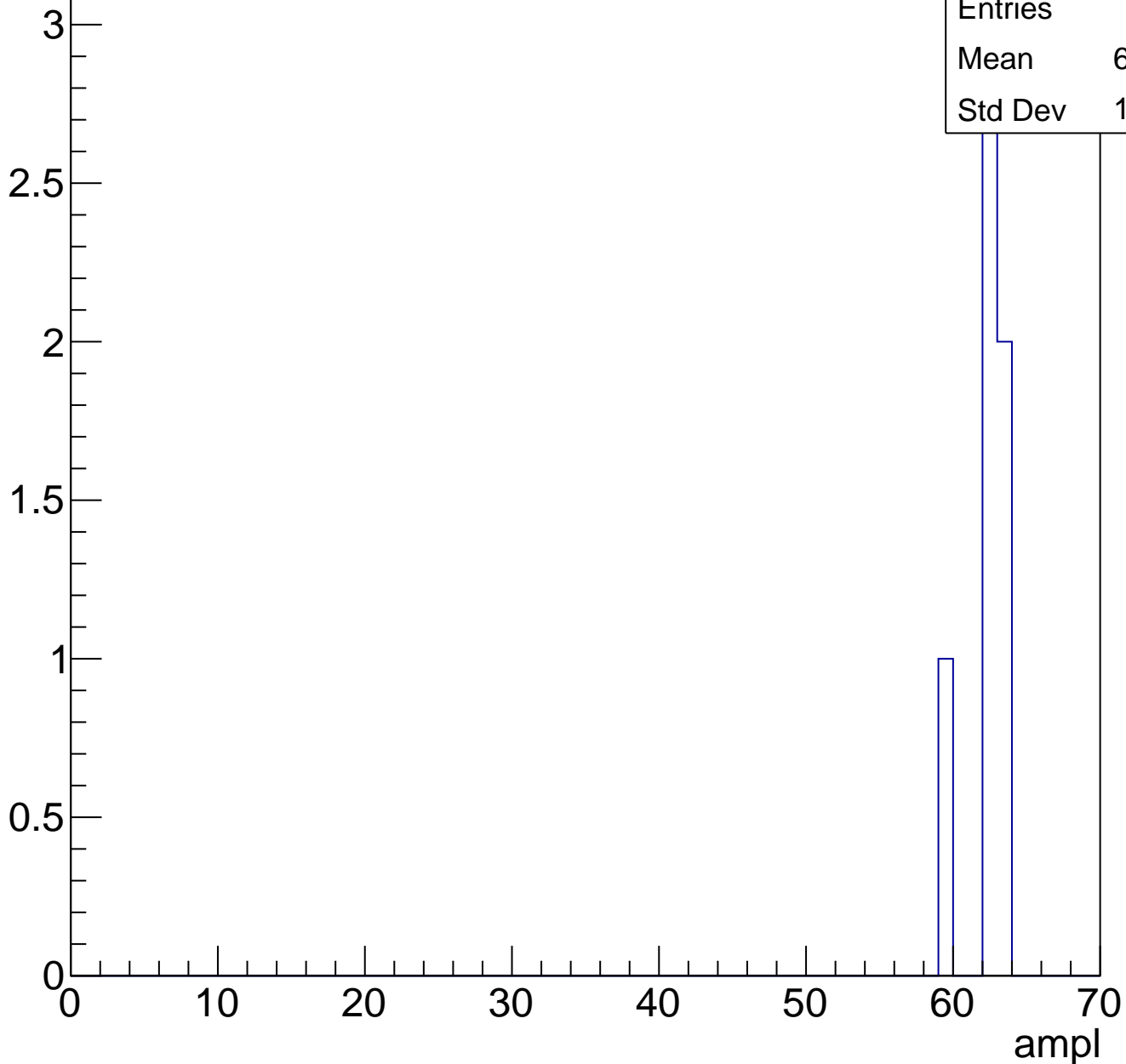
60

70

B1L101S, U17-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch56, adc0

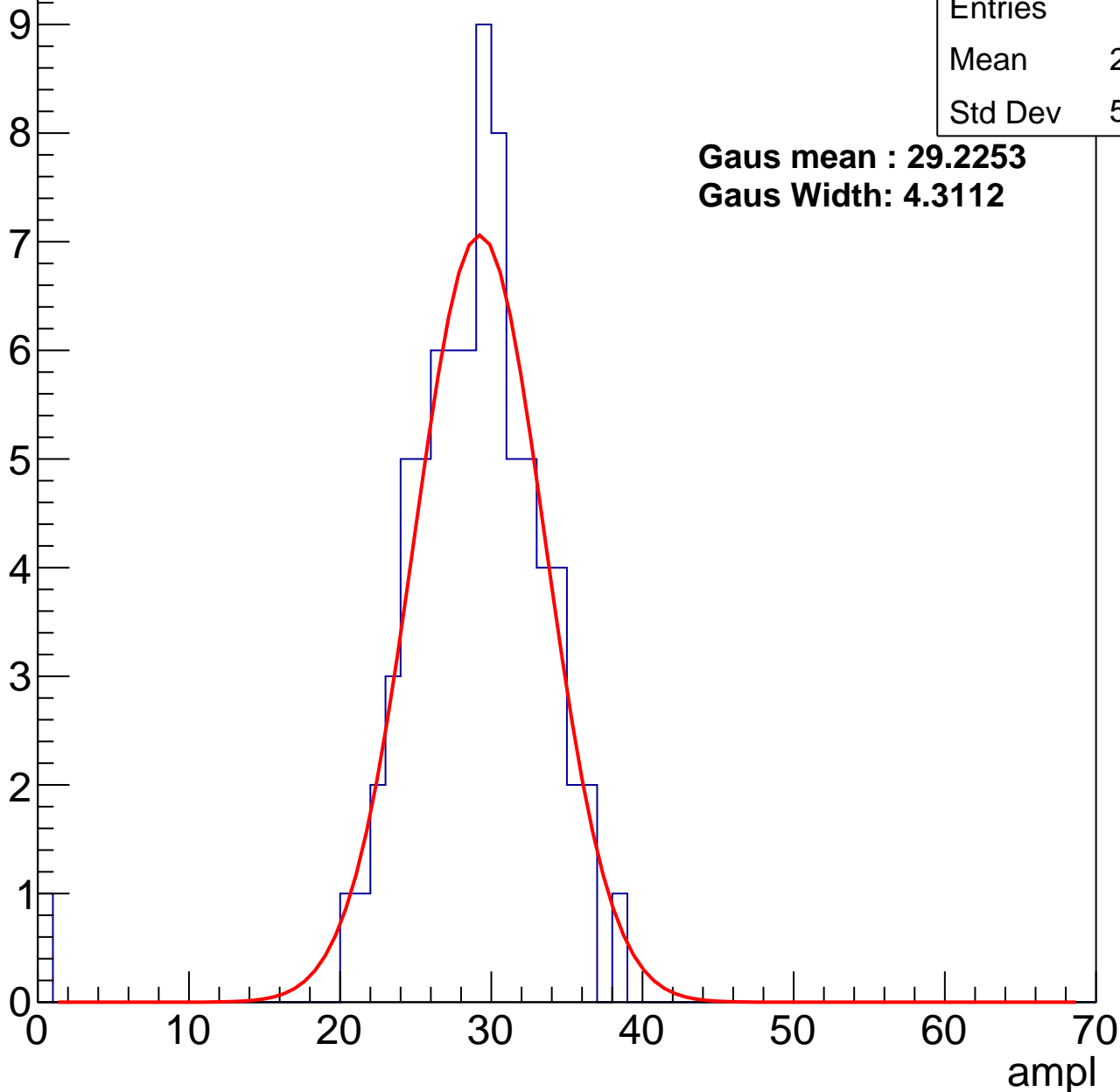
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.28
Std Dev	5.038

Gaus mean : 29.2253

Gaus Width: 4.3112



B1L101S, U17-ch56, adc1

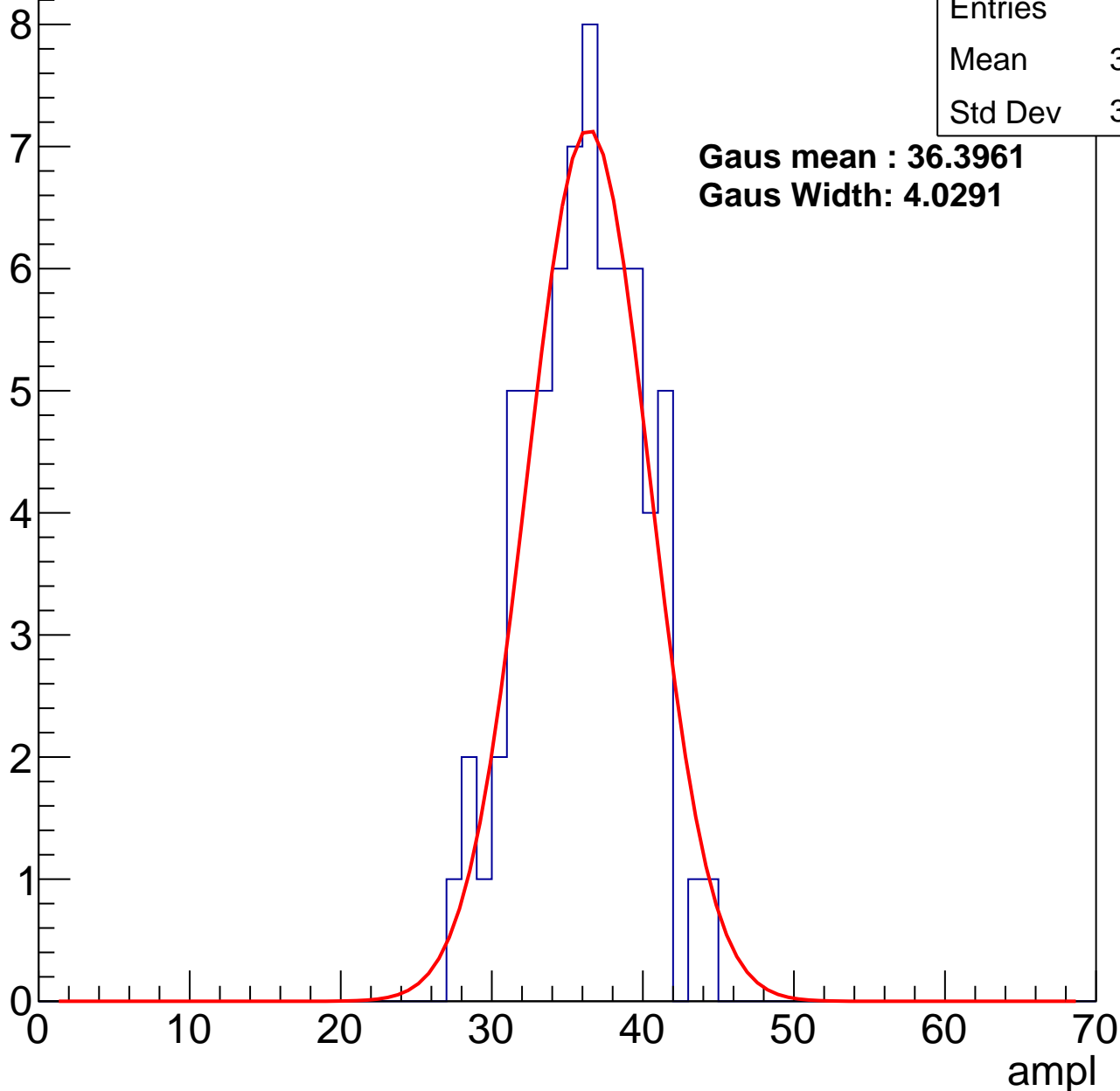
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.56
Std Dev	3.722

Gaus mean : 36.3961

Gaus Width: 4.0291



B1L101S, U17-ch56, adc2

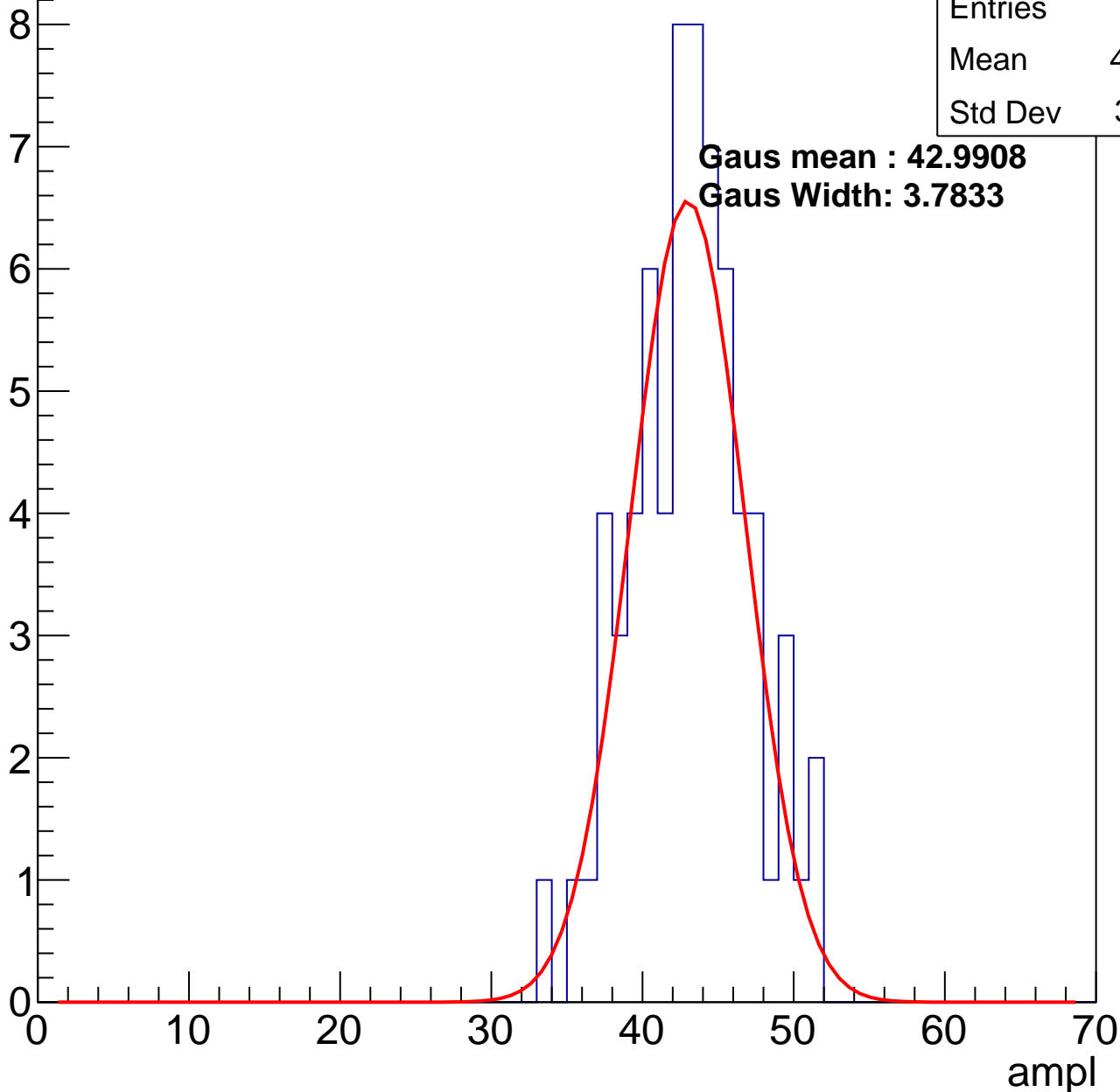
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.69
Std Dev	3.851

Gaus mean : 42.9908

Gaus Width: 3.7833

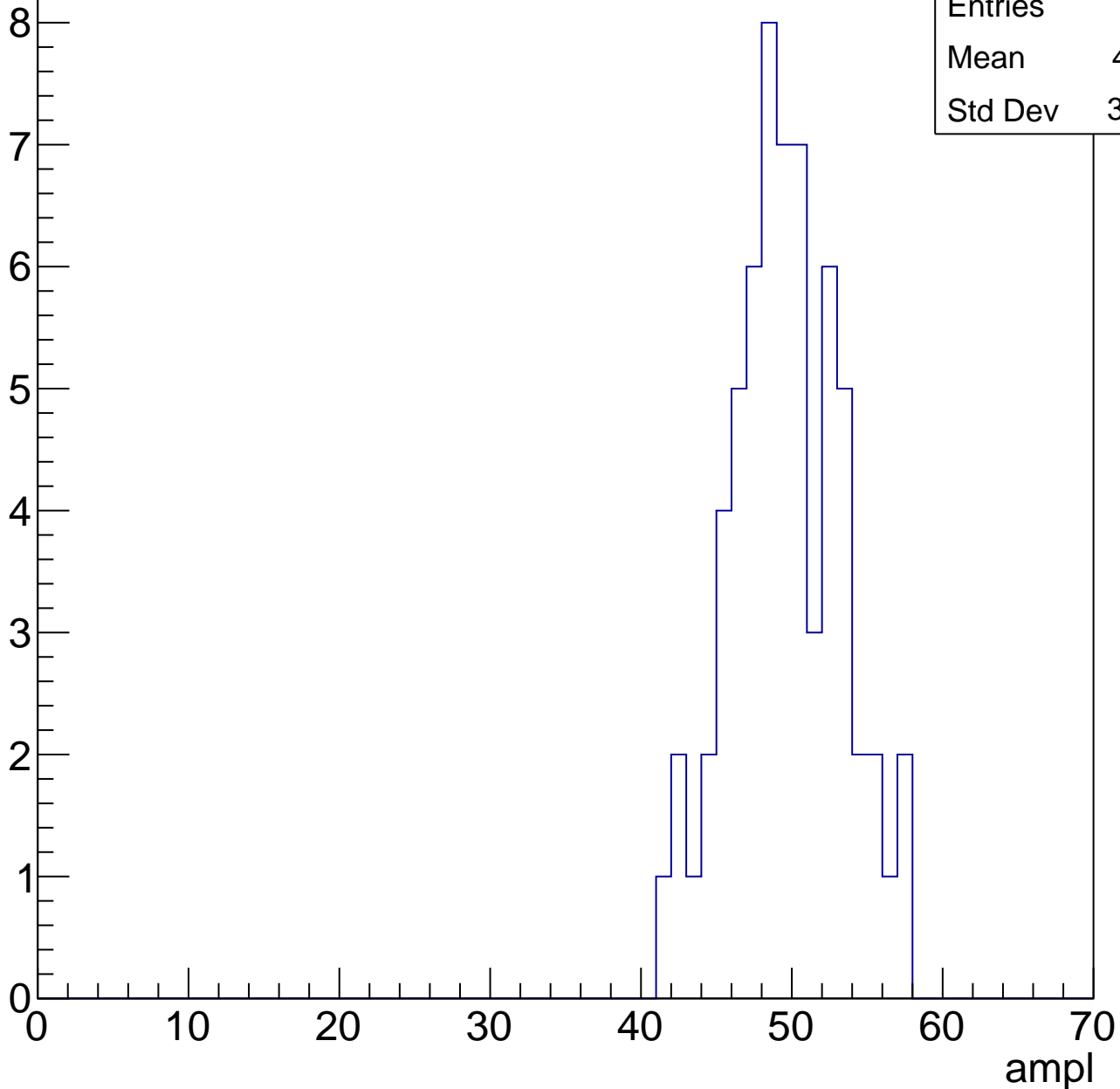


B1L101S, U17-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.11
Std Dev	3.623

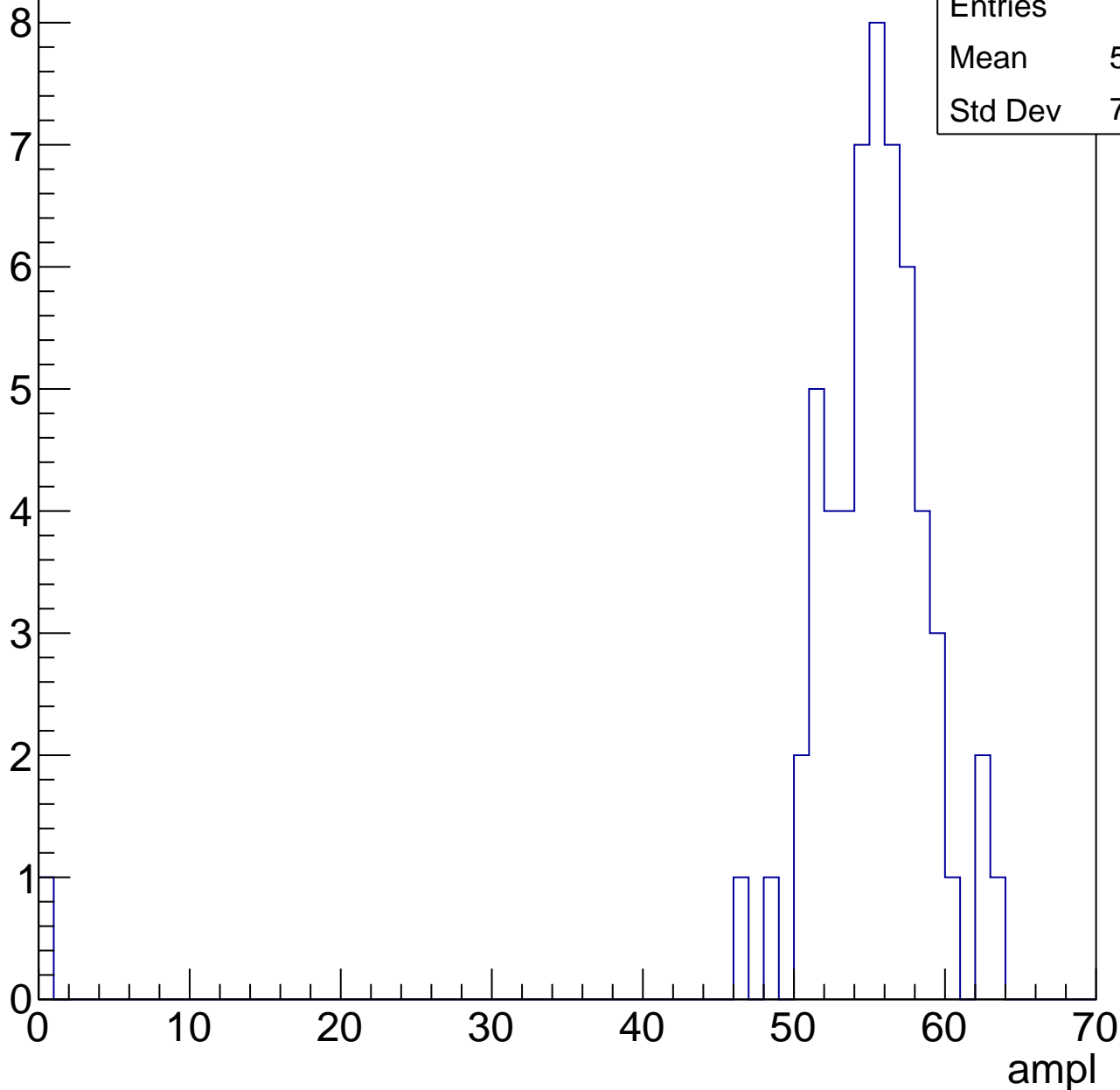


B1L101S, U17-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	53.98
Std Dev	7.933



B1L101S, U17-ch56, adc5

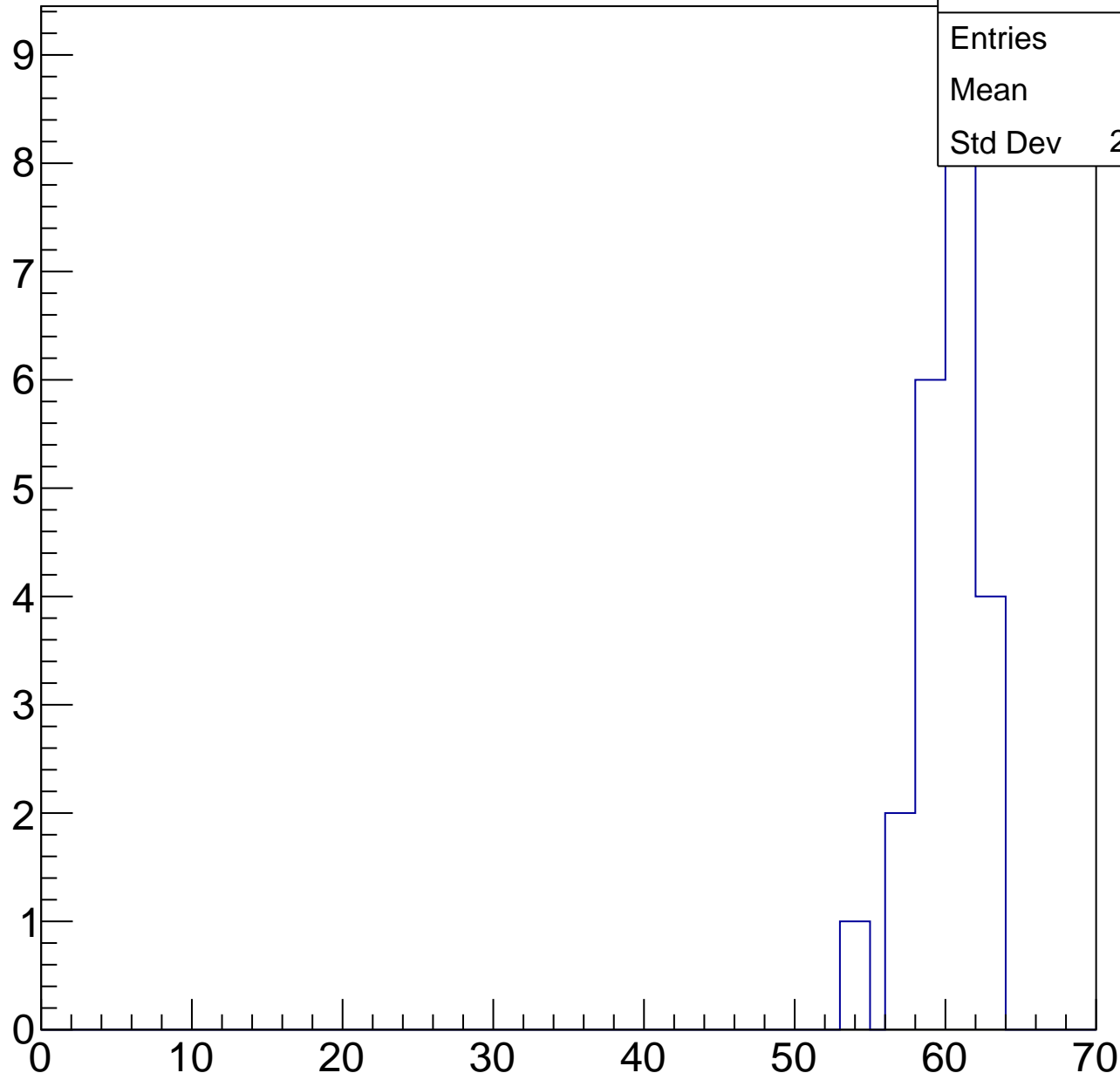
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.6
Std Dev	2.253

ampl

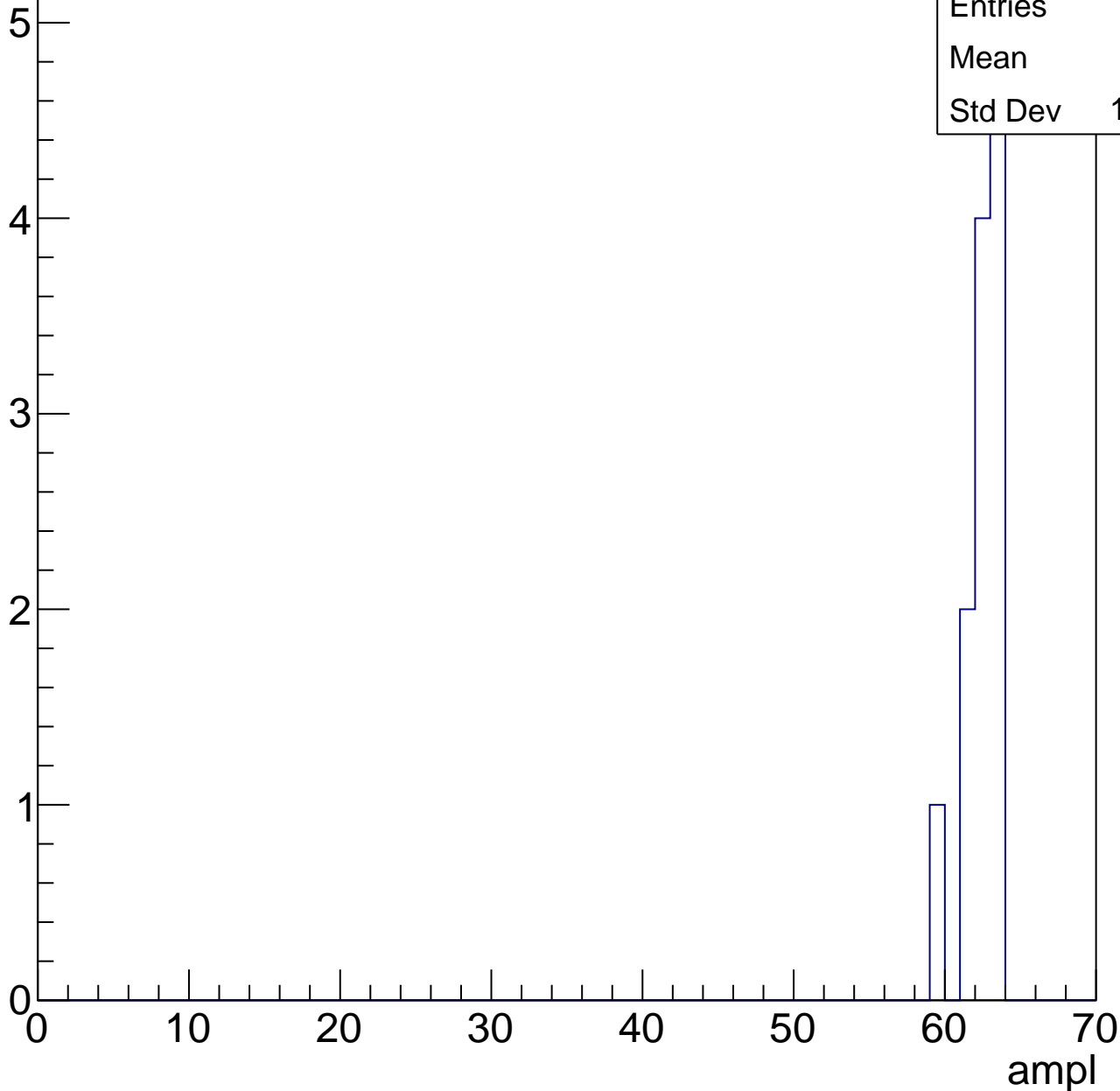


B1L101S, U17-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

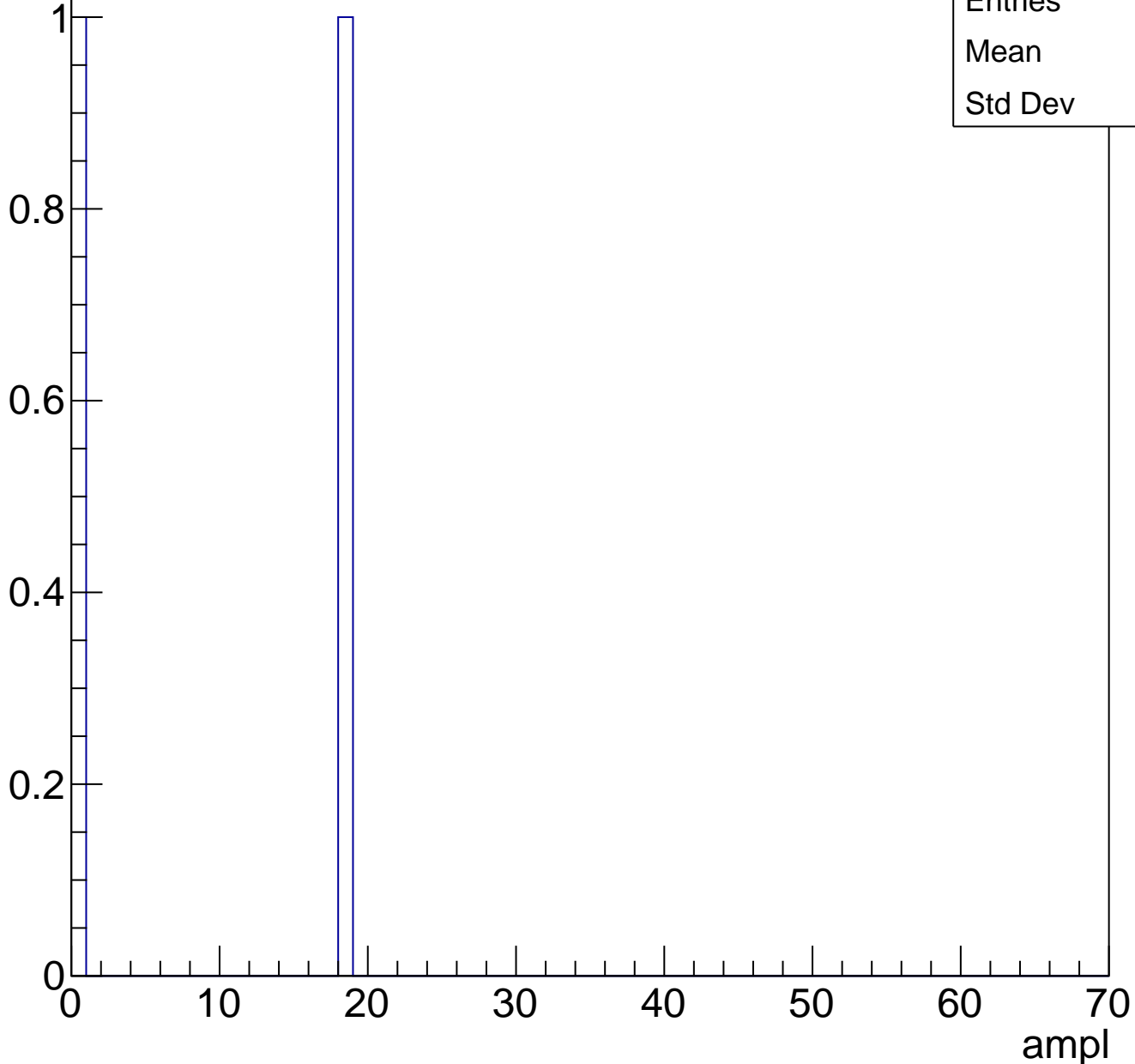
Entries	12
Mean	62
Std Dev	1.155



B1L101S, U17-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	9
Std Dev	9

B1L101S, U17-ch57, adc0

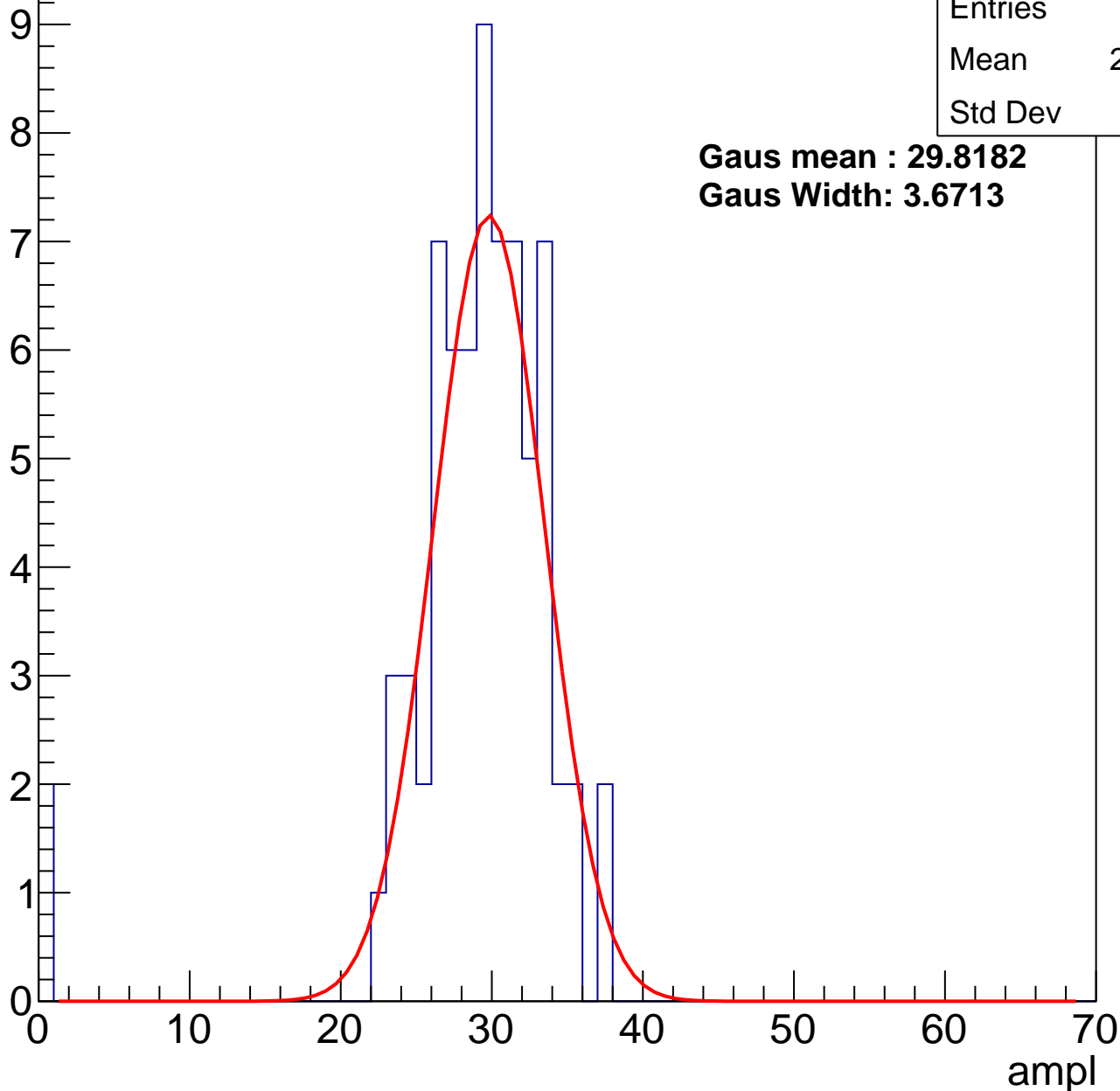
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.39
Std Dev	5.88

Gaus mean : 29.8182

Gaus Width: 3.6713



B1L101S, U17-ch57, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	35.84
Std Dev	3.529

Gaus mean : 36.5036

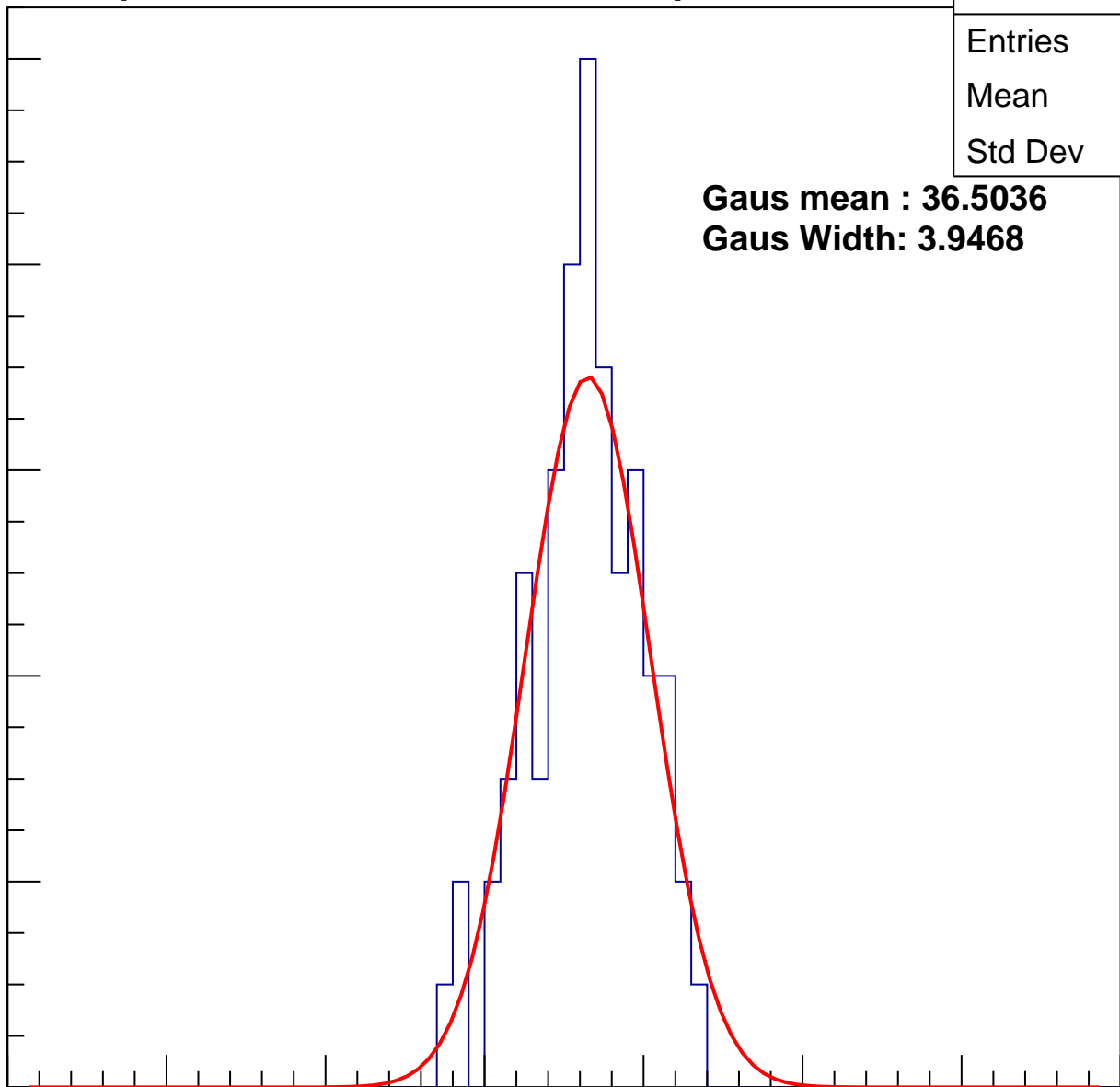
Gaus Width: 3.9468

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch57, adc2

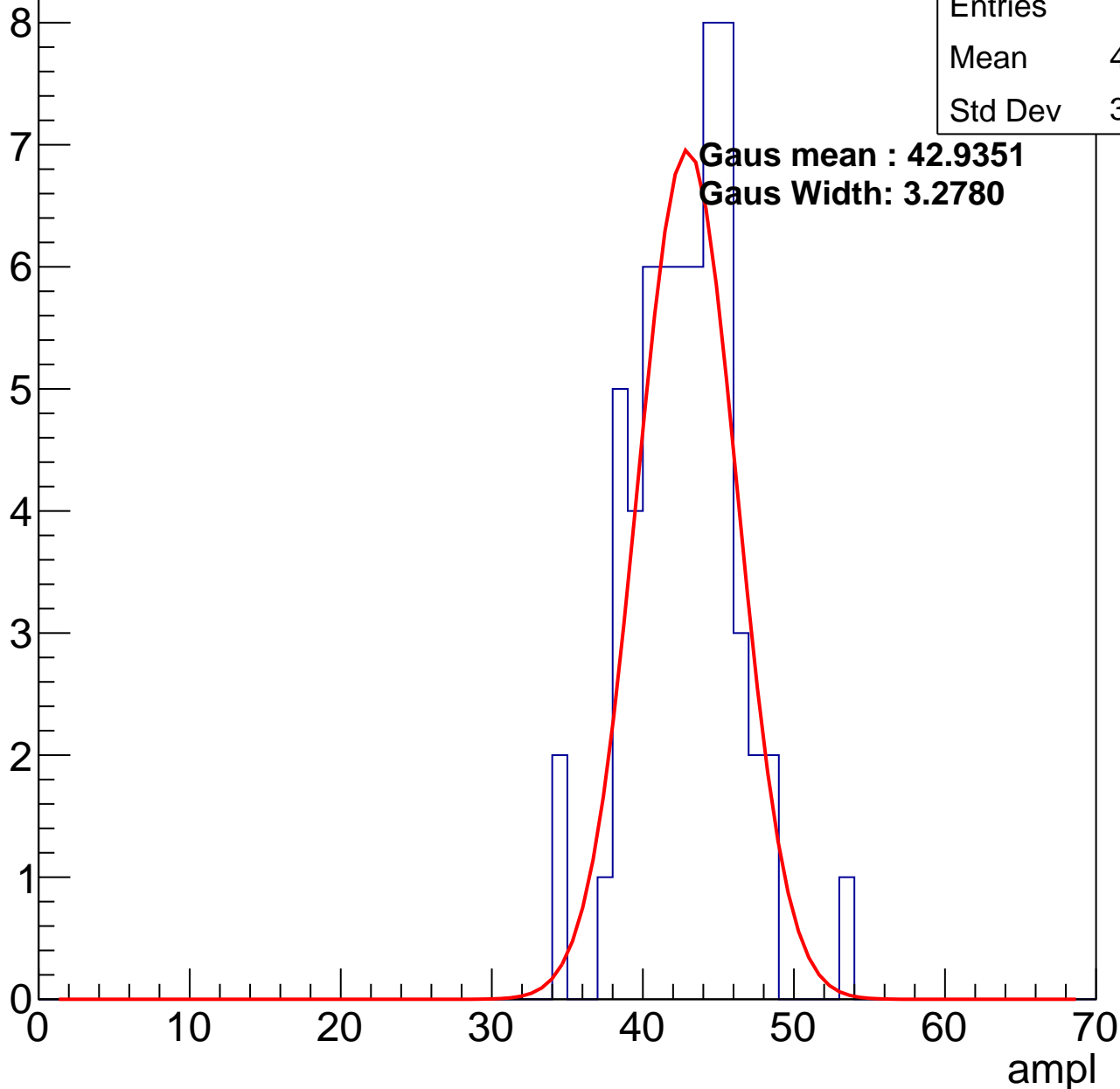
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.33
Std Dev	3.399

Gaus mean : 42.9351

Gaus Width: 3.2780

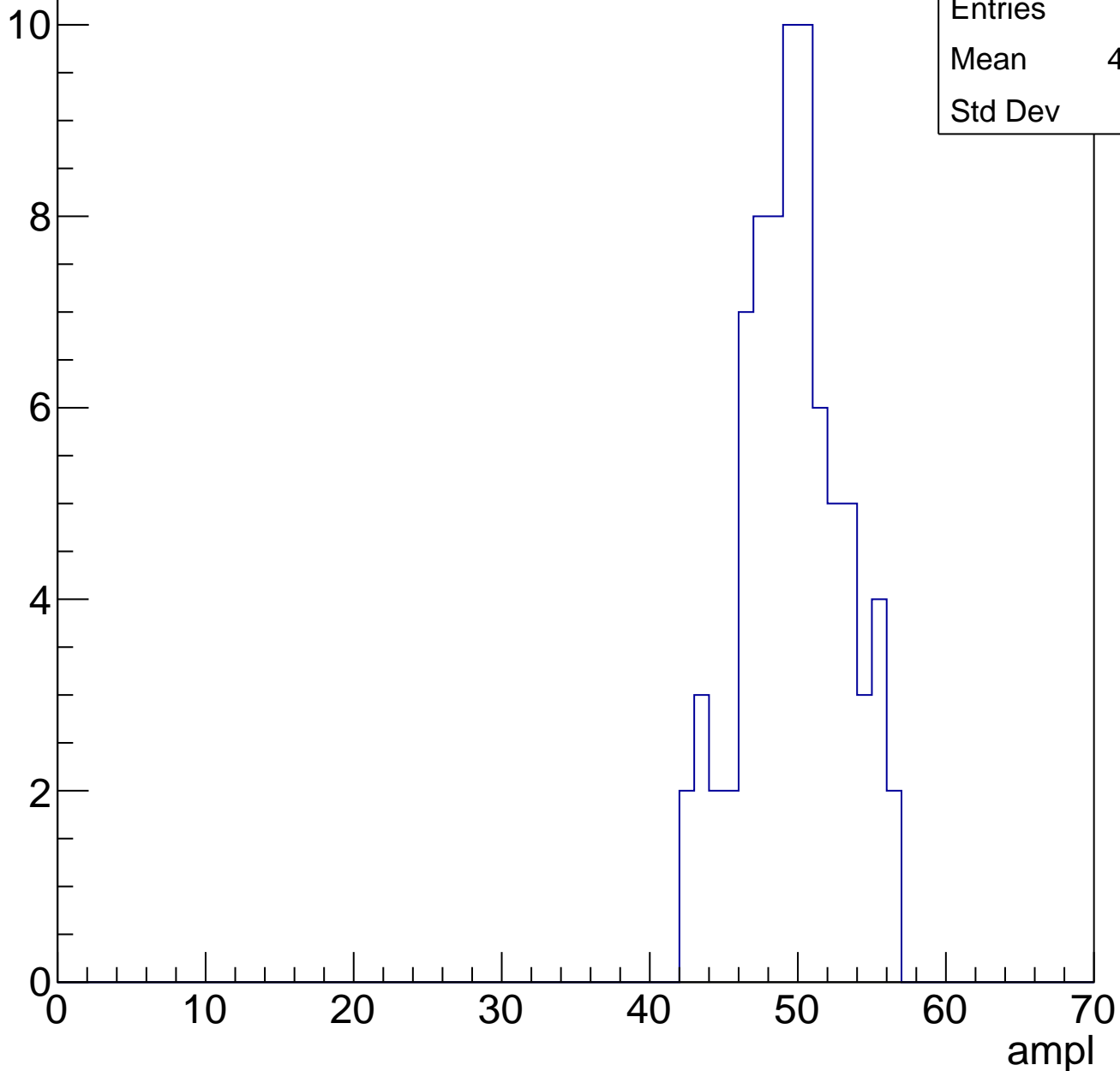


B1L101S, U17-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	49.19
Std Dev	3.35

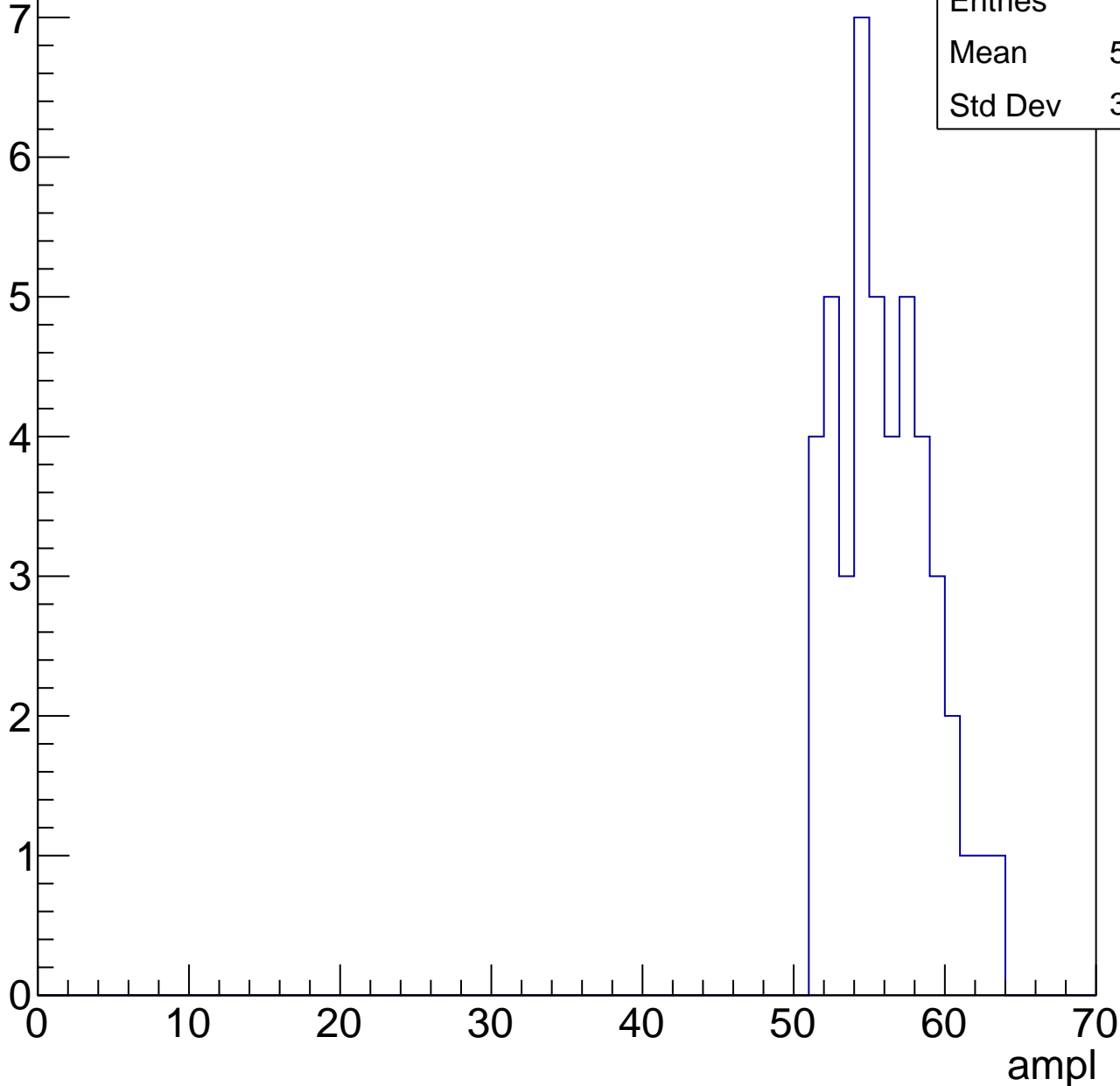


B1L101S, U17-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.56
Std Dev	3.059

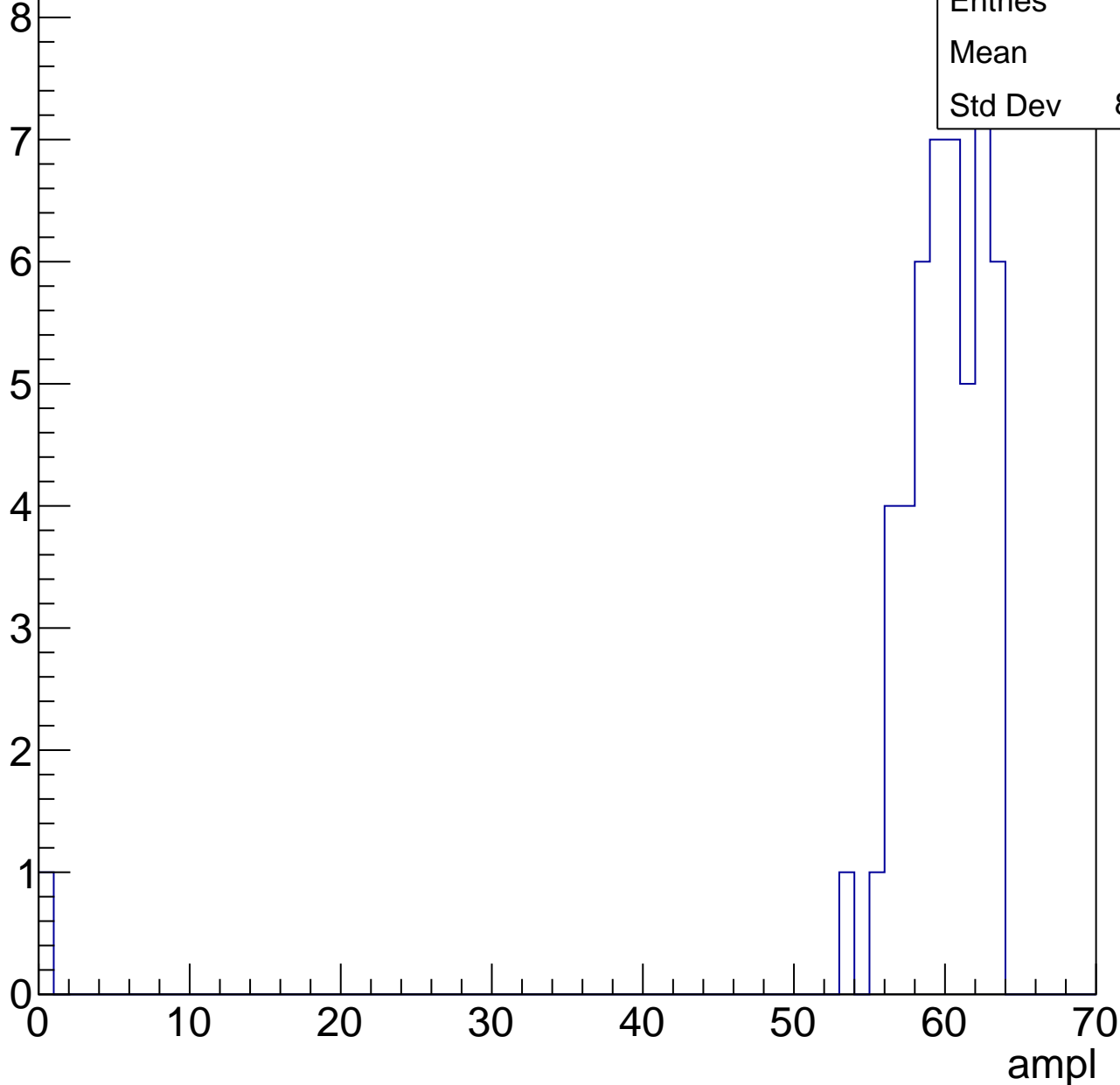


B1L101S, U17-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

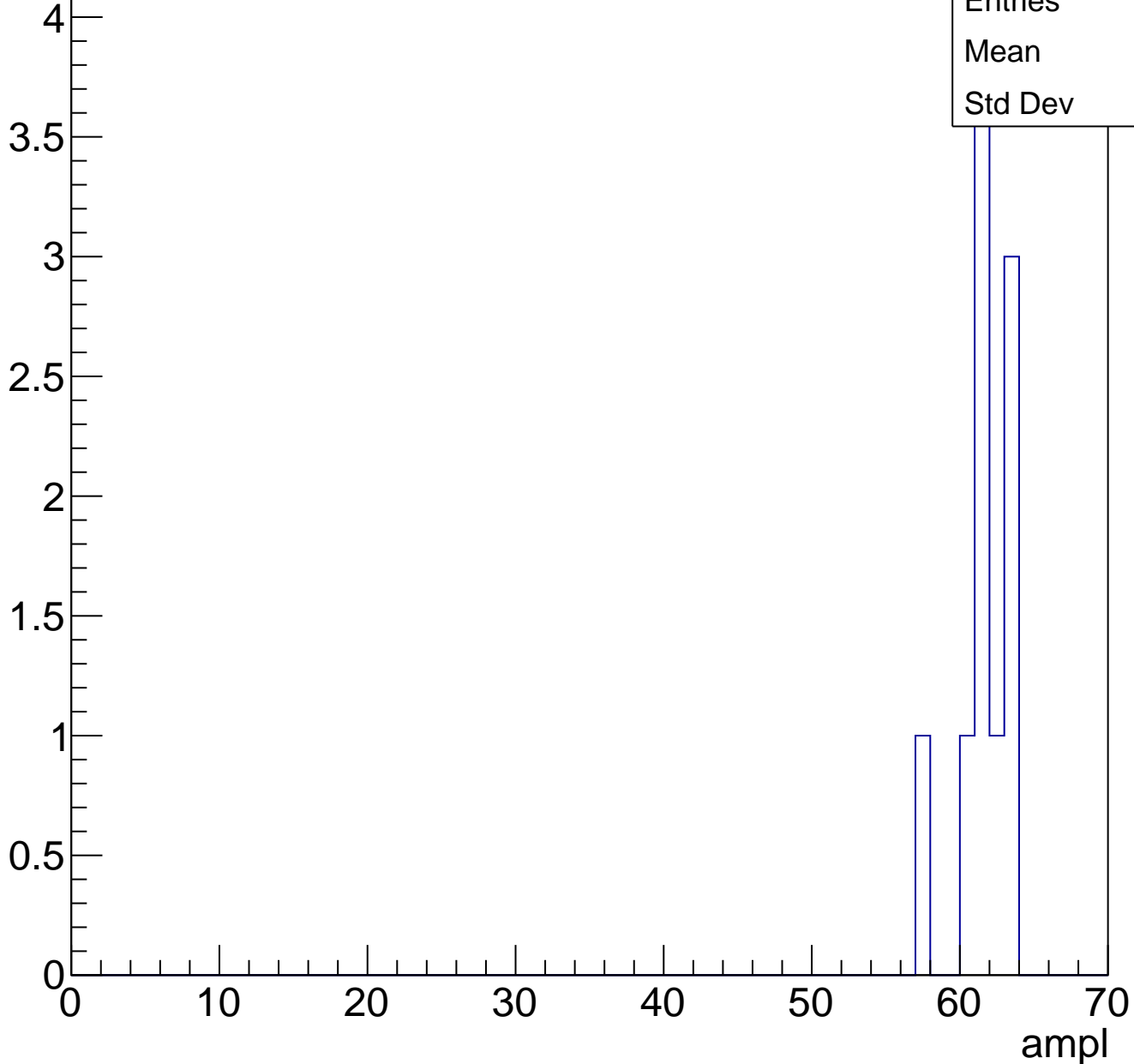
Entries	50
Mean	58.4
Std Dev	8.681



B1L101S, U17-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch58, adc0

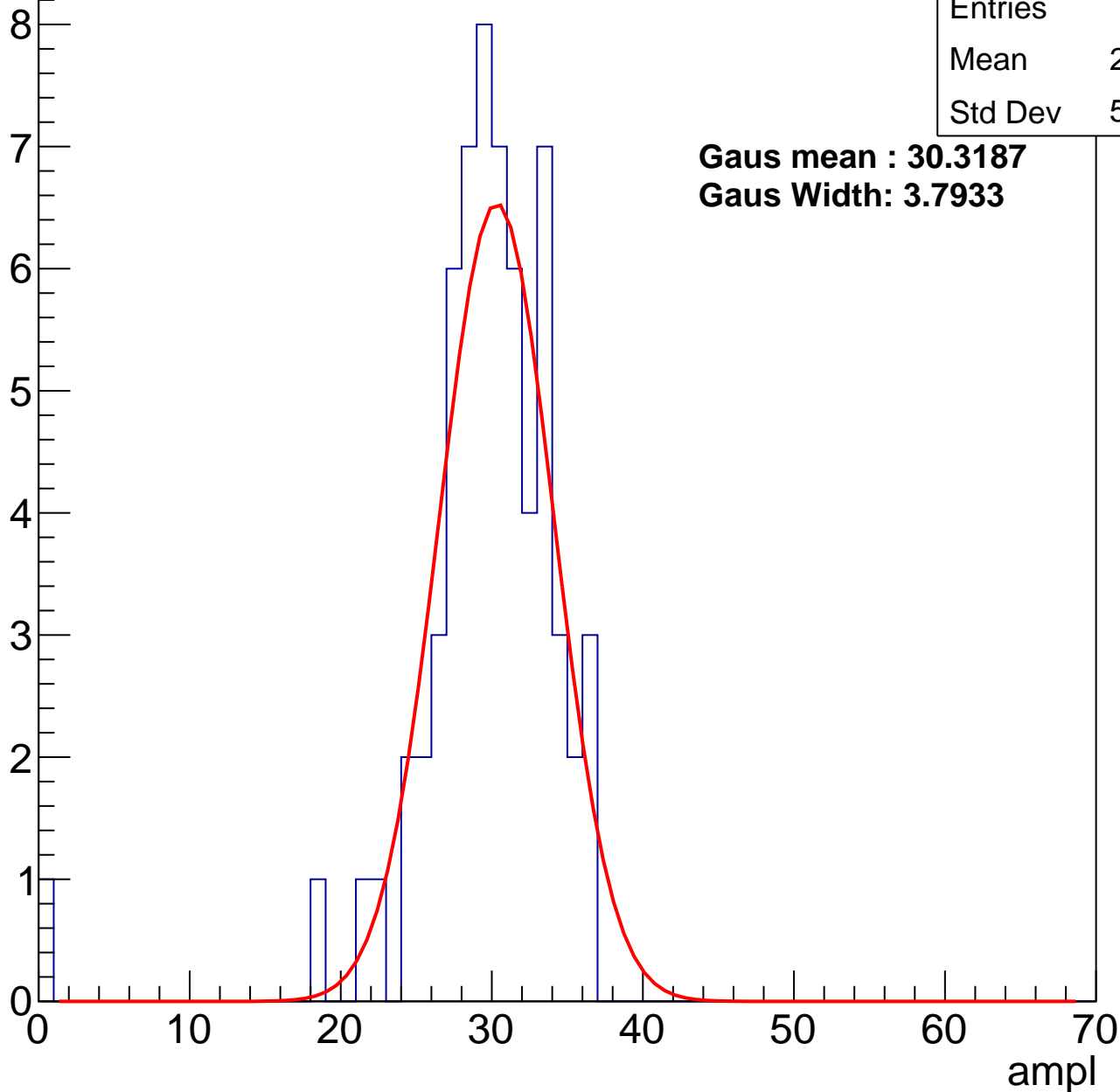
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.09
Std Dev	5.135

Gaus mean : 30.3187

Gaus Width: 3.7933



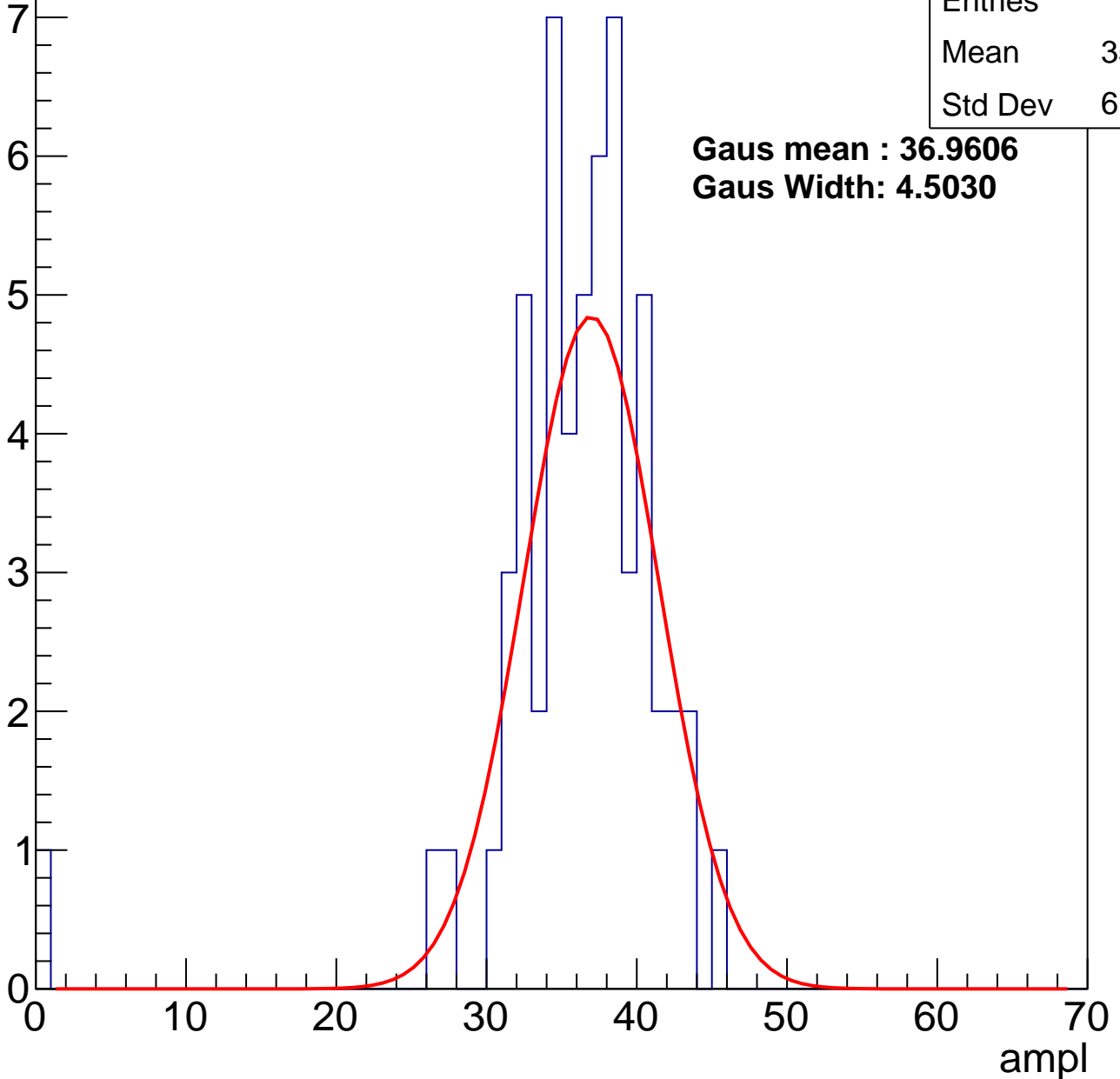
B1L101S, U17-ch58, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.55
Std Dev	6.086

Gaus mean : 36.9606
Gaus Width: 4.5030



B1L101S, U17-ch58, adc2

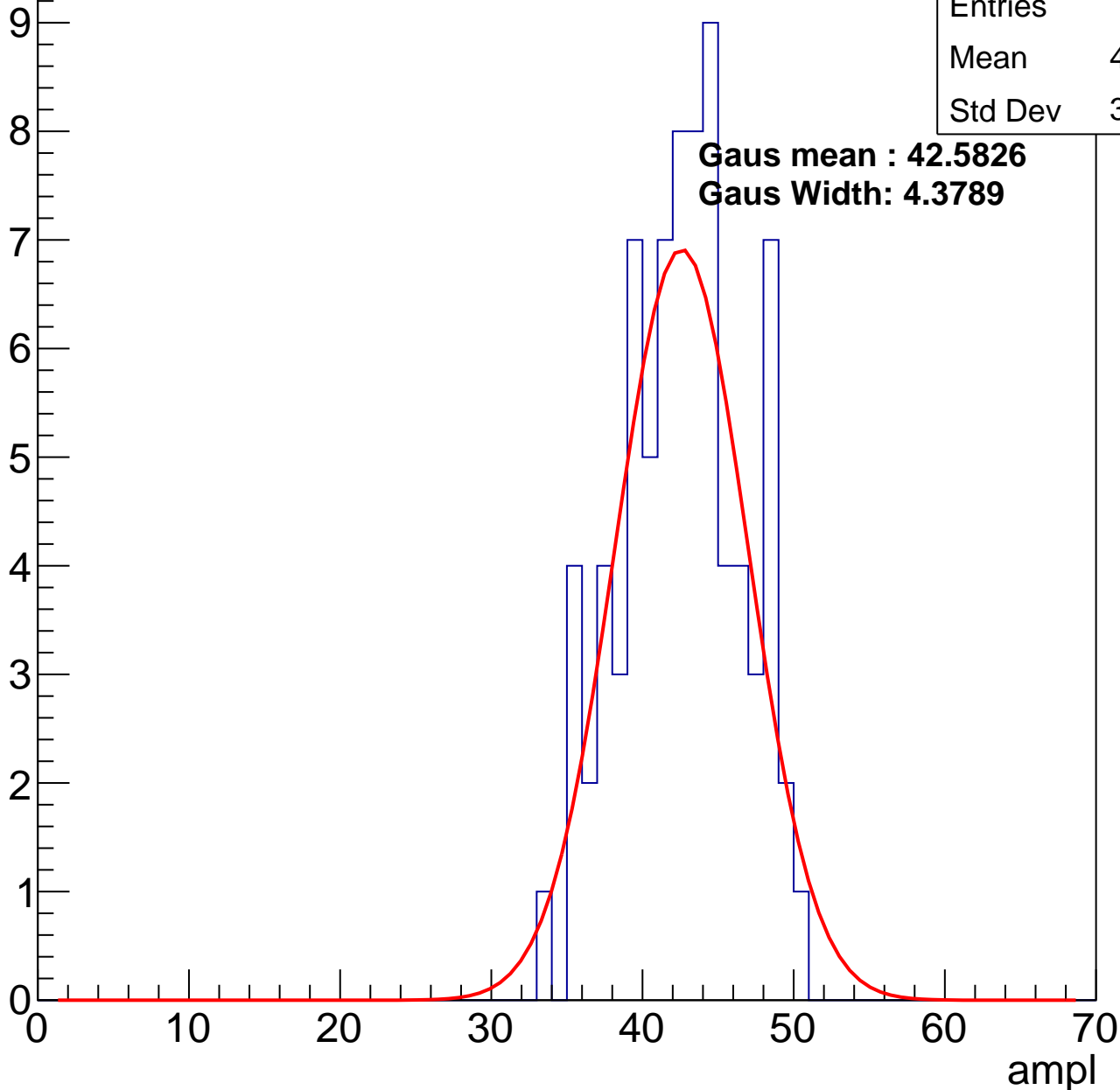
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	42.18
Std Dev	3.926

Gaus mean : 42.5826

Gaus Width: 4.3789

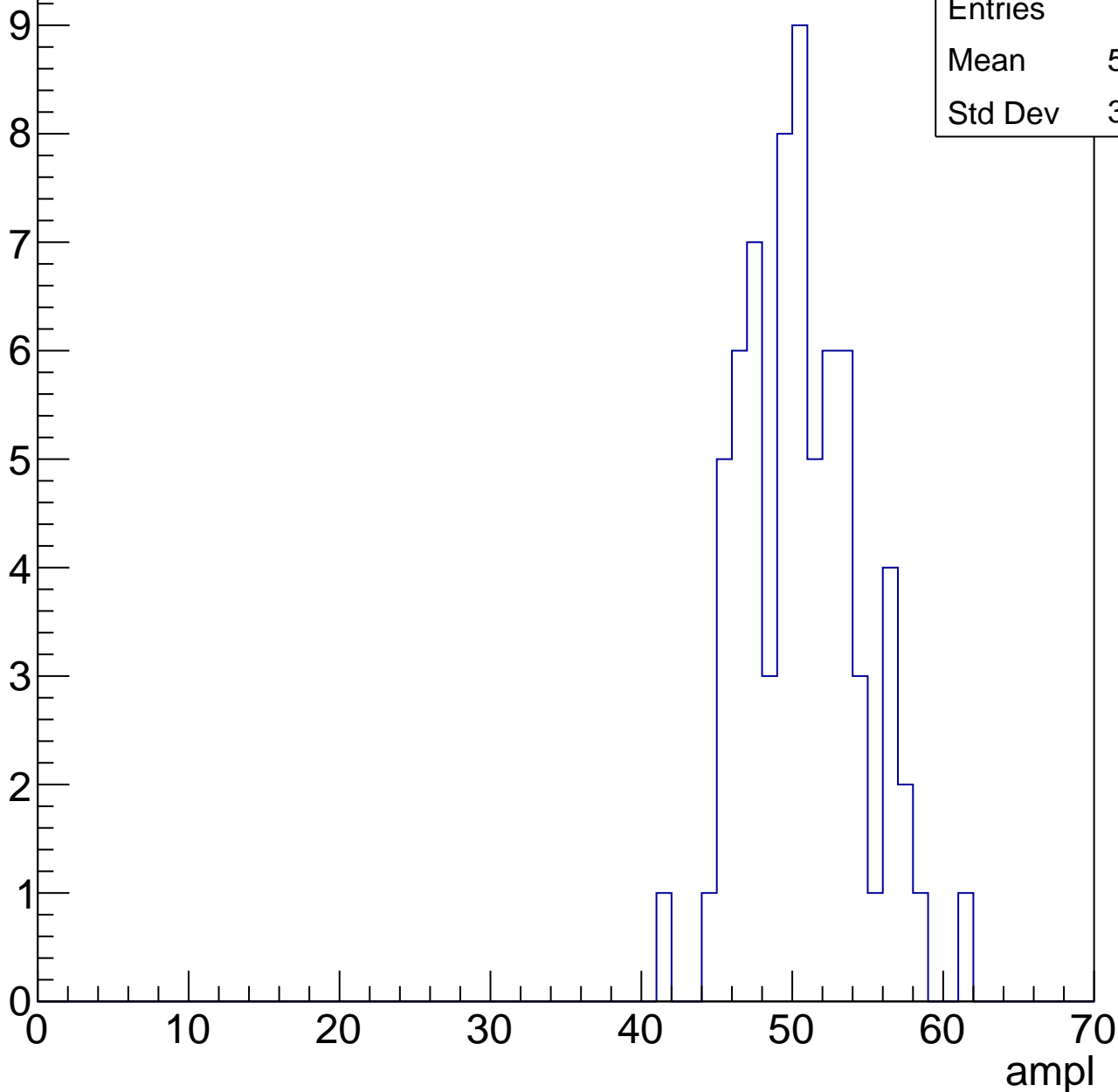


B1L101S, U17-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

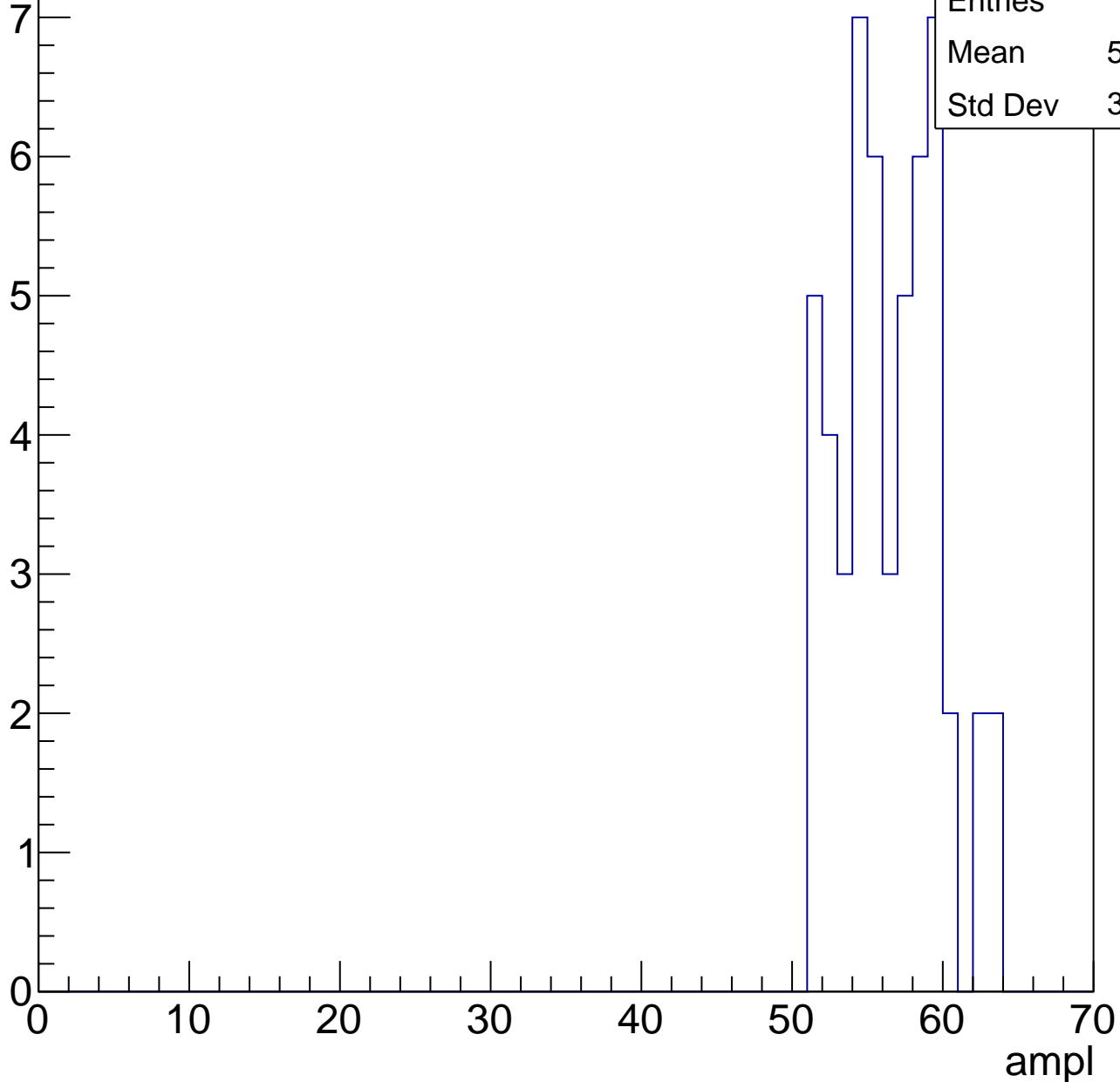
Entries	69
Mean	50.14
Std Dev	3.812



B1L101S, U17-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



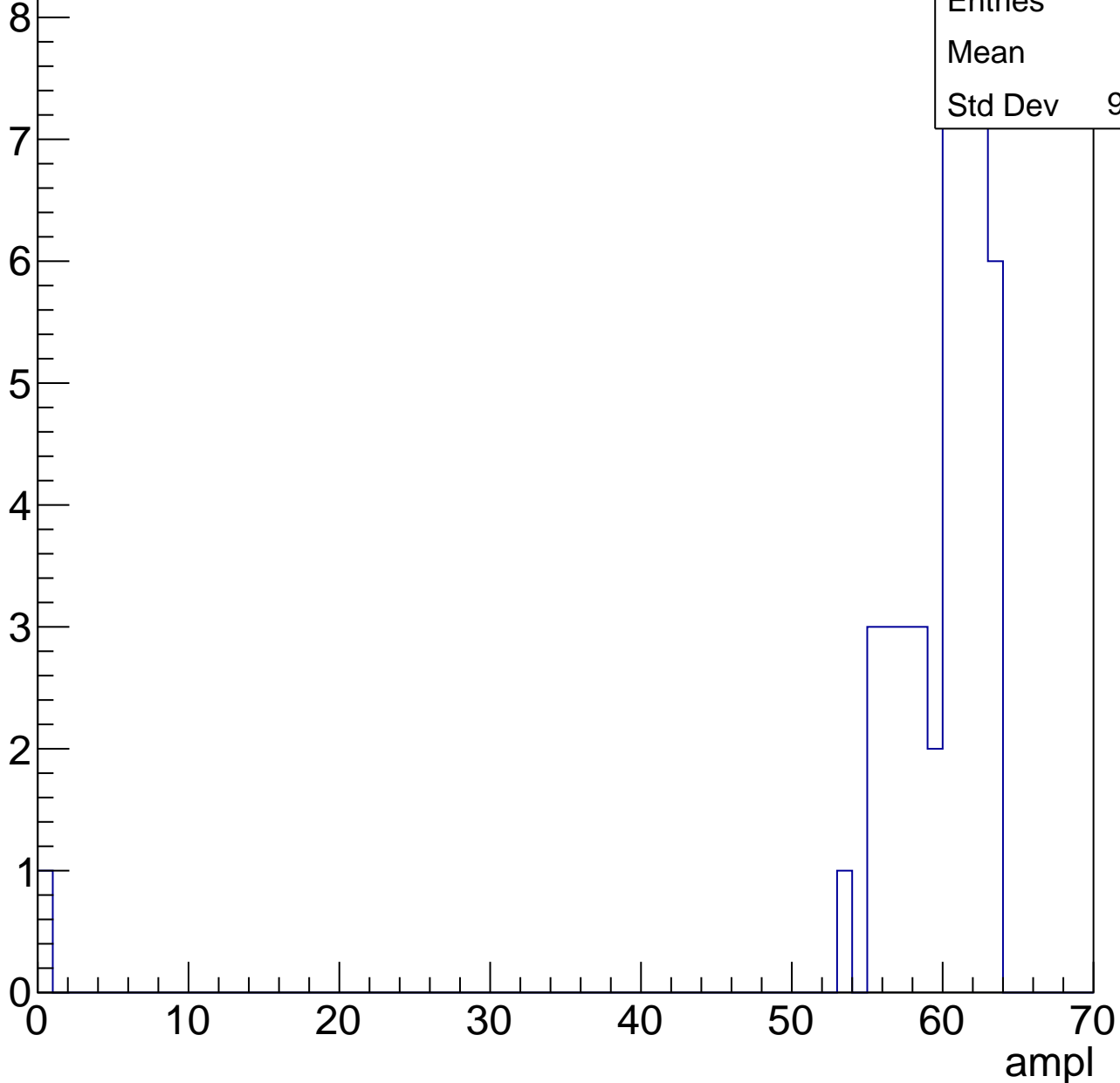
Entries	52
Mean	56.04
Std Dev	3.234

B1L101S, U17-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

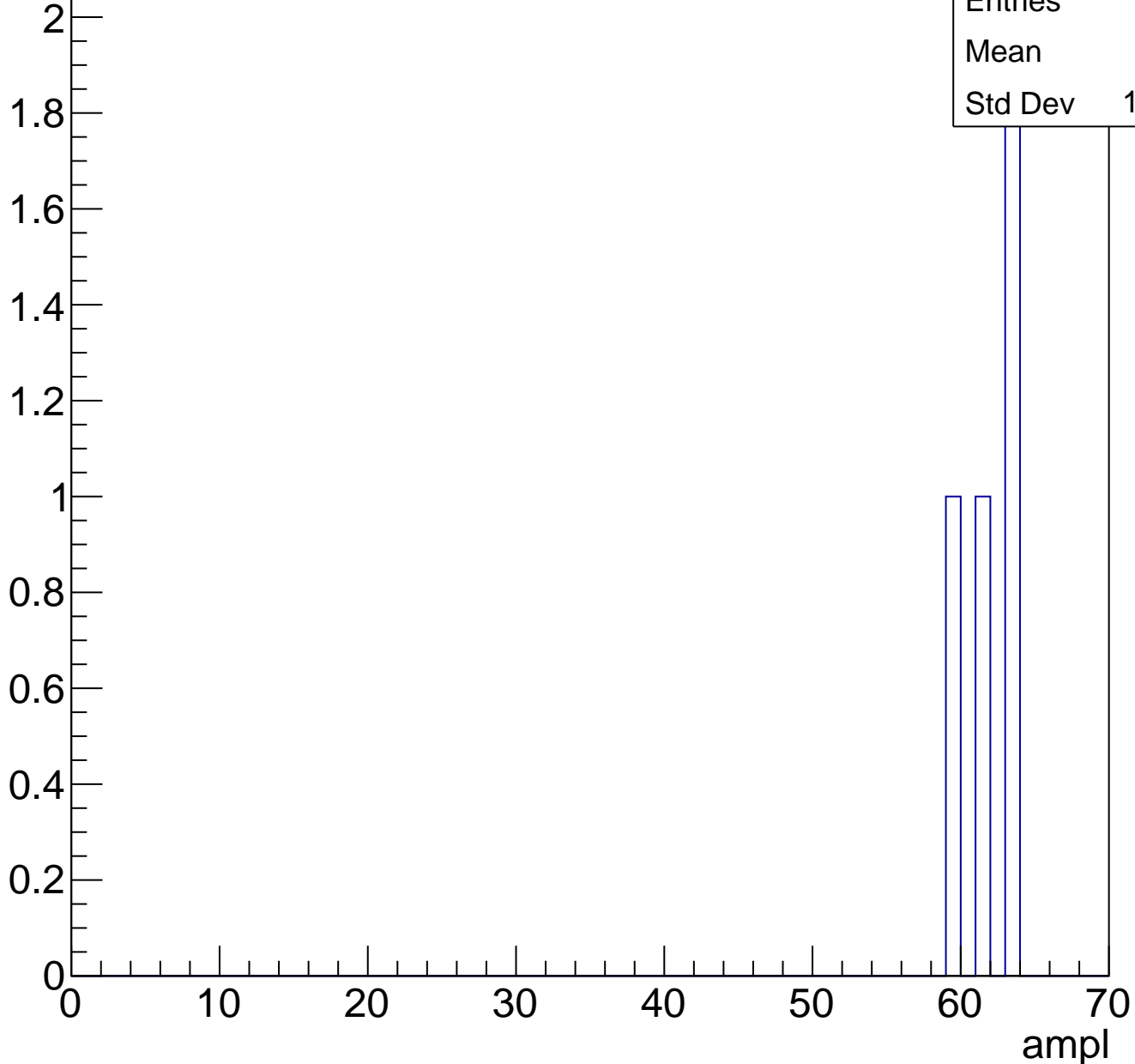
Entries	46
Mean	58.5
Std Dev	9.093



B1L101S, U17-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	61.5
Std Dev	1.658

B1L101S, U17-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch59, adc0

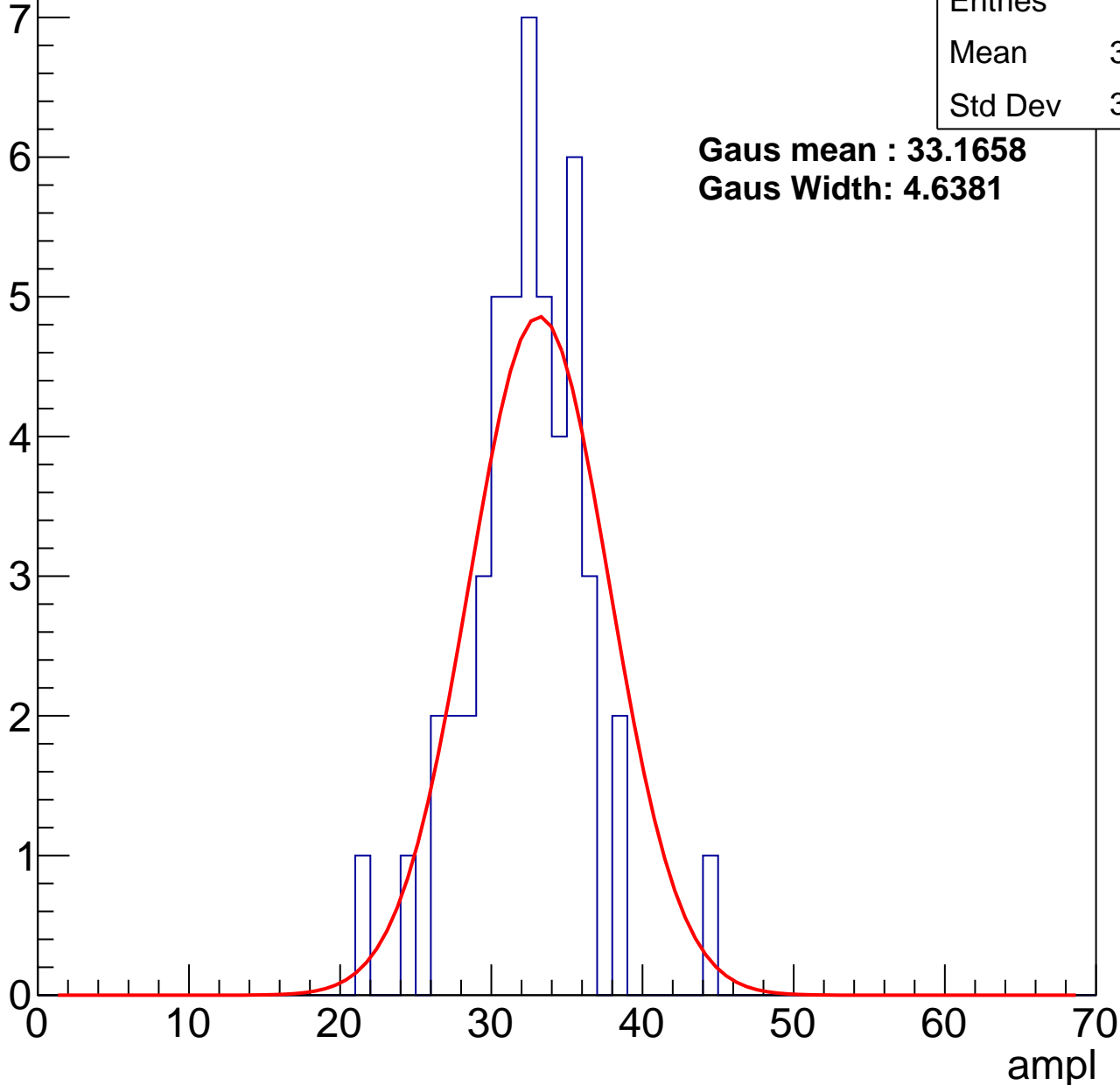
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	31.88
Std Dev	3.858

Gaus mean : 33.1658

Gaus Width: 4.6381



B1L101S, U17-ch59, adc1

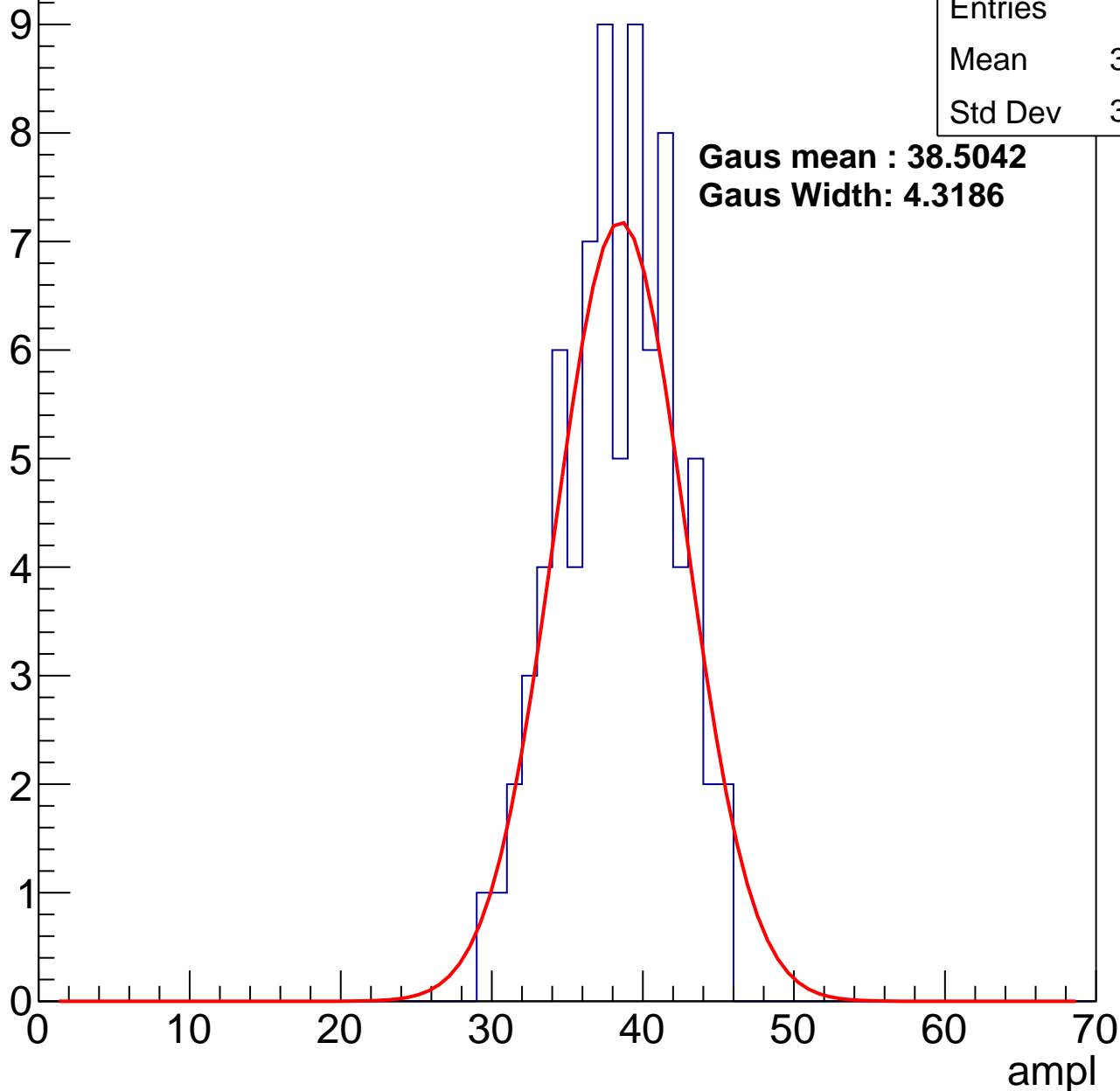
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.79
Std Dev	3.722

Gaus mean : 38.5042

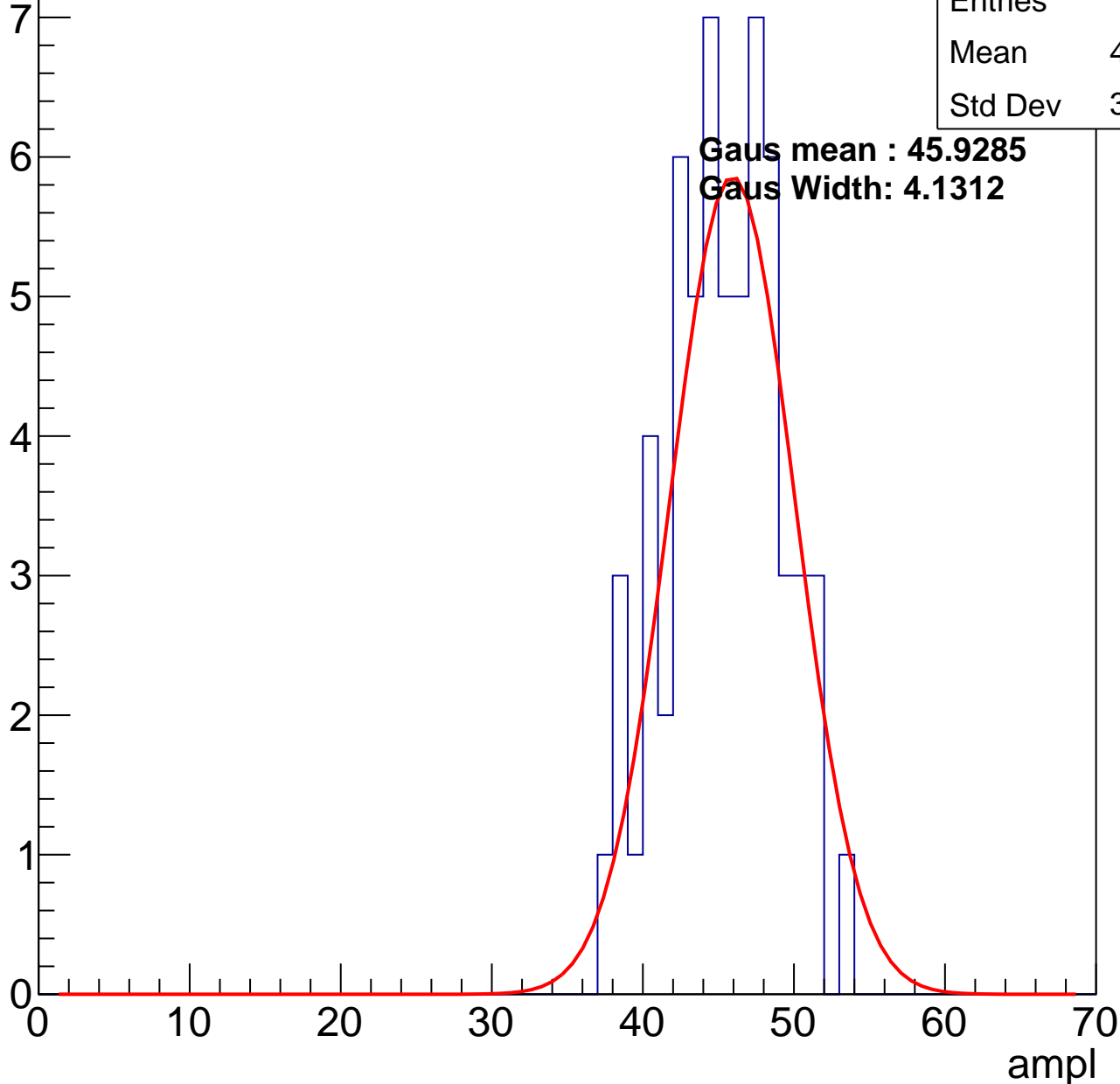
Gaus Width: 4.3186



B1L101S, U17-ch59, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

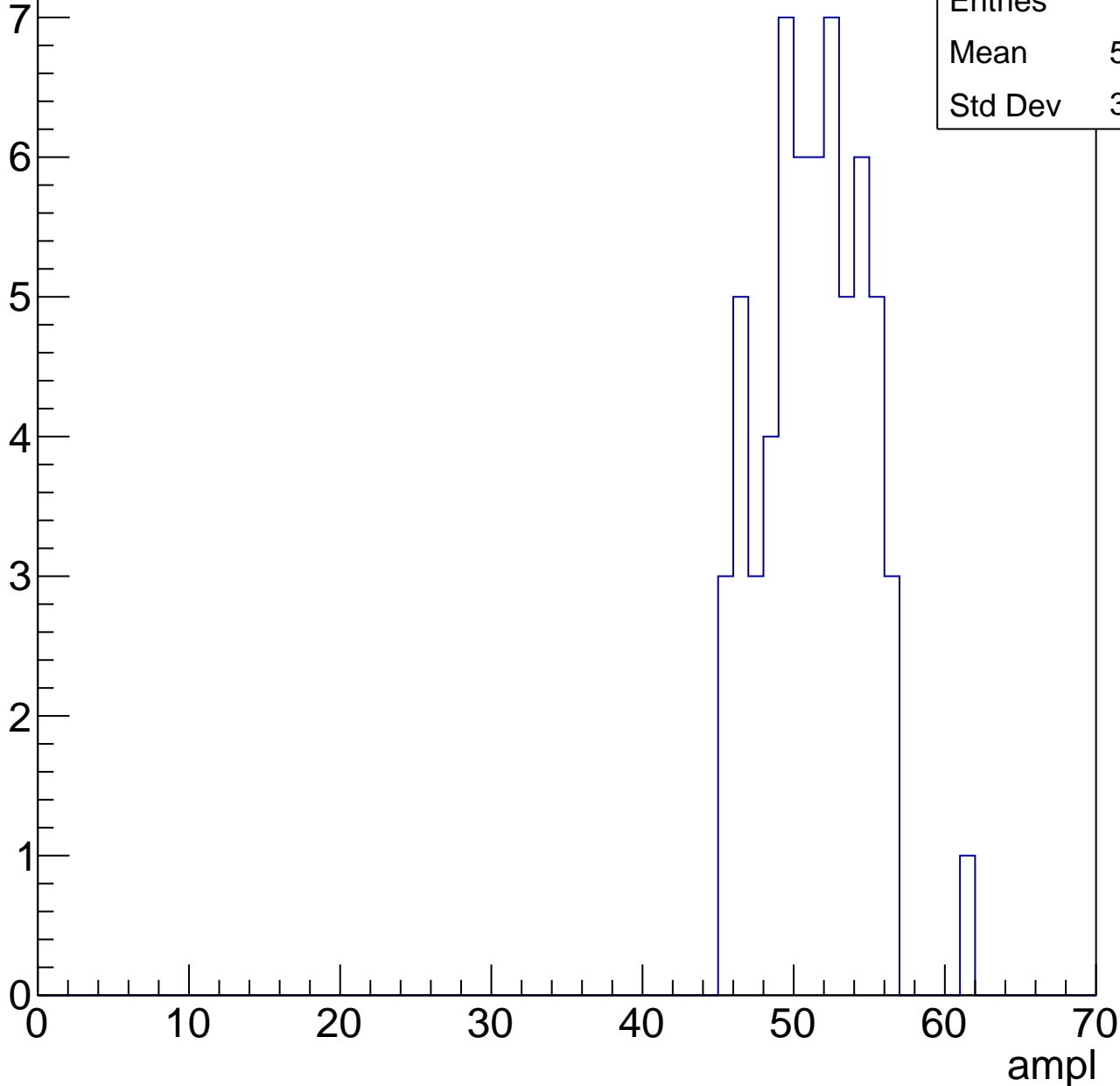


B1L101S, U17-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

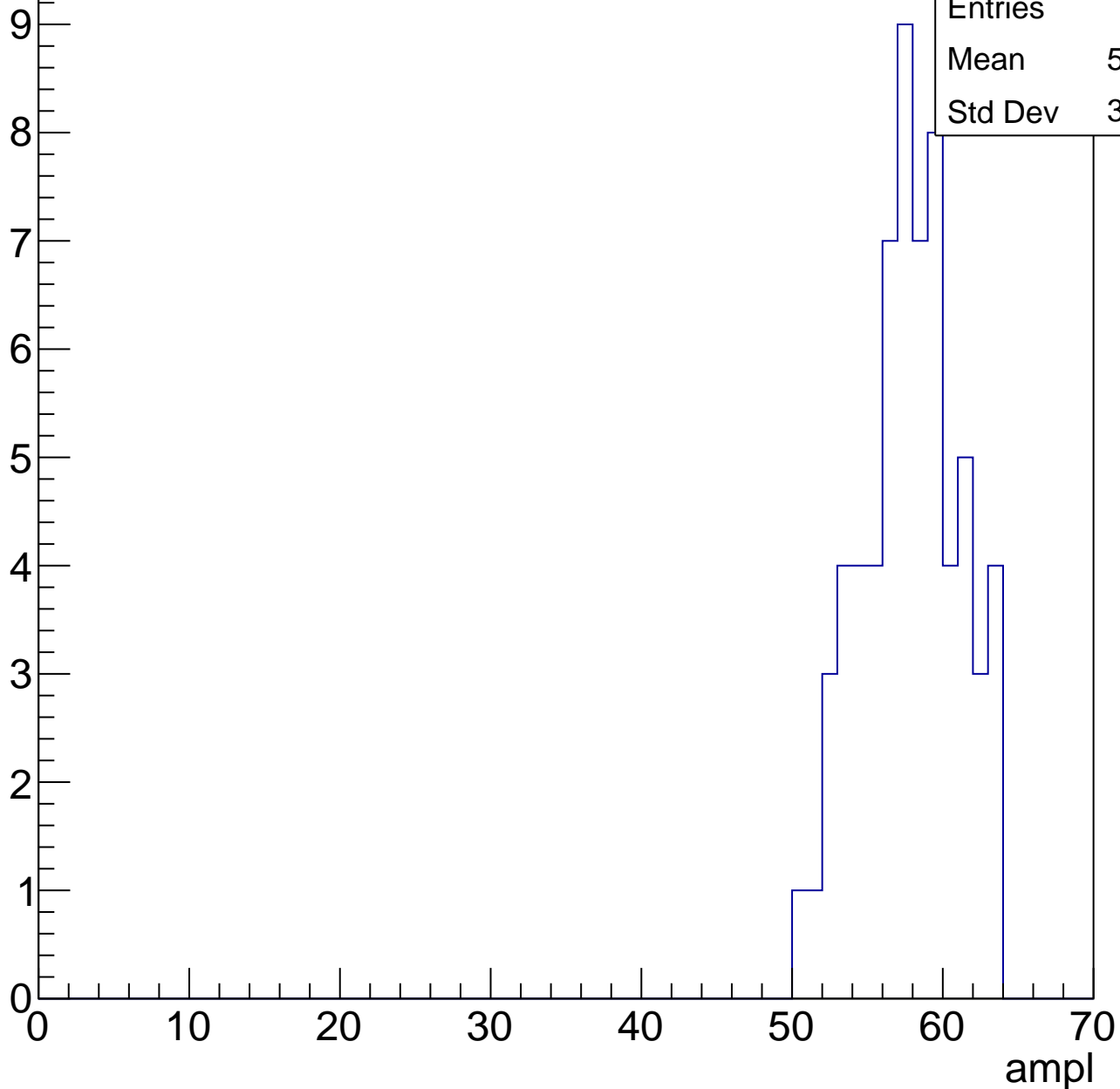
Entries	61
Mean	50.89
Std Dev	3.354



B1L101S, U17-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



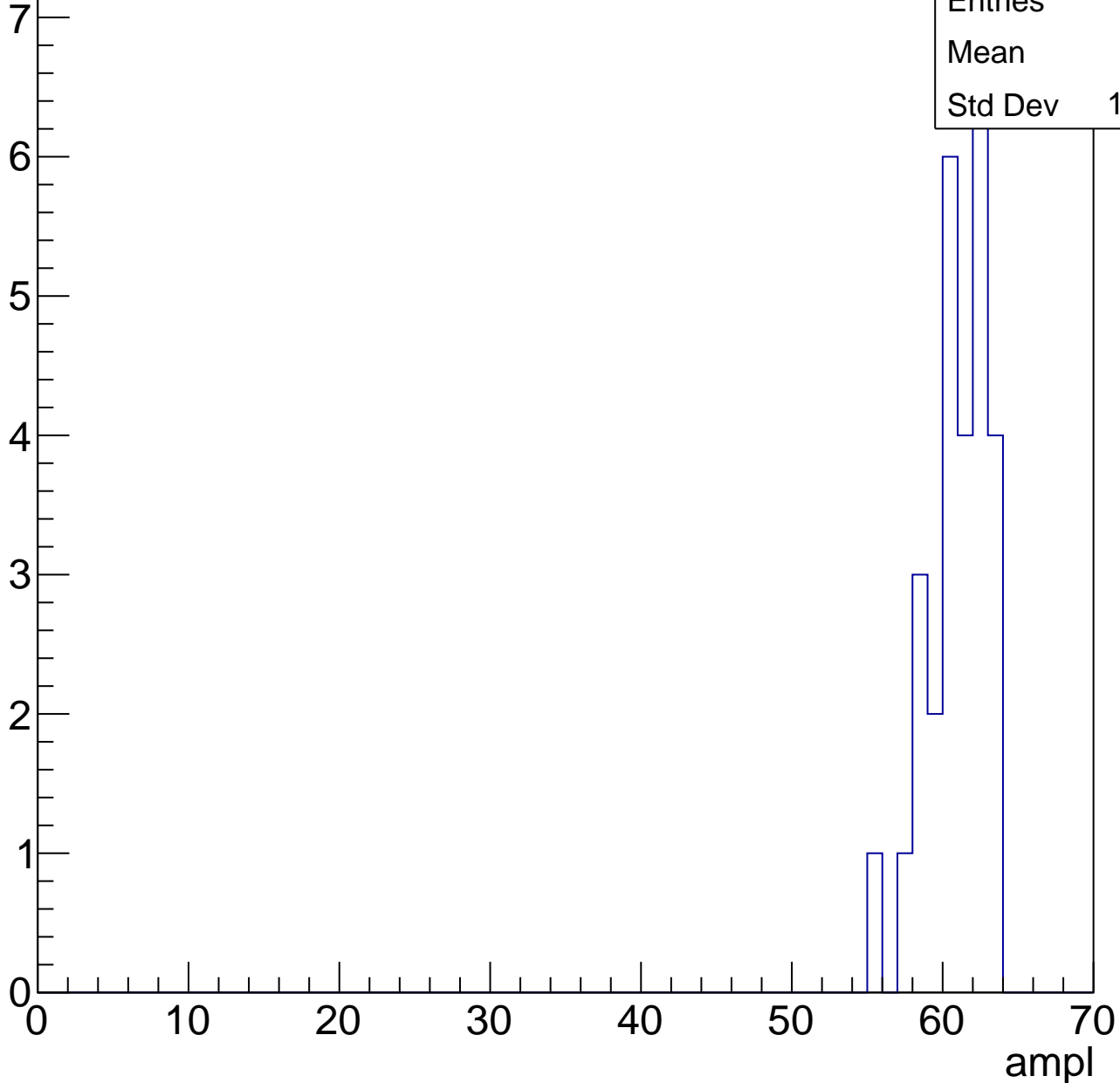
Entries	64
Mean	57.36
Std Dev	3.184

B1L101S, U17-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	60.5
Std Dev	1.973



B1L101S, U17-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch60, adc0

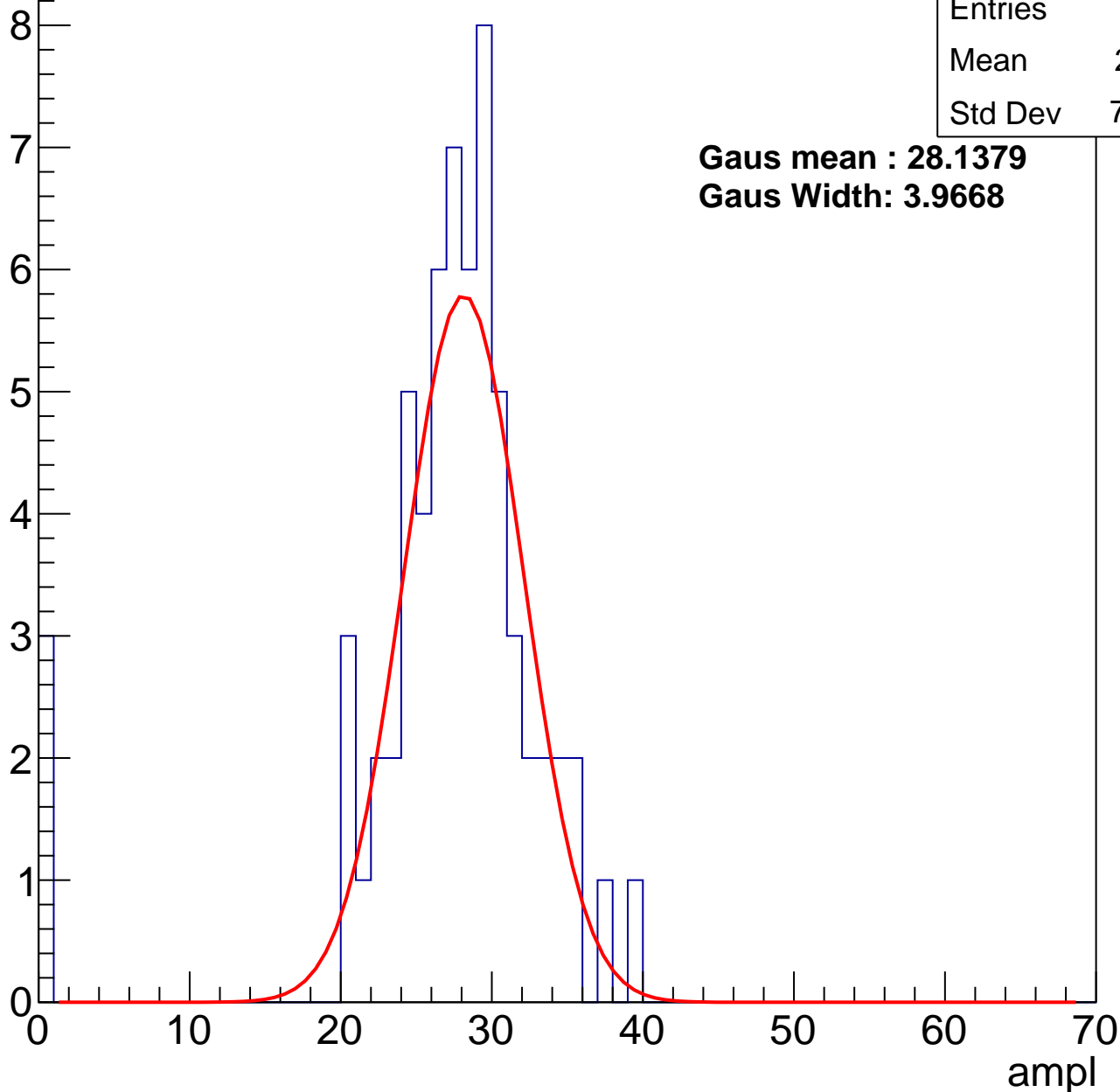
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	26.51
Std Dev	7.045

Gaus mean : 28.1379

Gaus Width: 3.9668



B1L101S, U17-ch60, adc1

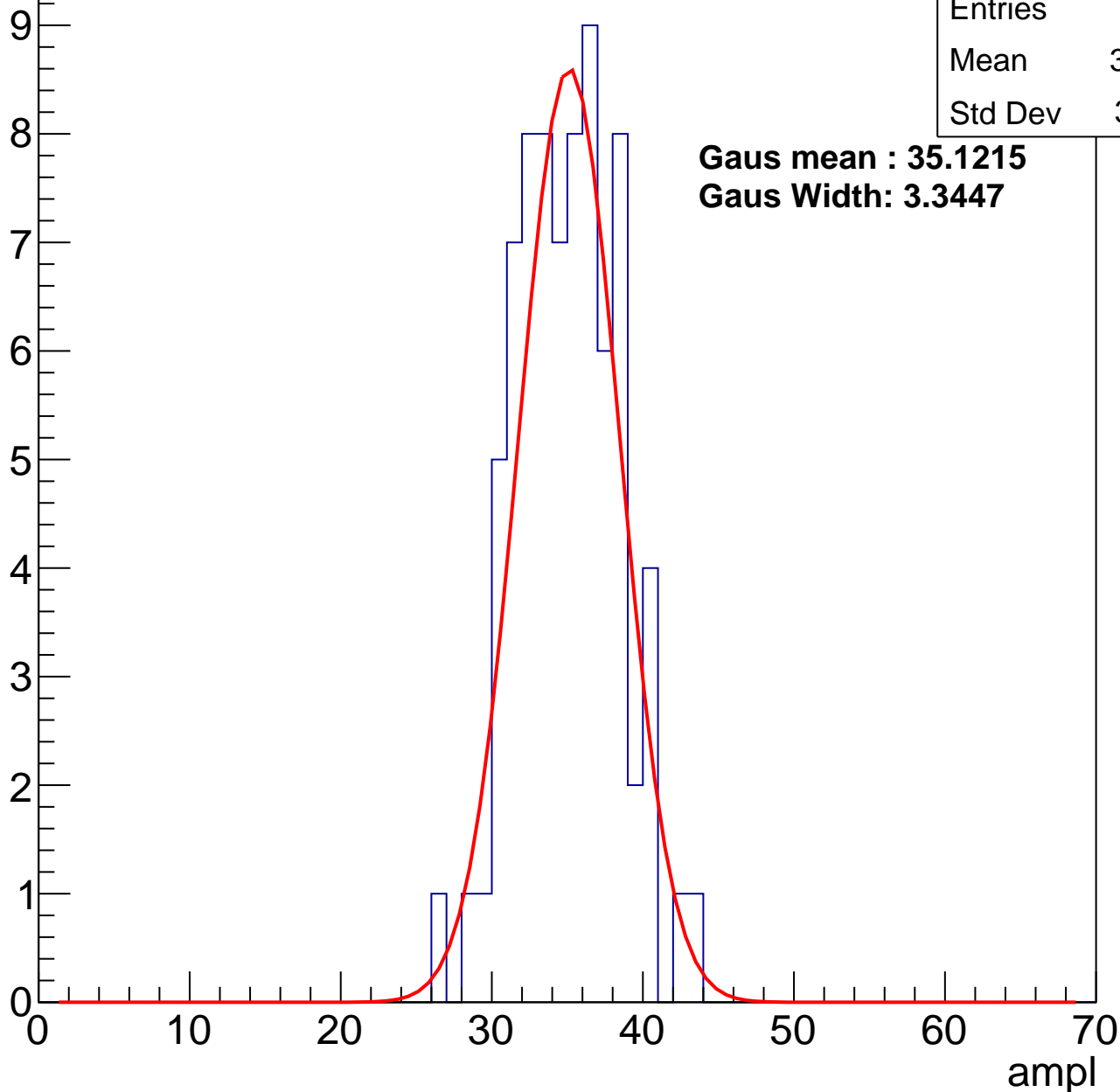
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	34.56
Std Dev	3.321

Gaus mean : 35.1215

Gaus Width: 3.3447



B1L101S, U17-ch60, adc2

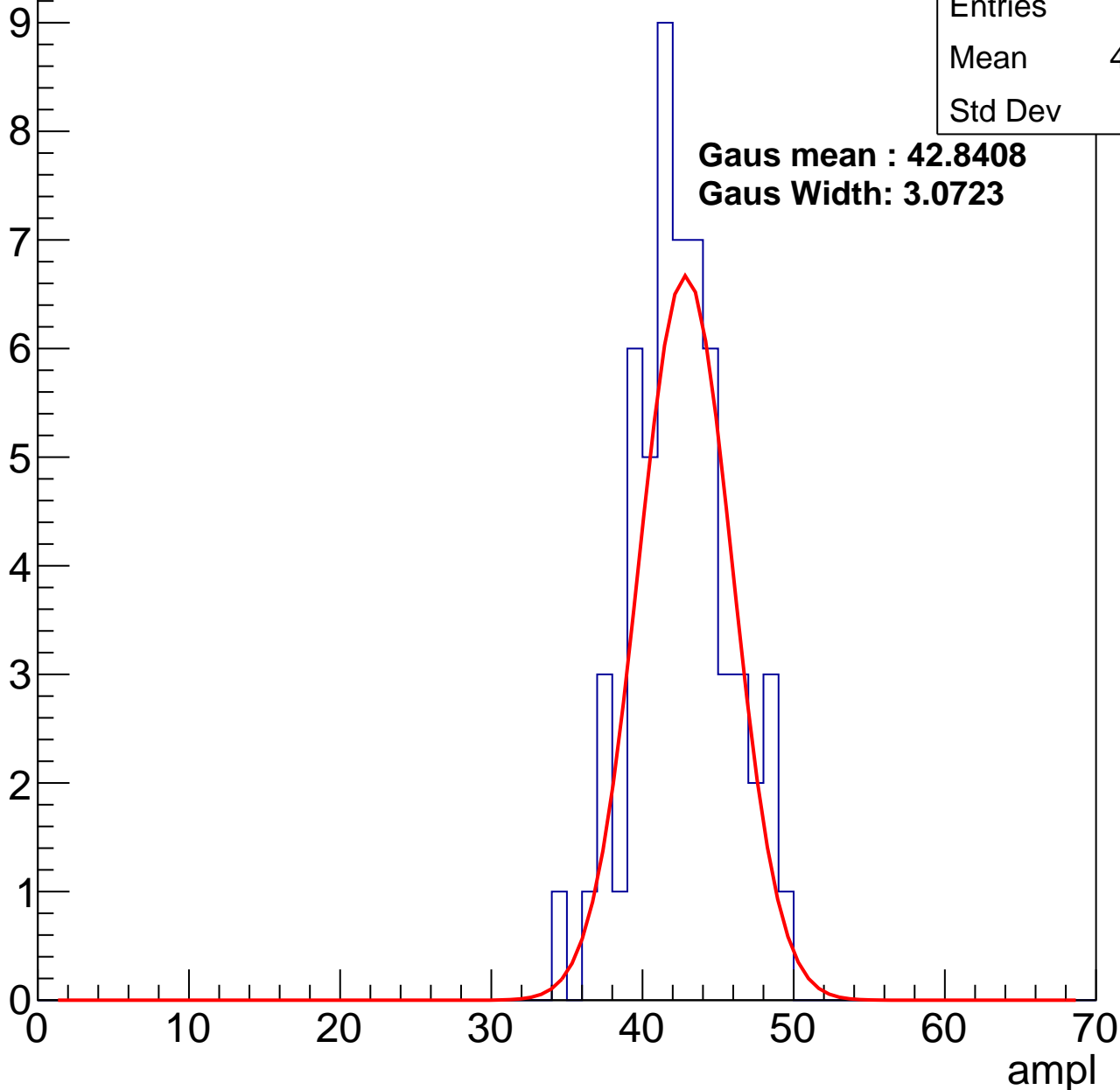
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.09
Std Dev	3.18

Gaus mean : 42.8408

Gaus Width: 3.0723

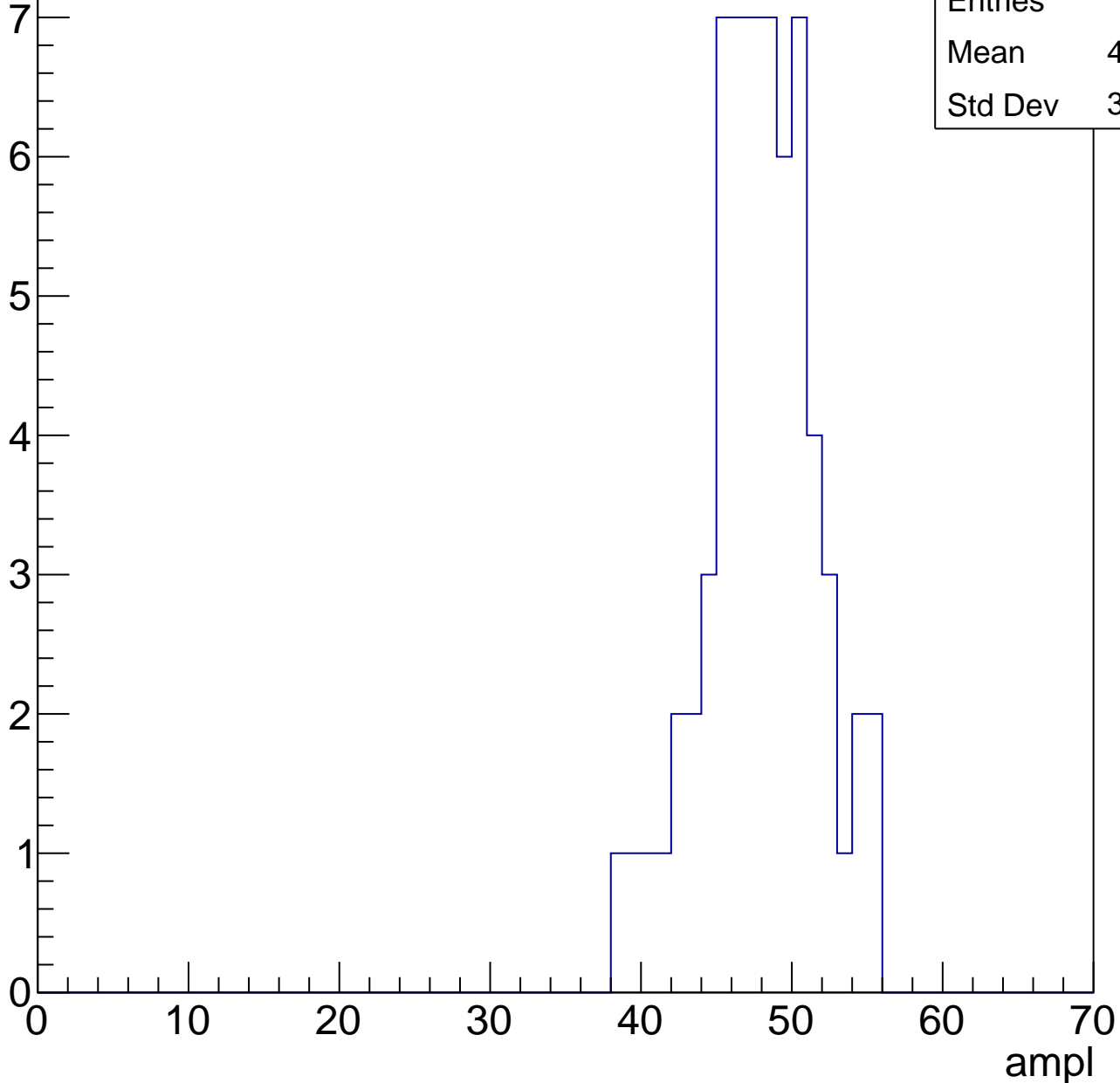


B1L101S, U17-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	47.45
Std Dev	3.657

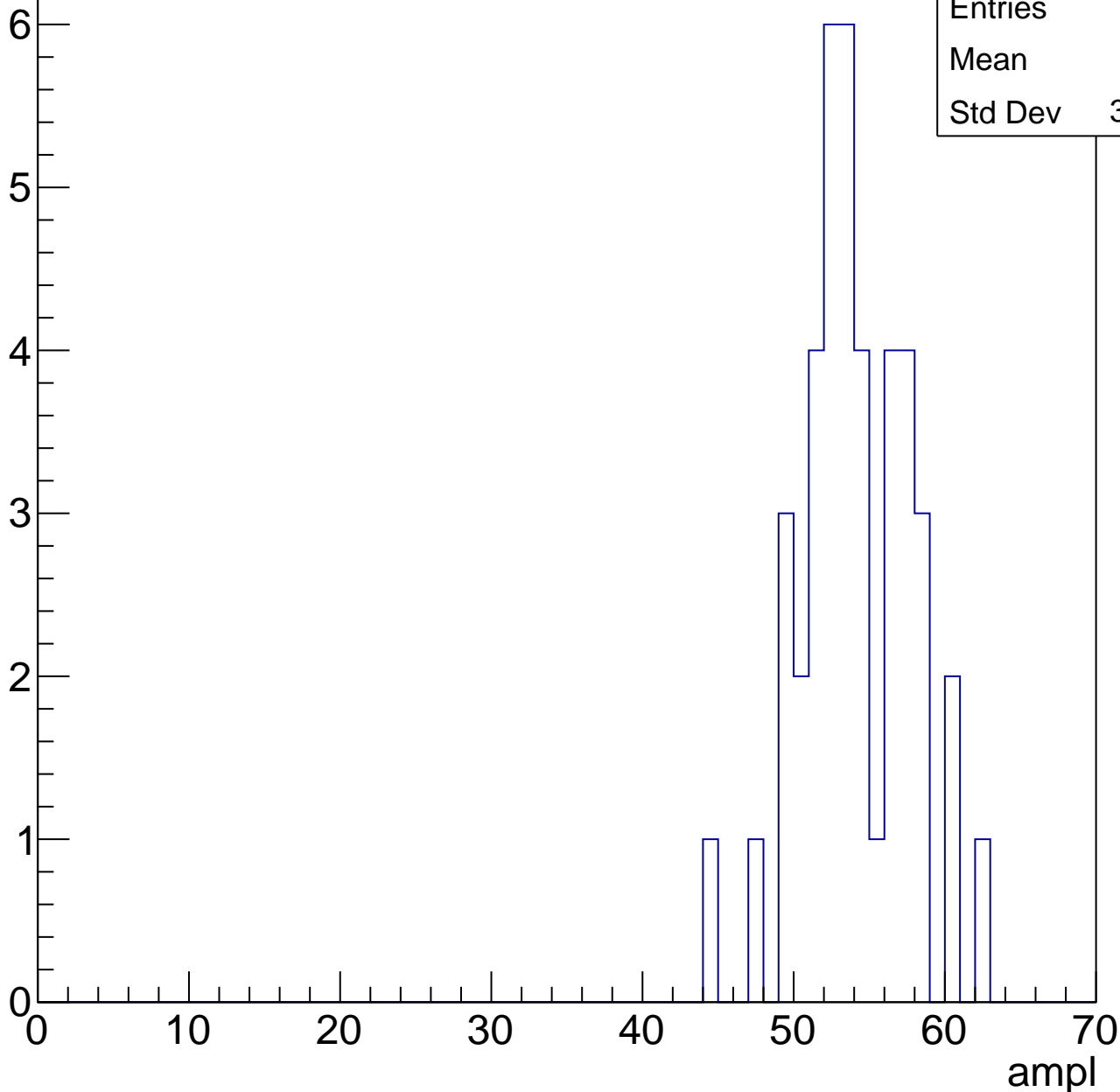


B1L101S, U17-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	53.6
Std Dev	3.626

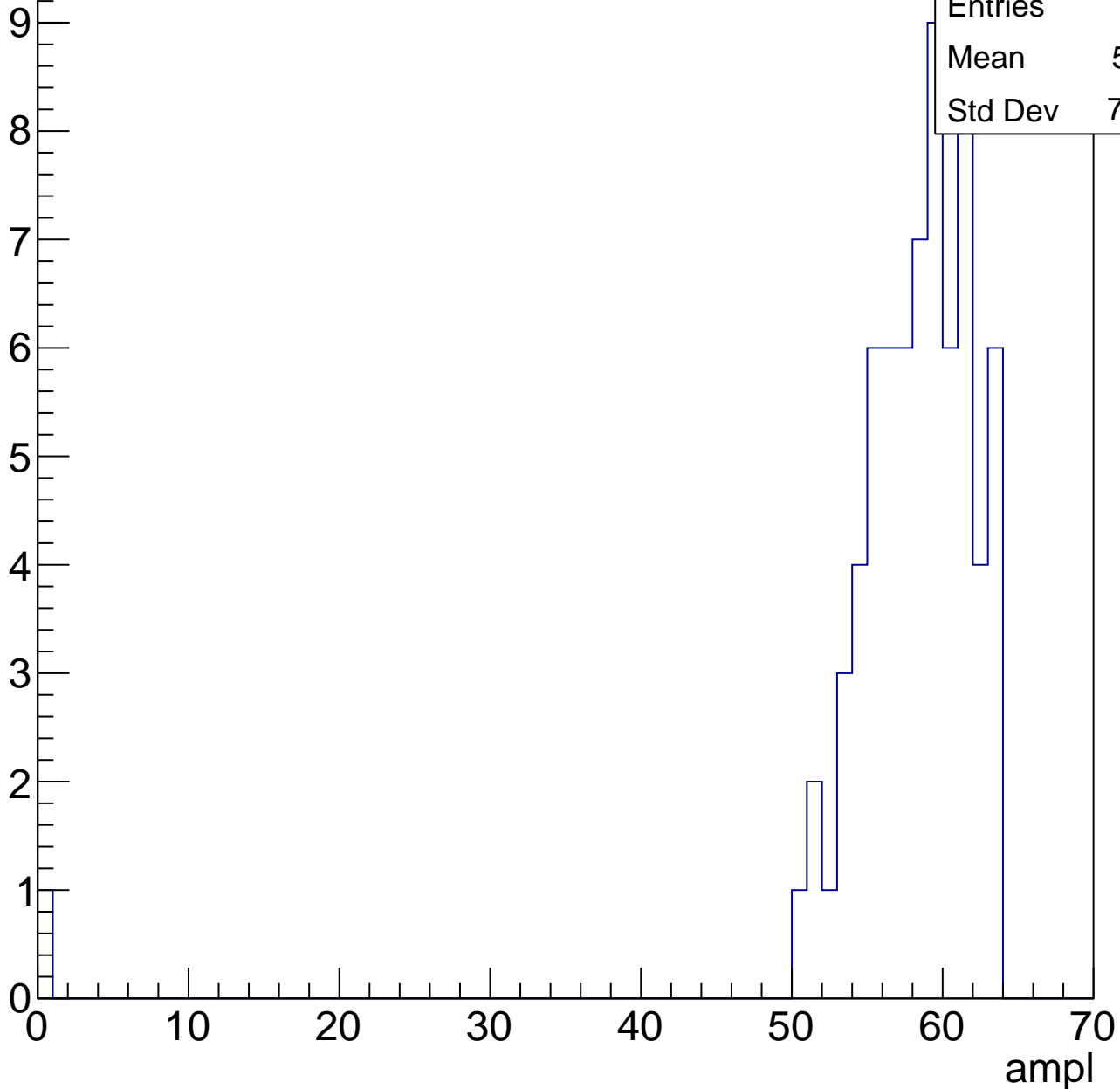


B1L101S, U17-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

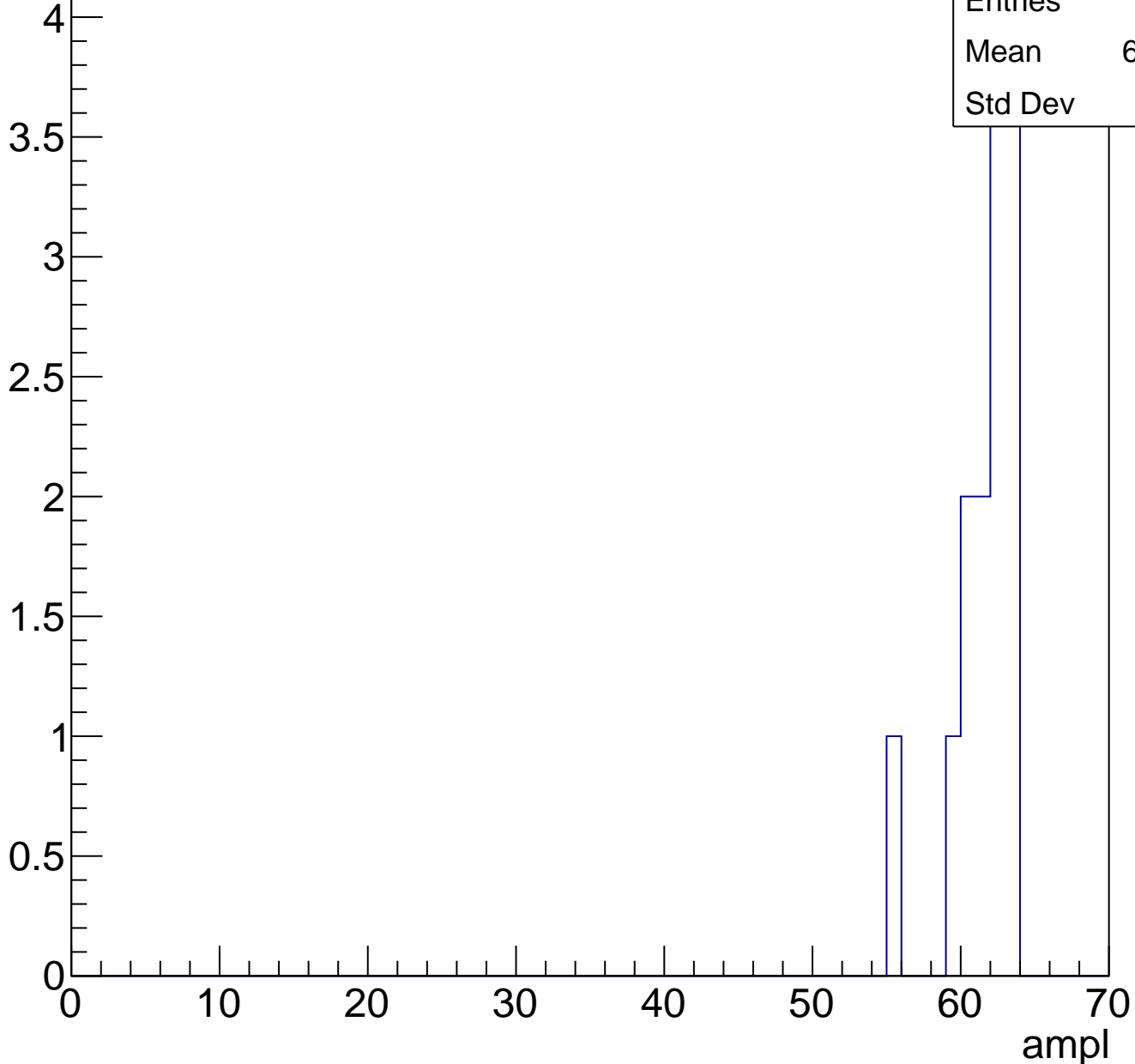
Entries	70
Mean	57.11
Std Dev	7.606



B1L101S, U17-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L101S, U17-ch61, adc0

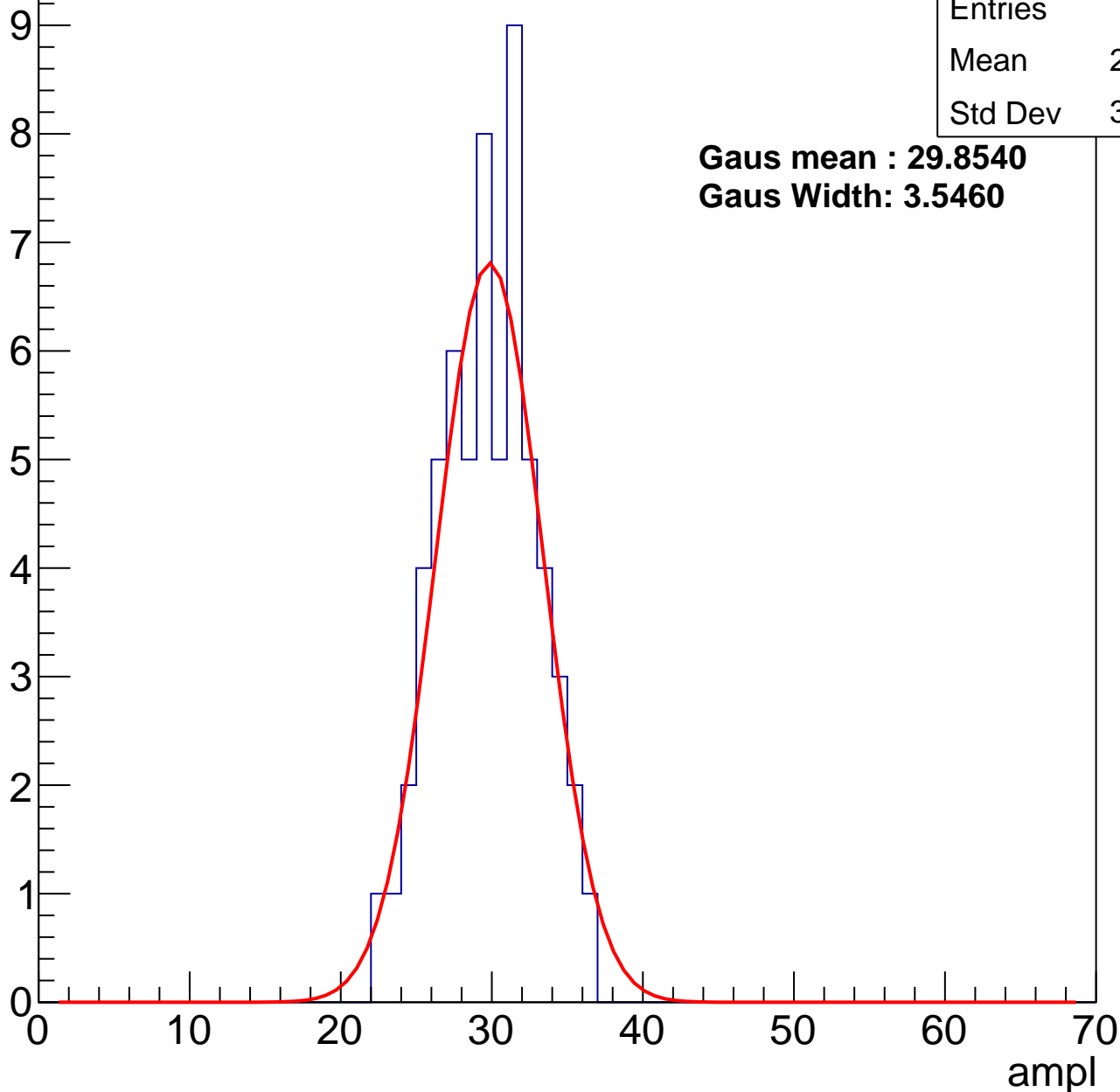
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.28
Std Dev	3.173

Gaus mean : 29.8540

Gaus Width: 3.5460



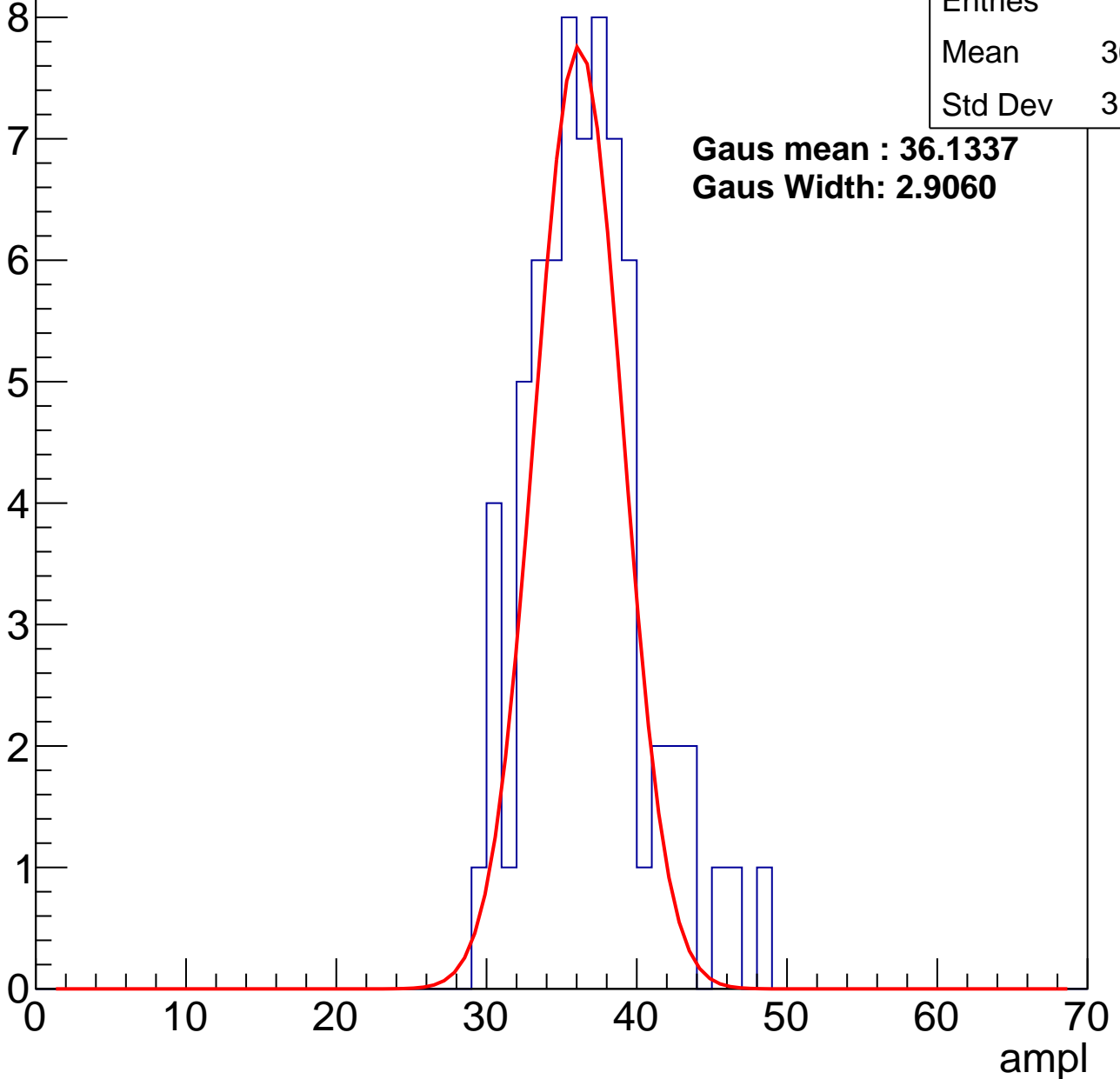
B1L101S, U17-ch61, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.25
Std Dev	3.865

Gaus mean : 36.1337
Gaus Width: 2.9060



B1L101S, U17-ch61, adc2

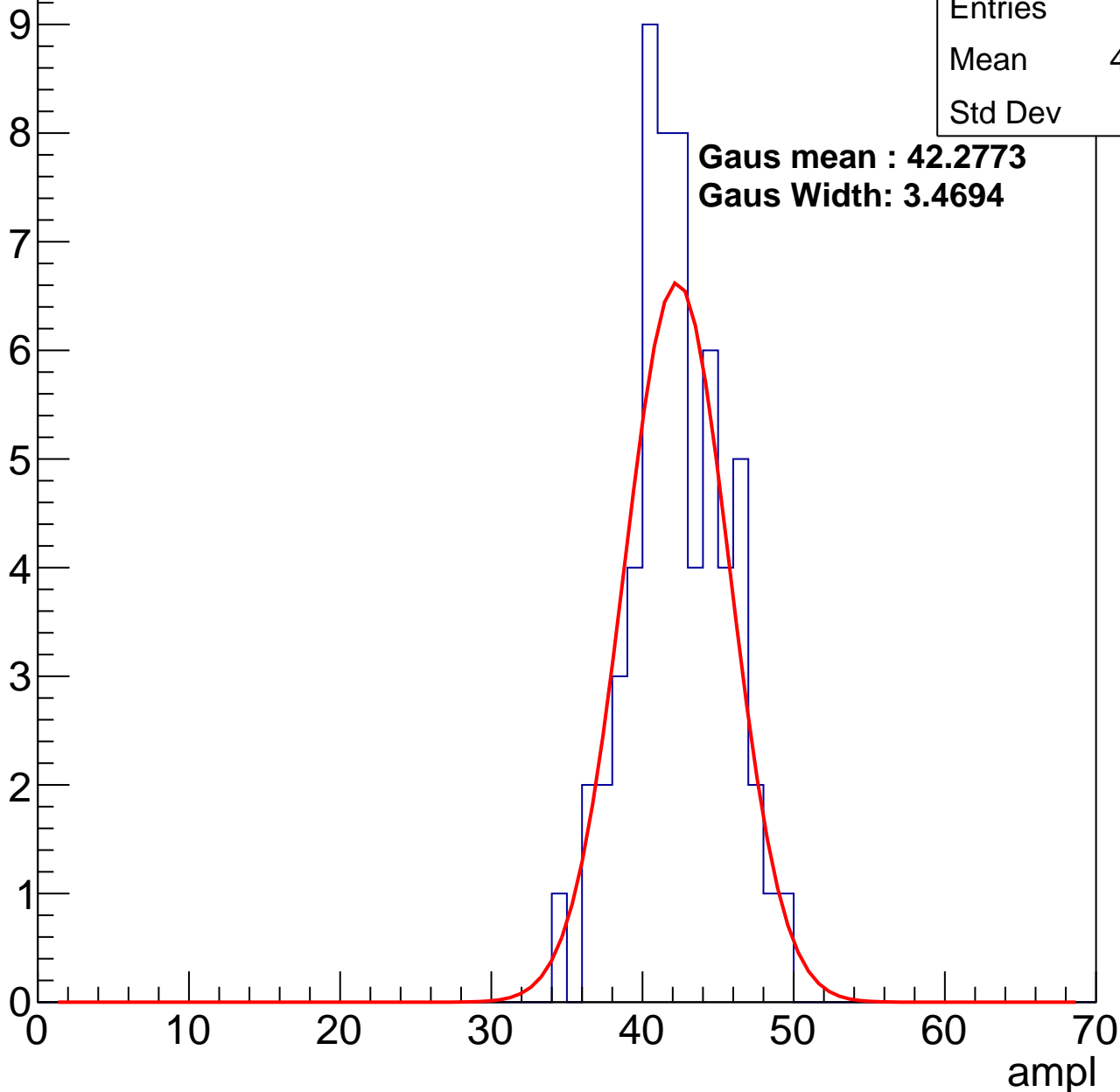
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.85
Std Dev	3.14

Gaus mean : 42.2773

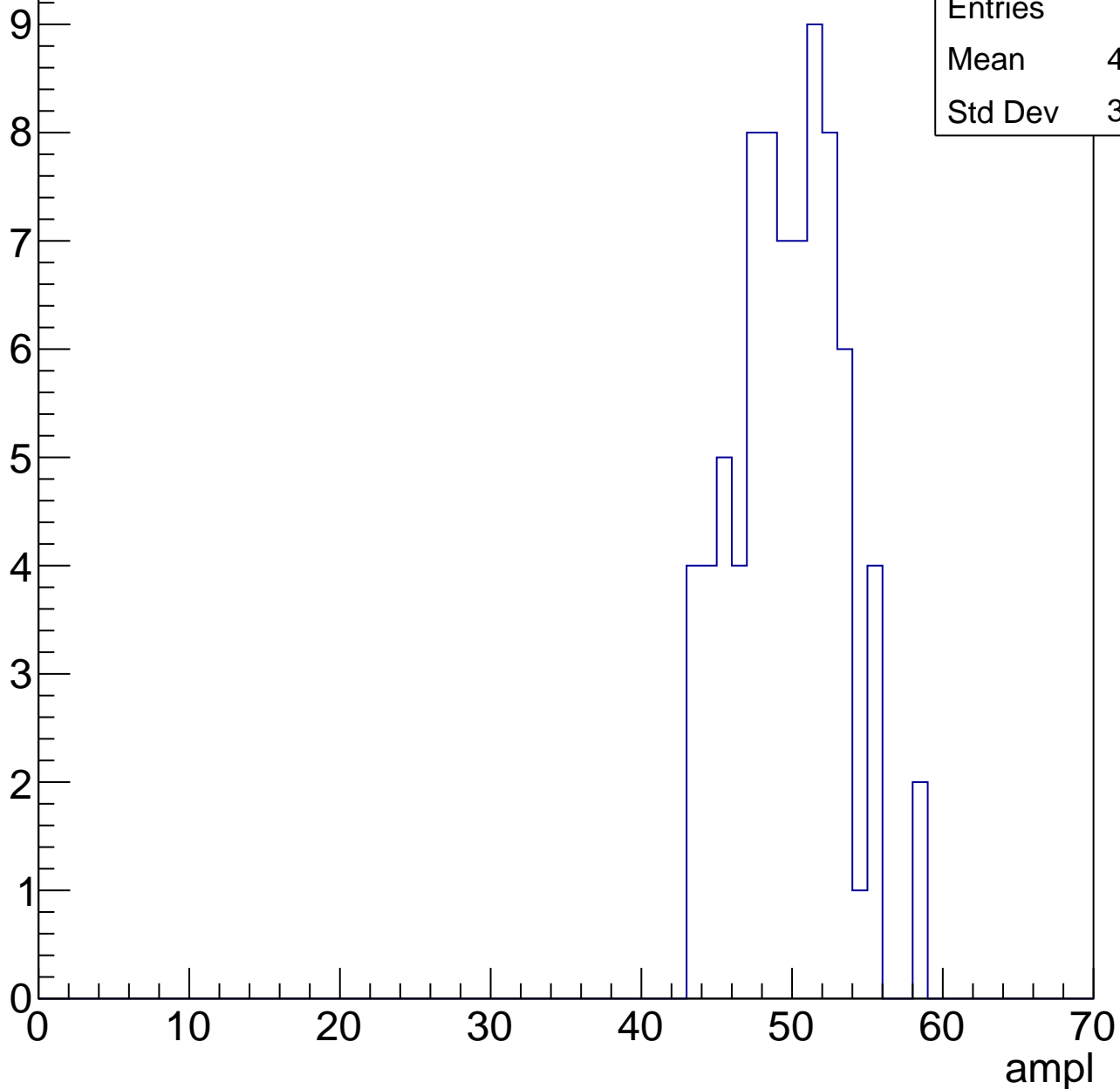
Gaus Width: 3.4694



B1L101S, U17-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

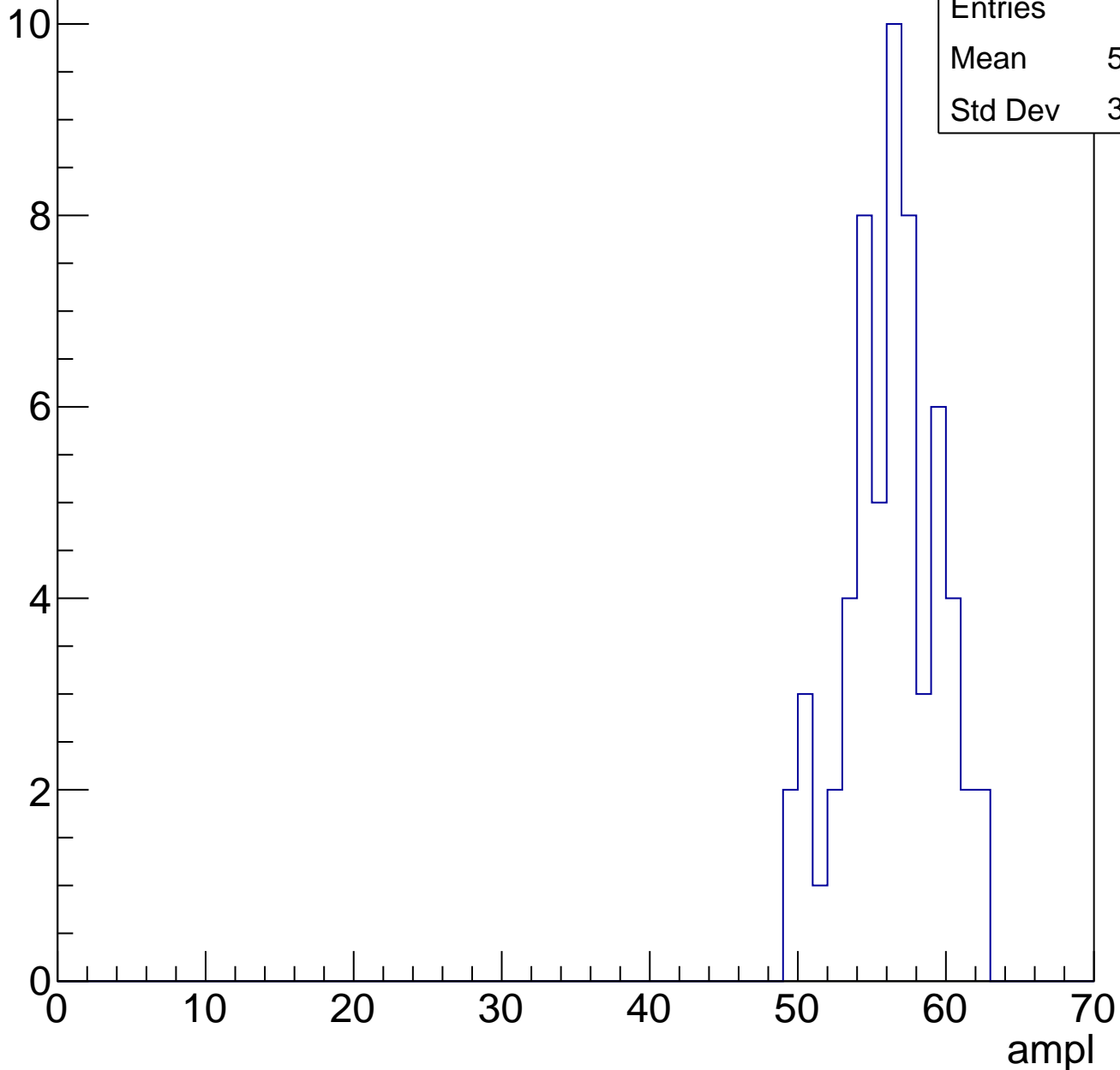


B1L101S, U17-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	55.87
Std Dev	3.149

Entry

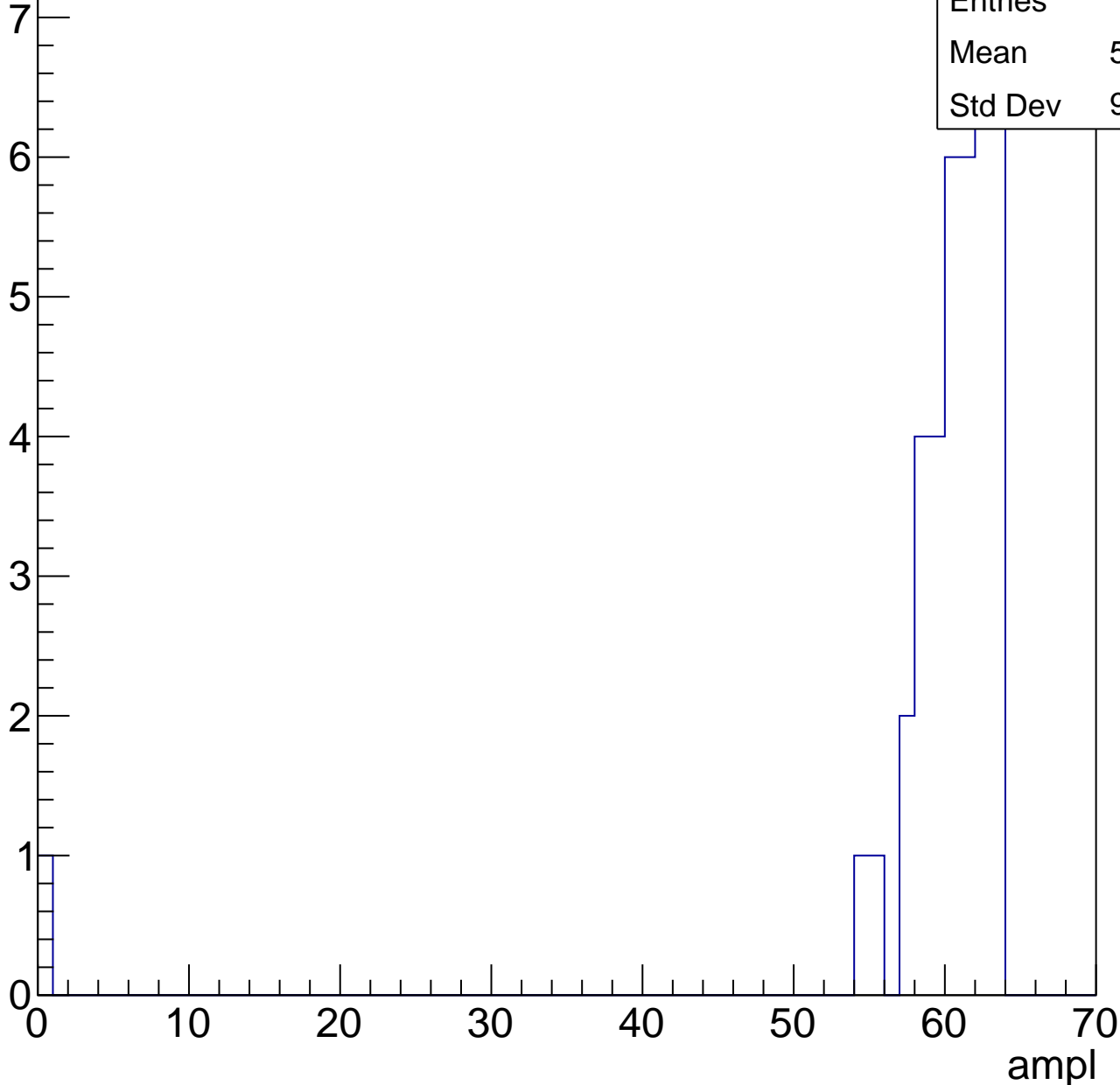


B1L101S, U17-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

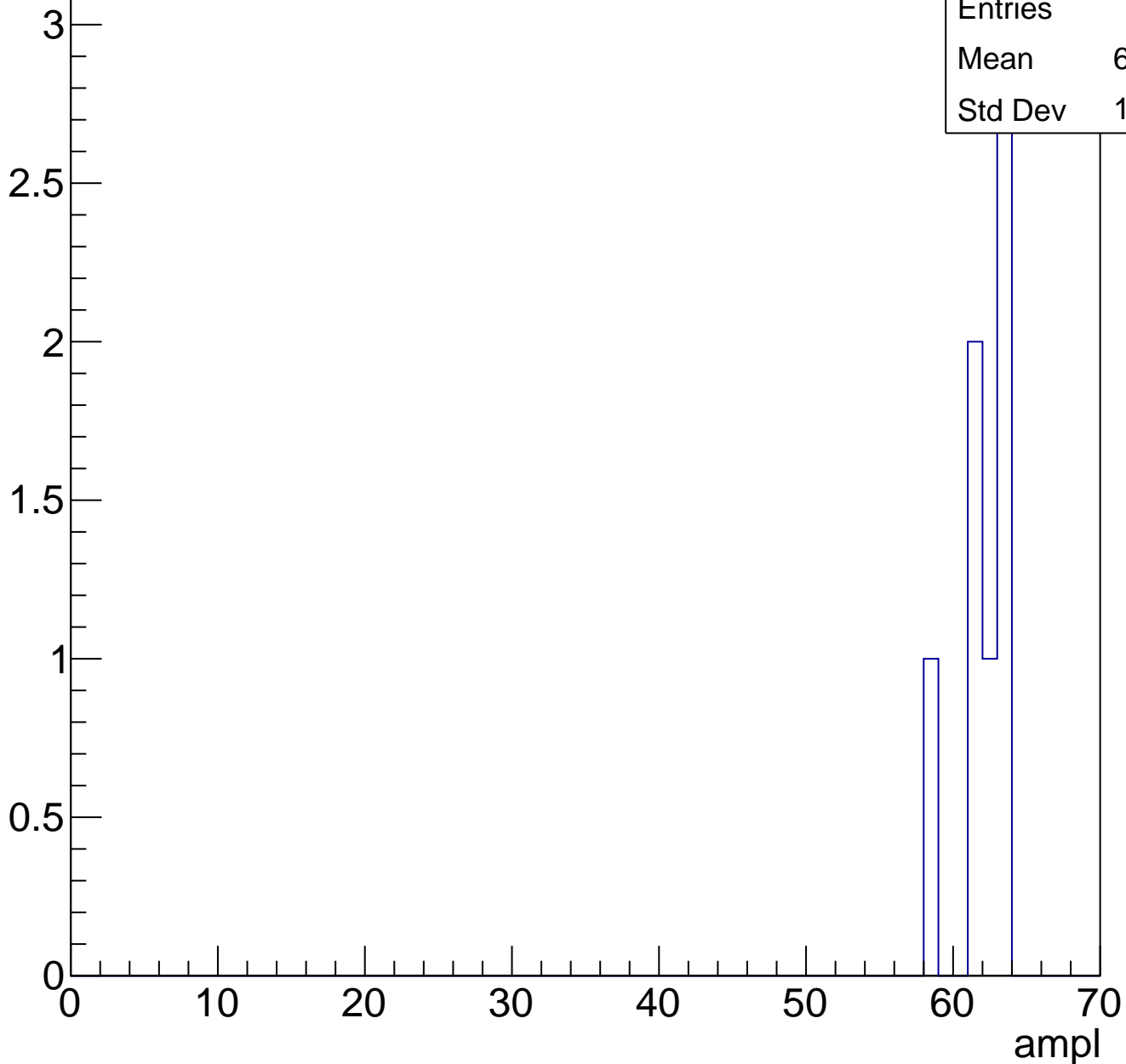
Entries	39
Mean	58.77
Std Dev	9.789



B1L101S, U17-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch62, adc0

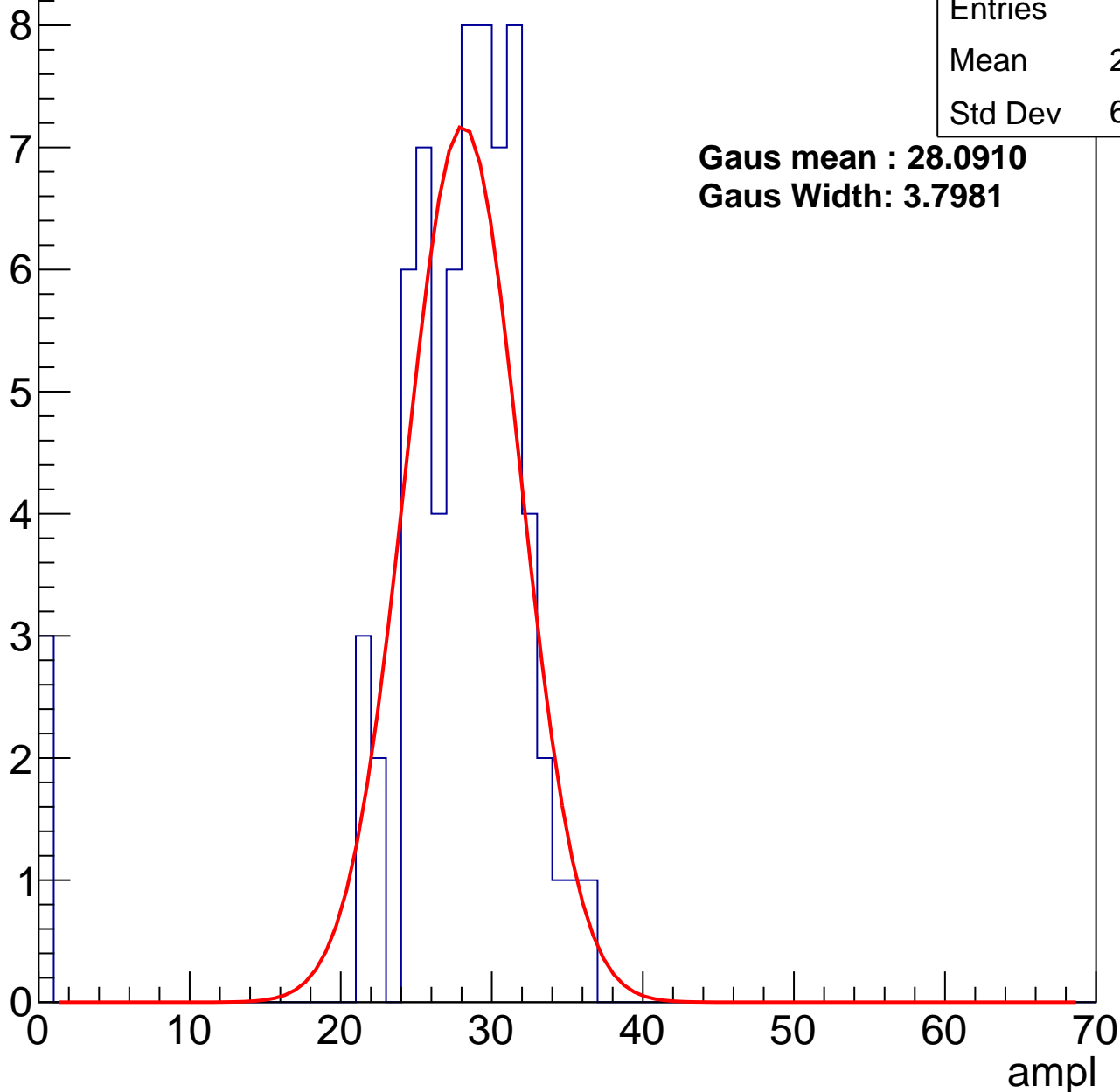
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	26.83
Std Dev	6.528

Gaus mean : 28.0910

Gaus Width: 3.7981



B1L101S, U17-ch62, adc1

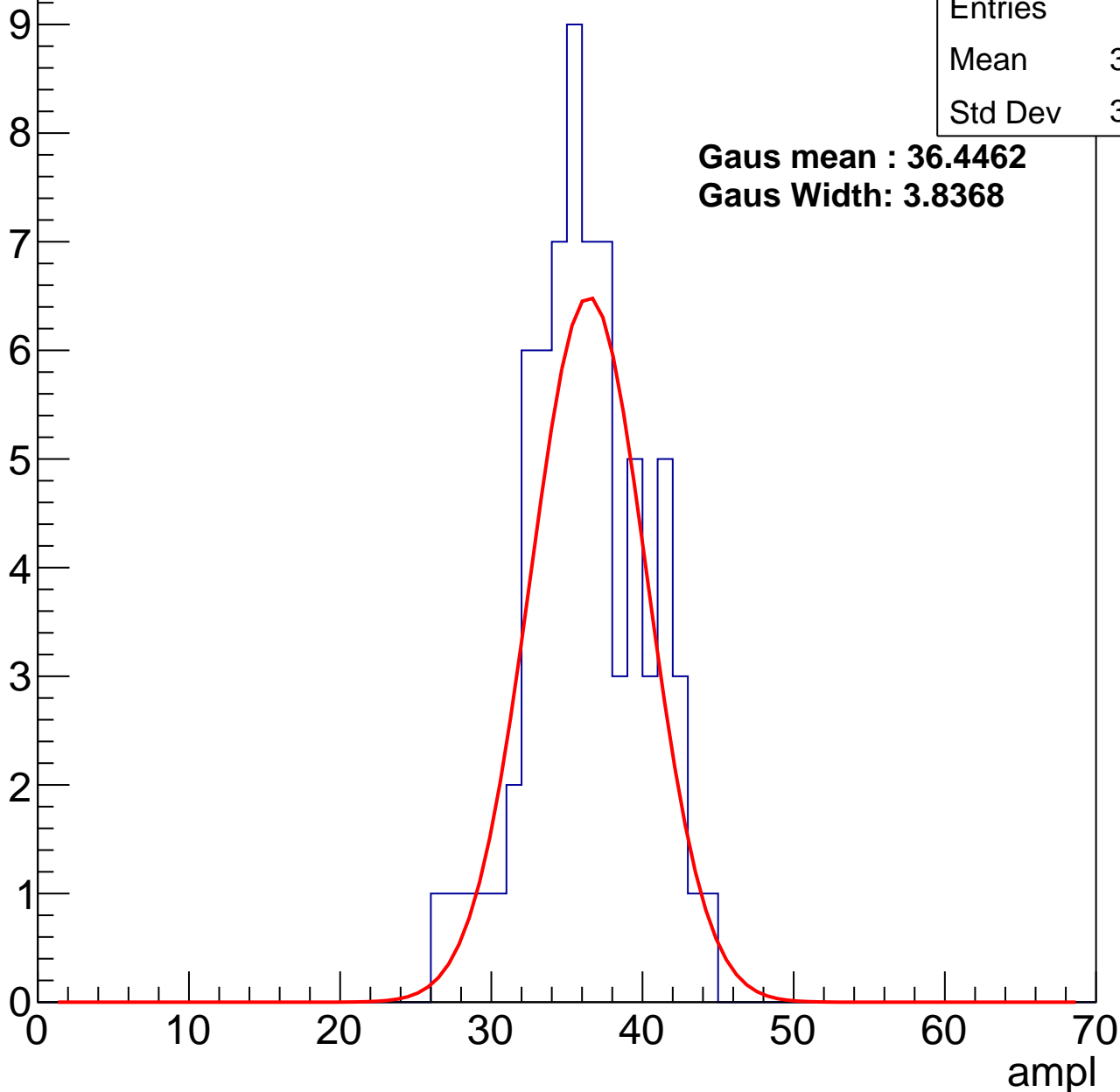
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.76
Std Dev	3.819

Gaus mean : 36.4462

Gaus Width: 3.8368



B1L101S, U17-ch62, adc2

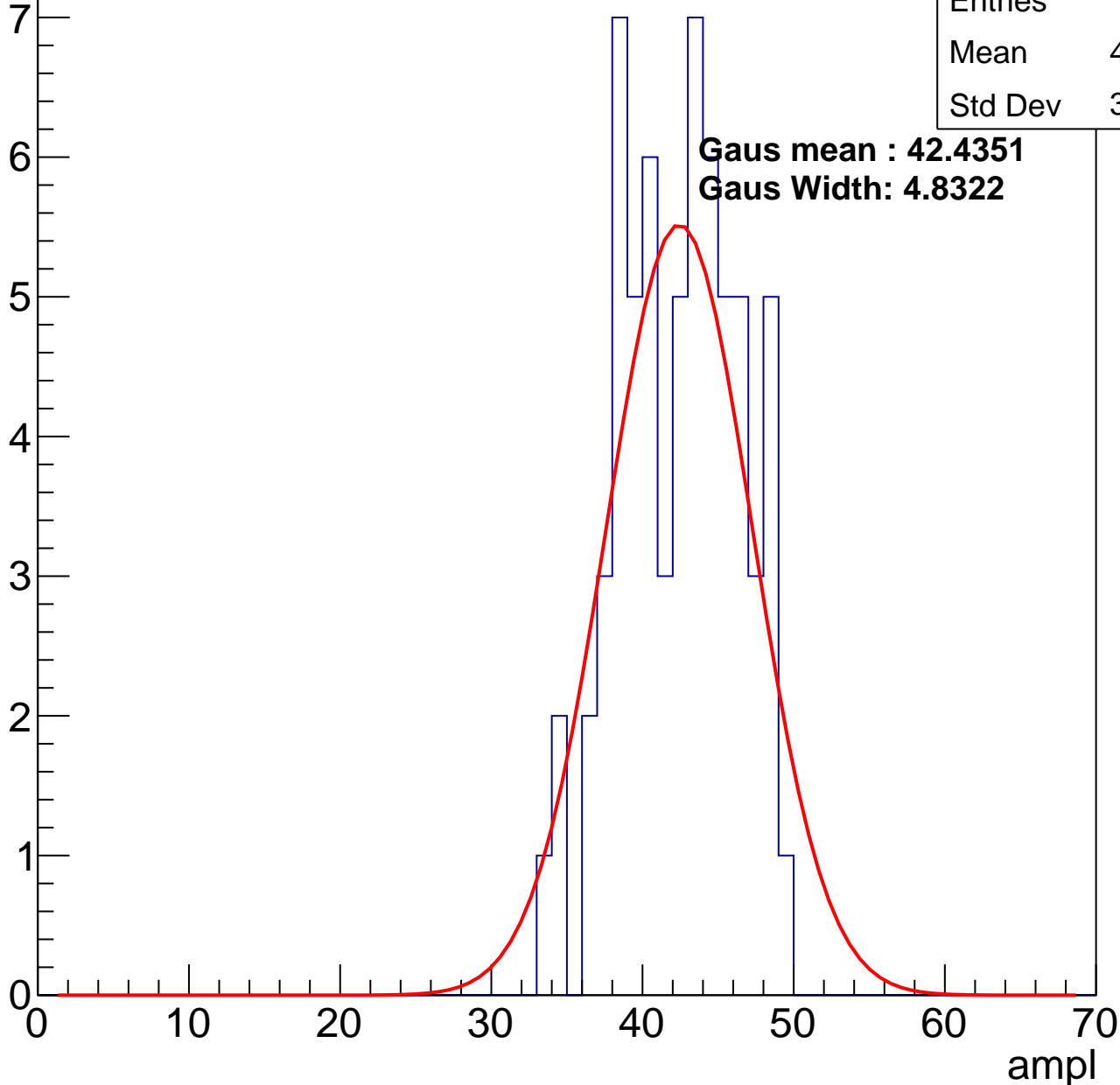
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.94
Std Dev	3.892

Gaus mean : 42.4351

Gaus Width: 4.8322

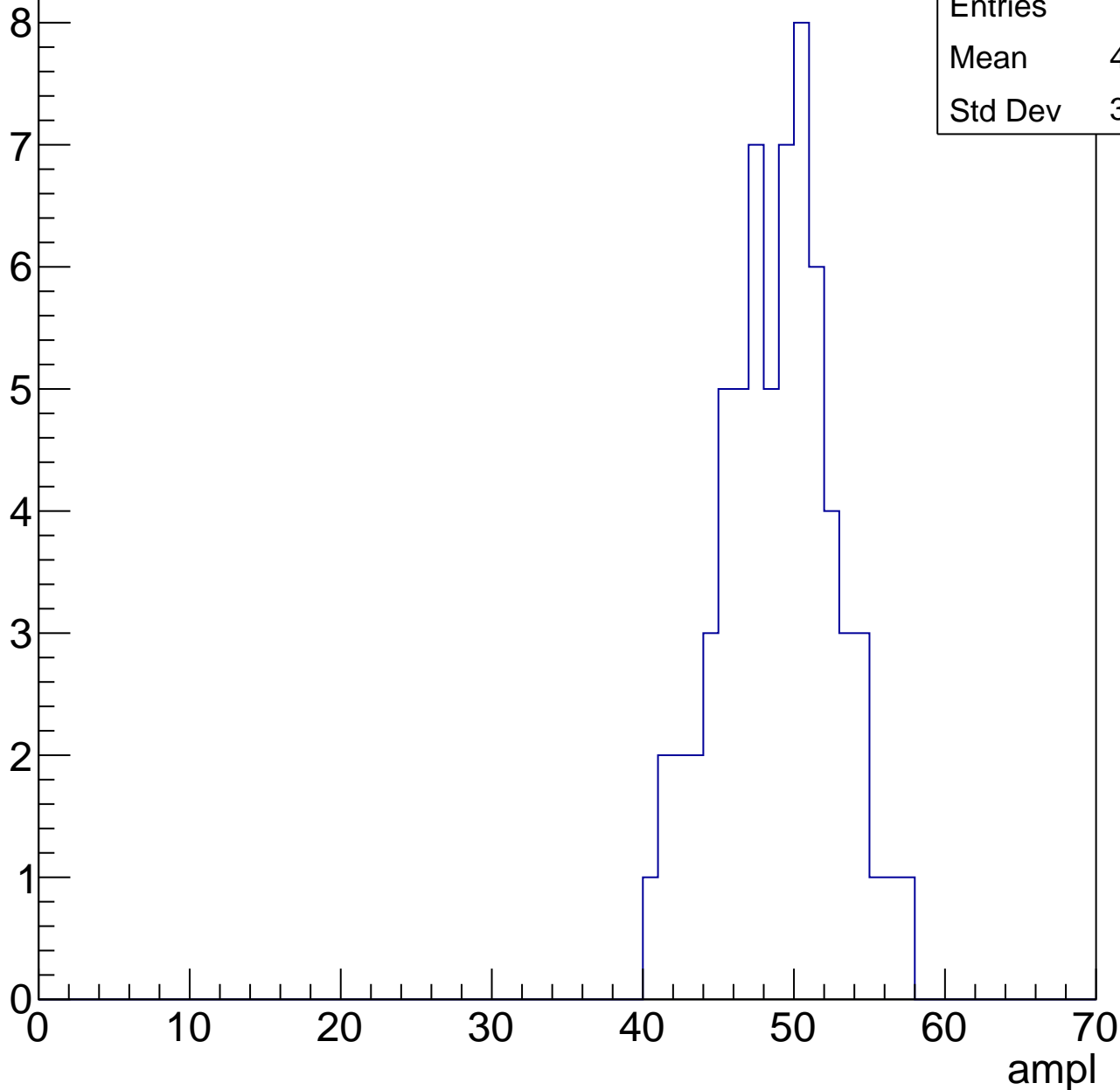


B1L101S, U17-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	48.39
Std Dev	3.749



B1L101S, U17-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

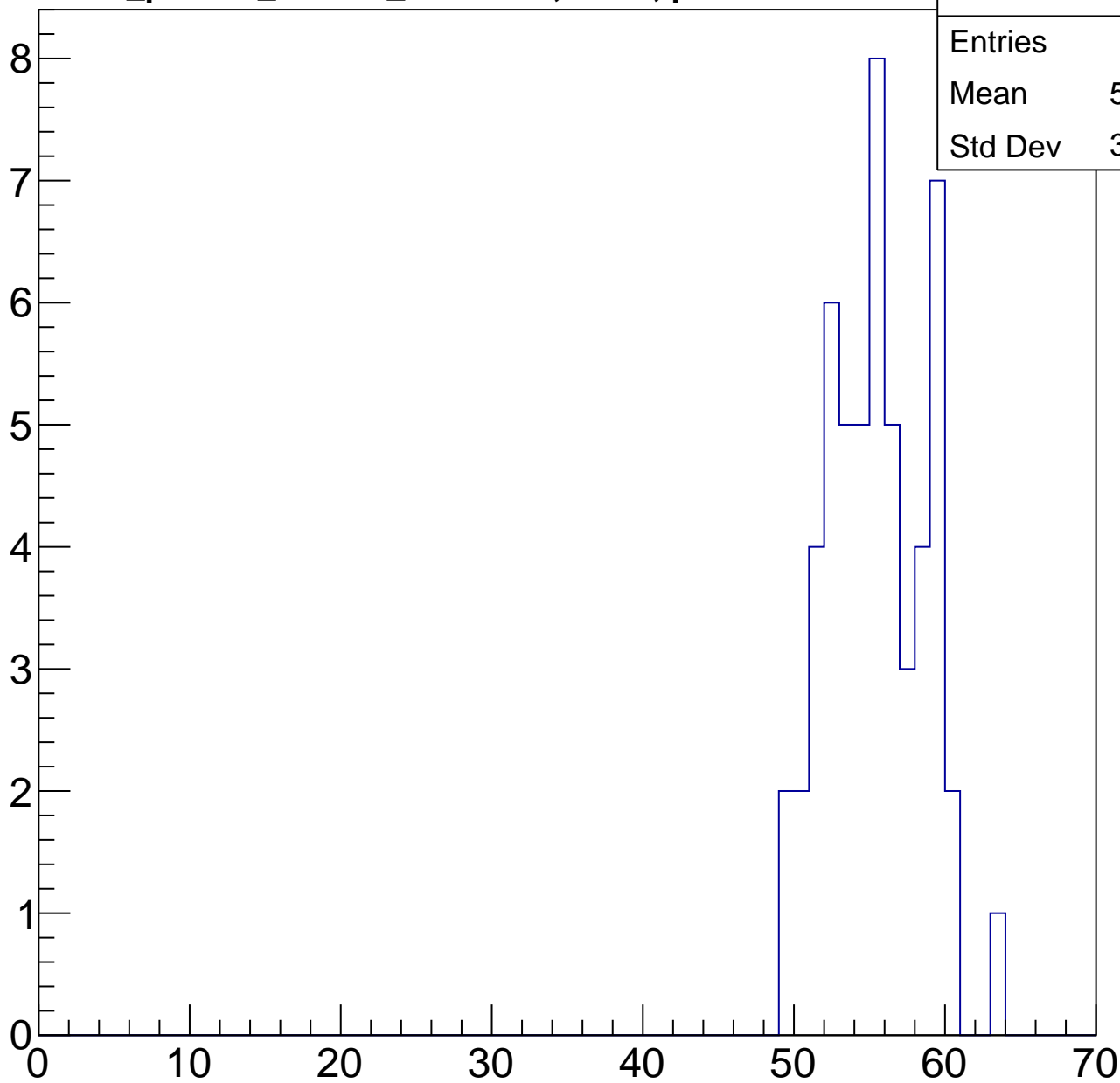
Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	54.96
Std Dev	3.174

ampl

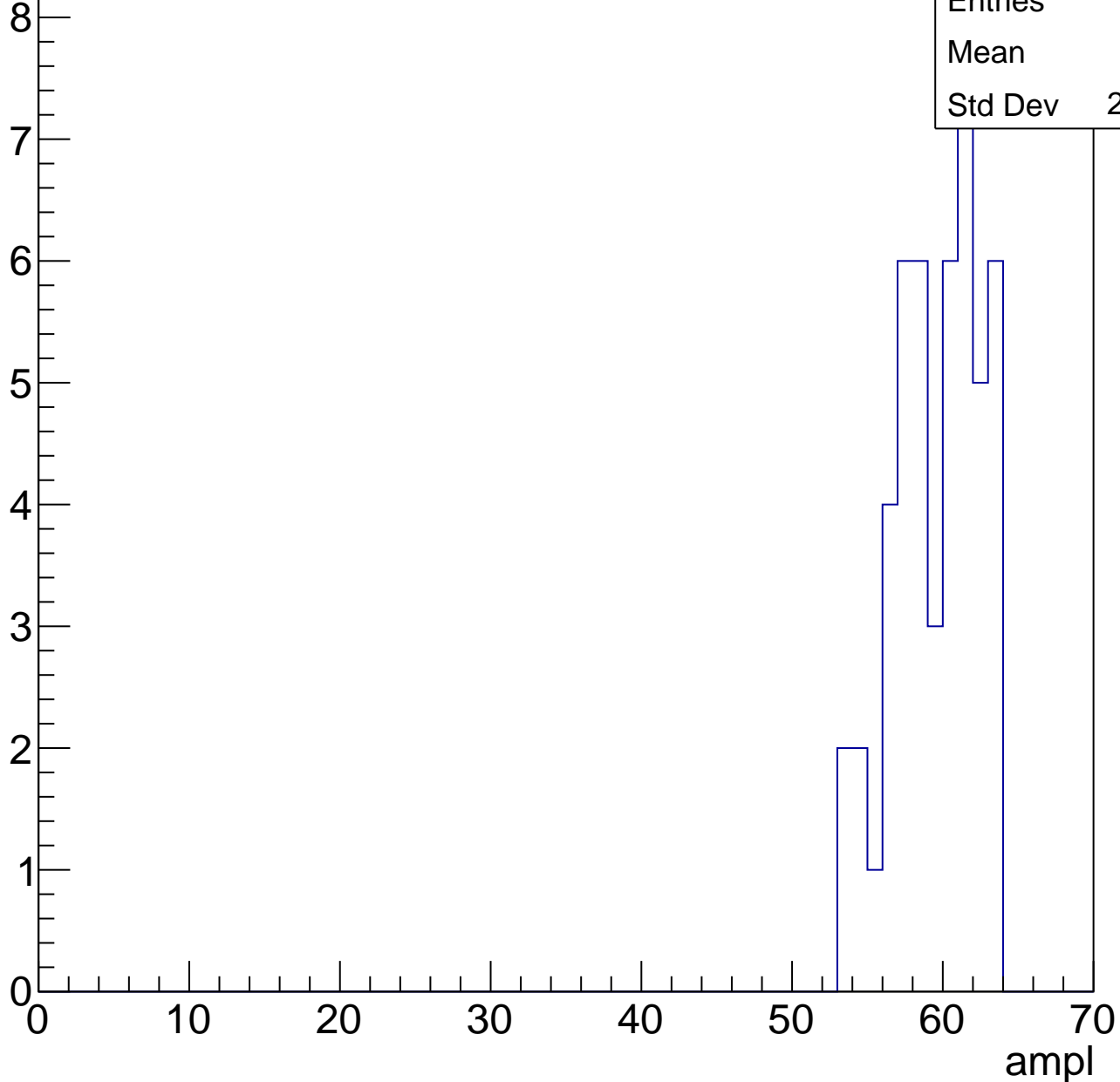
0 10 20 30 40 50 60 70



B1L101S, U17-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

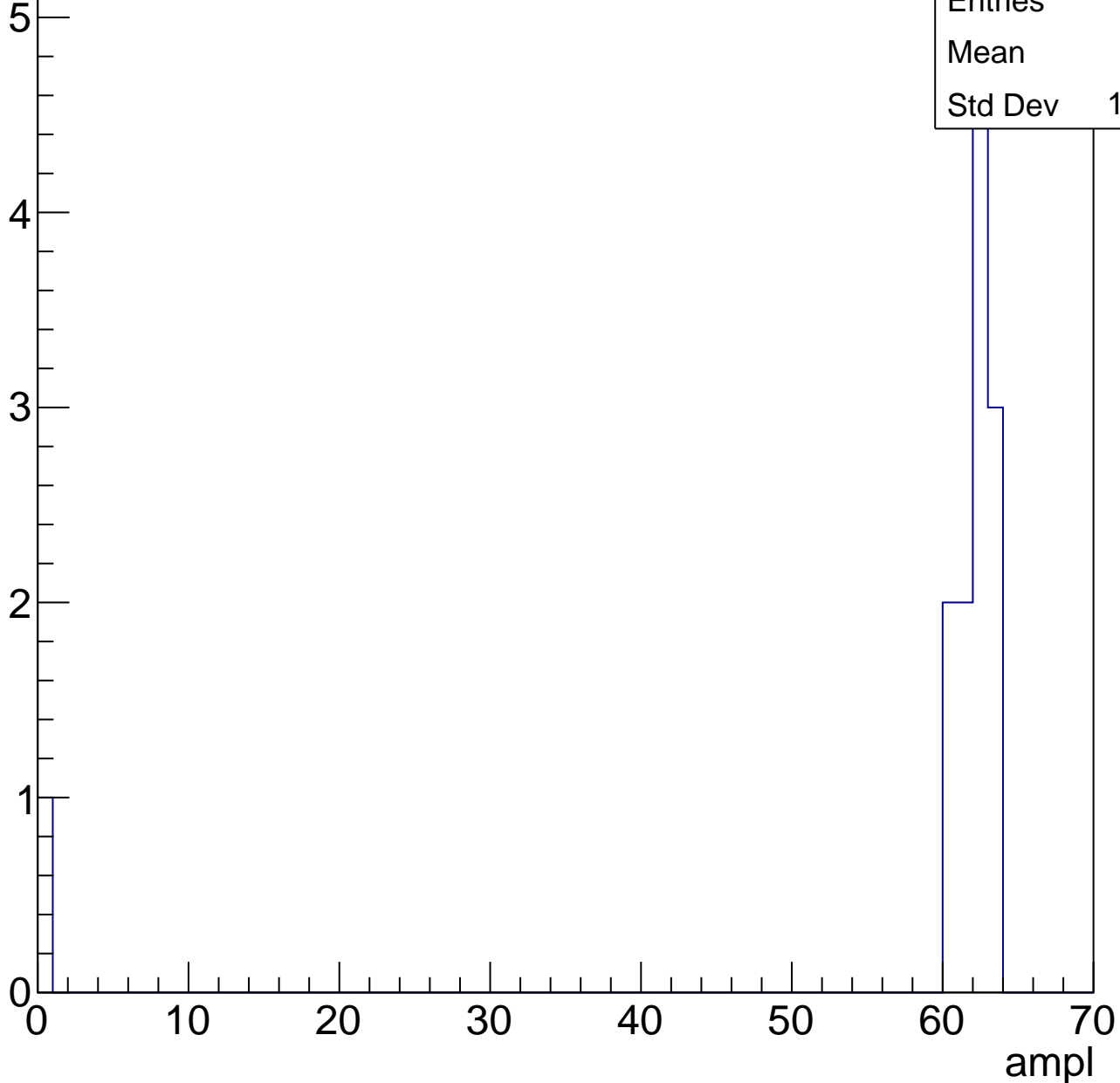


B1L101S, U17-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	57
Std Dev	16.48



B1L101S, U17-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch63, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	27.14
Std Dev	6.666

Gaus mean : 28.3969

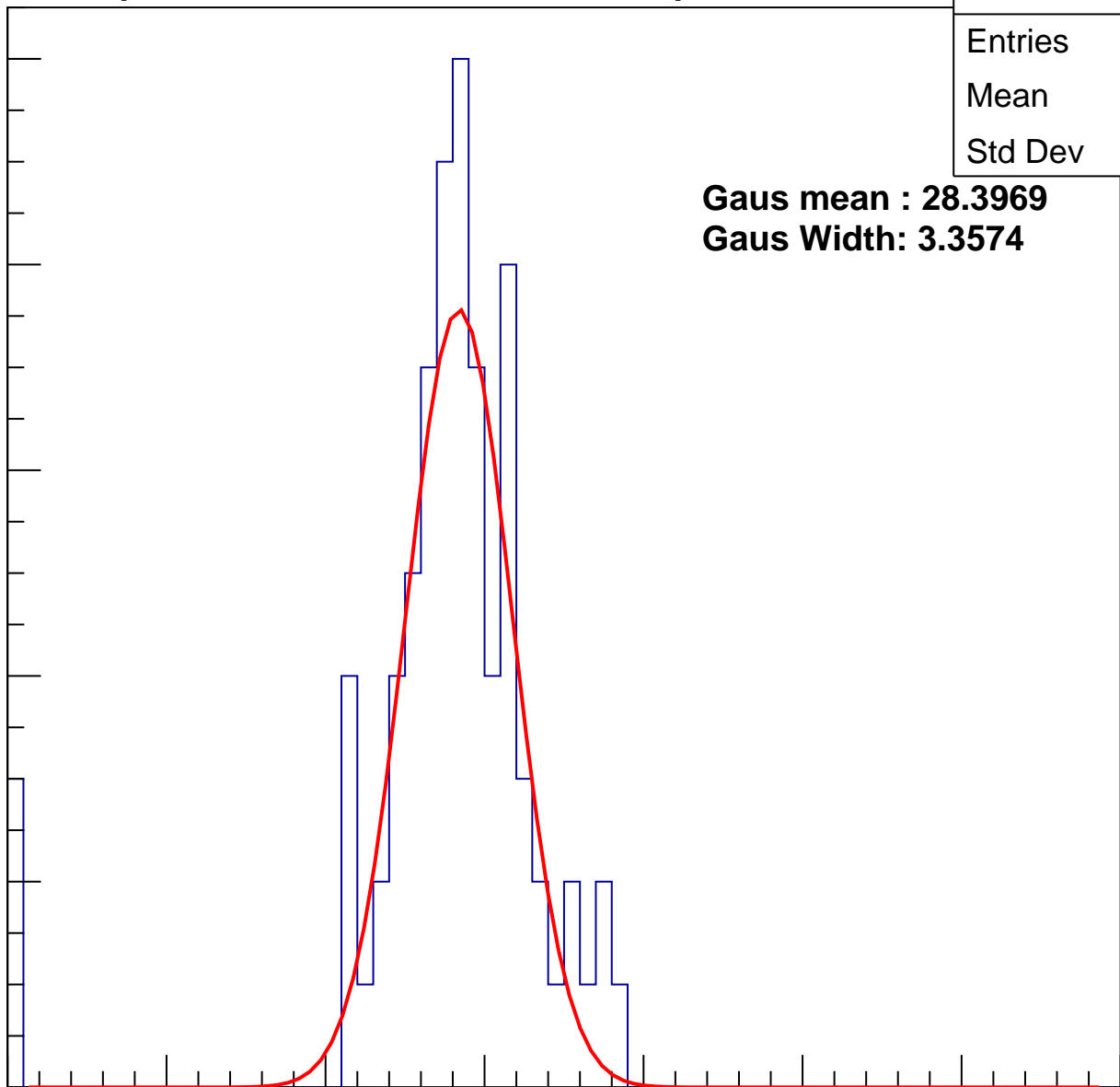
Gaus Width: 3.3574

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch63, adc1

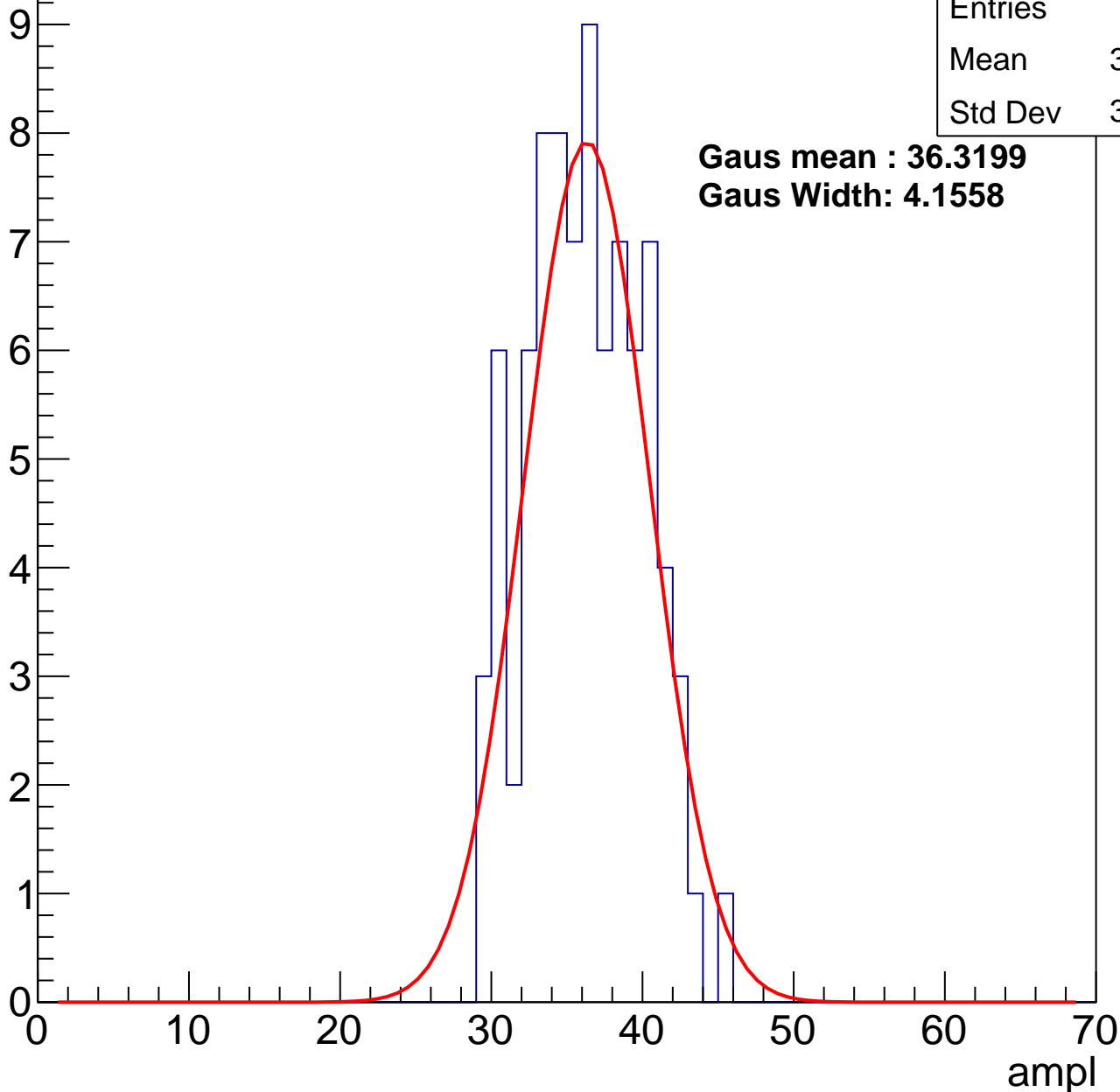
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	35.79
Std Dev	3.707

Gaus mean : 36.3199

Gaus Width: 4.1558



B1L101S, U17-ch63, adc2

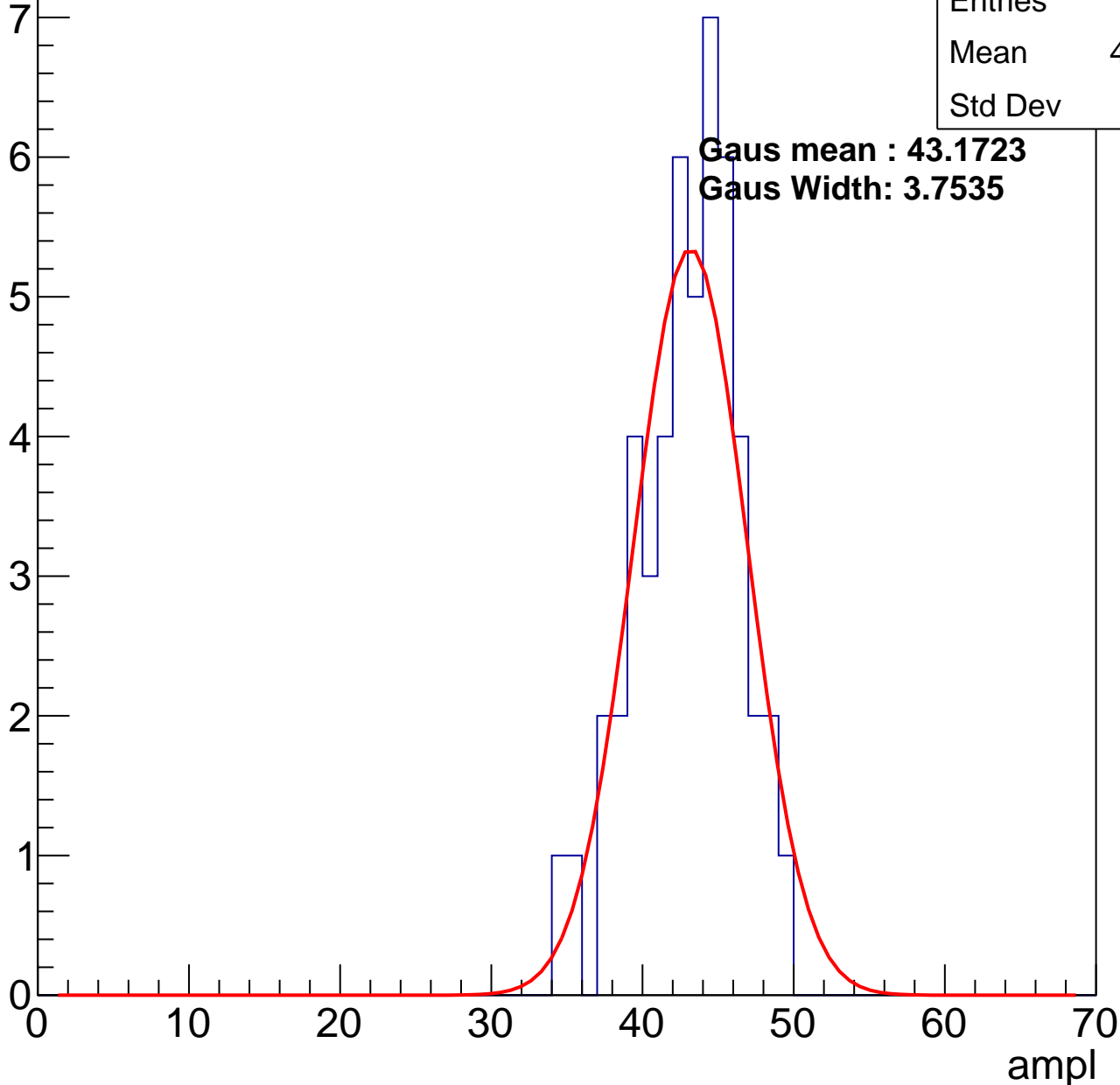
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.54
Std Dev	3.33

Gaus mean : 43.1723

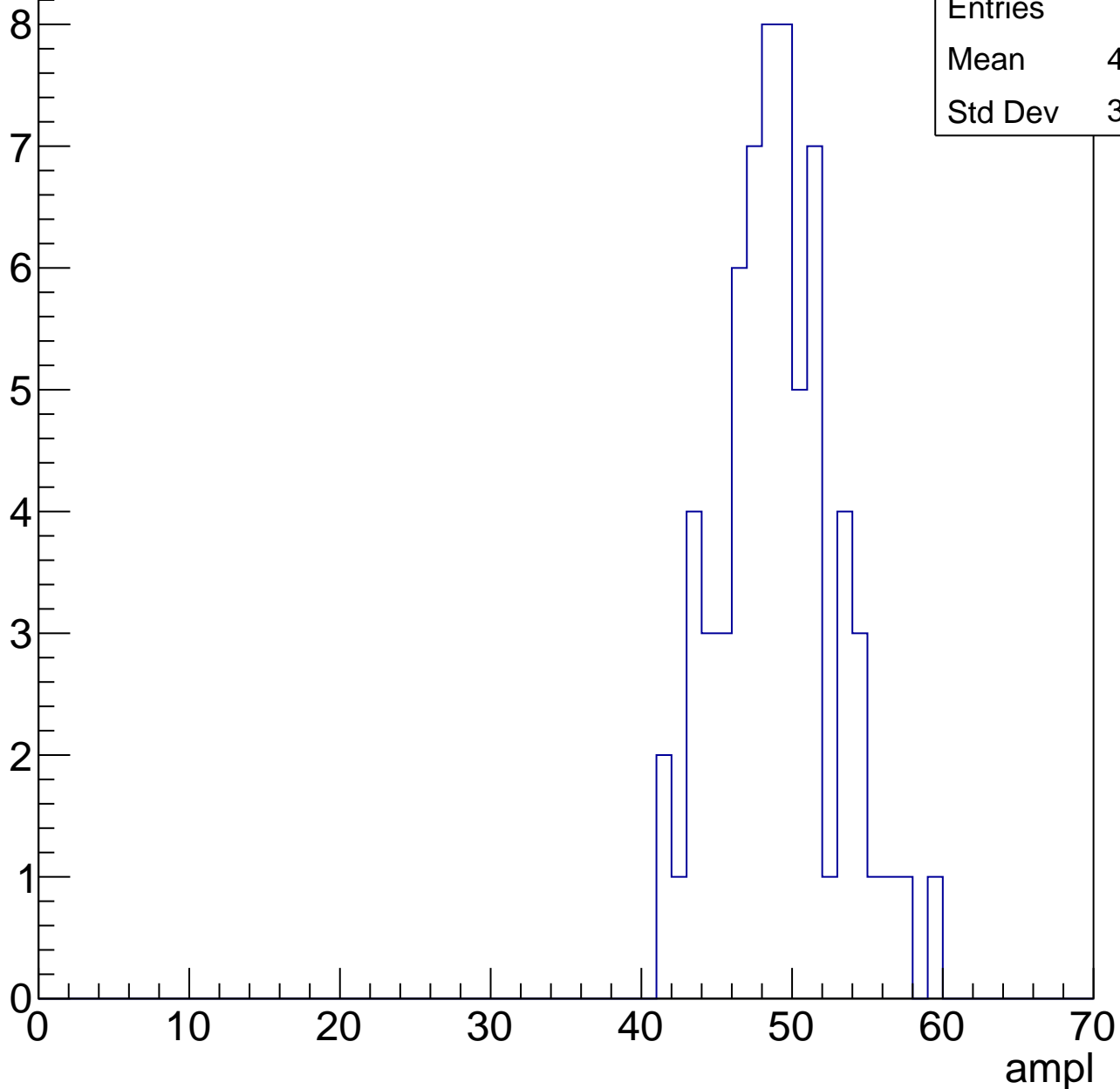
Gaus Width: 3.7535



B1L101S, U17-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



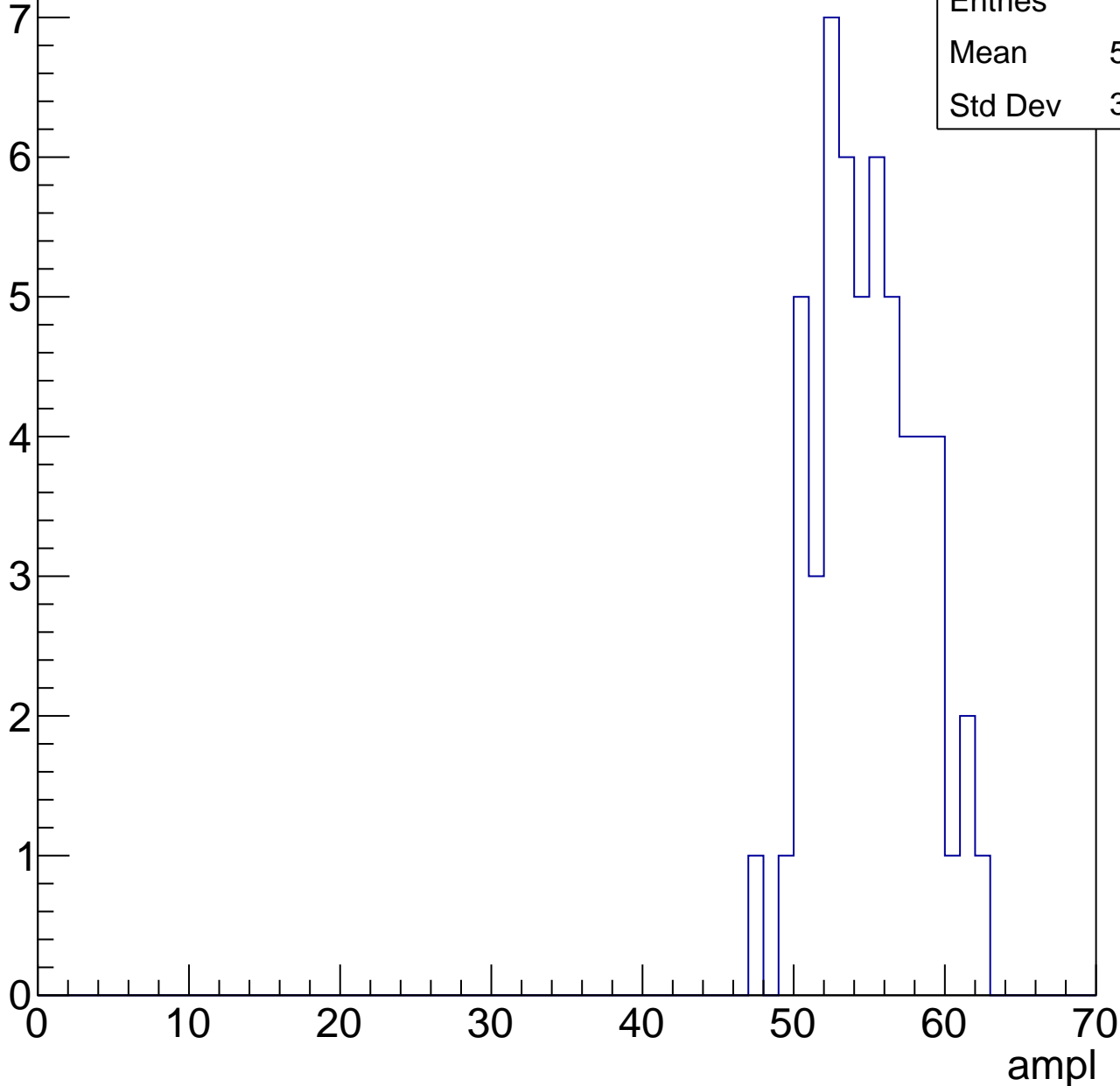
Entries	66
Mean	48.55
Std Dev	3.795

B1L101S, U17-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.56
Std Dev	3.362

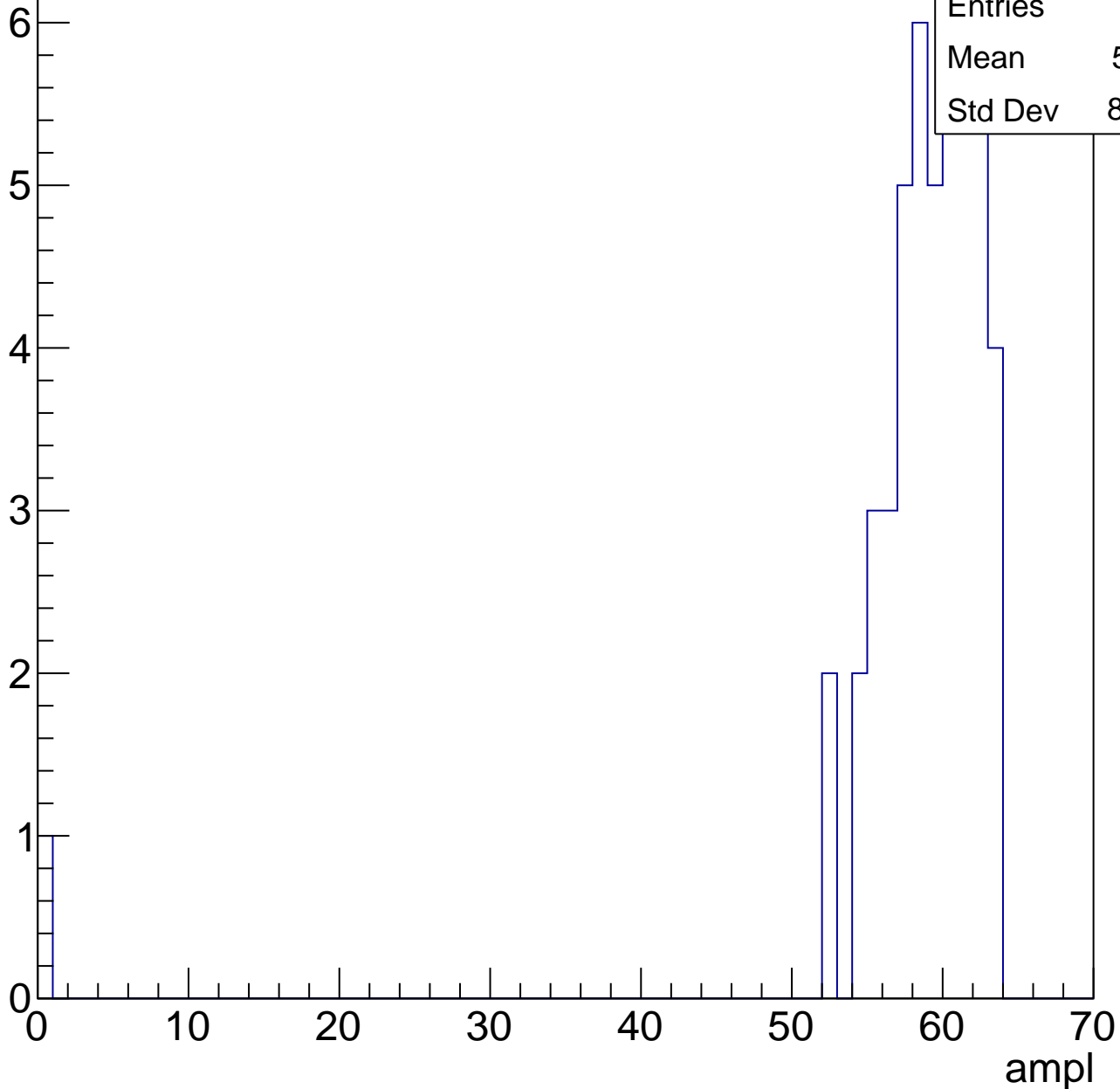


B1L101S, U17-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.61
Std Dev	8.787

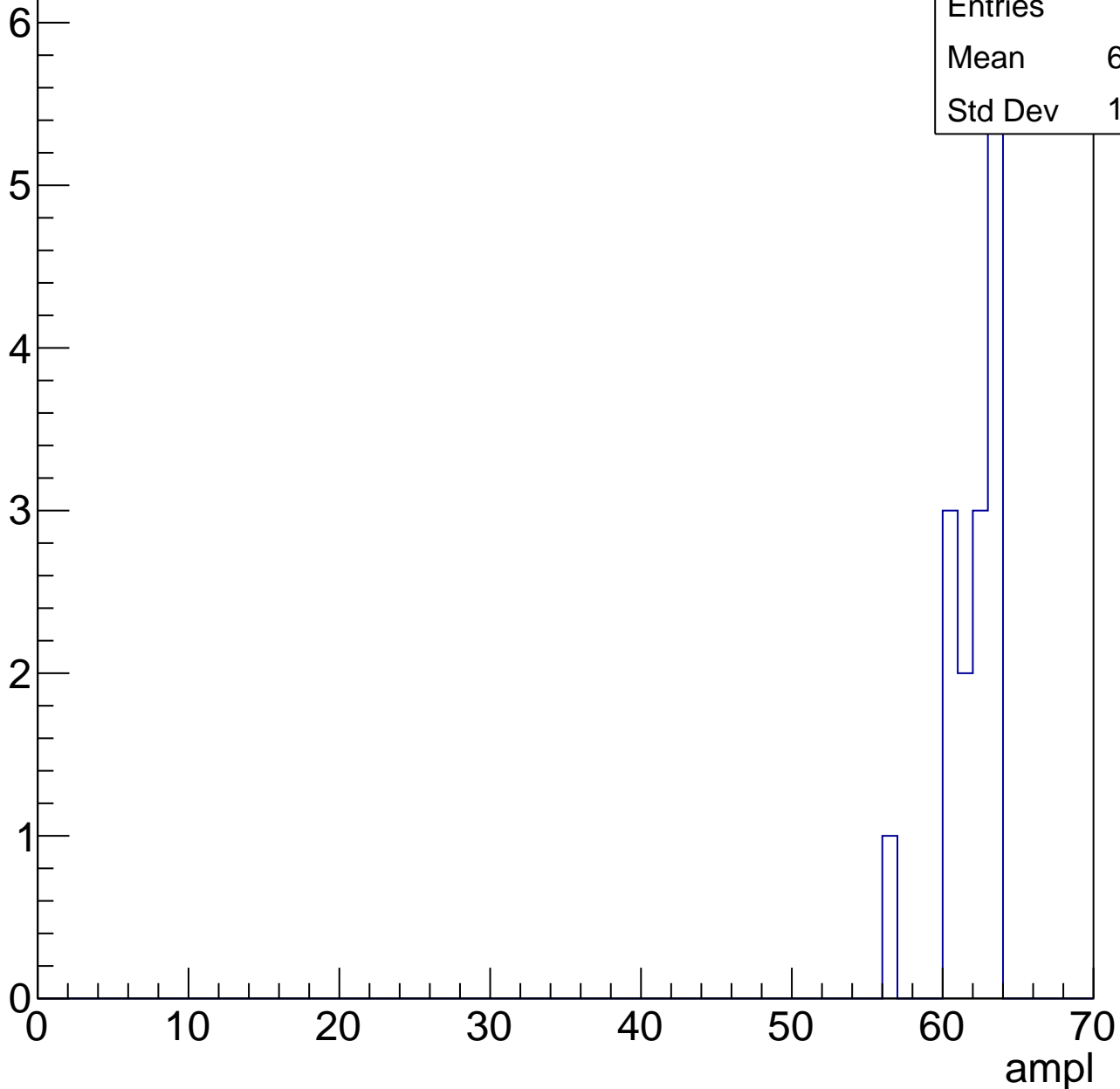


B1L101S, U17-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.47
Std Dev	1.857



B1L101S, U17-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L101S, U17-ch64, adc0

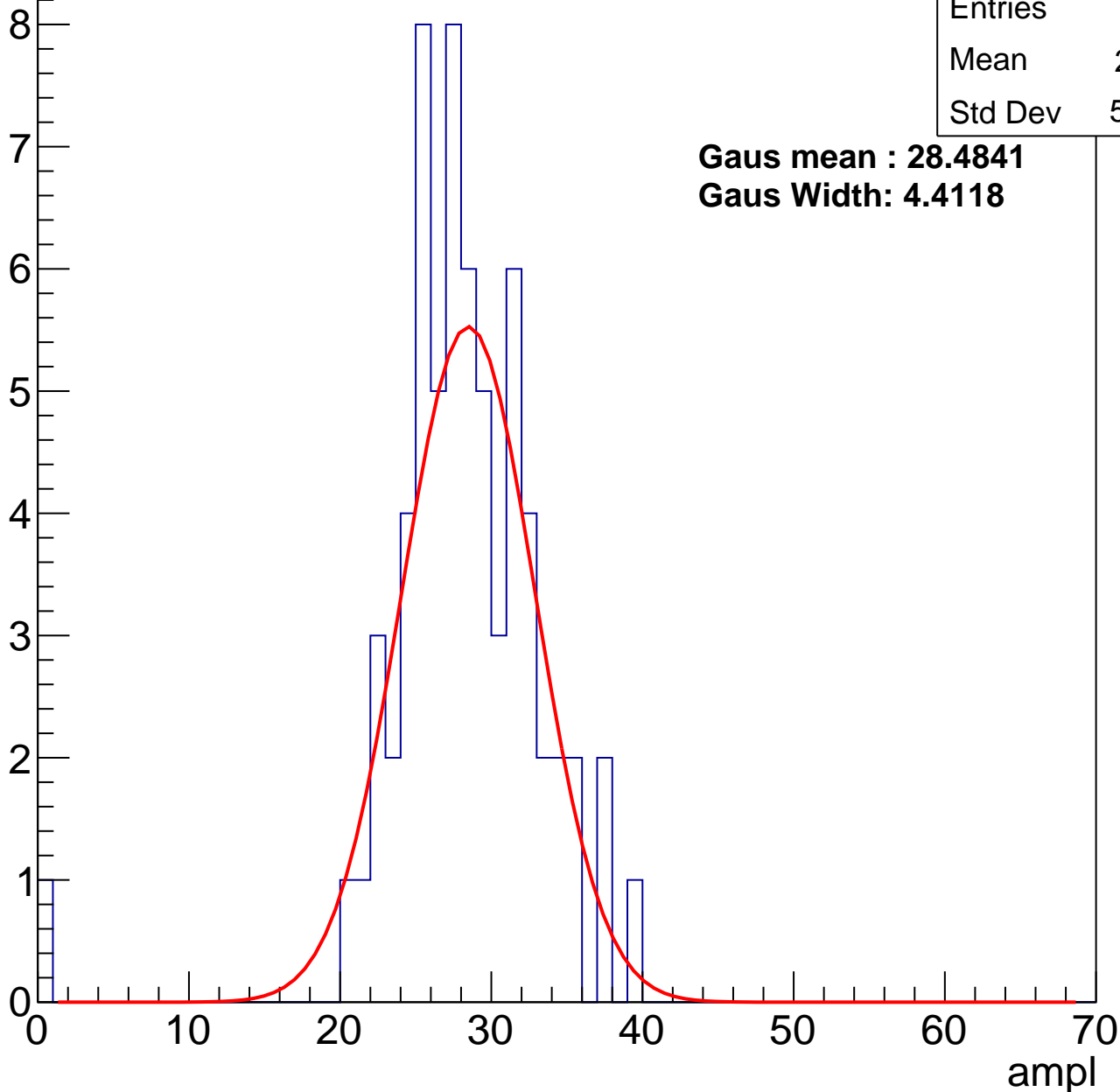
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.71
Std Dev	5.297

Gaus mean : 28.4841

Gaus Width: 4.4118



B1L101S, U17-ch64, adc1

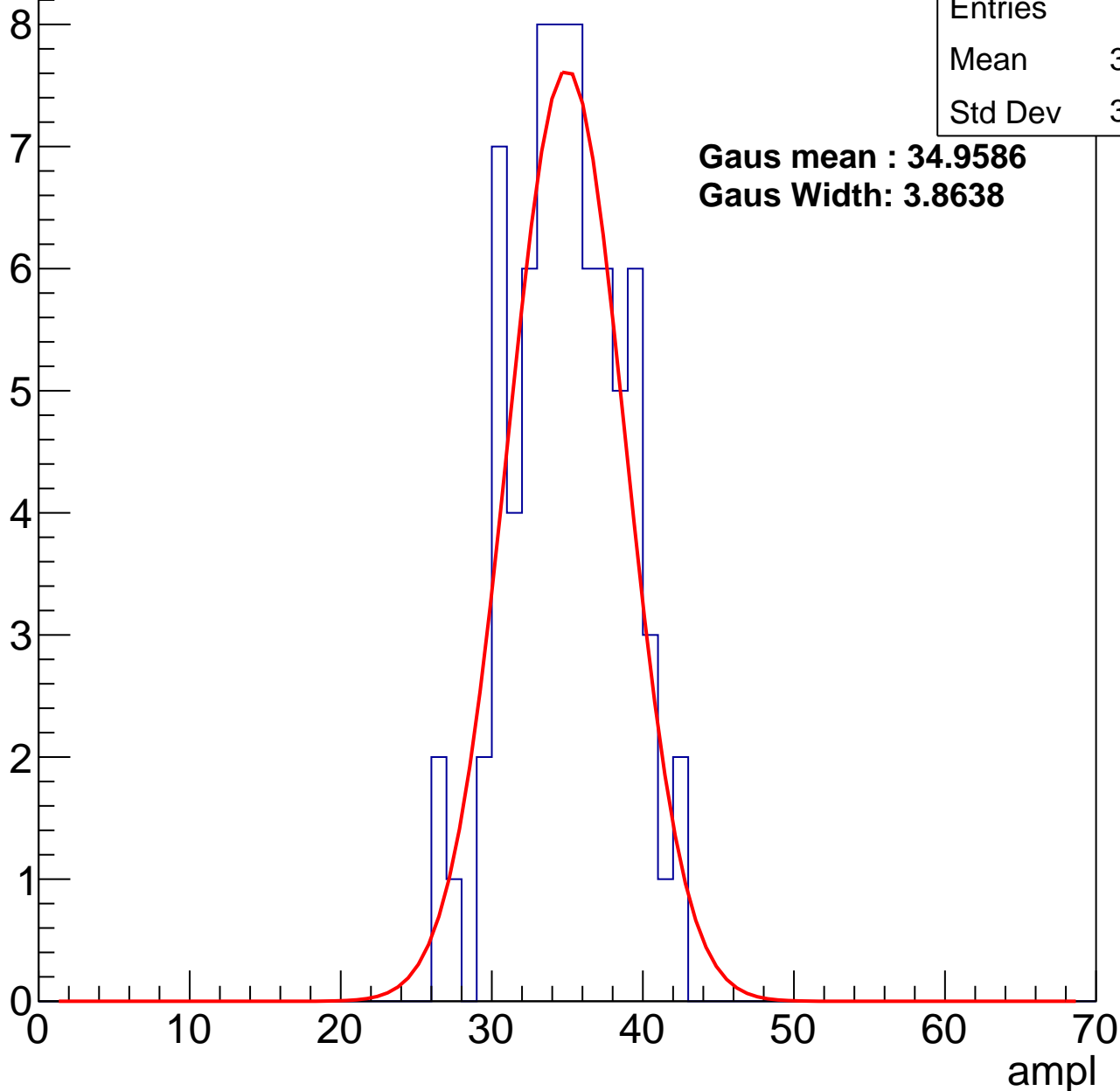
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	34.48
Std Dev	3.635

Gaus mean : 34.9586

Gaus Width: 3.8638



B1L101S, U17-ch64, adc2

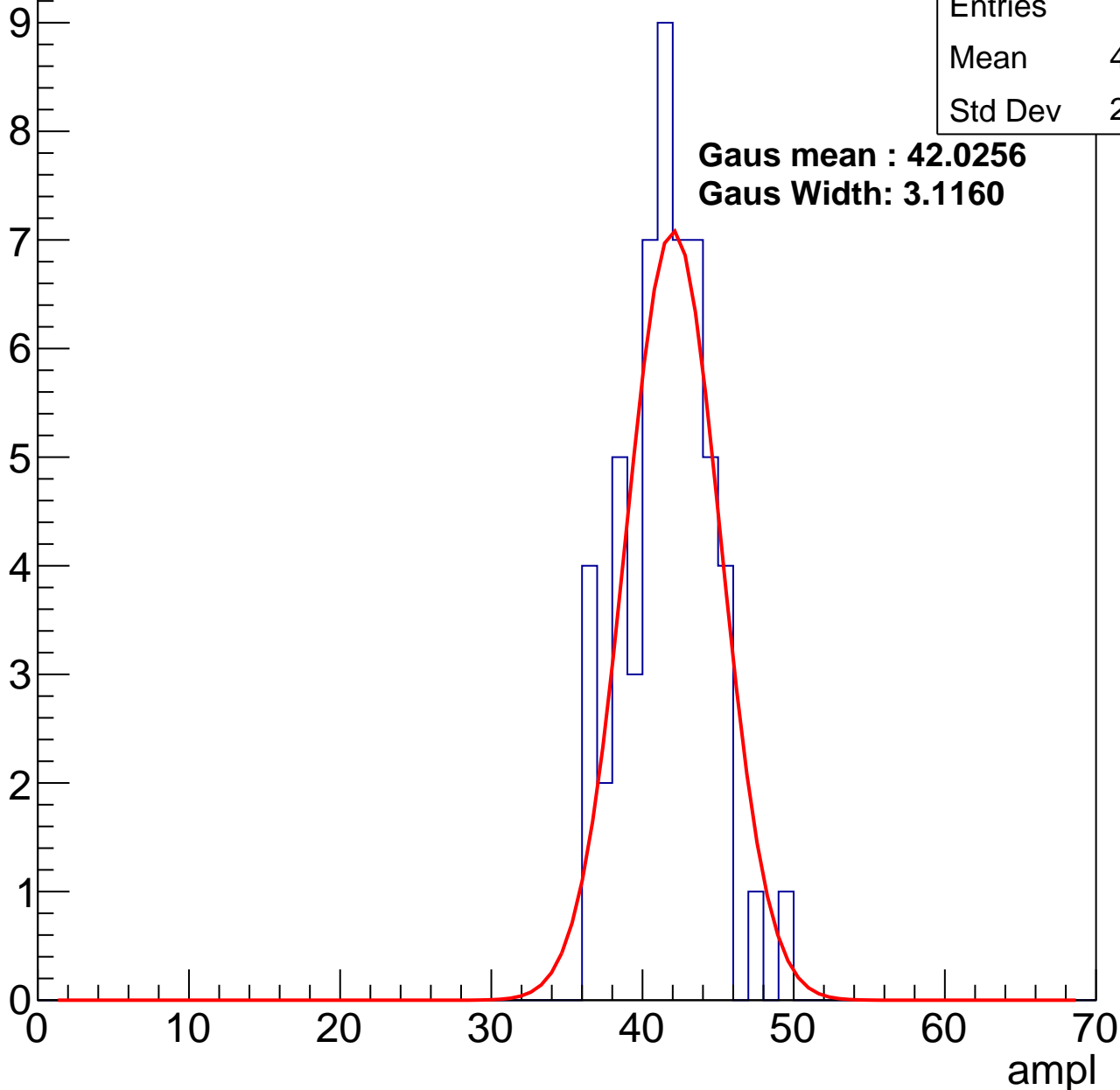
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.18
Std Dev	2.823

Gaus mean : 42.0256

Gaus Width: 3.1160

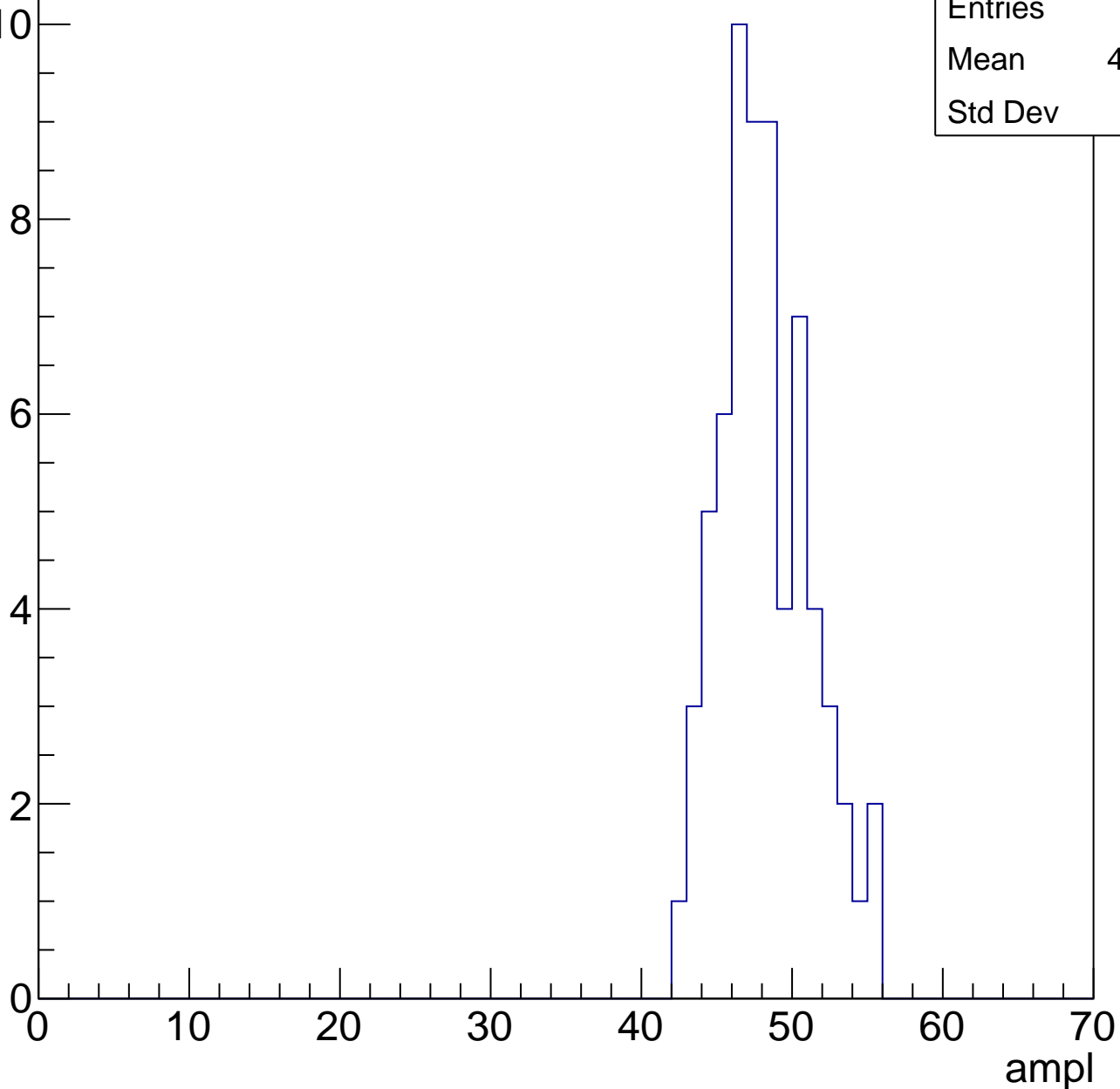


B1L101S, U17-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	47.76
Std Dev	2.99

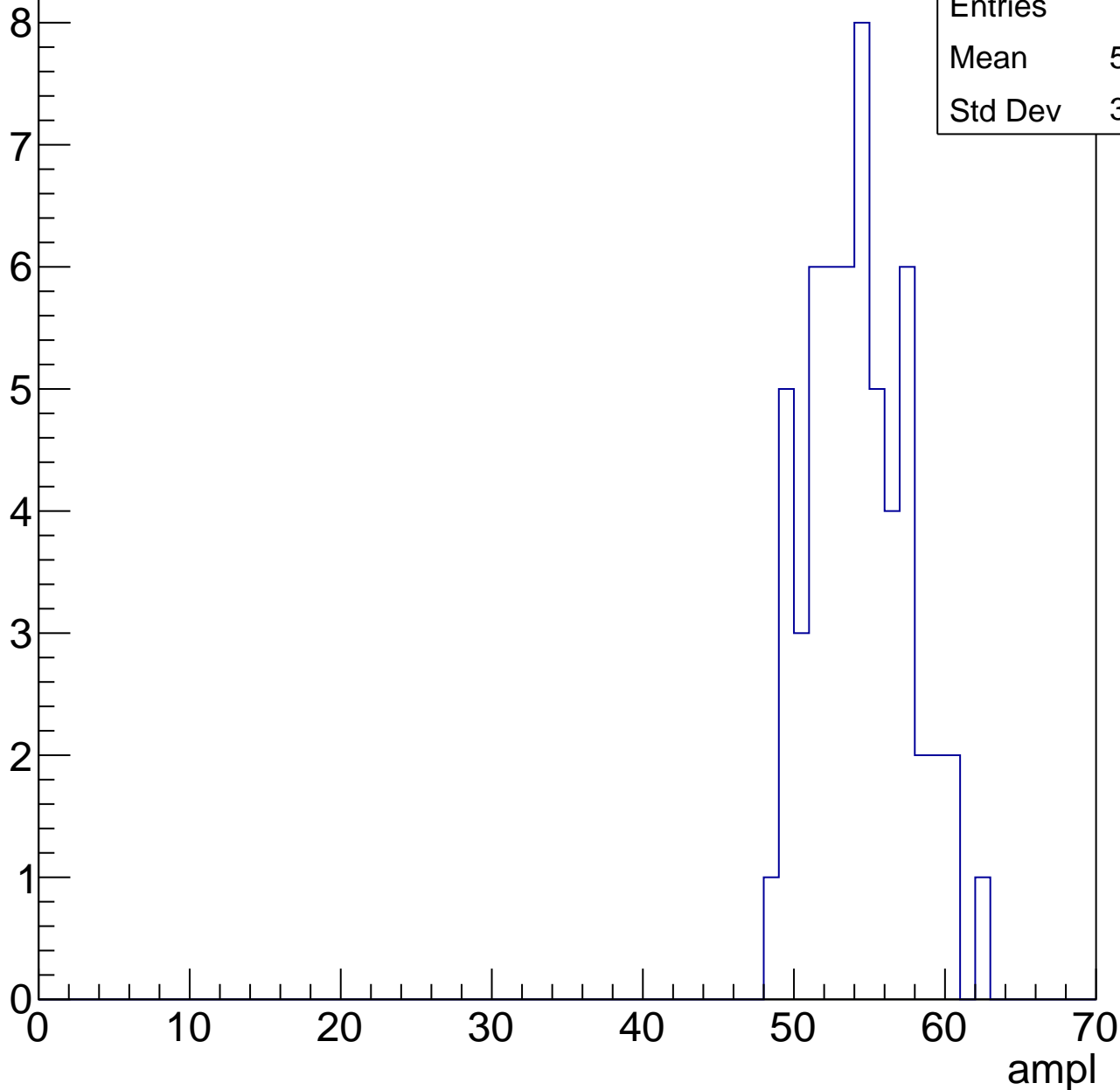


B1L101S, U17-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	53.82
Std Dev	3.202

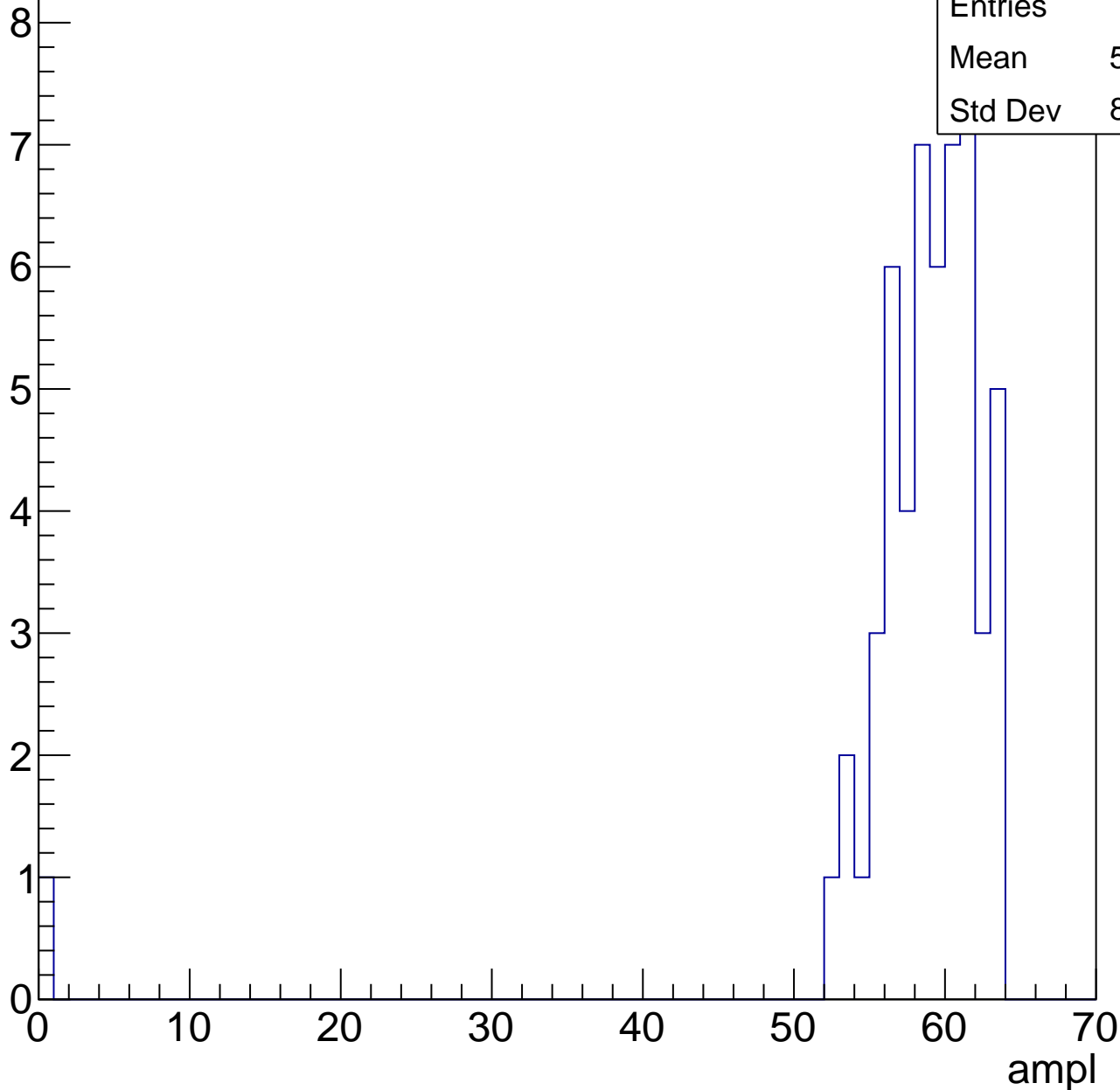


B1L101S, U17-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.59
Std Dev	8.379

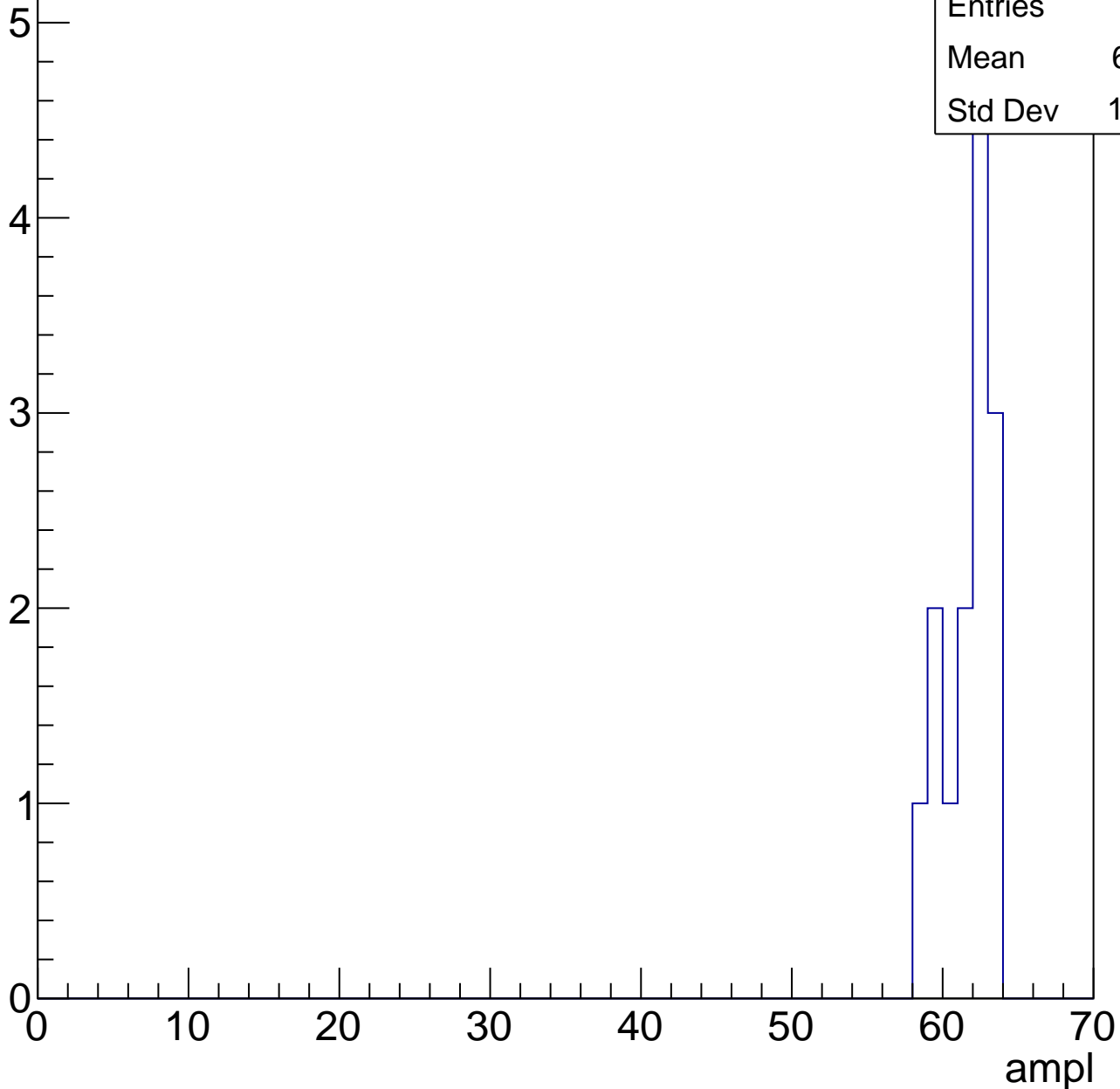


B1L101S, U17-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.21
Std Dev	1.567



B1L101S, U17-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch65, adc0

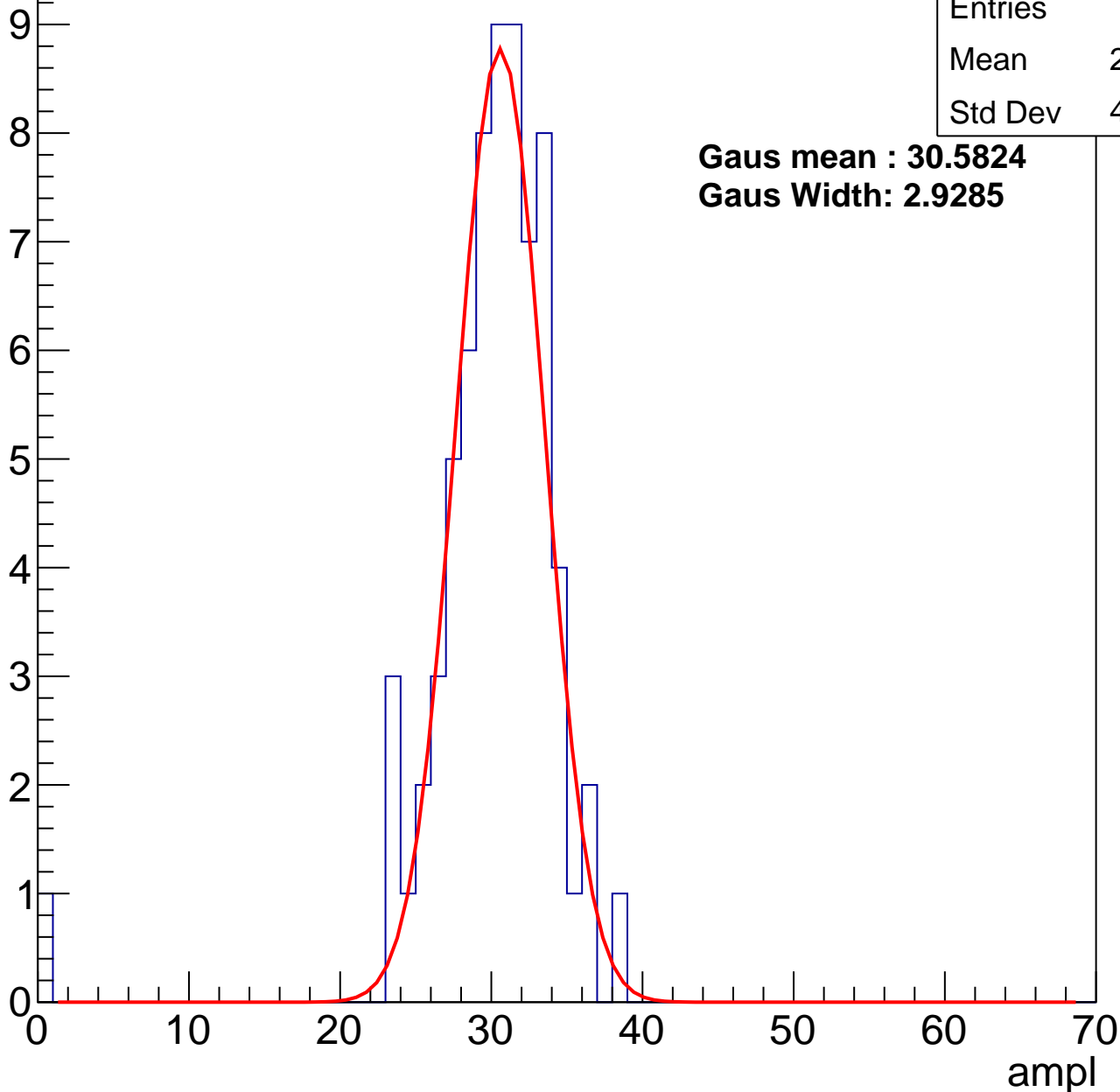
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.63
Std Dev	4.754

Gaus mean : 30.5824

Gaus Width: 2.9285



B1L101S, U17-ch65, adc1

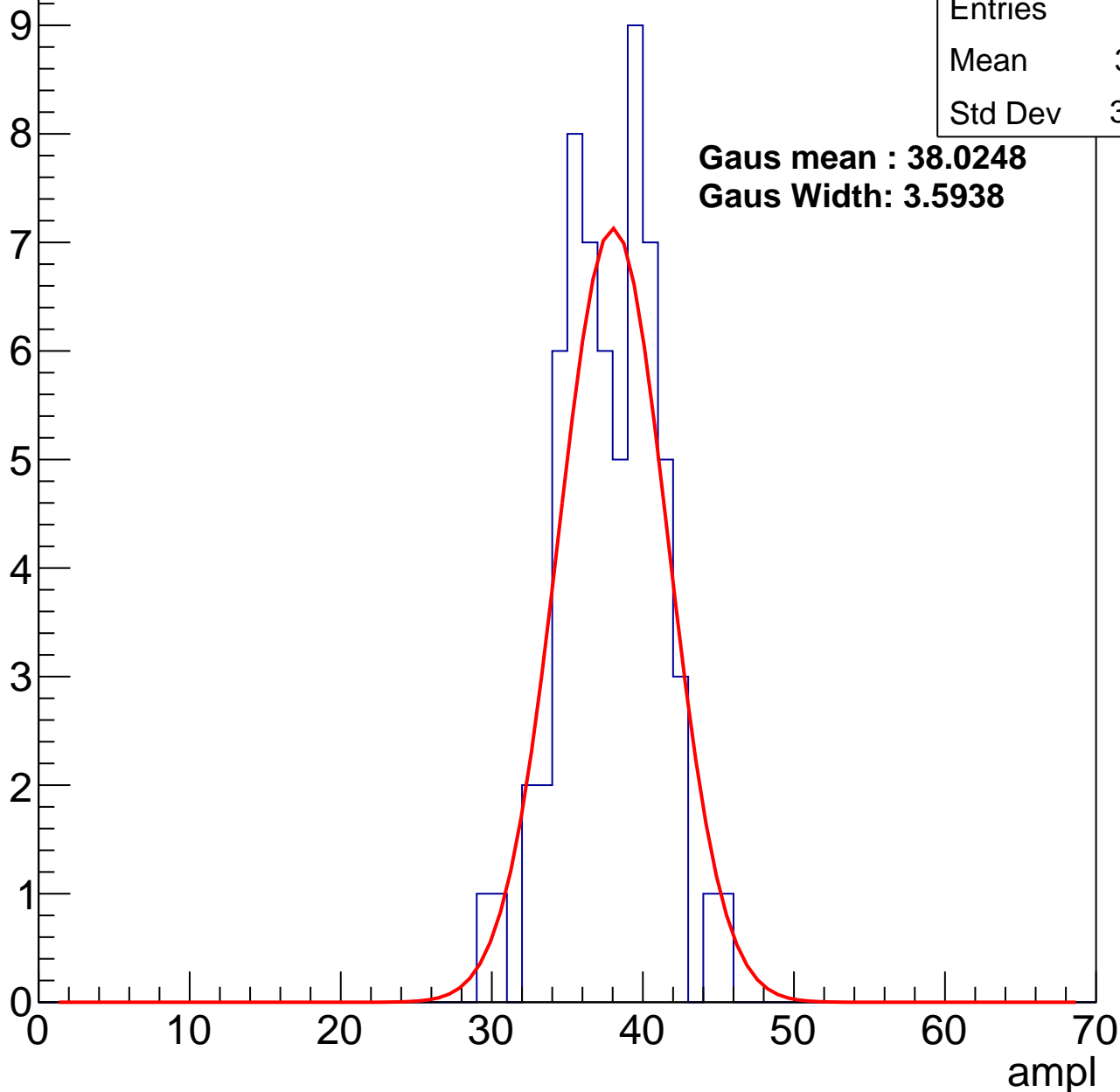
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.31
Std Dev	3.196

Gaus mean : 38.0248

Gaus Width: 3.5938



B1L101S, U17-ch65, adc2

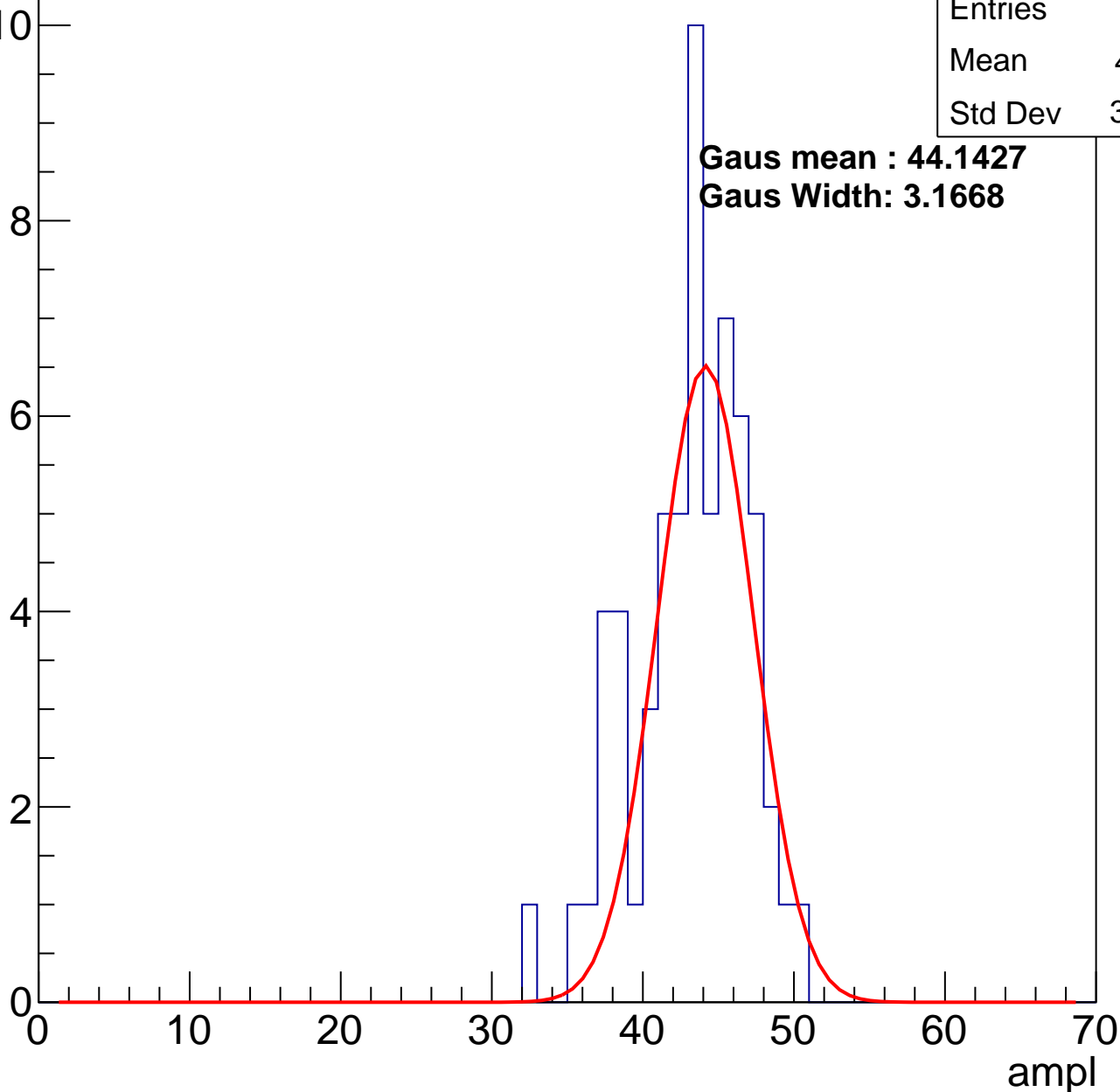
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.71
Std Dev	3.687

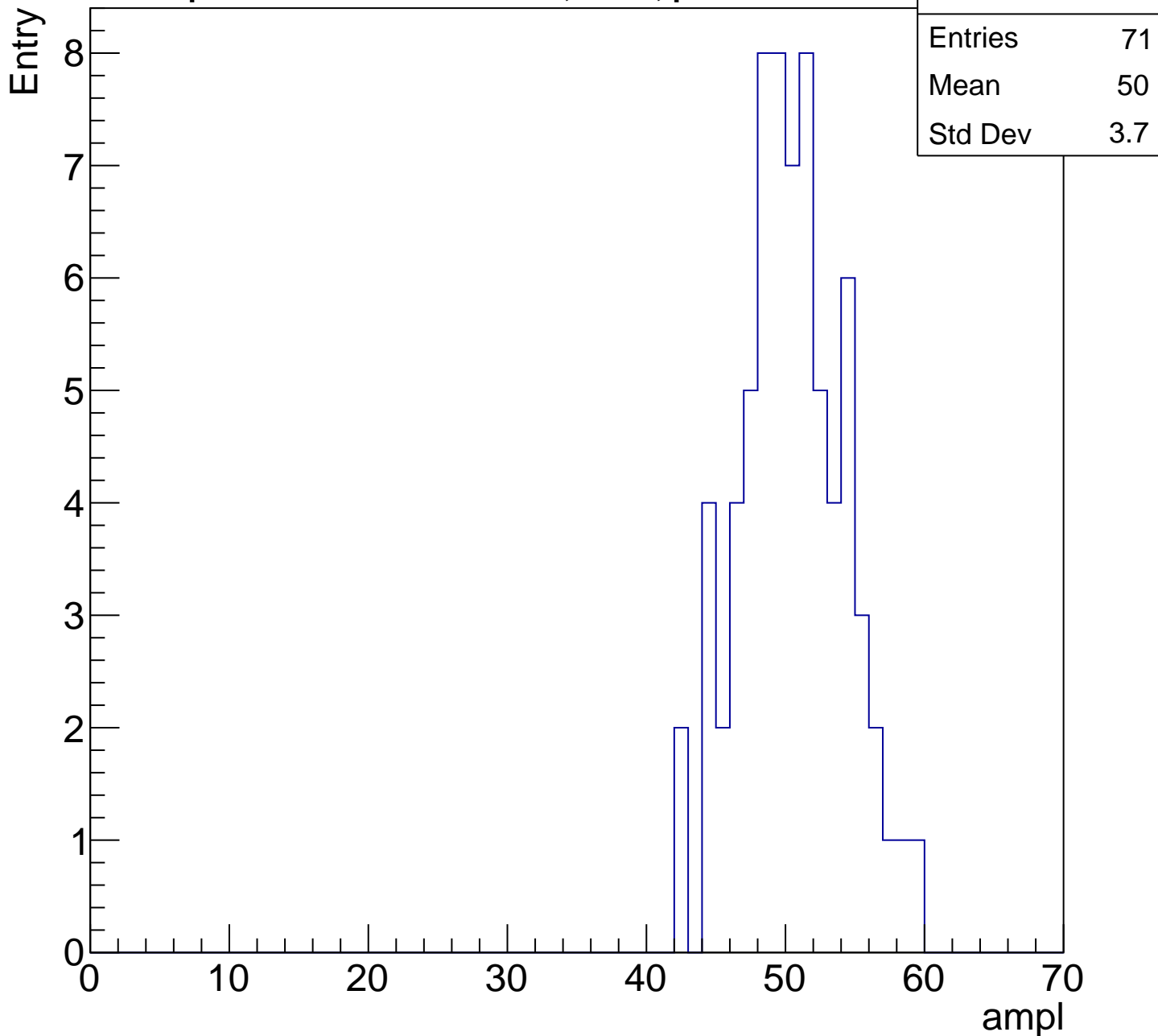
Gaus mean : 44.1427

Gaus Width: 3.1668



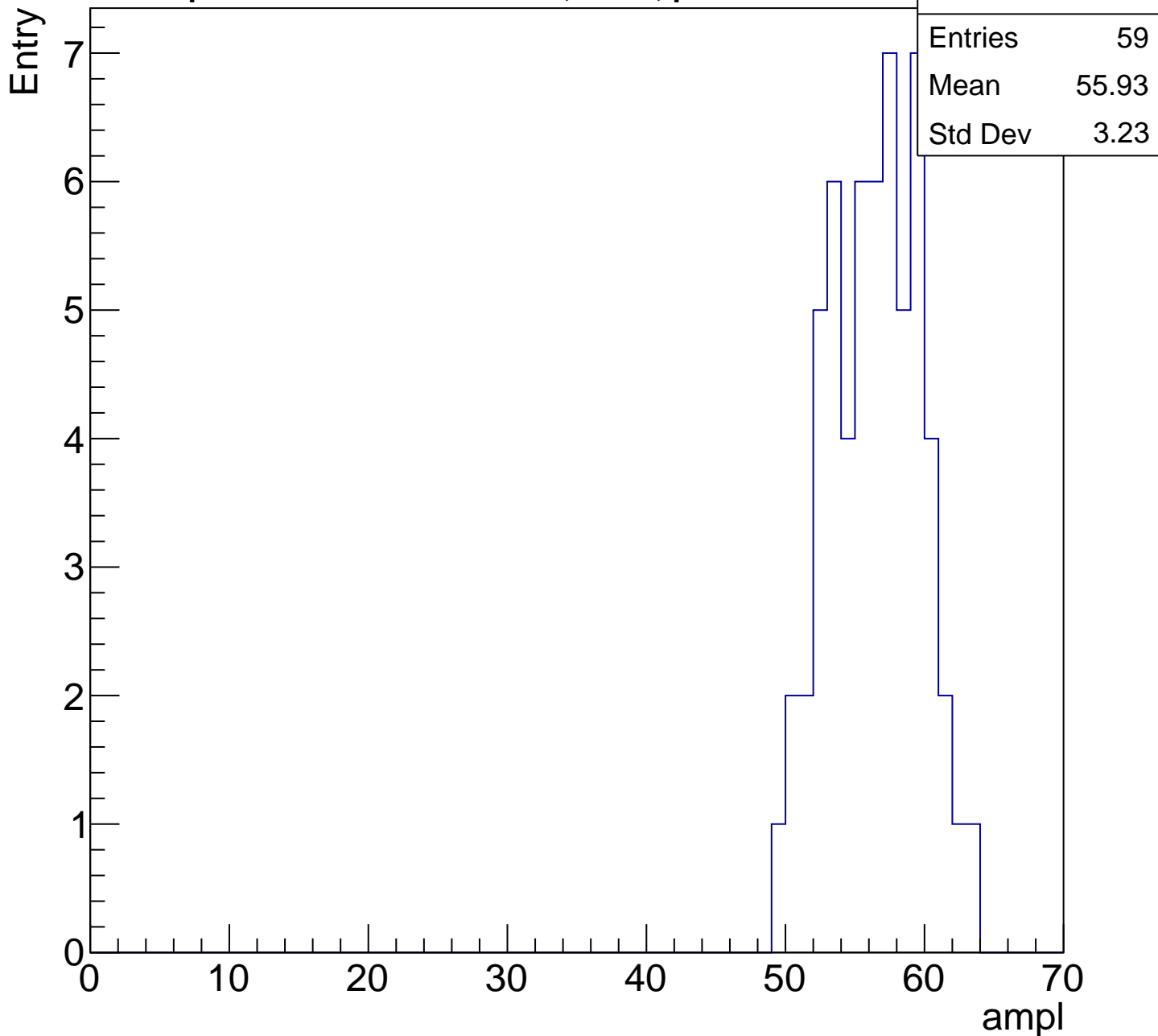
B1L101S, U17-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	37
Mean	60.27
Std Dev	2.201

ampl

0

10

20

30

40

50

60

70

B1L101S, U17-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch66, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	27.35
Std Dev	5.867

Gaus mean : 28.7184

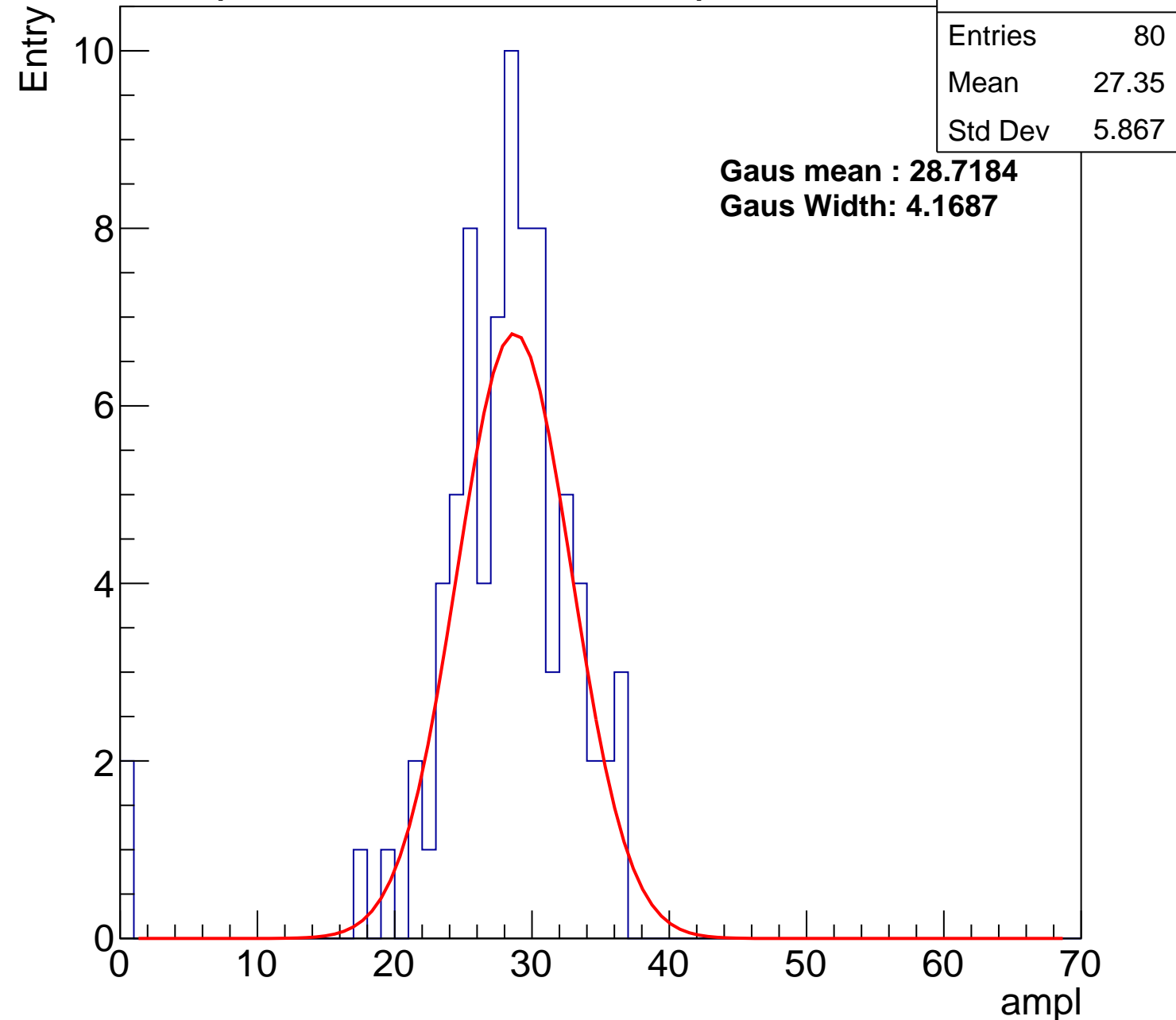
Gaus Width: 4.1687

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



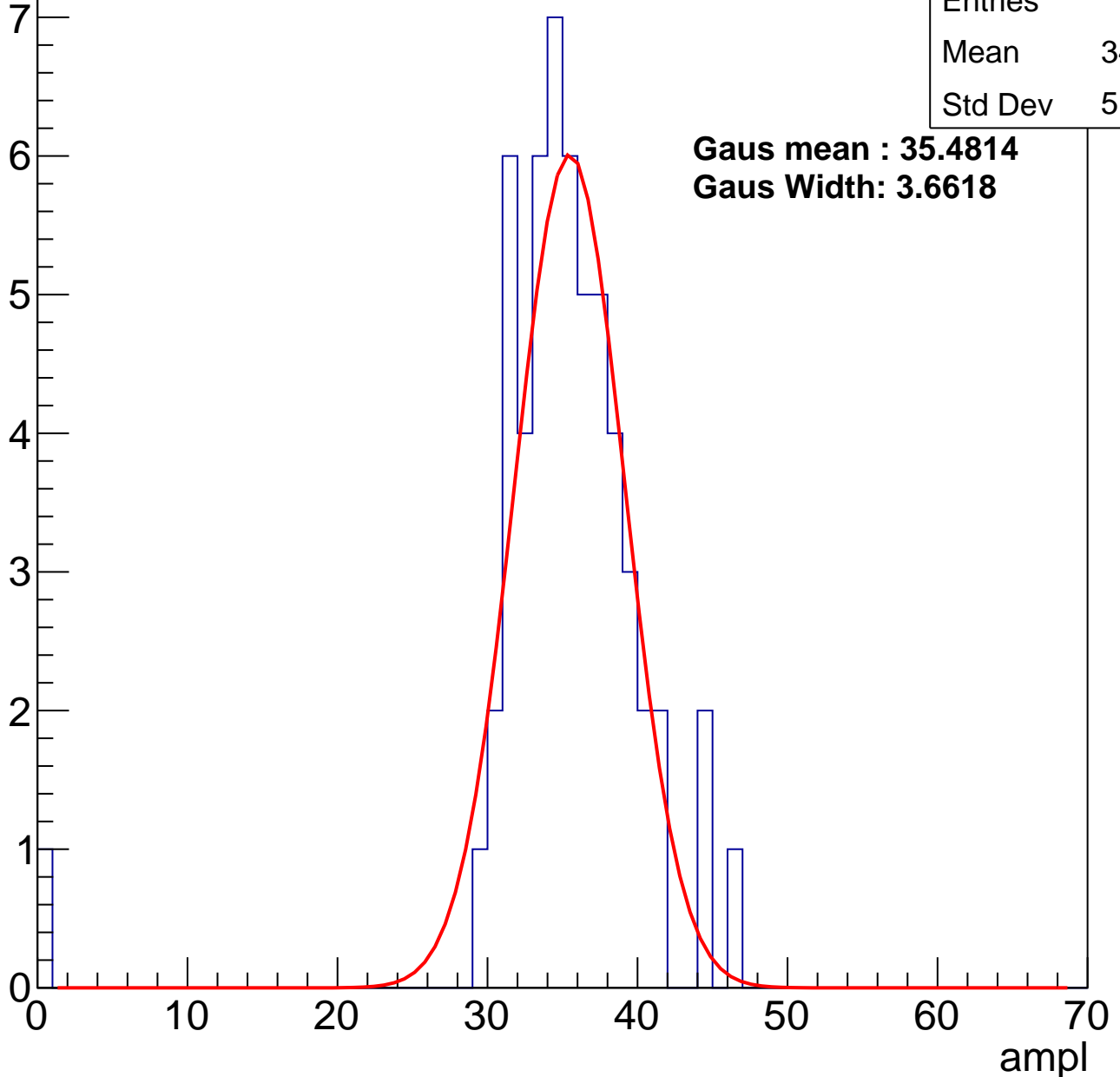
B1L101S, U17-ch66, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	34.72
Std Dev	5.899

Gaus mean : 35.4814
Gaus Width: 3.6618



B1L101S, U17-ch66, adc2

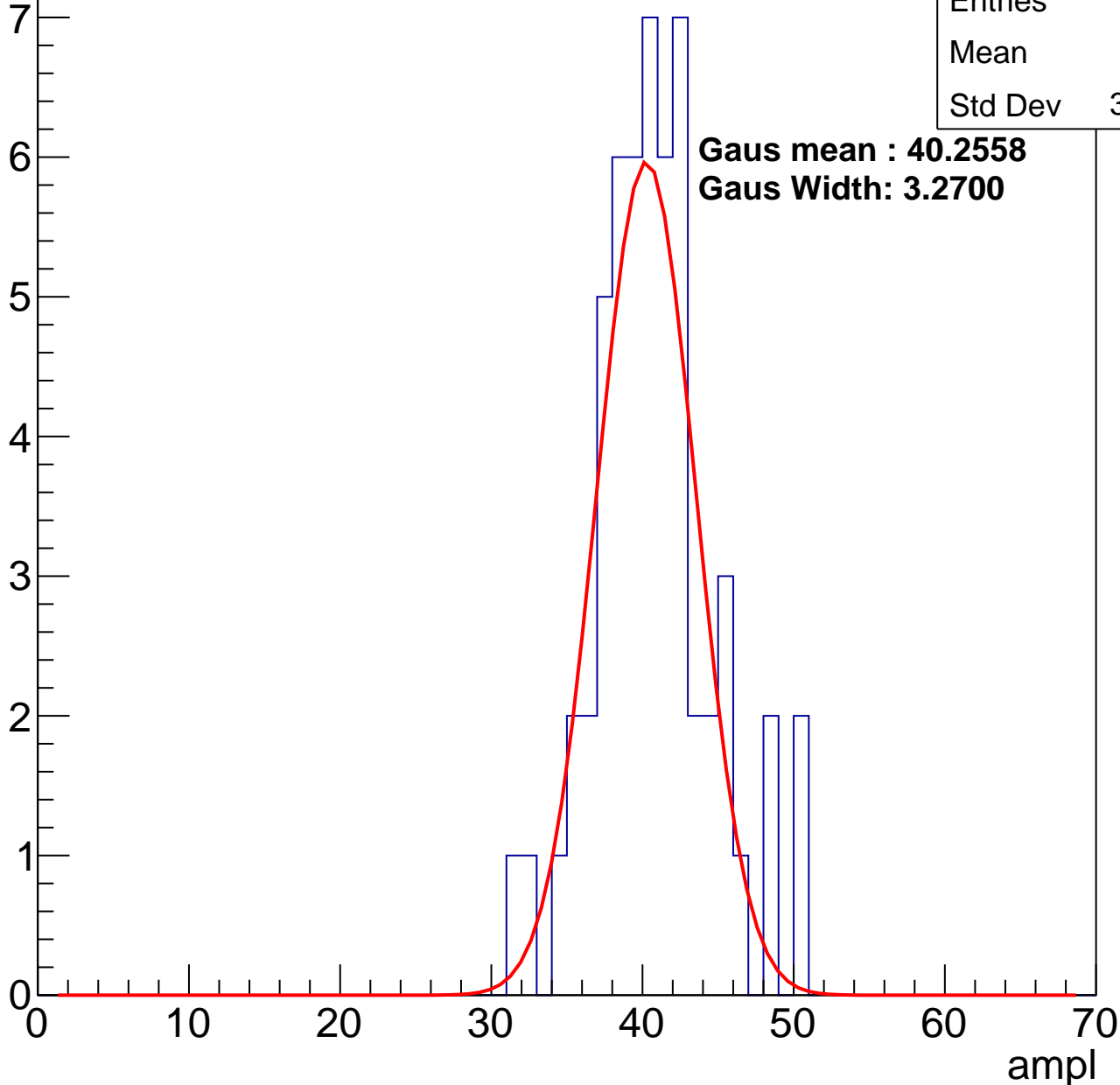
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	40.3
Std Dev	3.909

Gaus mean : 40.2558

Gaus Width: 3.2700

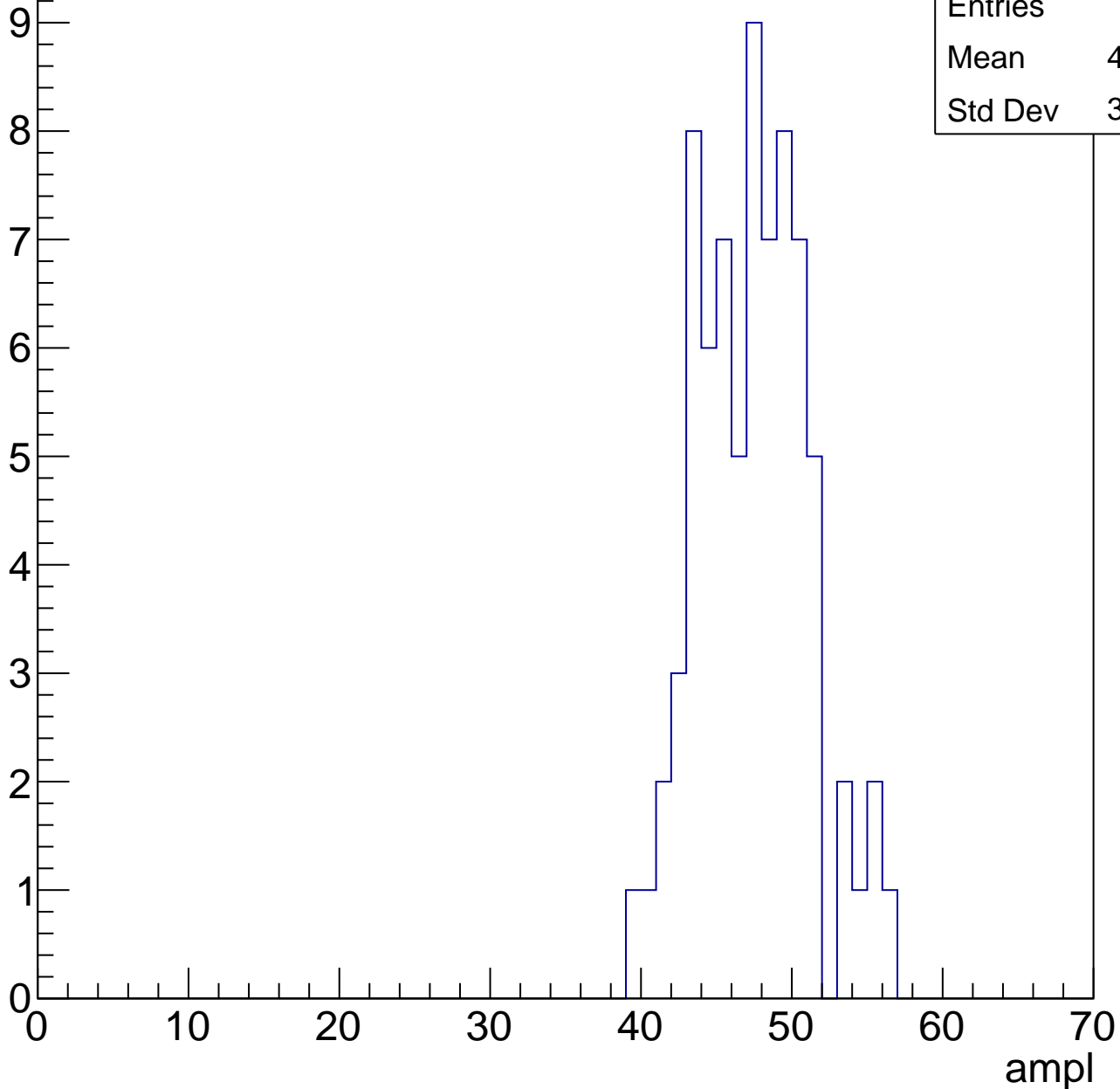


B1L101S, U17-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	46.96
Std Dev	3.627



B1L101S, U17-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	53.83
Std Dev	3.48

Entry

10

8

6

4

2

0

0

10

20

30

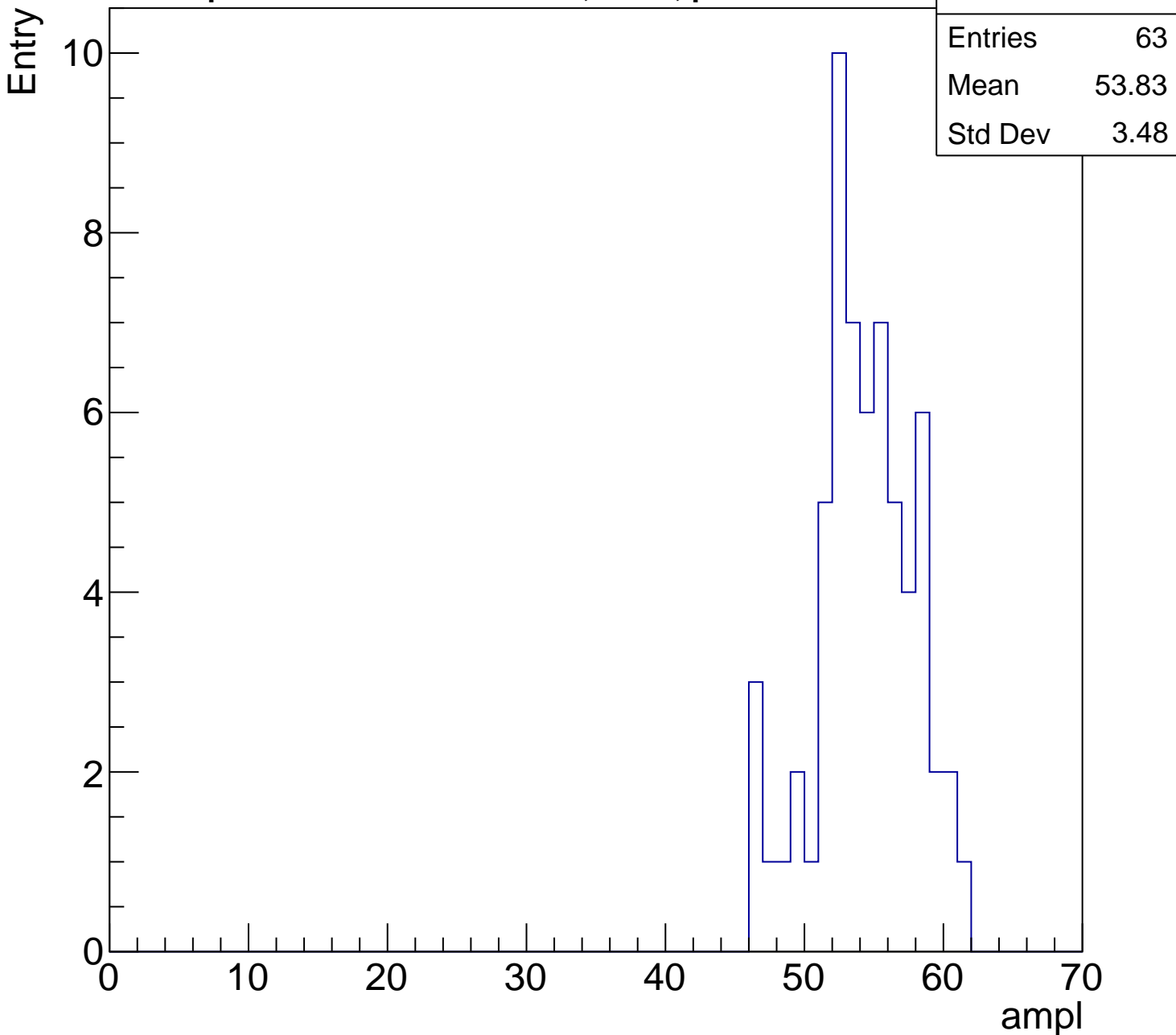
40

50

60

70

ampl

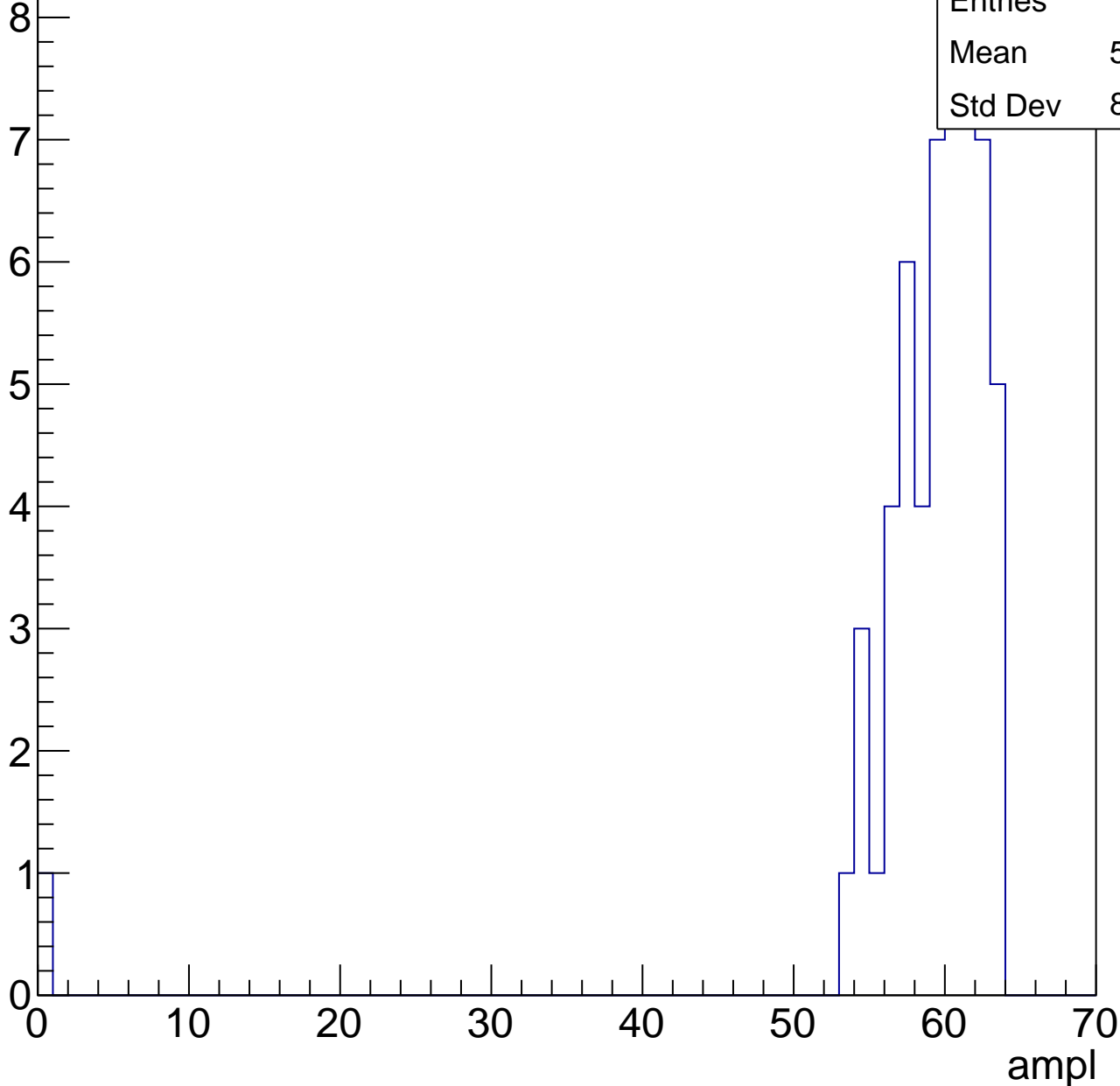


B1L101S, U17-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

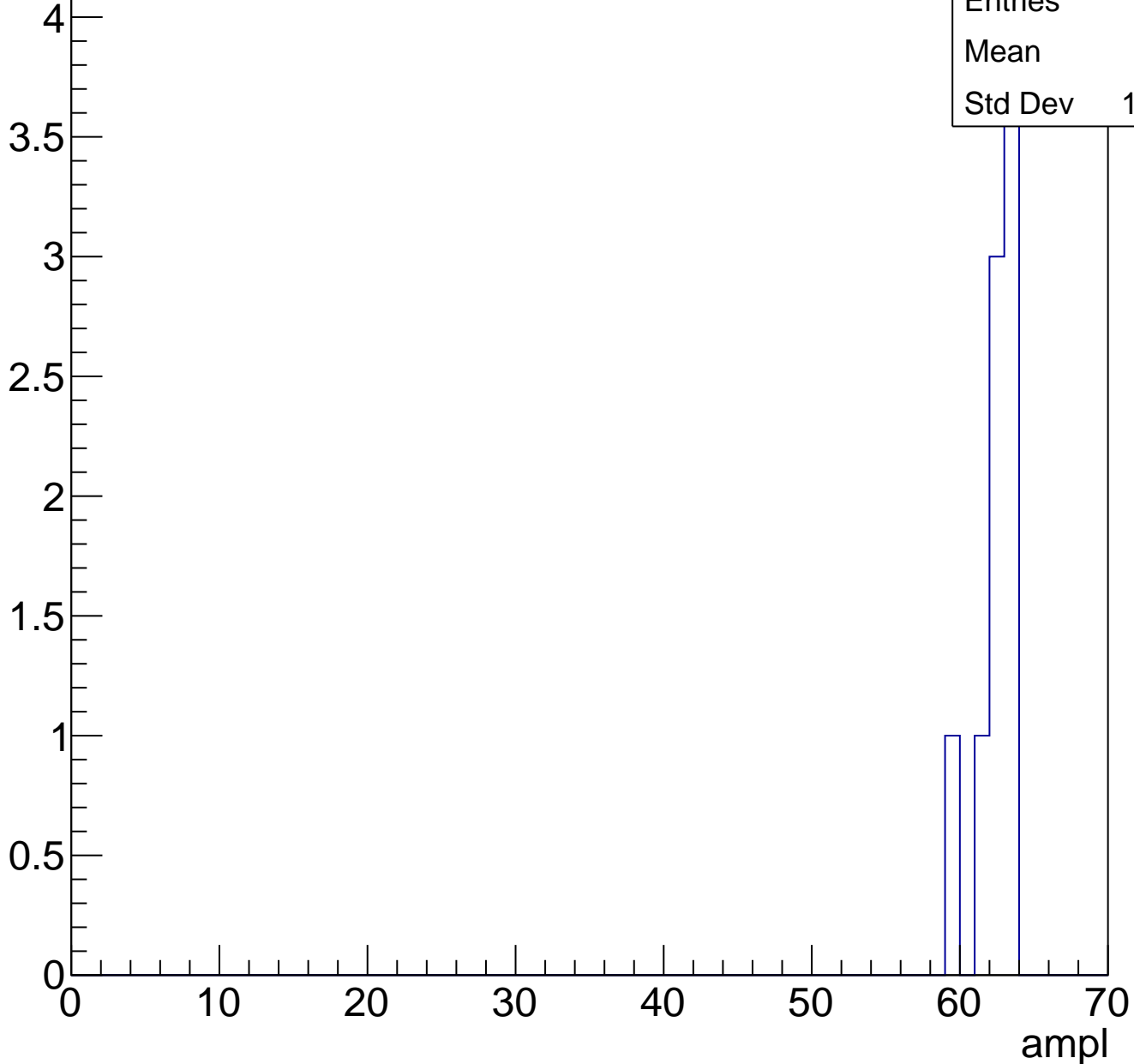
Entries	55
Mean	58.15
Std Dev	8.328



B1L101S, U17-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	9
Mean	62
Std Dev	1.247

B1L101S, U17-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U17-ch67, adc0

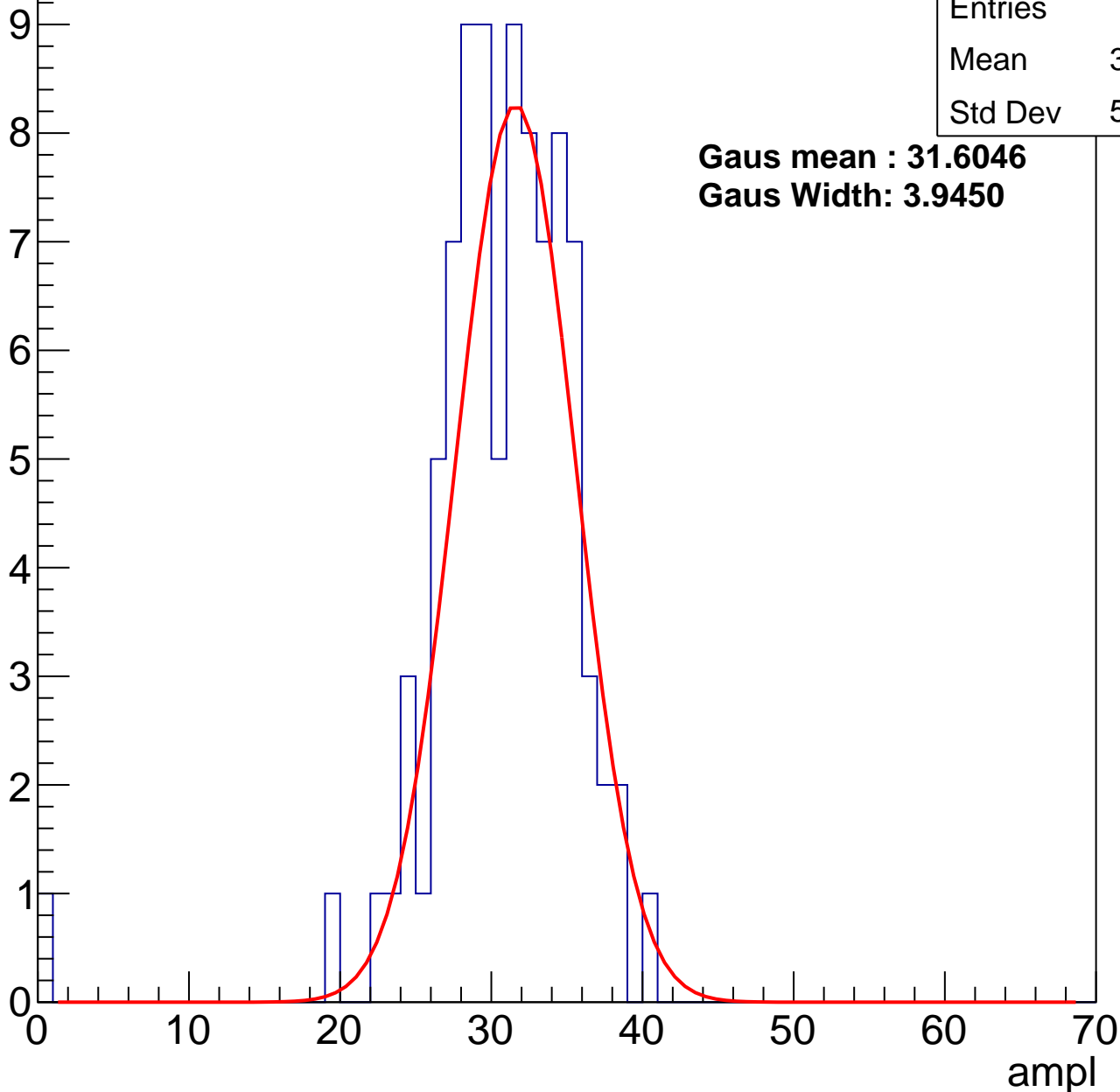
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	30.27
Std Dev	5.044

Gaus mean : 31.6046

Gaus Width: 3.9450



B1L101S, U17-ch67, adc1

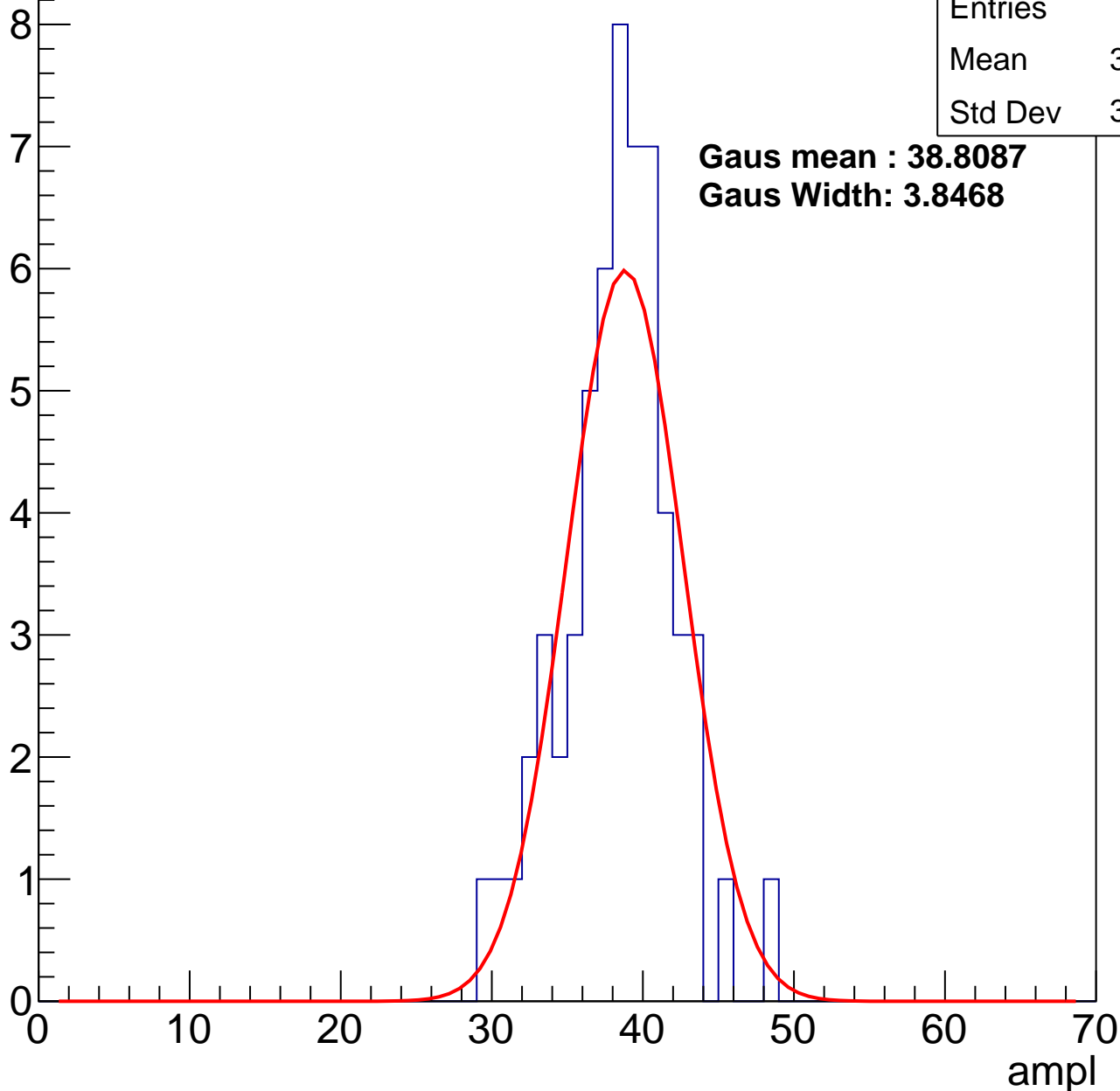
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.88
Std Dev	3.639

Gaus mean : 38.8087

Gaus Width: 3.8468



B1L101S, U17-ch67, adc2

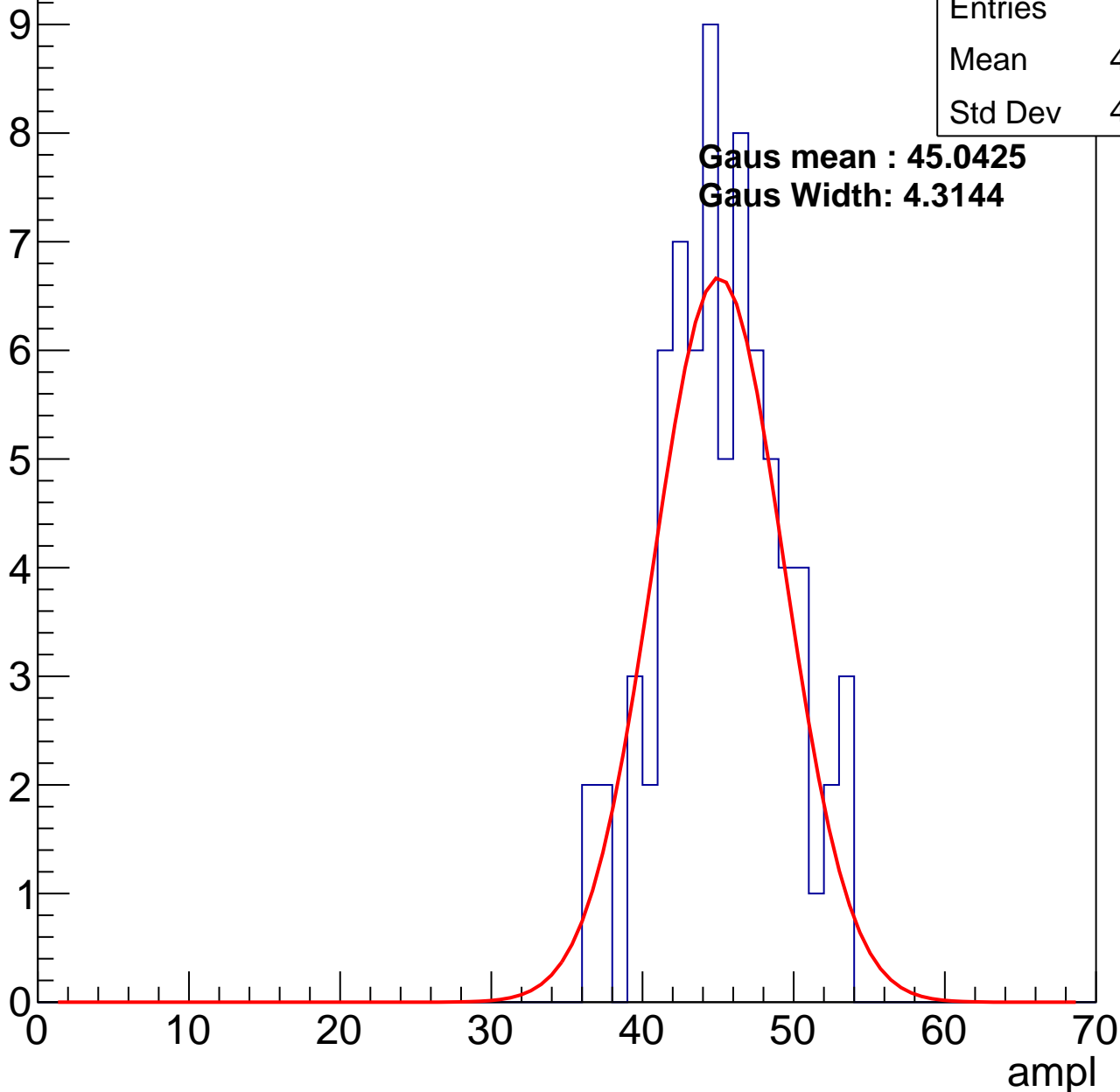
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	44.83
Std Dev	4.008

Gaus mean : 45.0425

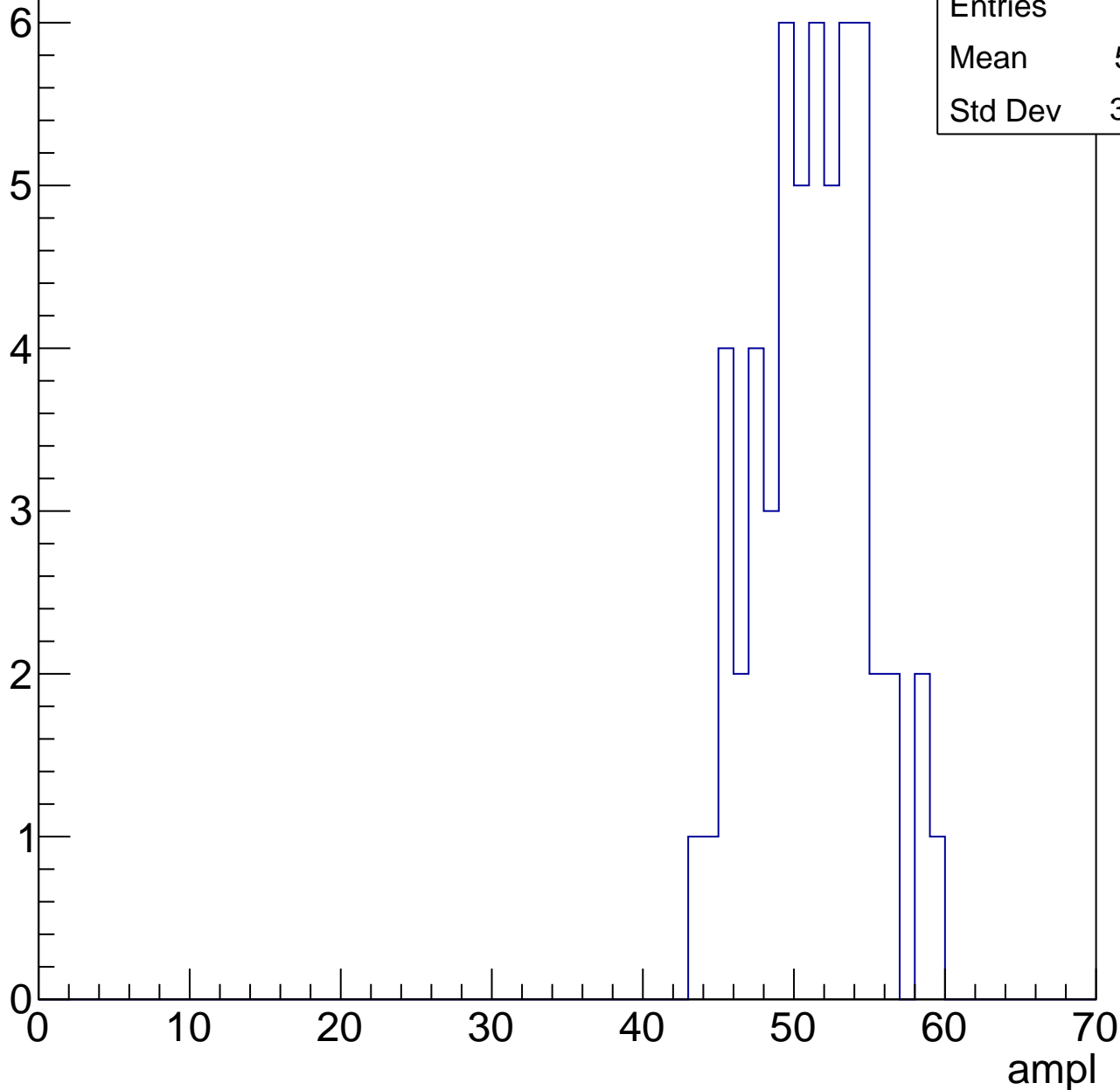
Gaus Width: 4.3144



B1L101S, U17-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



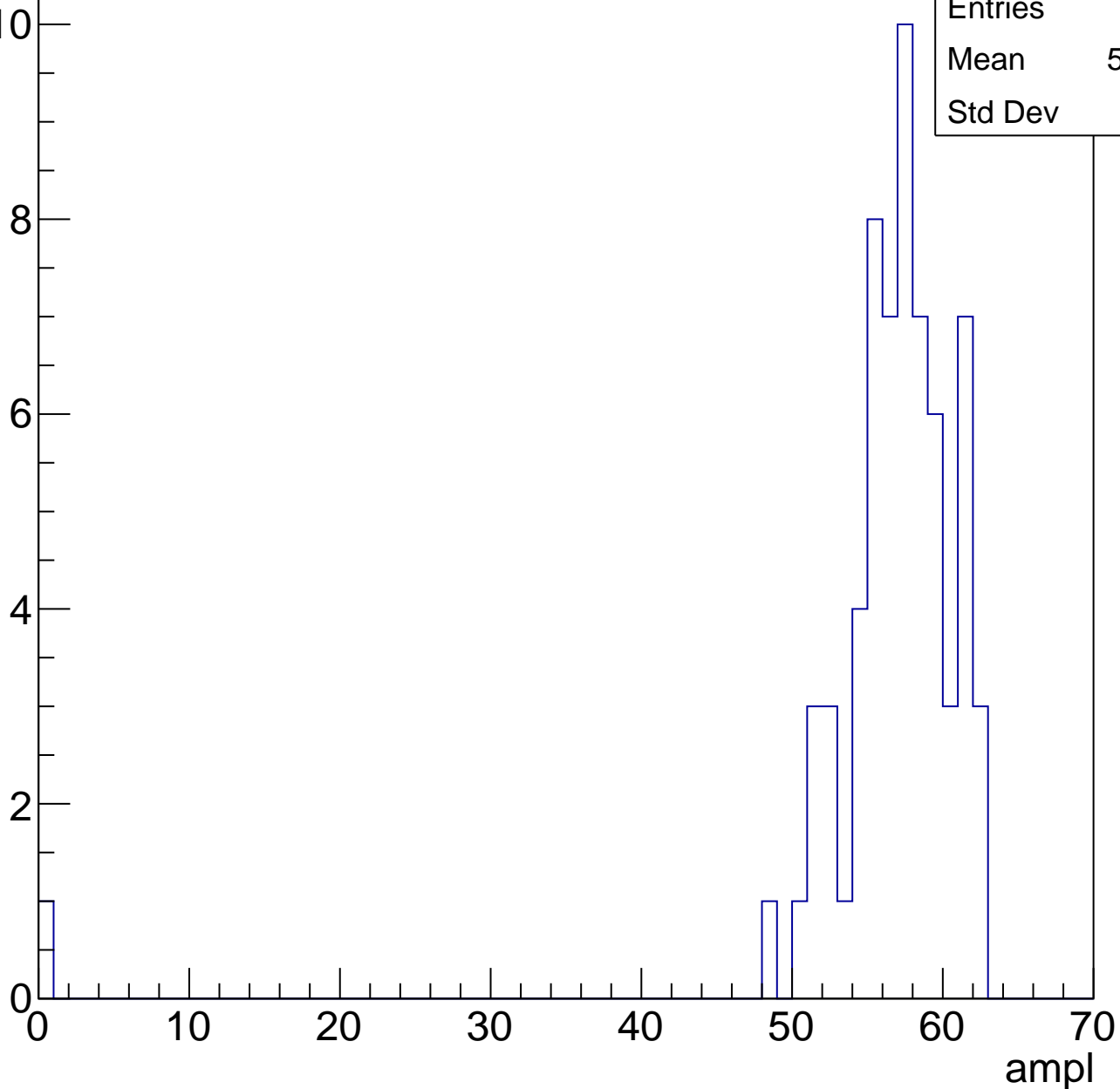
Entries	56
Mean	50.71
Std Dev	3.658

B1L101S, U17-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.86
Std Dev	7.66

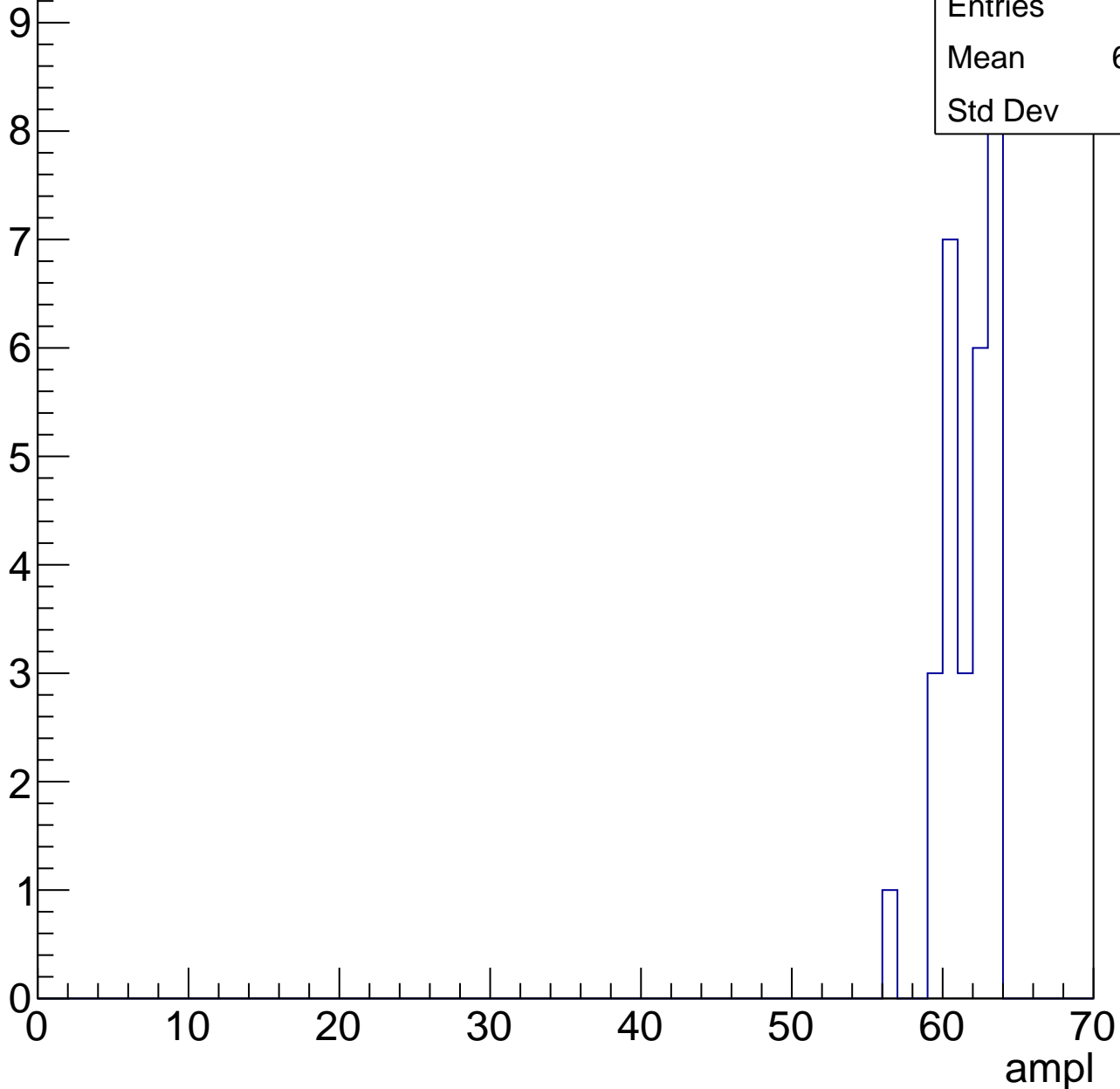


B1L101S, U17-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

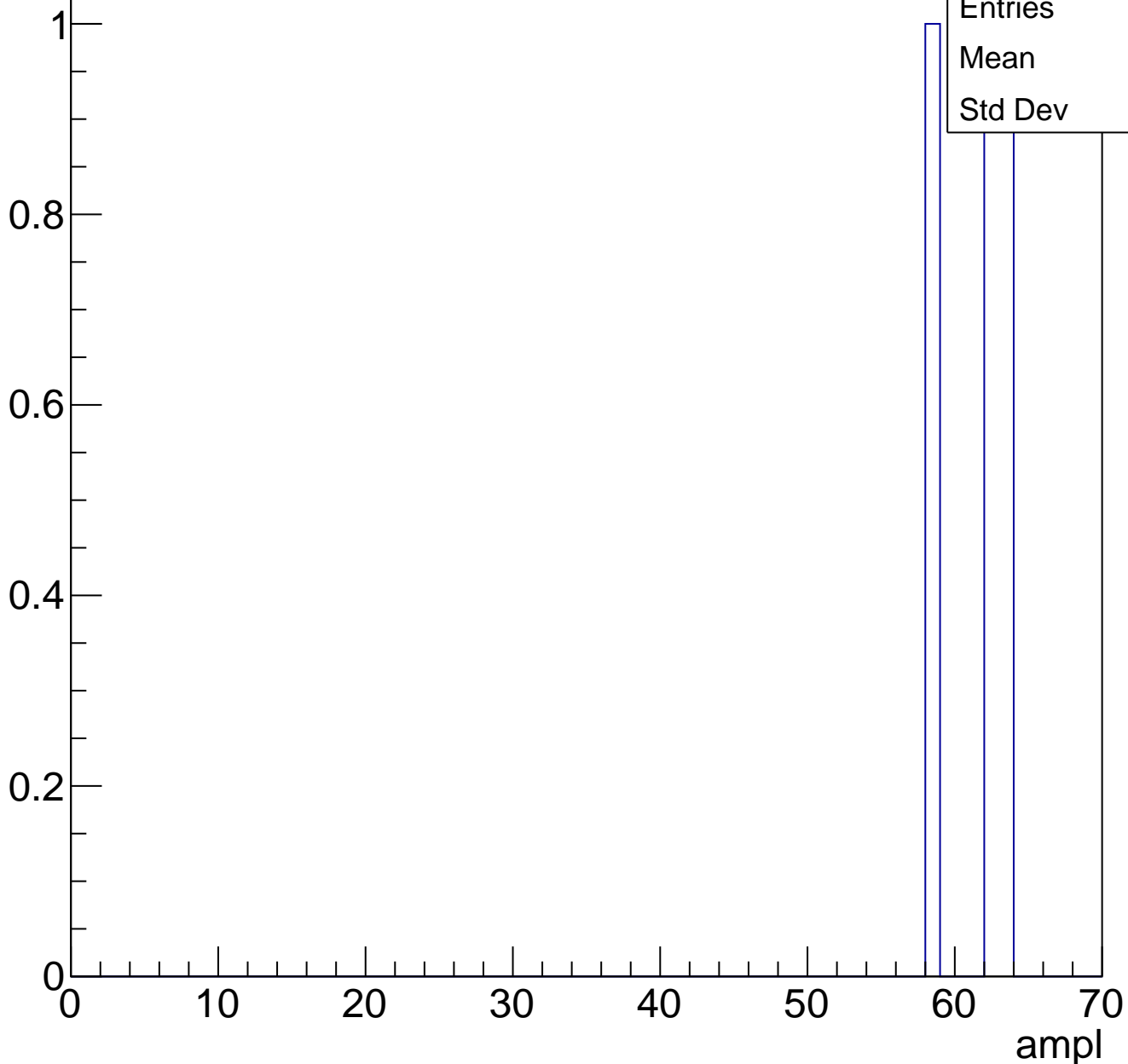
Entries	29
Mean	61.21
Std Dev	1.71



B1L101S, U17-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	61
Std Dev	2.16

B1L101S, U17-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch68, adc0

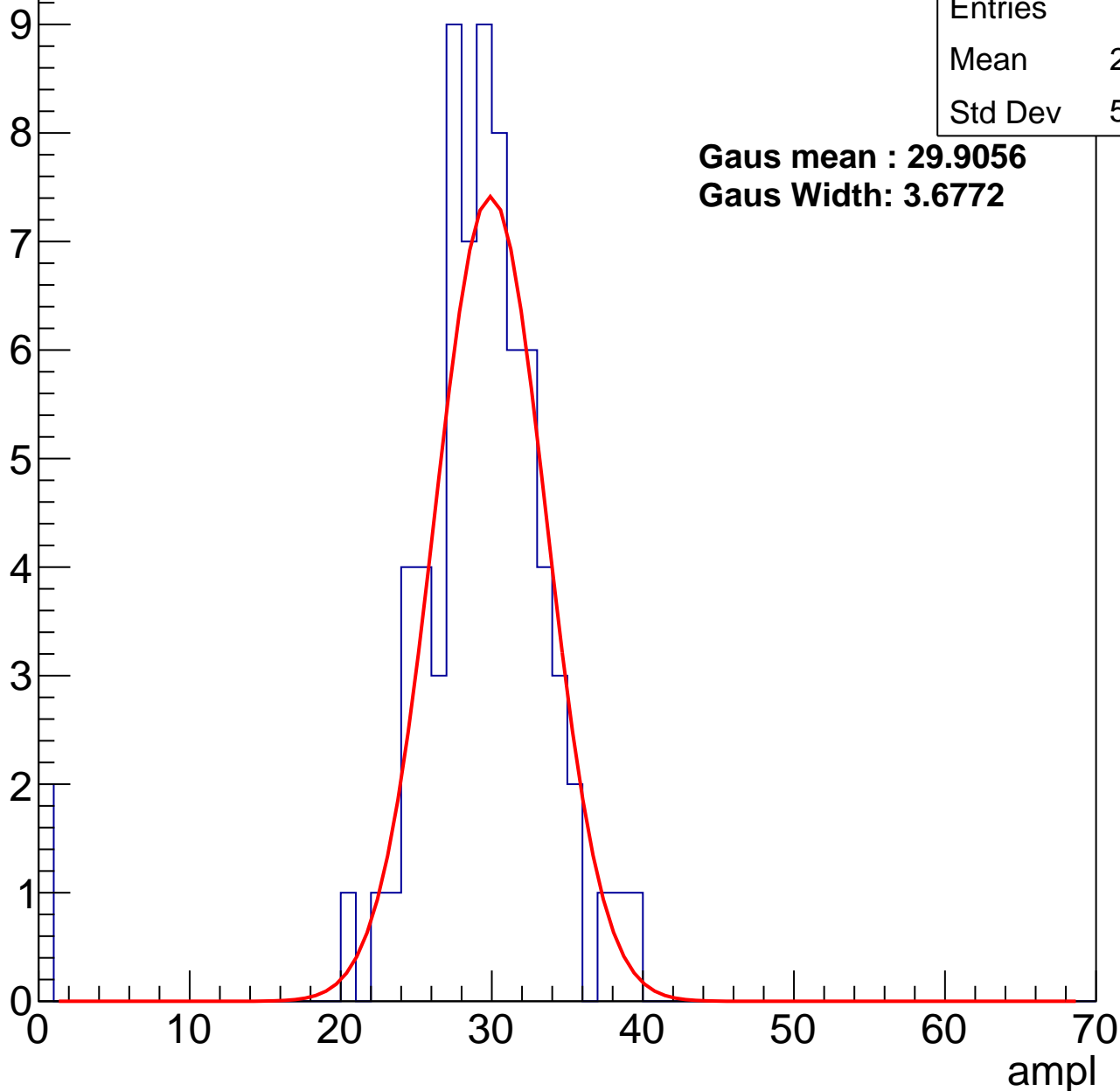
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.42
Std Dev	5.968

Gaus mean : 29.9056

Gaus Width: 3.6772



B1L101S, U17-ch68, adc1

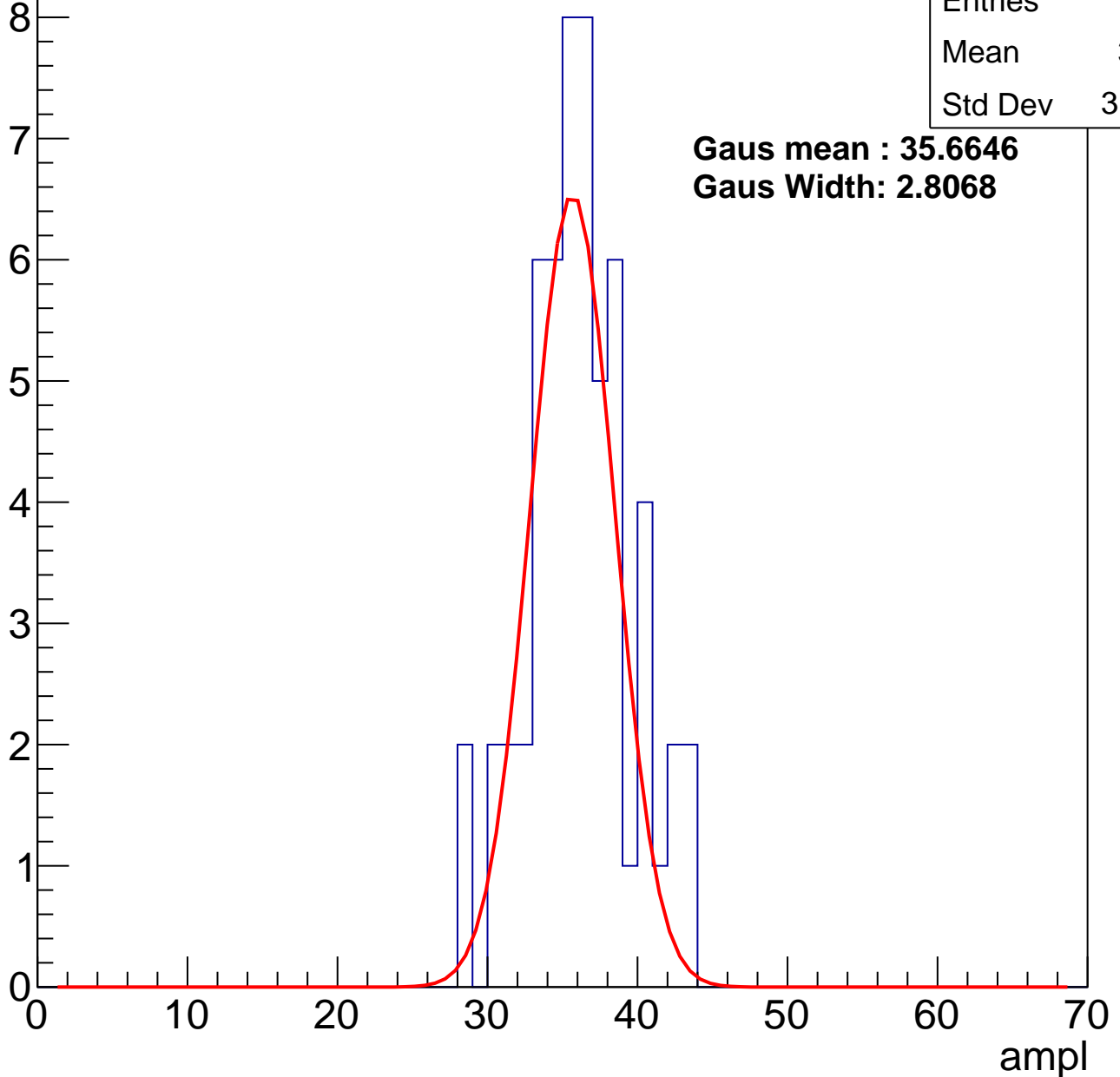
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.7
Std Dev	3.403

Gaus mean : 35.6646

Gaus Width: 2.8068



B1L101S, U17-ch68, adc2

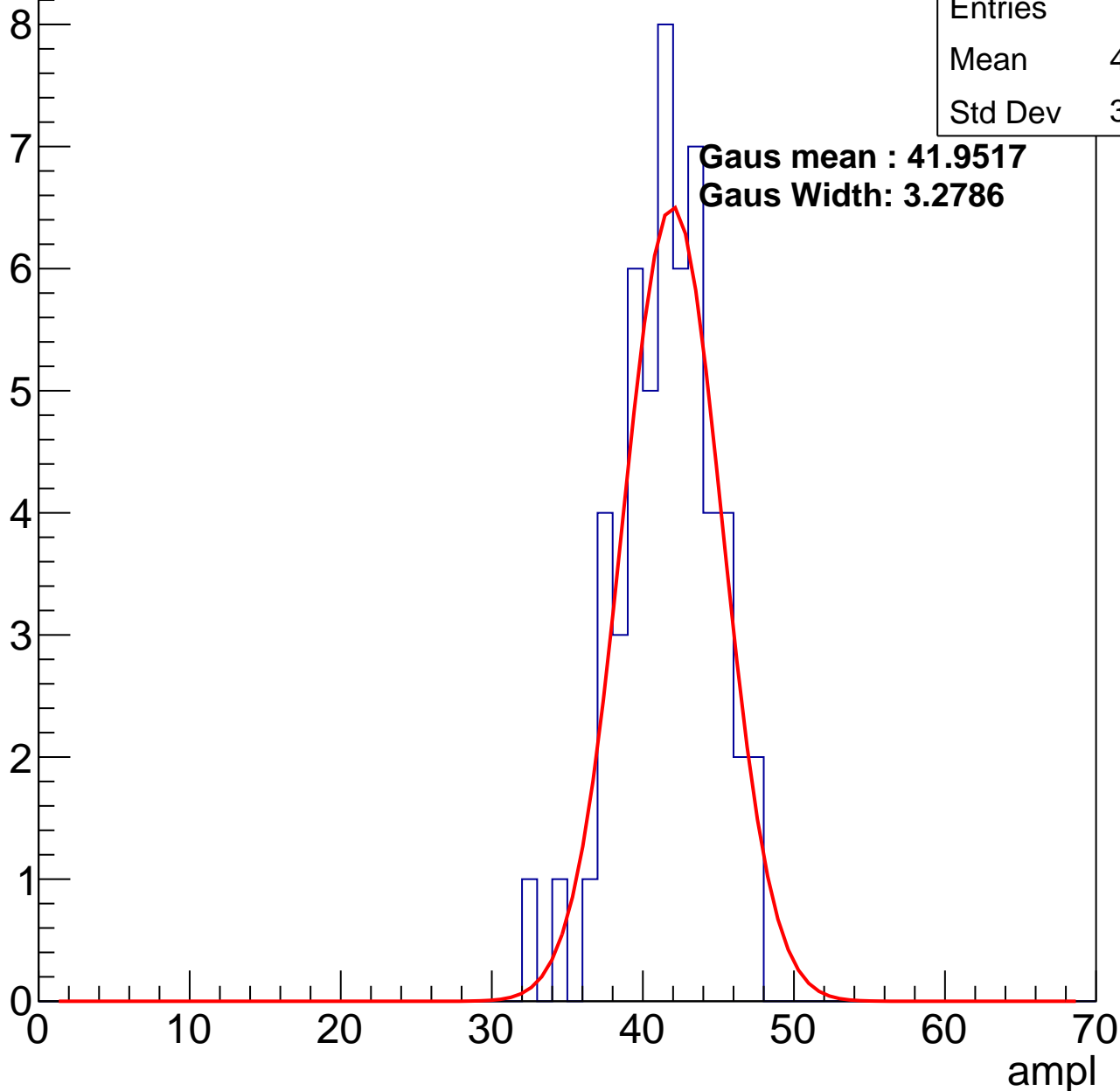
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.13
Std Dev	3.133

Gaus mean : 41.9517

Gaus Width: 3.2786



B1L101S, U17-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

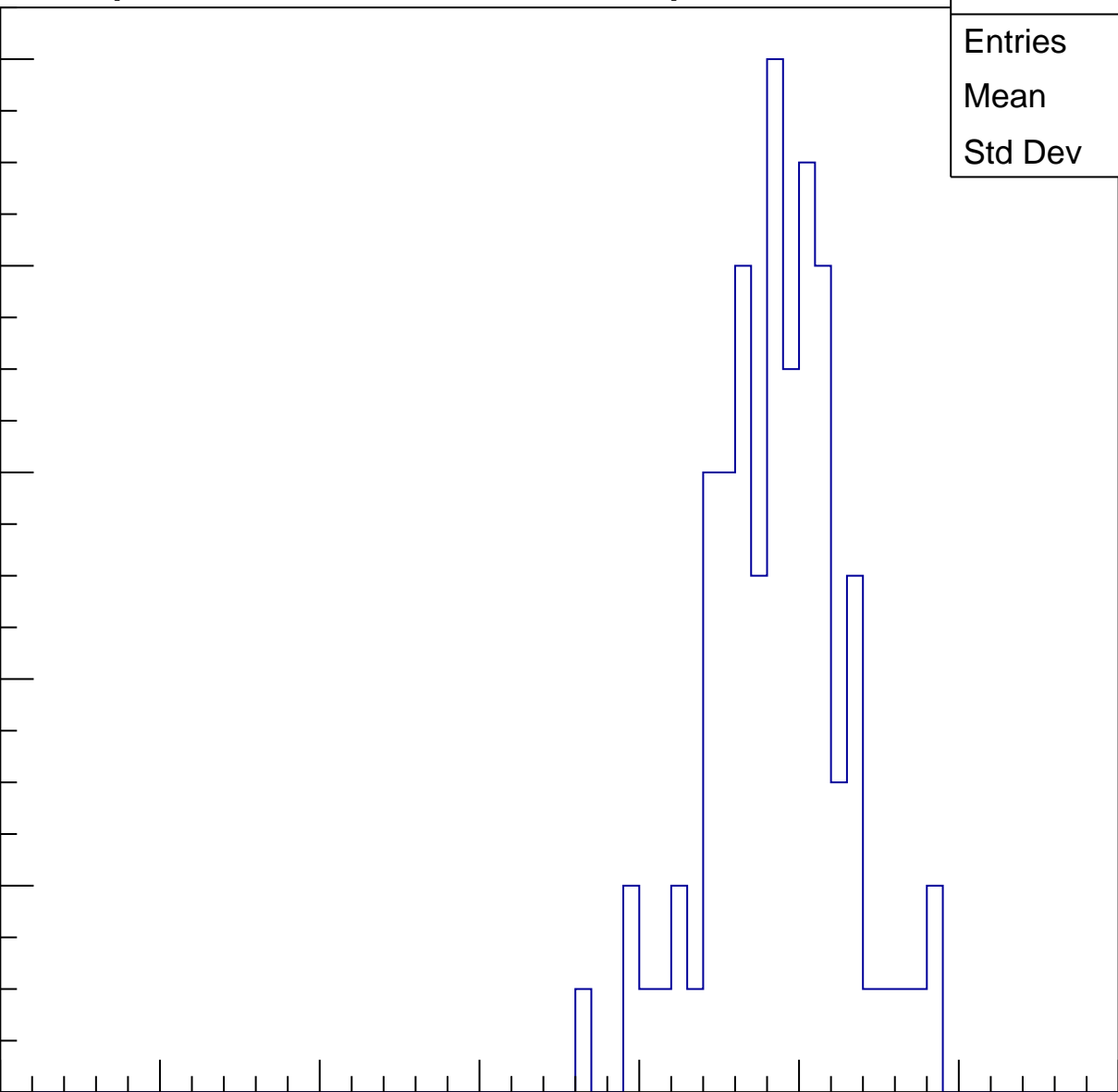
Entries	81
Mean	48.14
Std Dev	4.186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

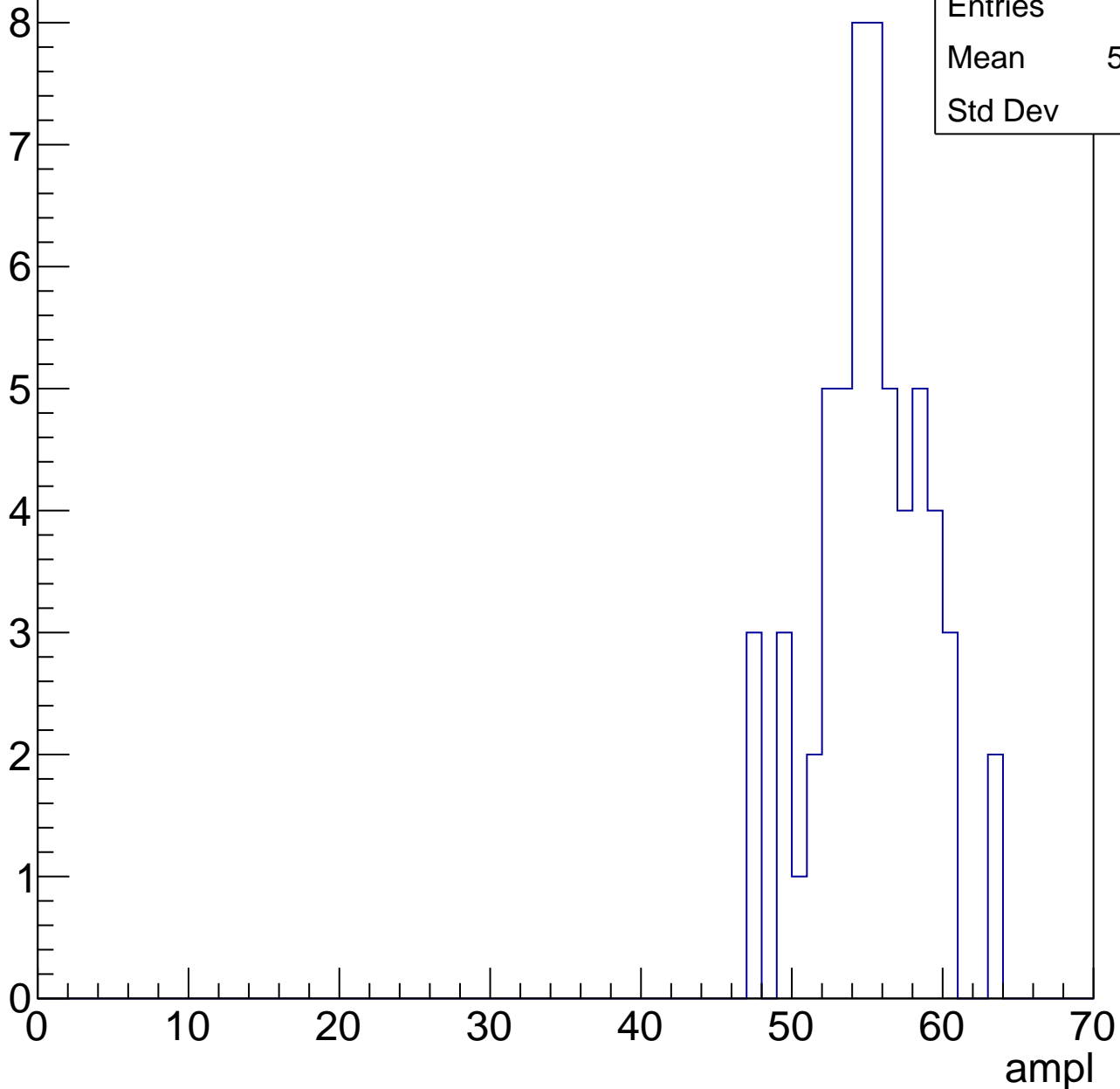


B1L101S, U17-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	54.78
Std Dev	3.62

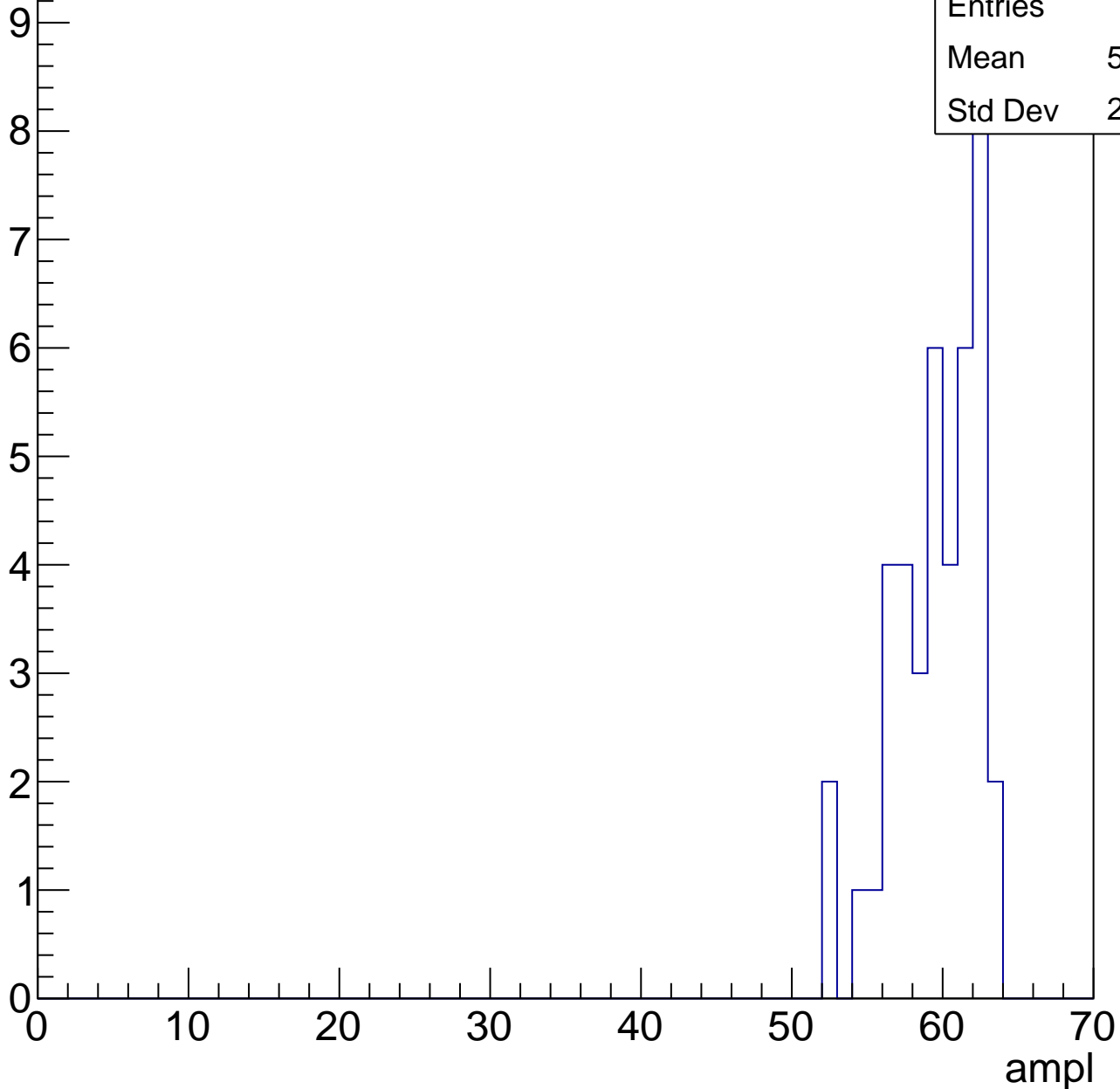


B1L101S, U17-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.12
Std Dev	2.822

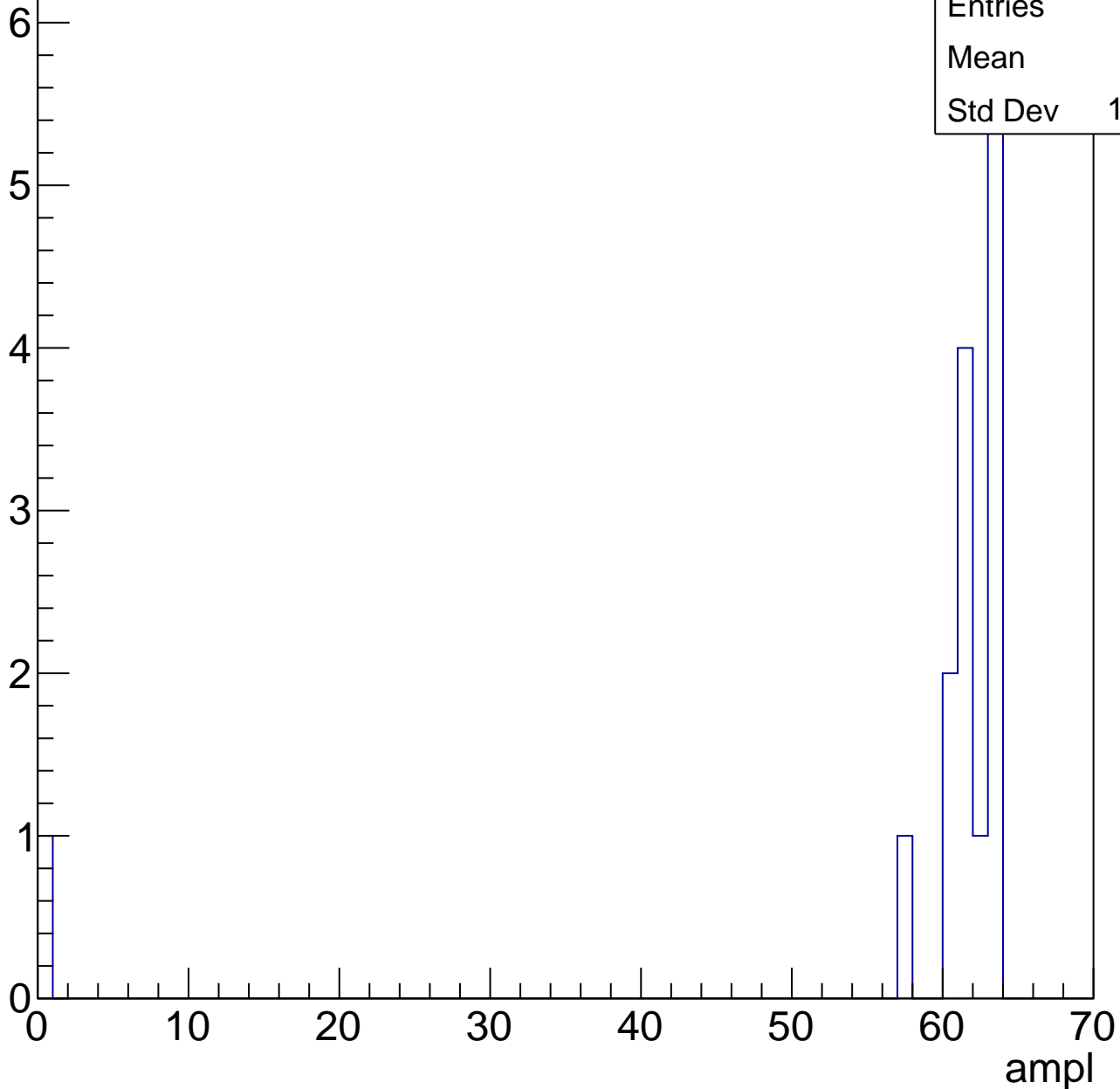


B1L101S, U17-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.4
Std Dev	15.43



B1L101S, U17-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0

B1L101S, U17-ch69, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	29.41
Std Dev	4.82

Gaus mean : 30.3950

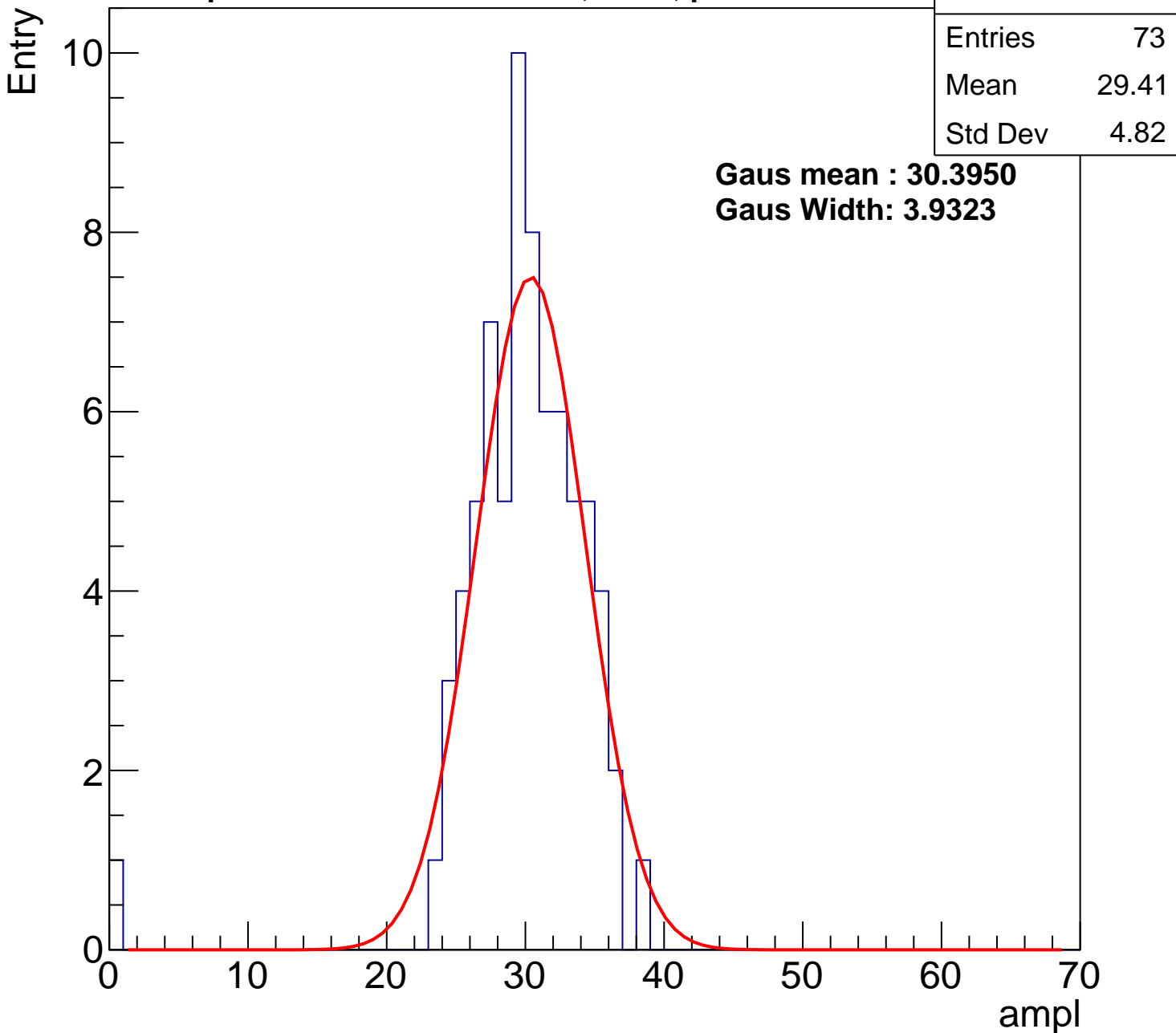
Gaus Width: 3.9323

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

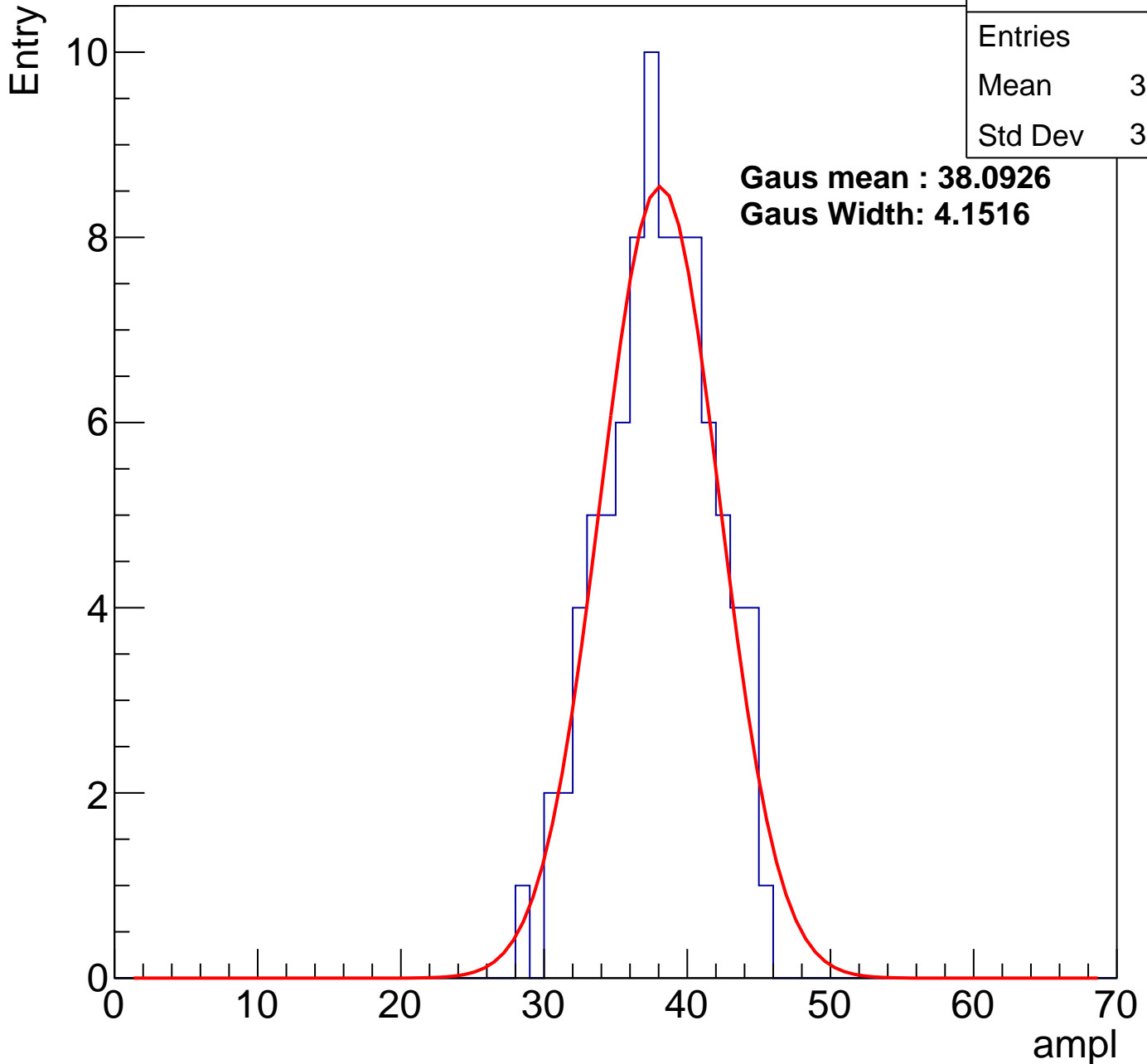


B1L101S, U17-ch69, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	37.54
Std Dev	3.744

Gaus mean : 38.0926
Gaus Width: 4.1516



B1L101S, U17-ch69, adc2

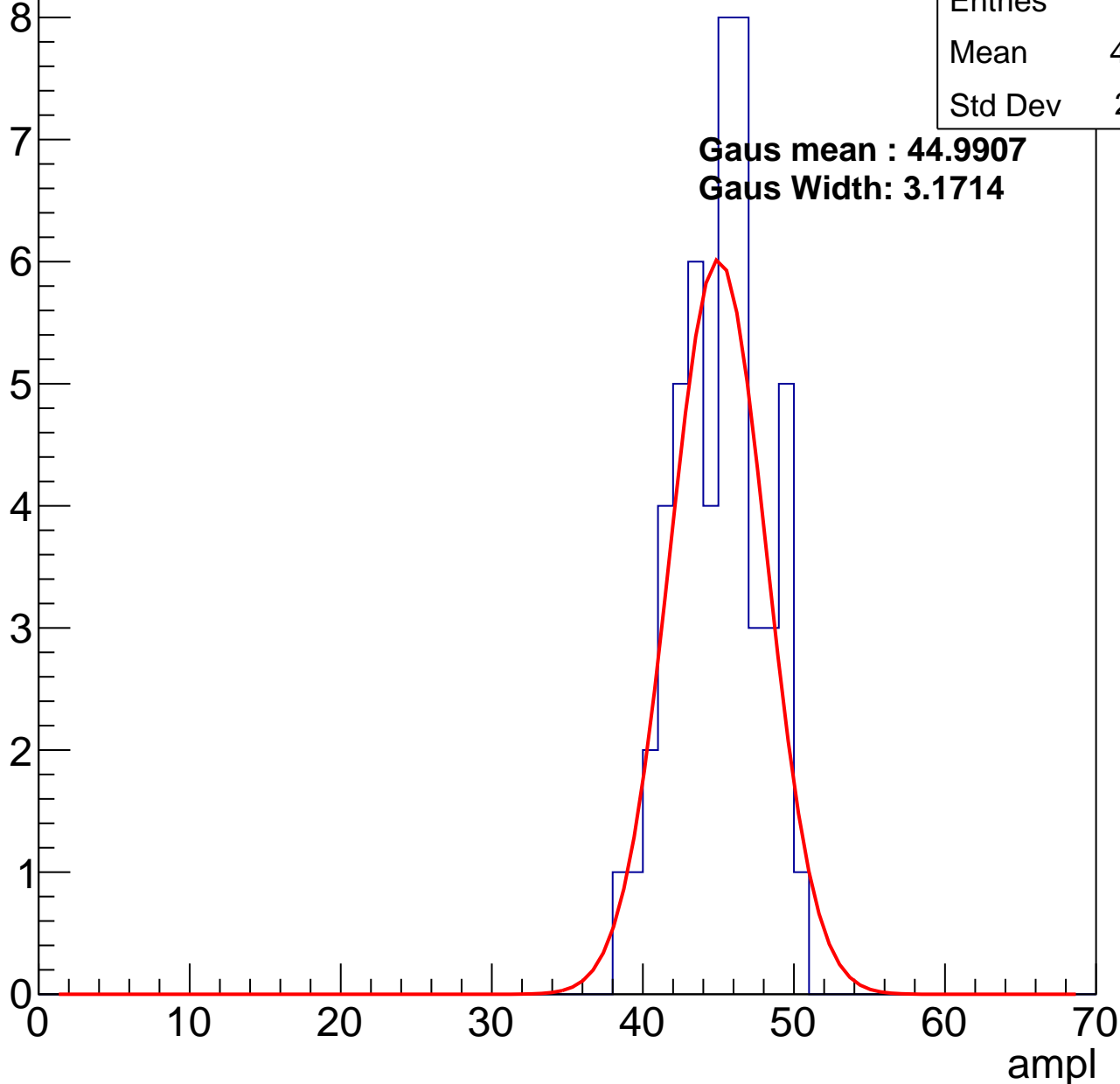
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	44.57
Std Dev	2.851

Gaus mean : 44.9907

Gaus Width: 3.1714

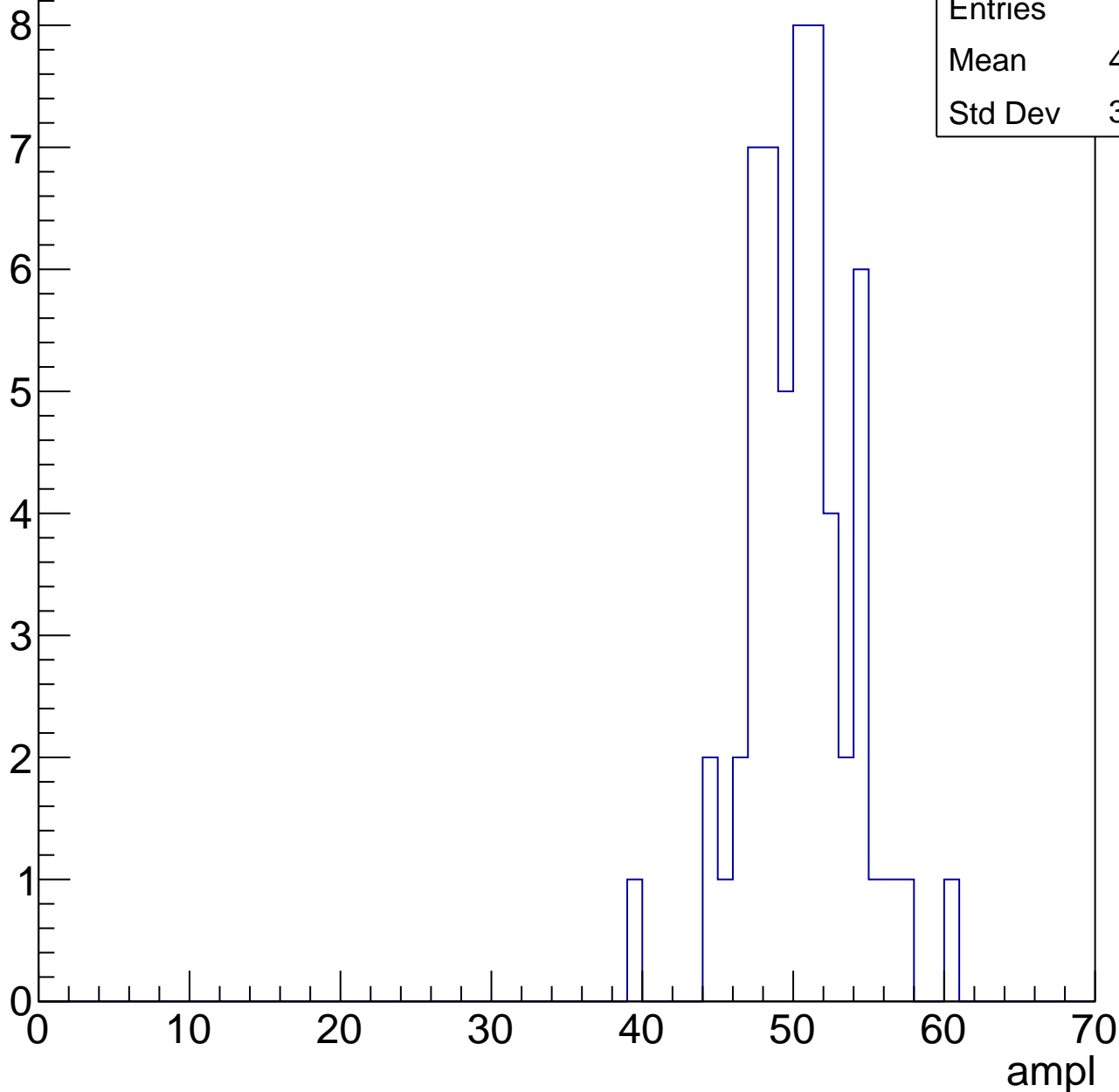


B1L101S, U17-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

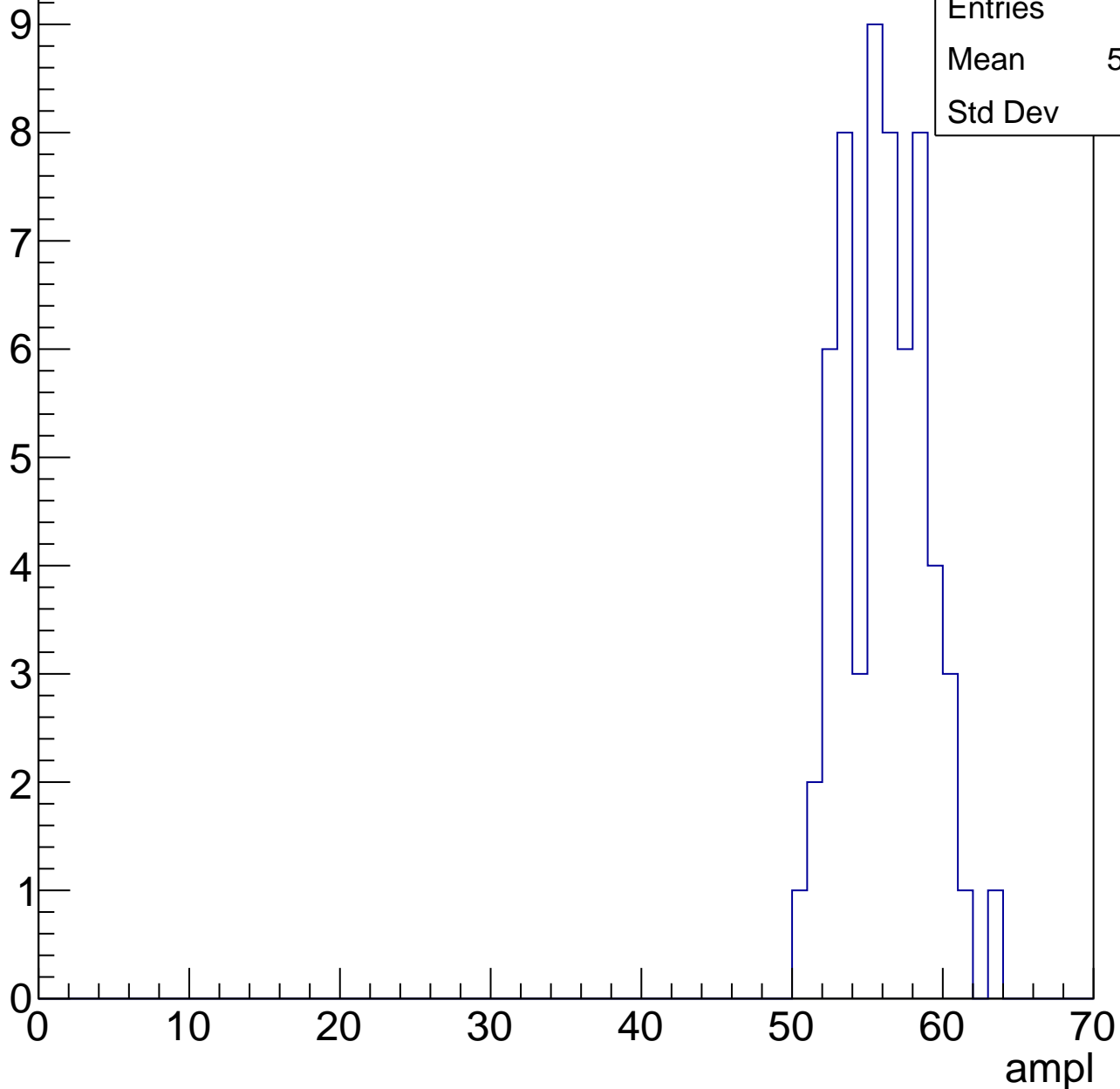
Entries	57
Mean	49.96
Std Dev	3.489



B1L101S, U17-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

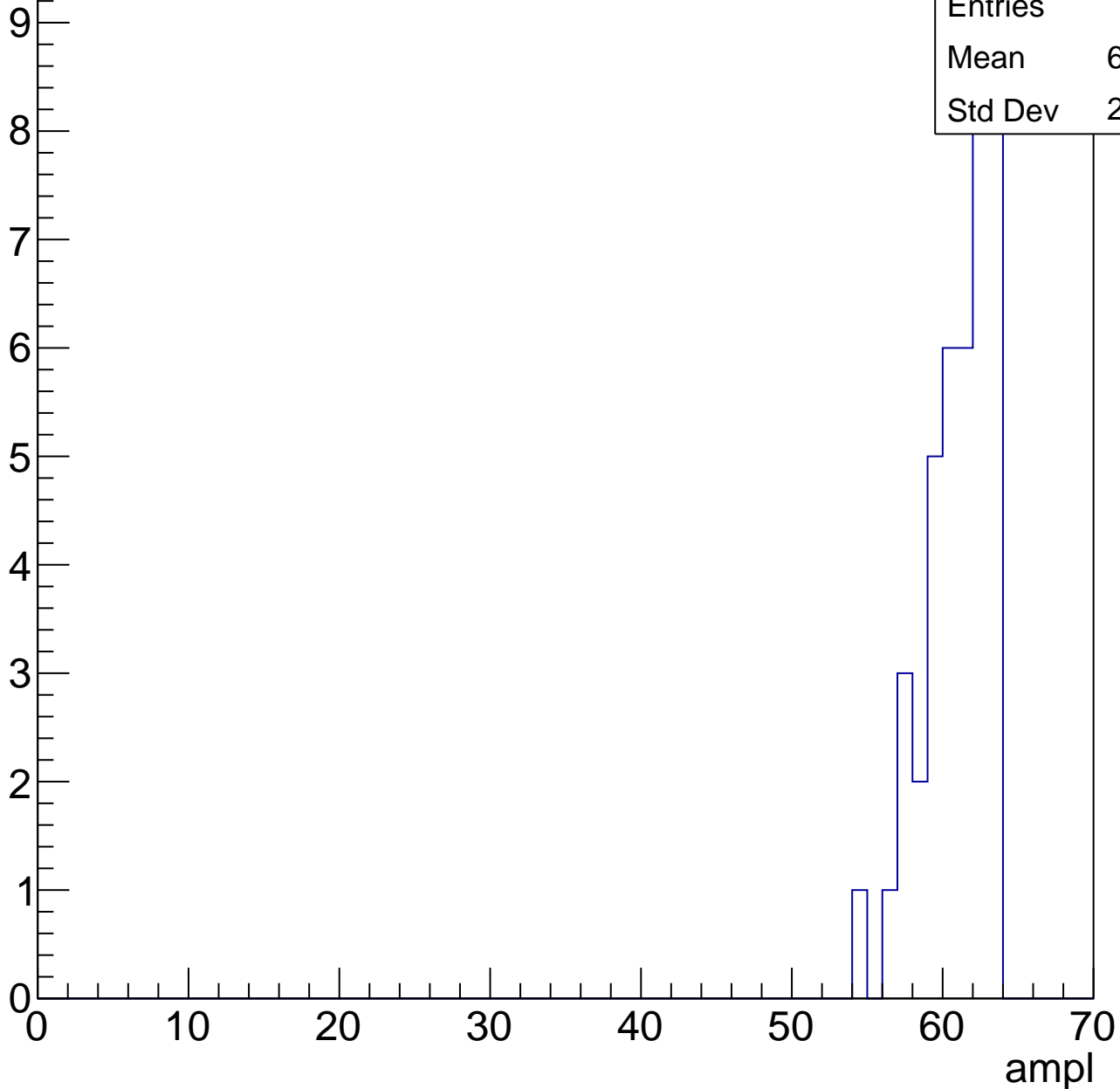


B1L101S, U17-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

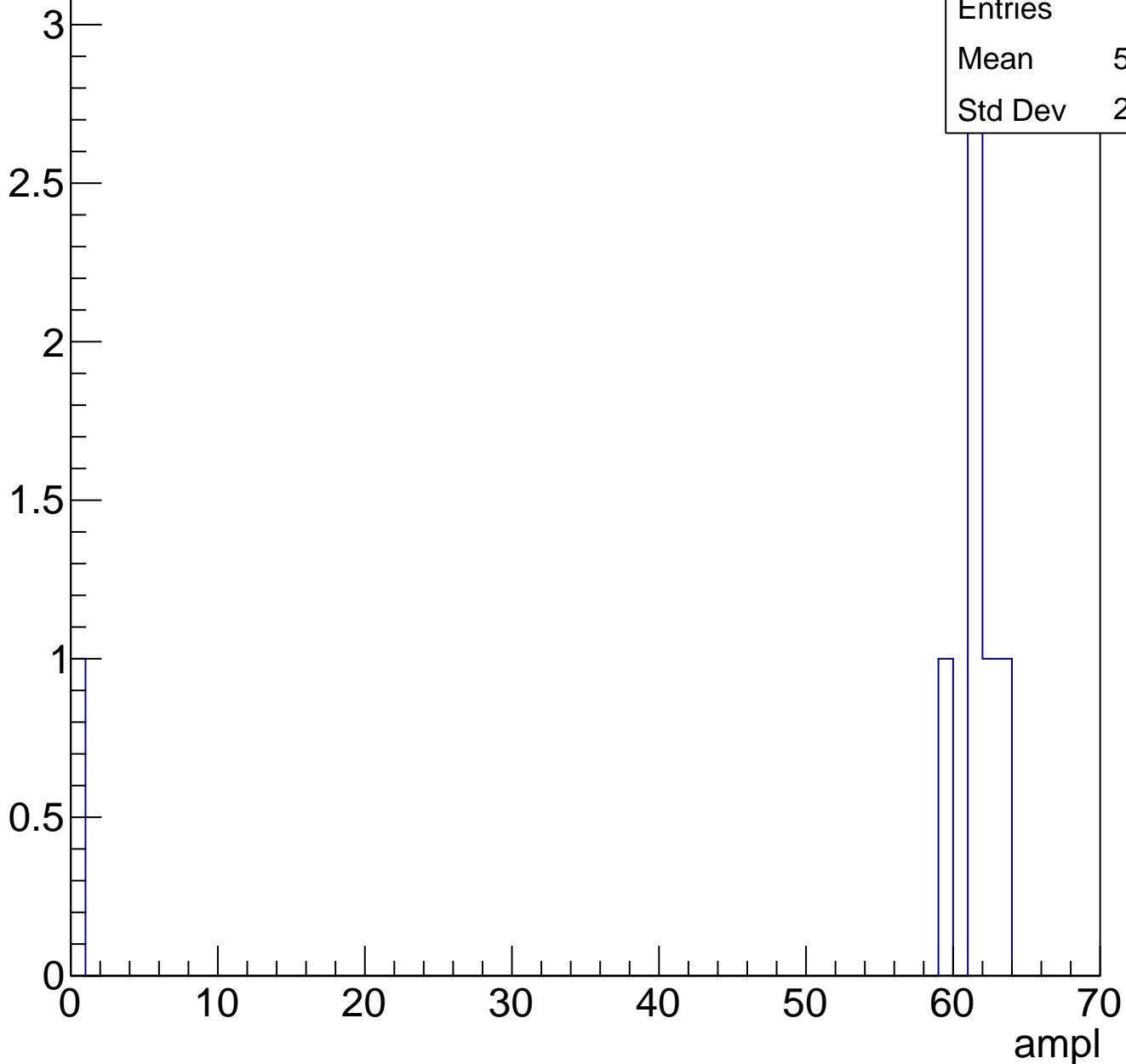
Entries	41
Mean	60.49
Std Dev	2.188



B1L101S, U17-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch70, adc0

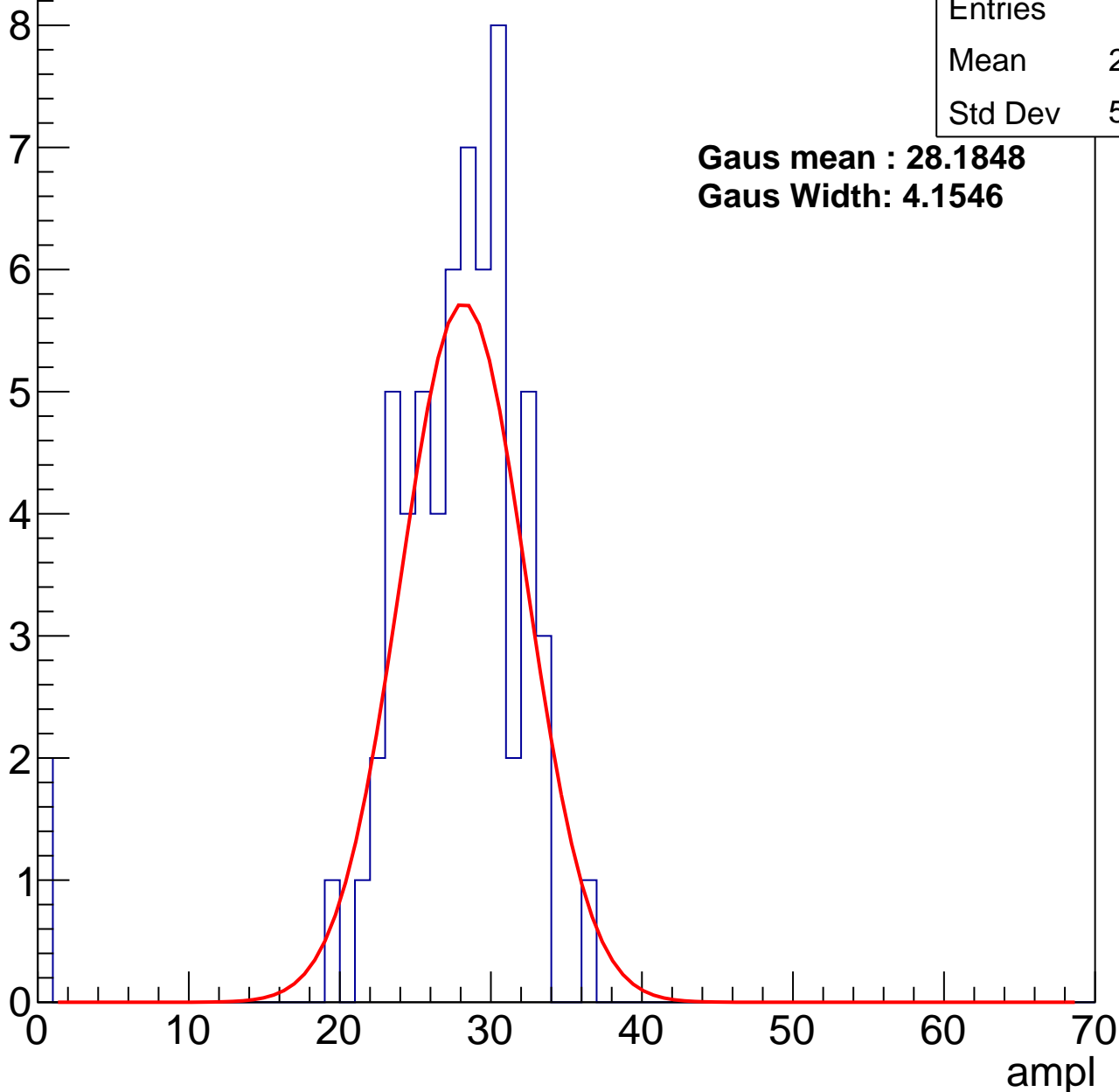
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	26.66
Std Dev	5.943

Gaus mean : 28.1848

Gaus Width: 4.1546

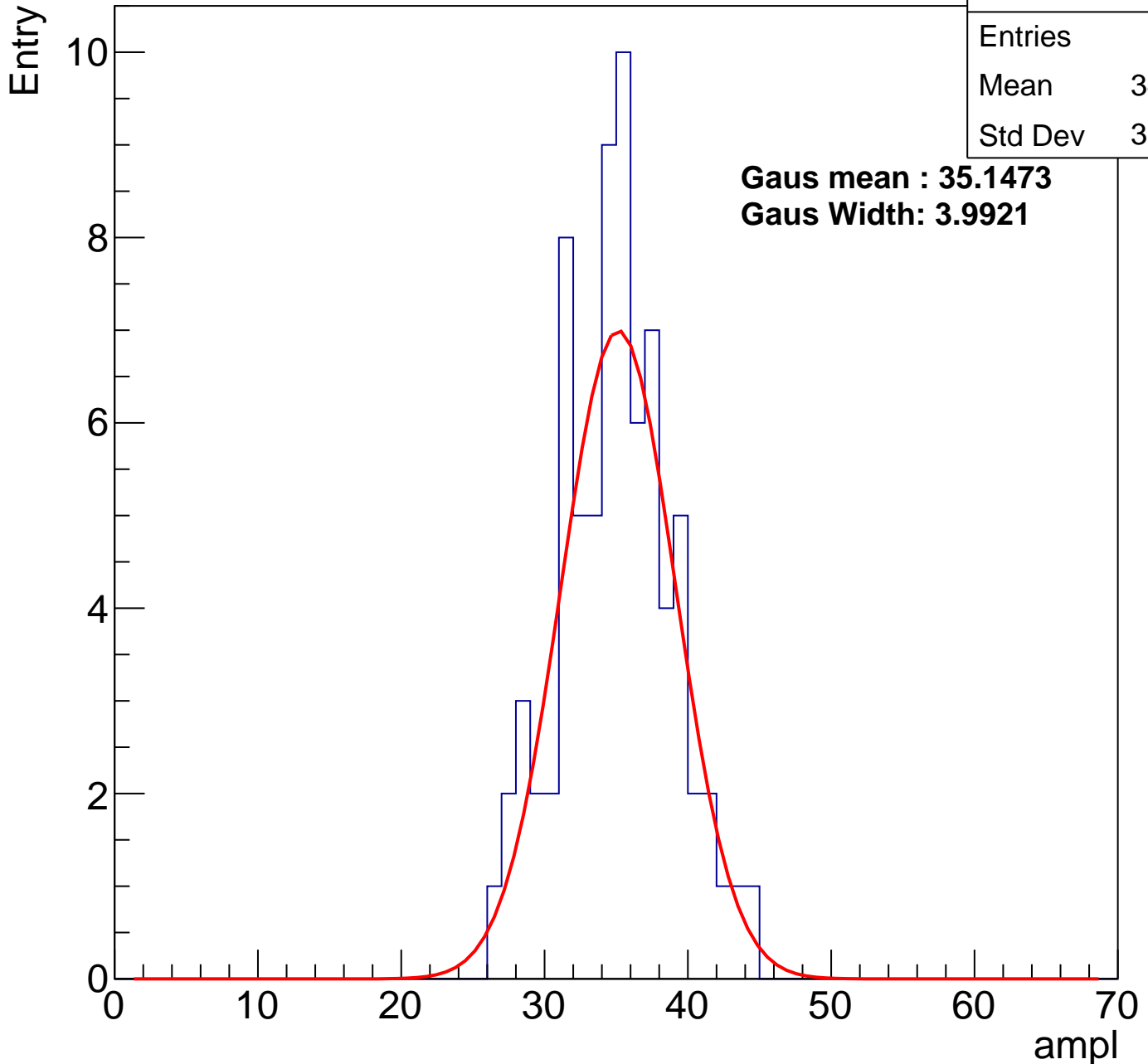


B1L101S, U17-ch70, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	34.53
Std Dev	3.854

Gaus mean : 35.1473
Gaus Width: 3.9921



B1L101S, U17-ch70, adc2

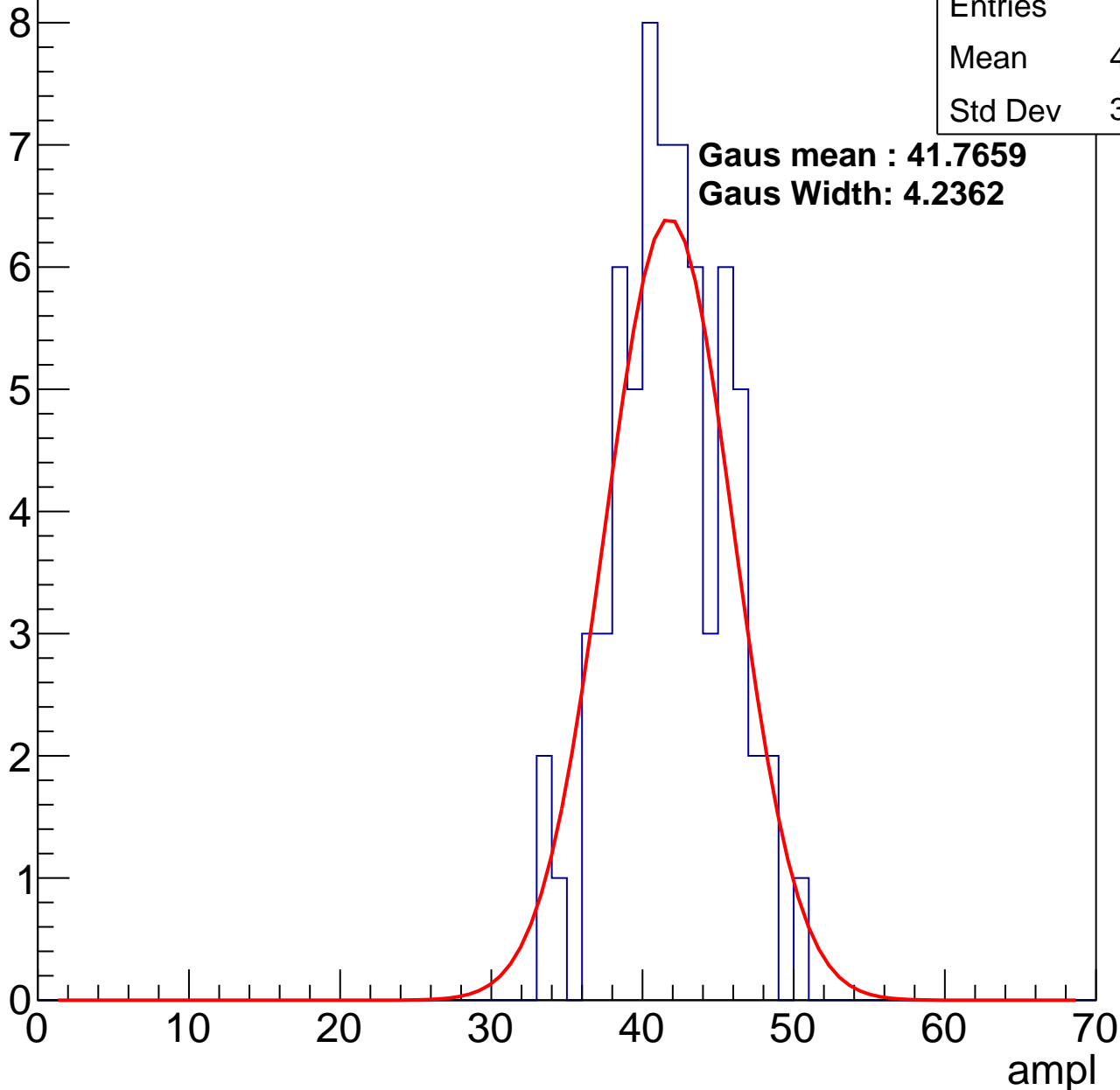
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	41.39
Std Dev	3.669

Gaus mean : 41.7659

Gaus Width: 4.2362

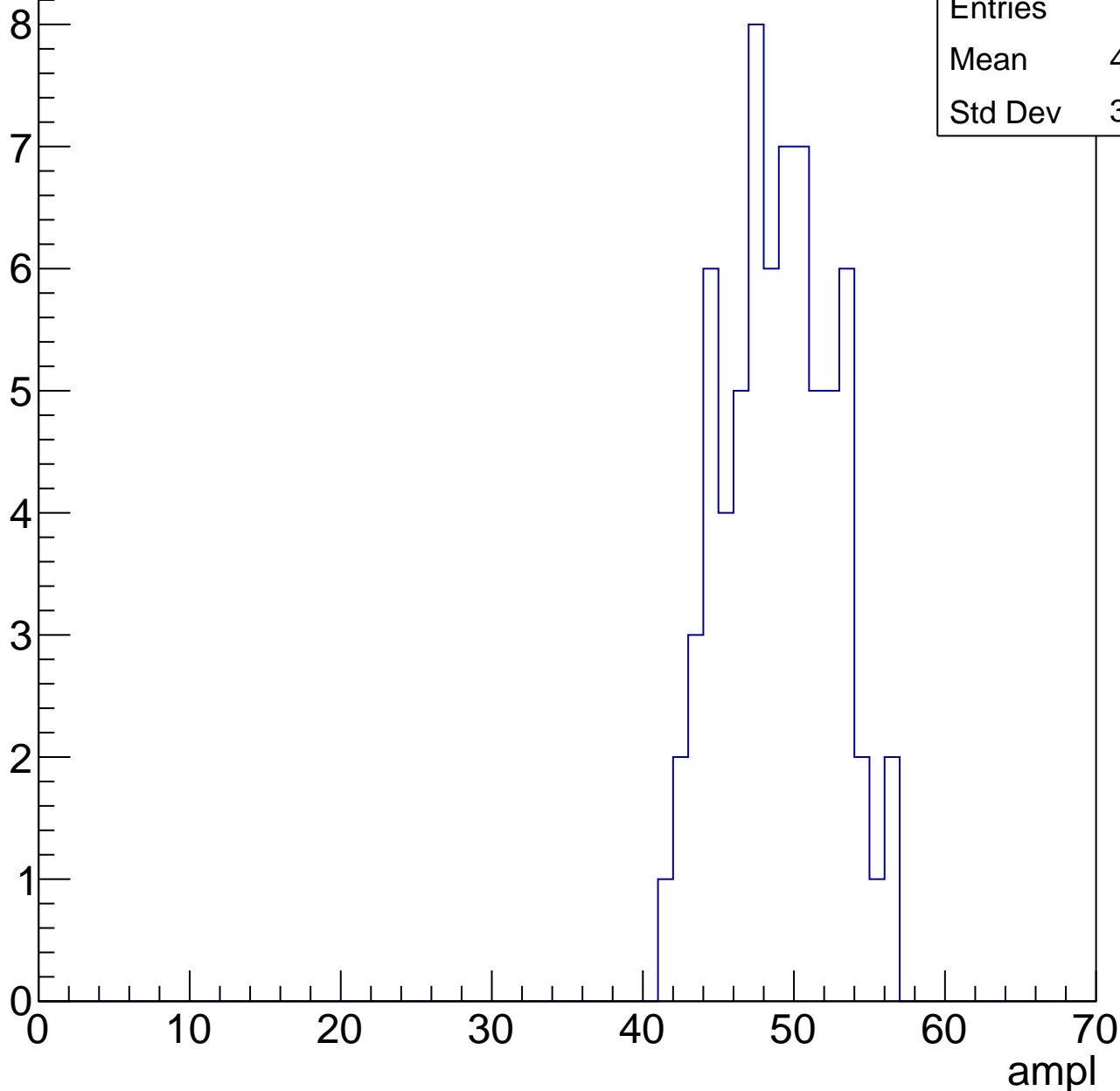


B1L101S, U17-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.47
Std Dev	3.585

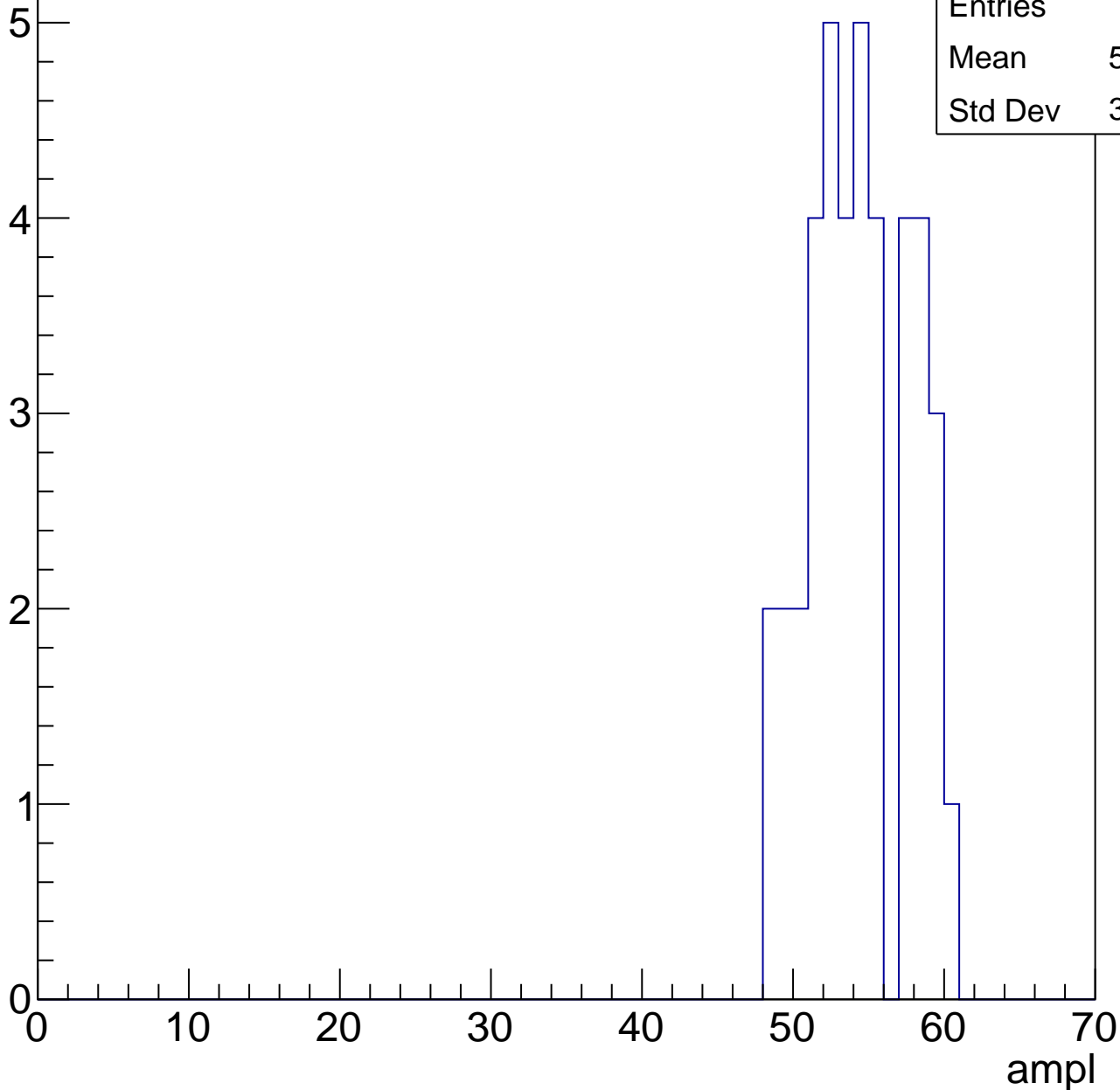


B1L101S, U17-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	53.92
Std Dev	3.274

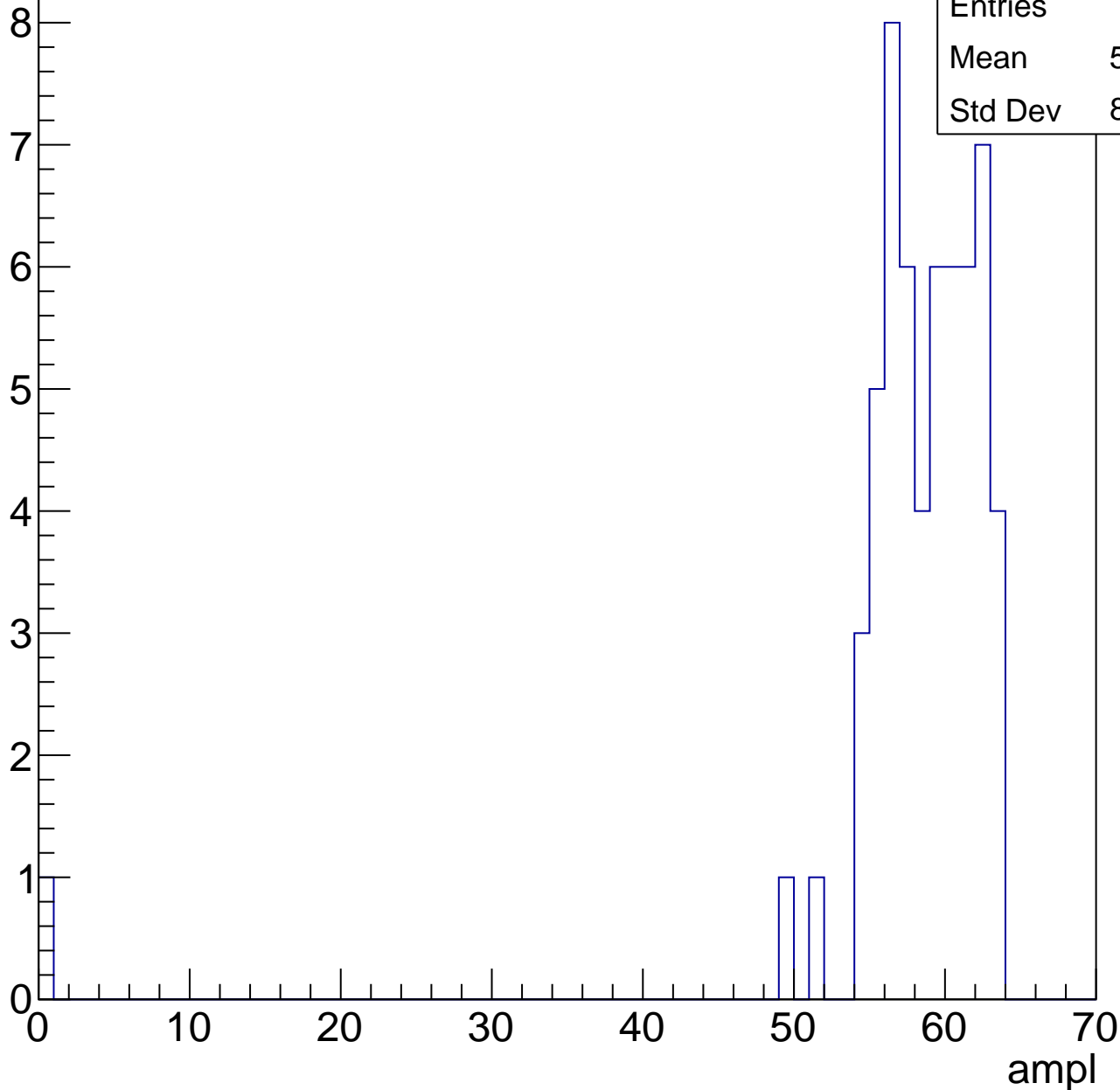


B1L101S, U17-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	57.33
Std Dev	8.195

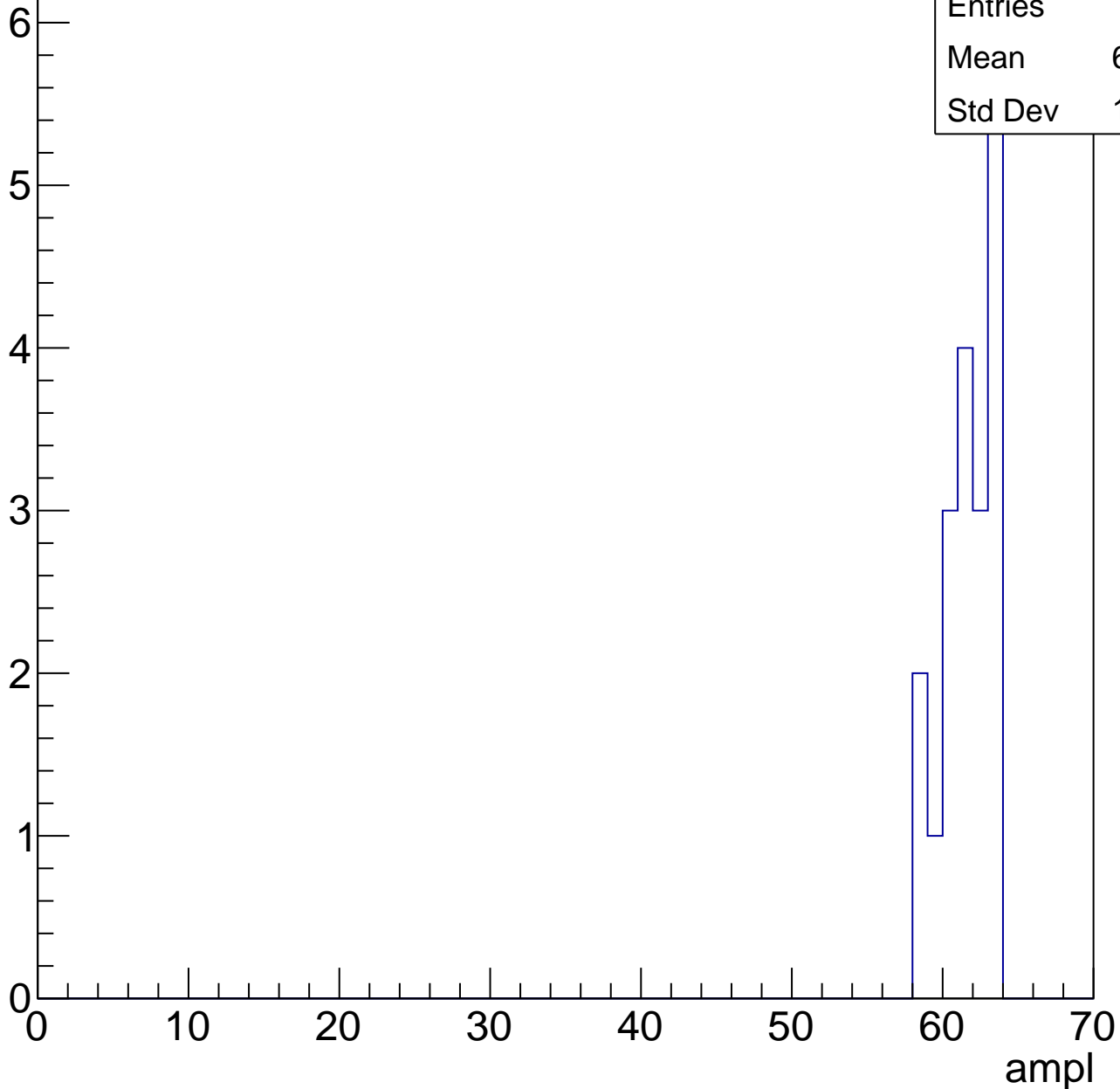


B1L101S, U17-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.21
Std Dev	1.641



B1L101S, U17-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch71, adc0

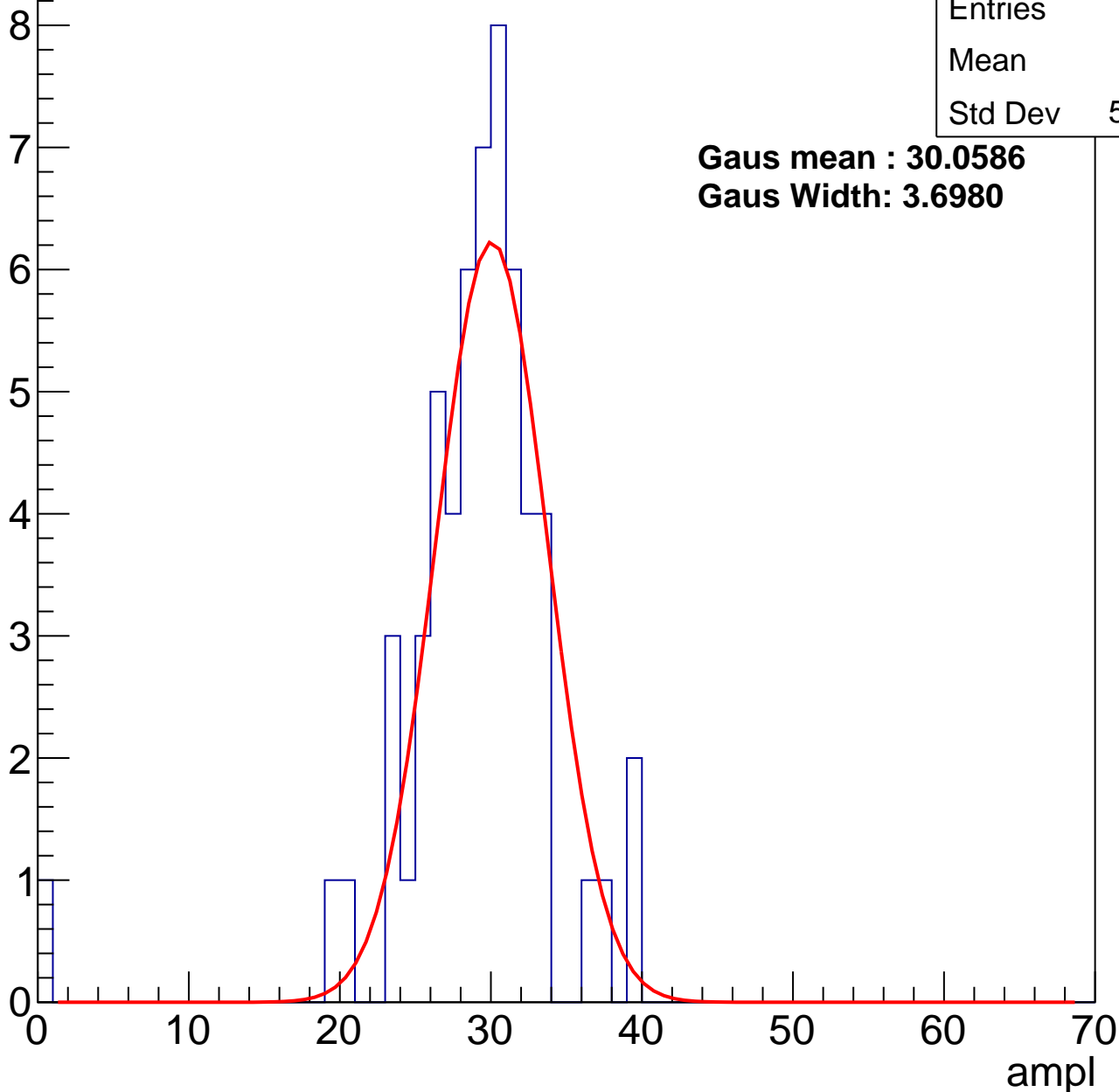
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	28.5
Std Dev	5.415

Gaus mean : 30.0586

Gaus Width: 3.6980



B1L101S, U17-ch71, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	35.46
Std Dev	3.825

Gaus mean : 36.2950

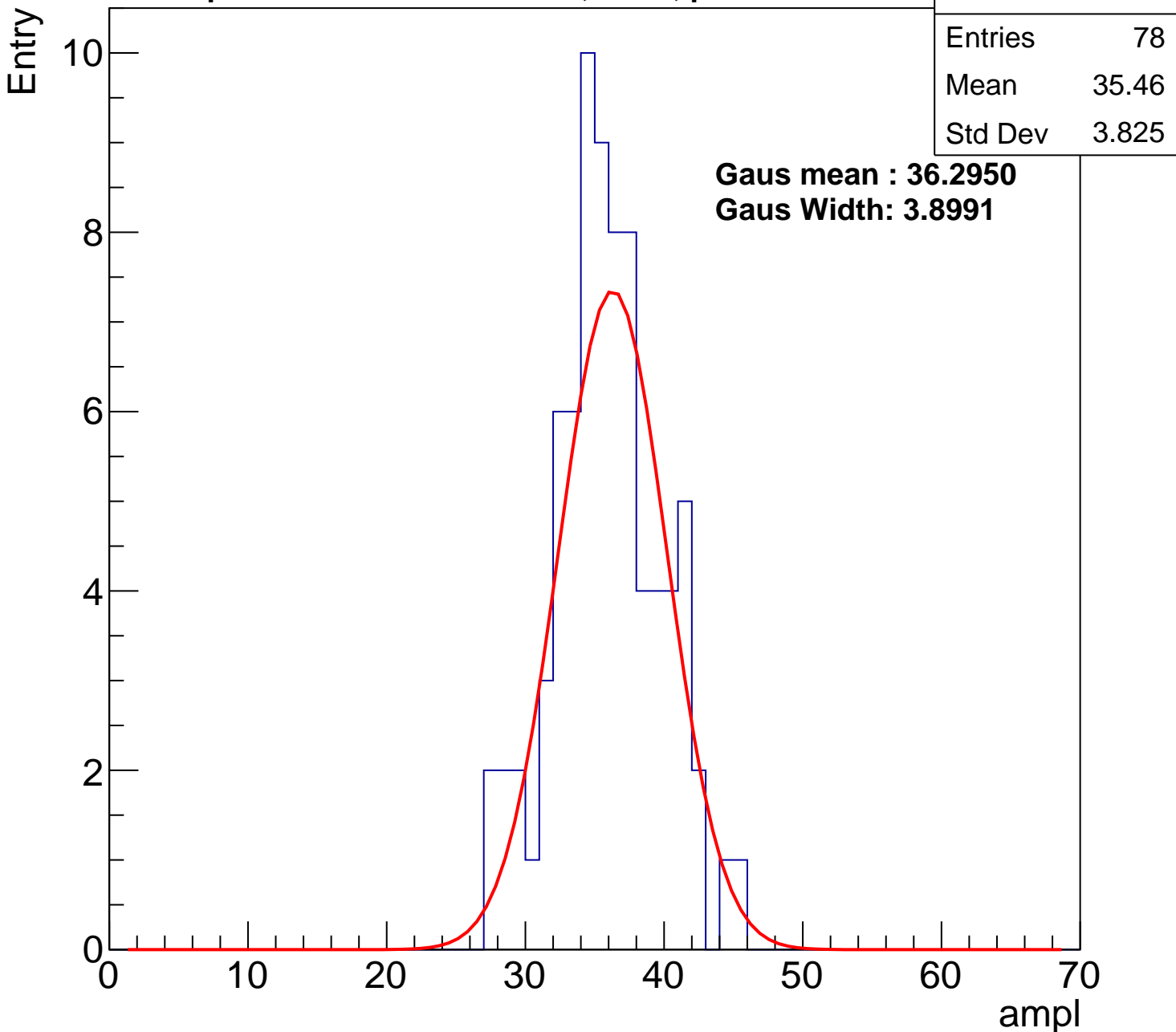
Gaus Width: 3.8991

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch71, adc2

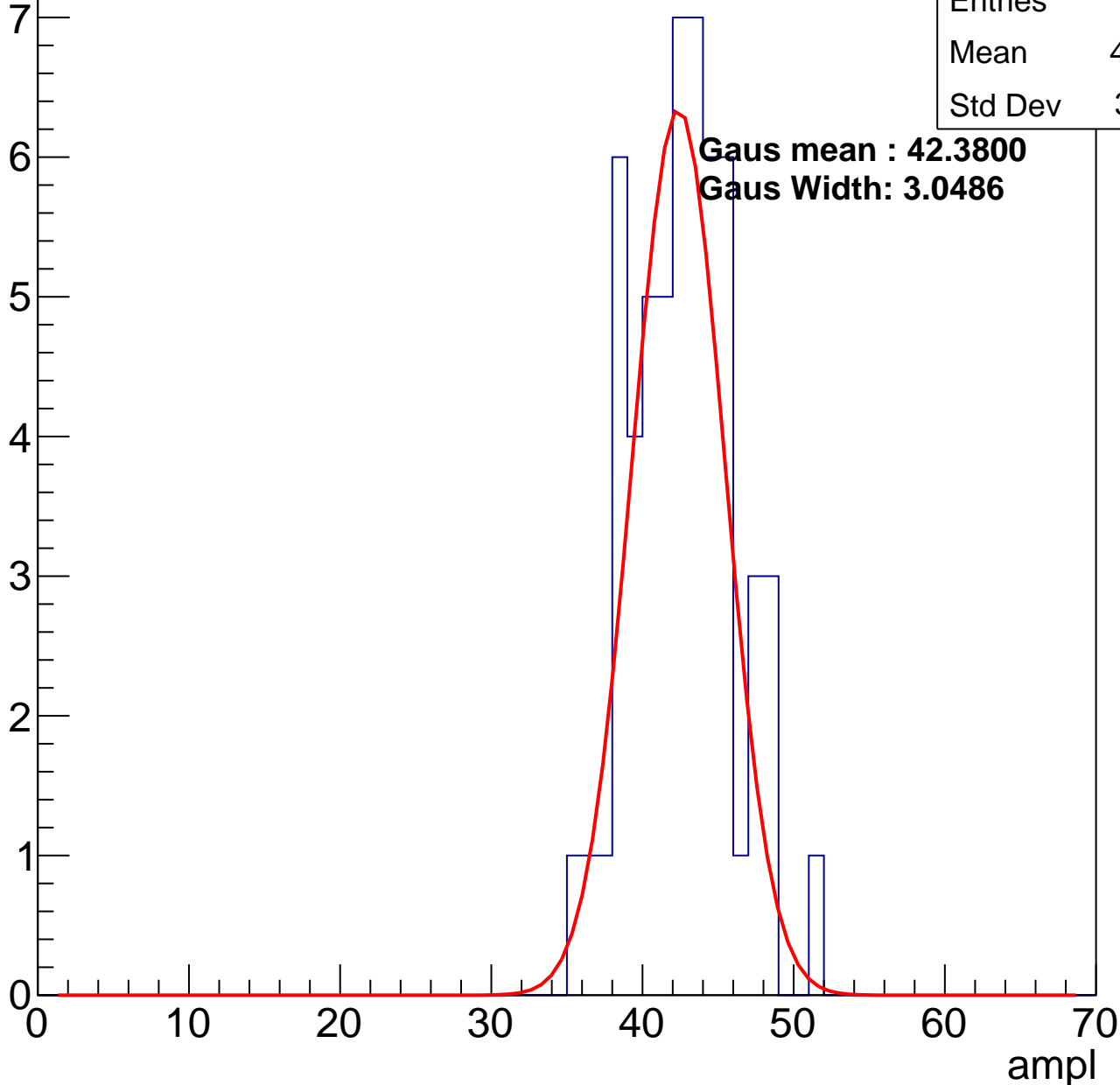
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.25
Std Dev	3.321

Gaus mean : 42.3800

Gaus Width: 3.0486



B1L101S, U17-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	48.7
Std Dev	3.381

Entry

10

8

6

4

2

0

0

10

20

30

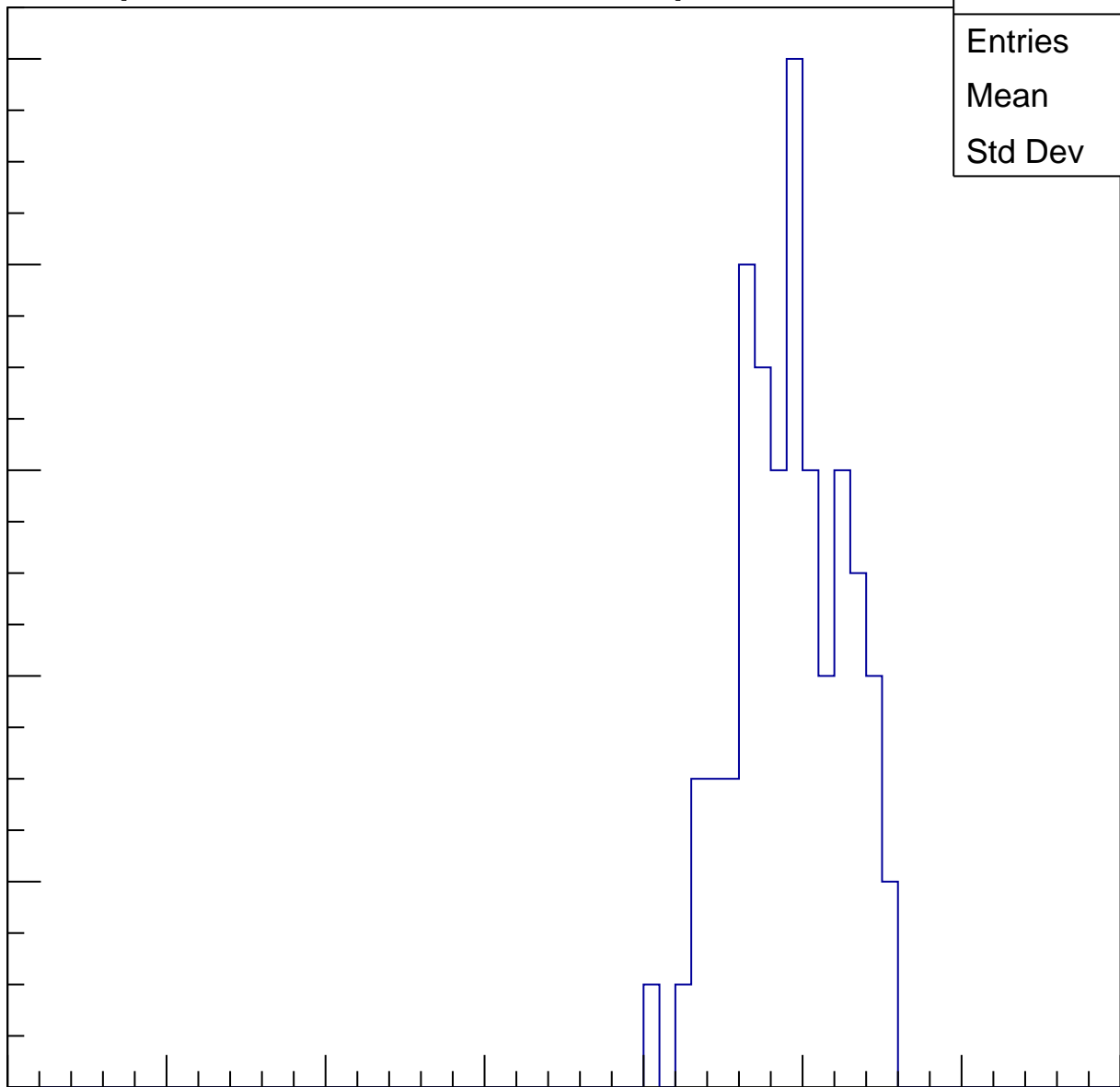
40

50

60

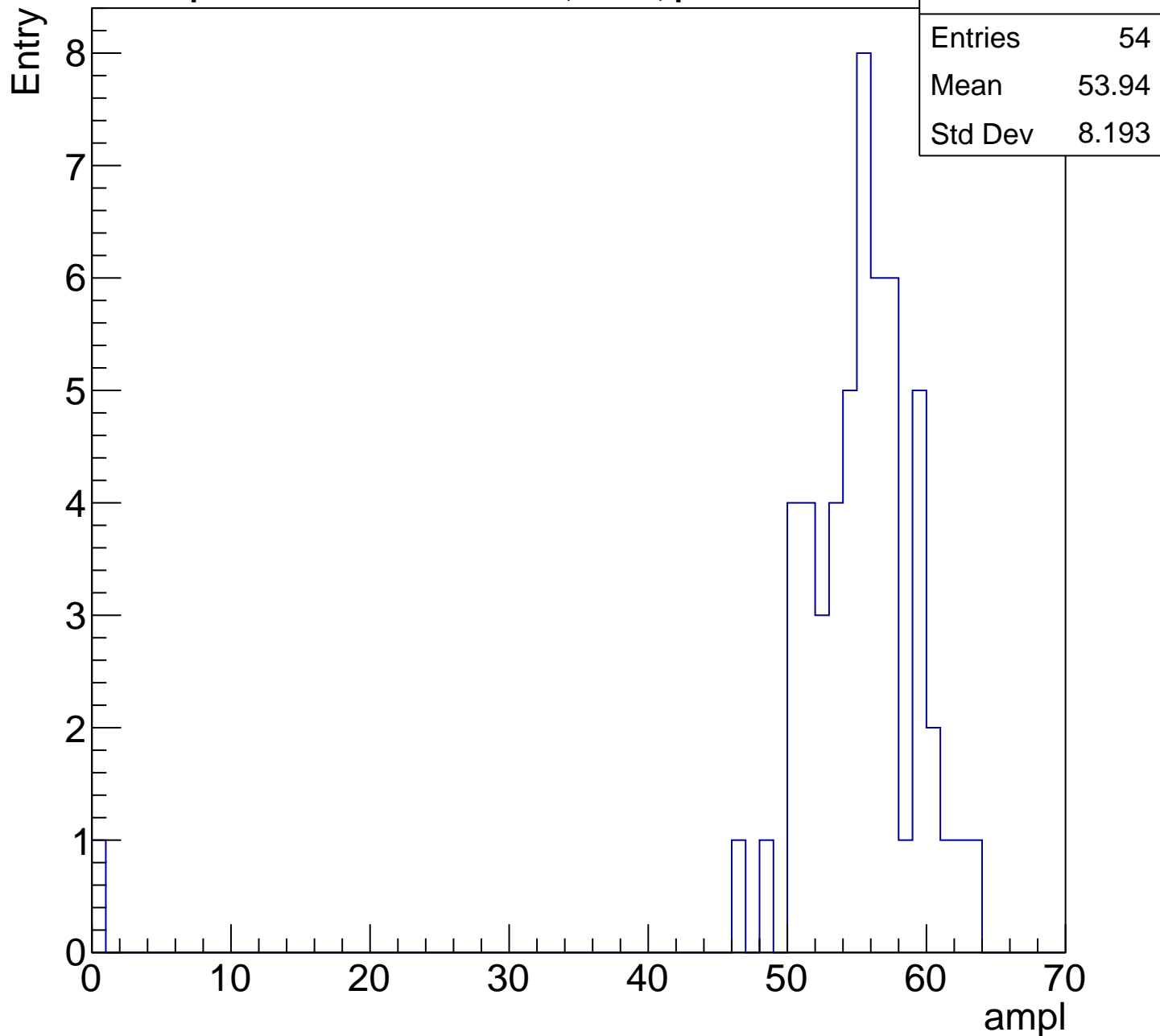
70

ampl



B1L101S, U17-ch71, adc4

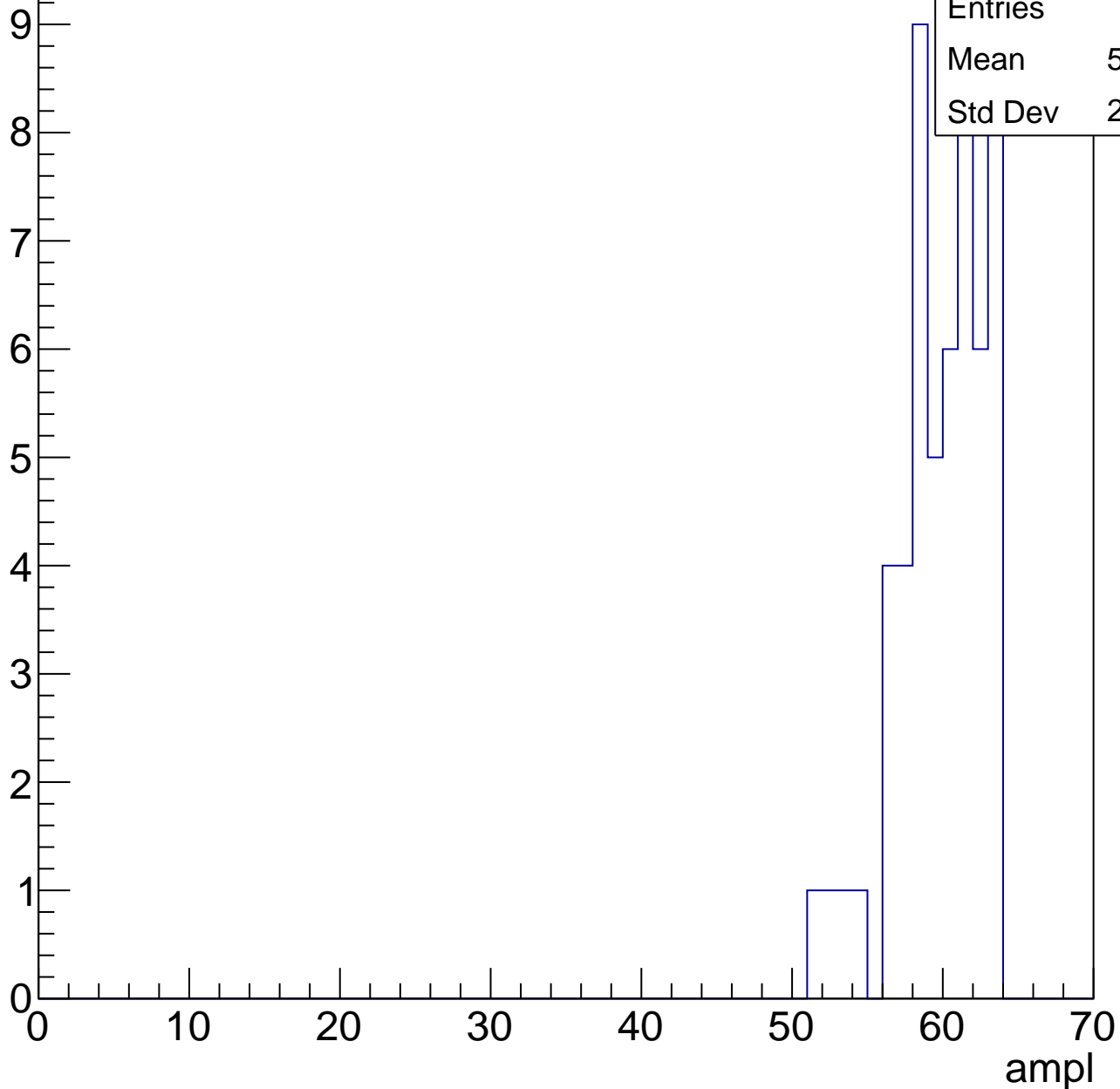
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

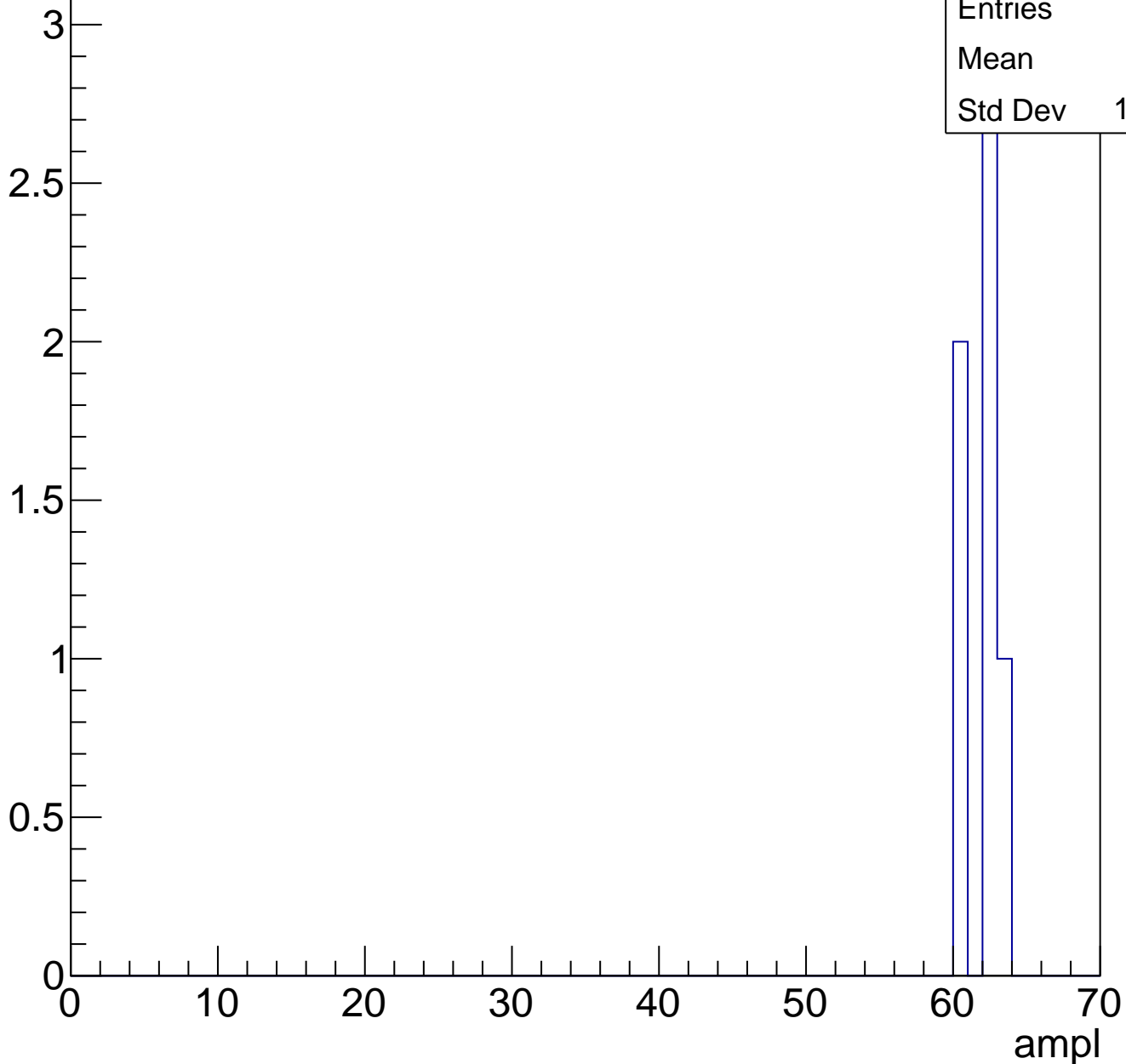
Entry



B1L101S, U17-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch72, adc0

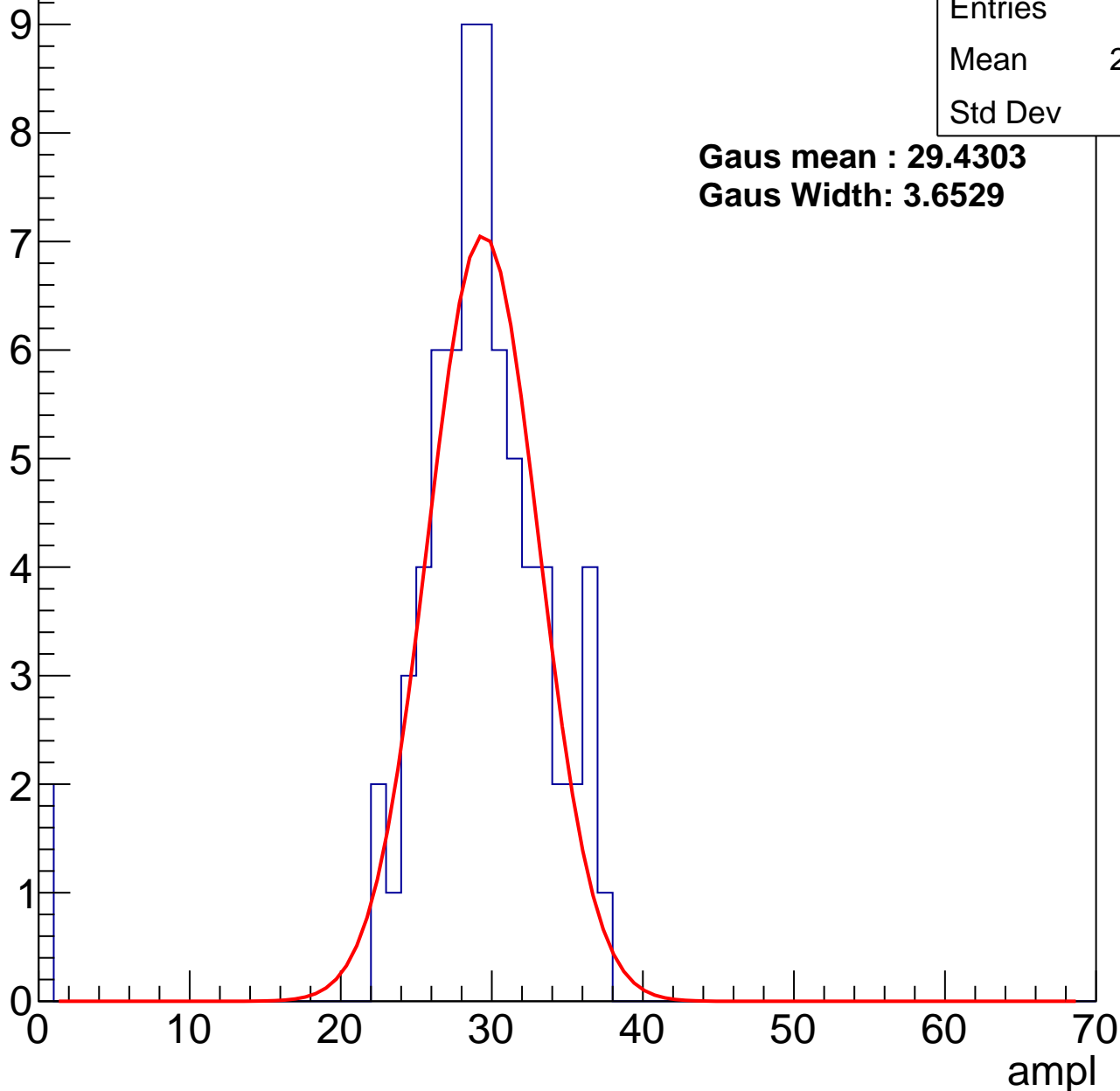
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.34
Std Dev	6

Gaus mean : 29.4303

Gaus Width: 3.6529



B1L101S, U17-ch72, adc1

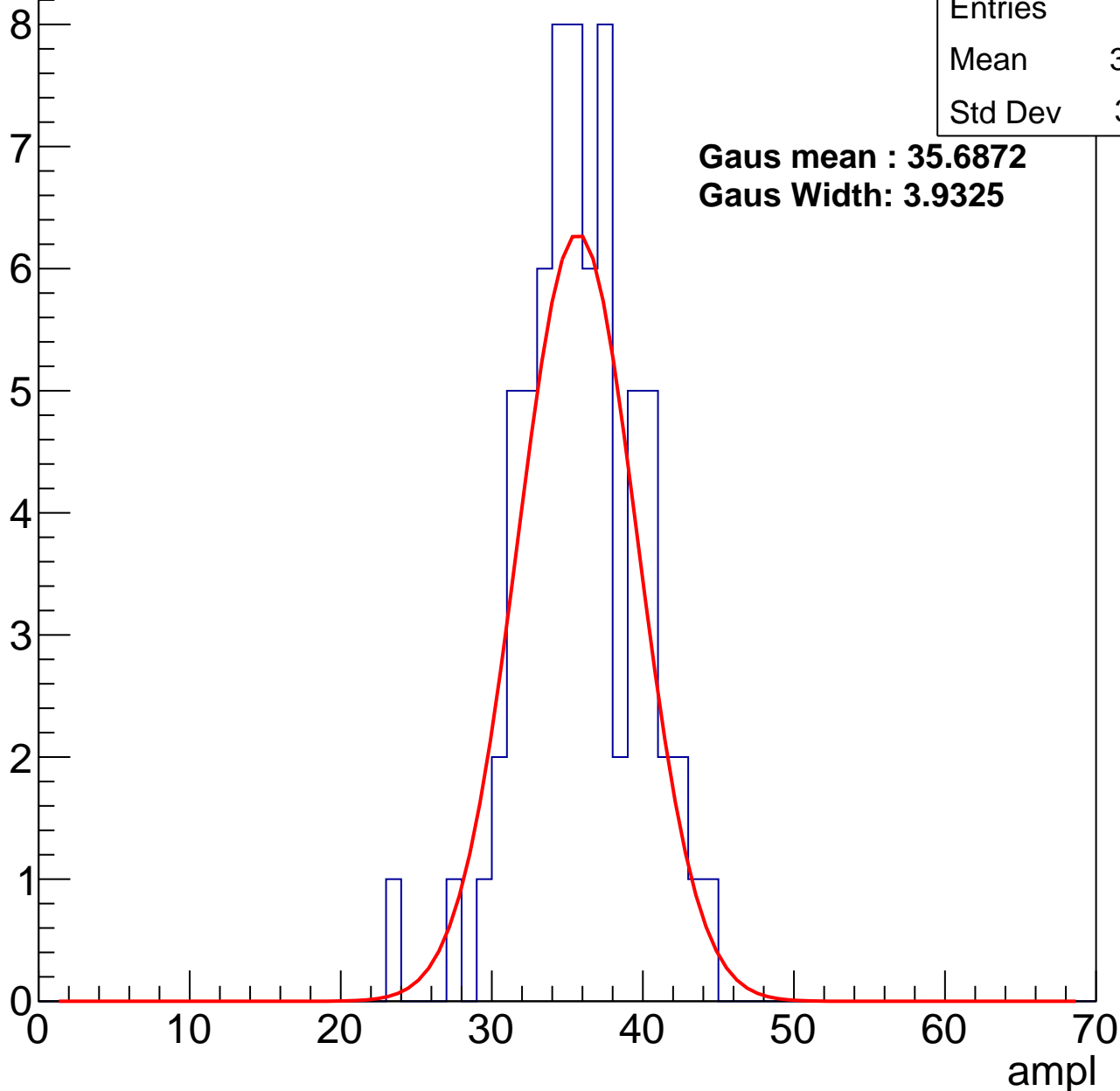
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.36
Std Dev	3.841

Gaus mean : 35.6872

Gaus Width: 3.9325



B1L101S, U17-ch72, adc2

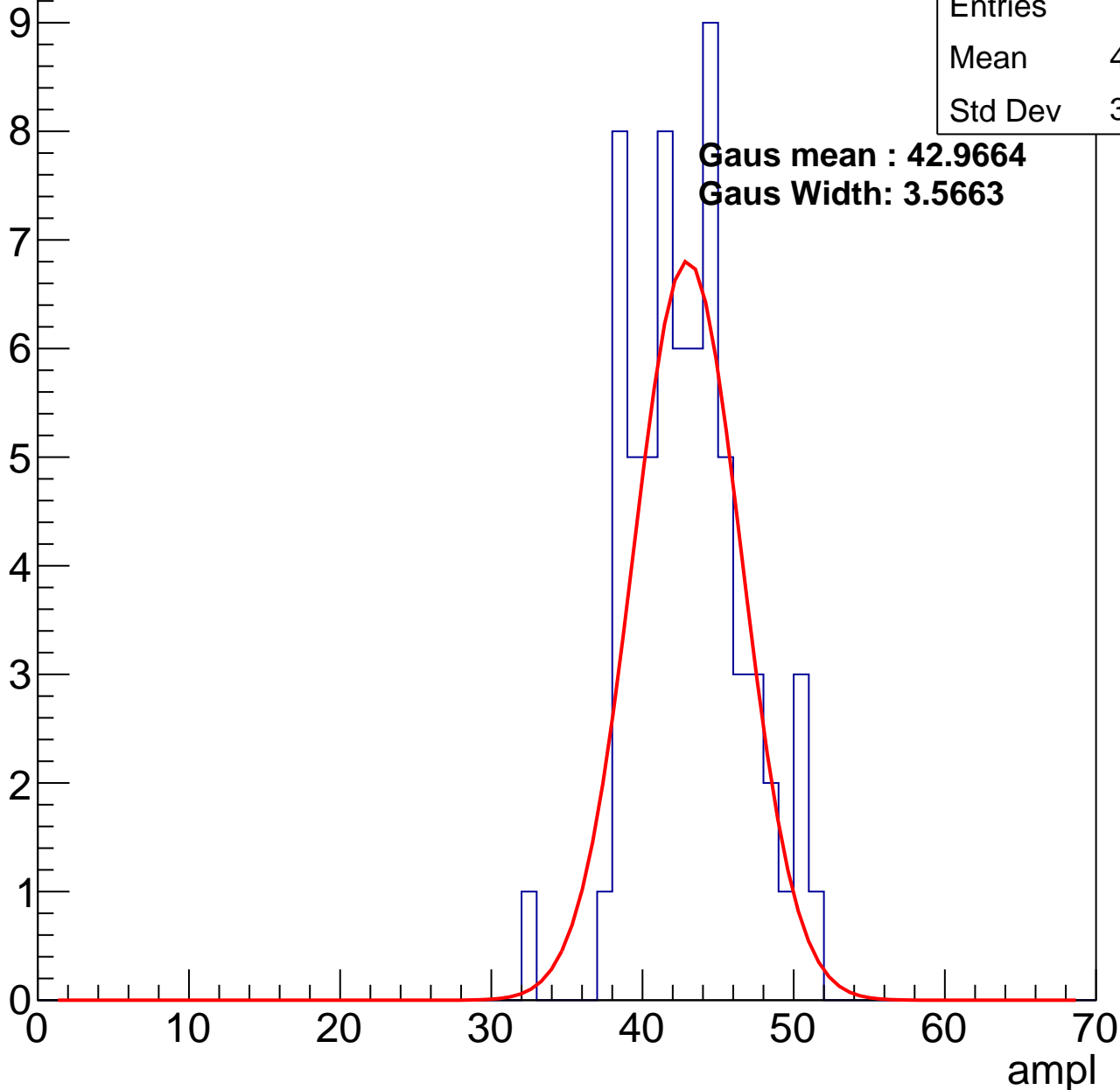
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.57
Std Dev	3.682

Gaus mean : 42.9664

Gaus Width: 3.5663

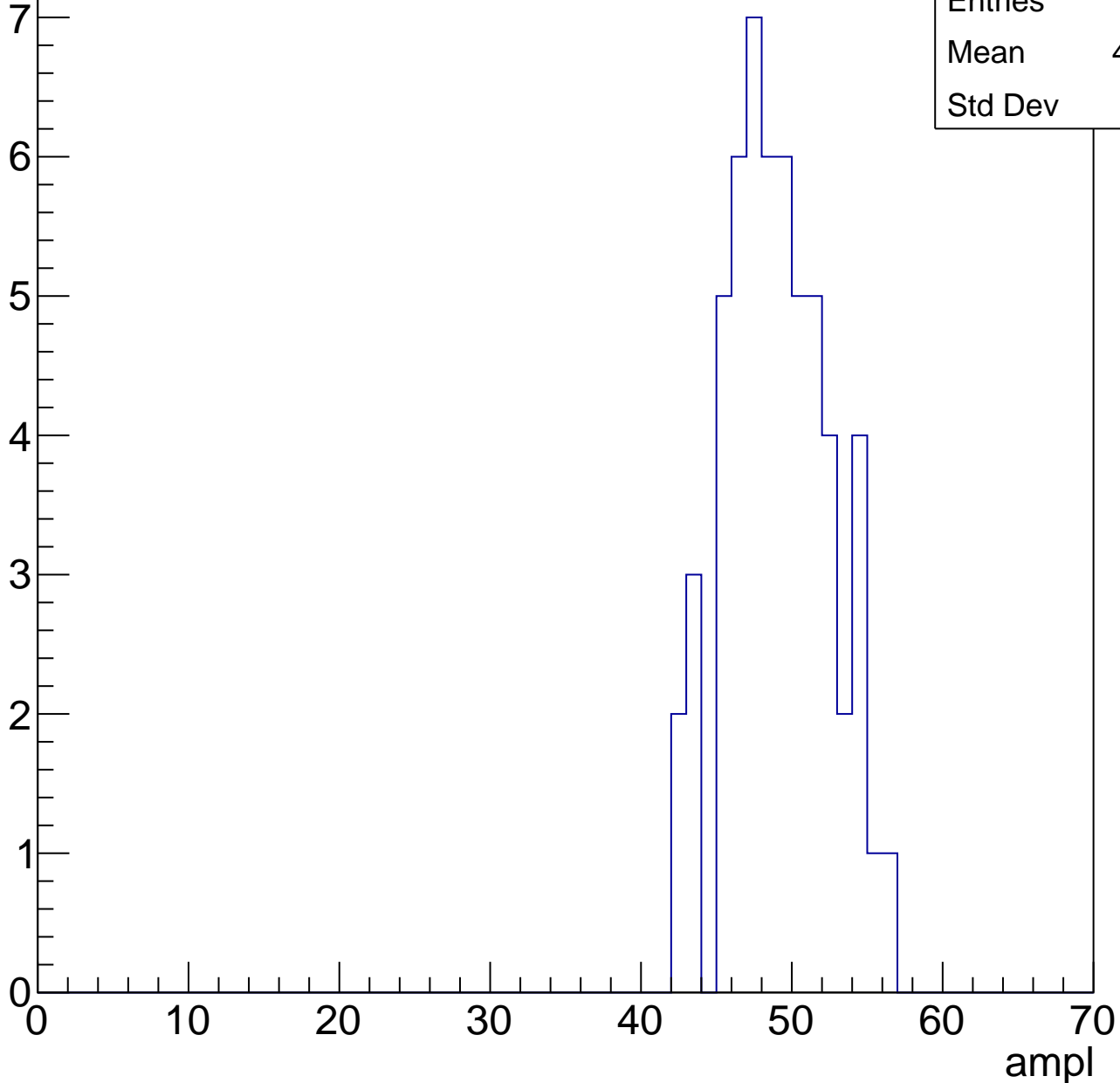


B1L101S, U17-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.61
Std Dev	3.36

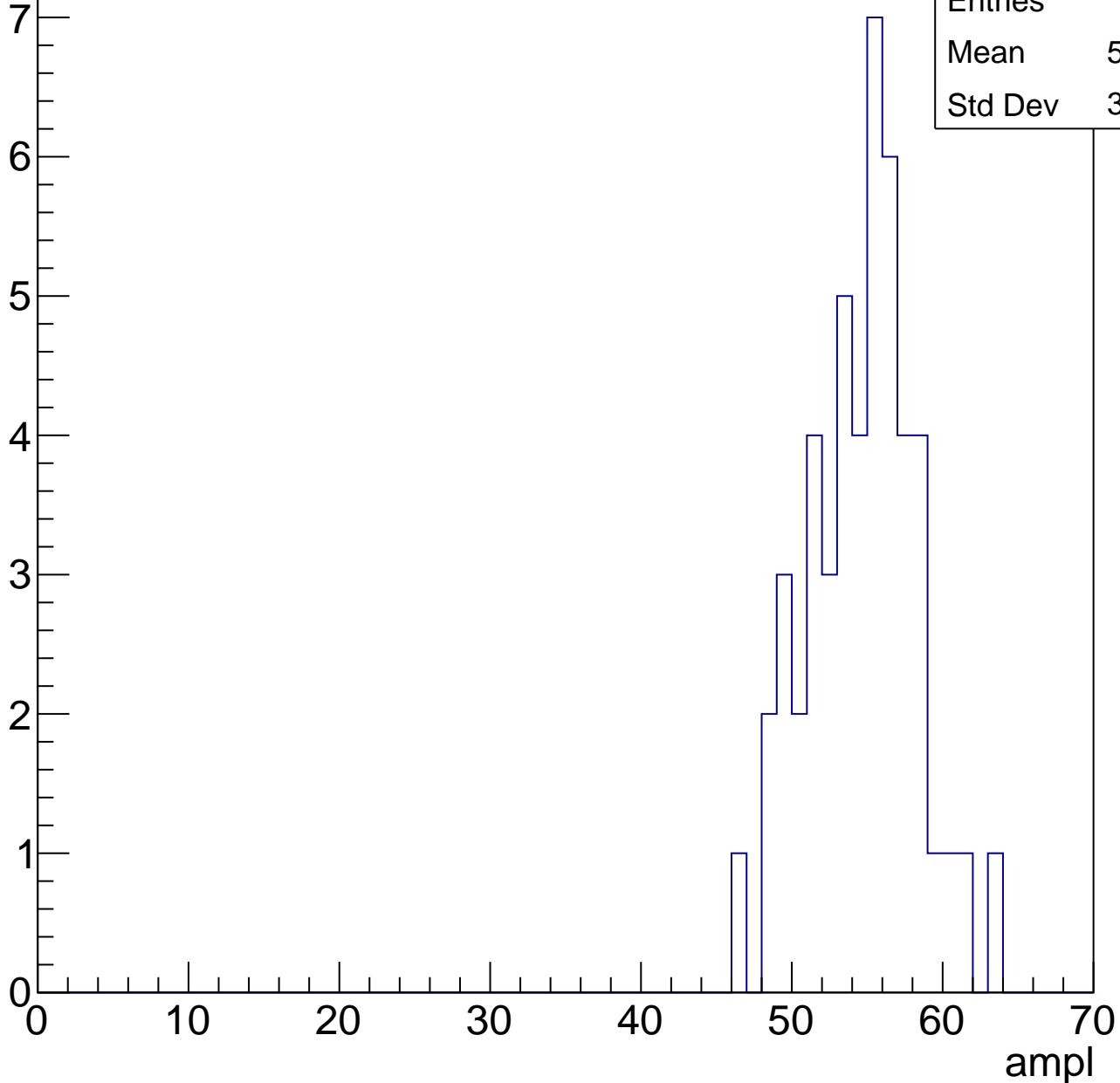


B1L101S, U17-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	54.16
Std Dev	3.548

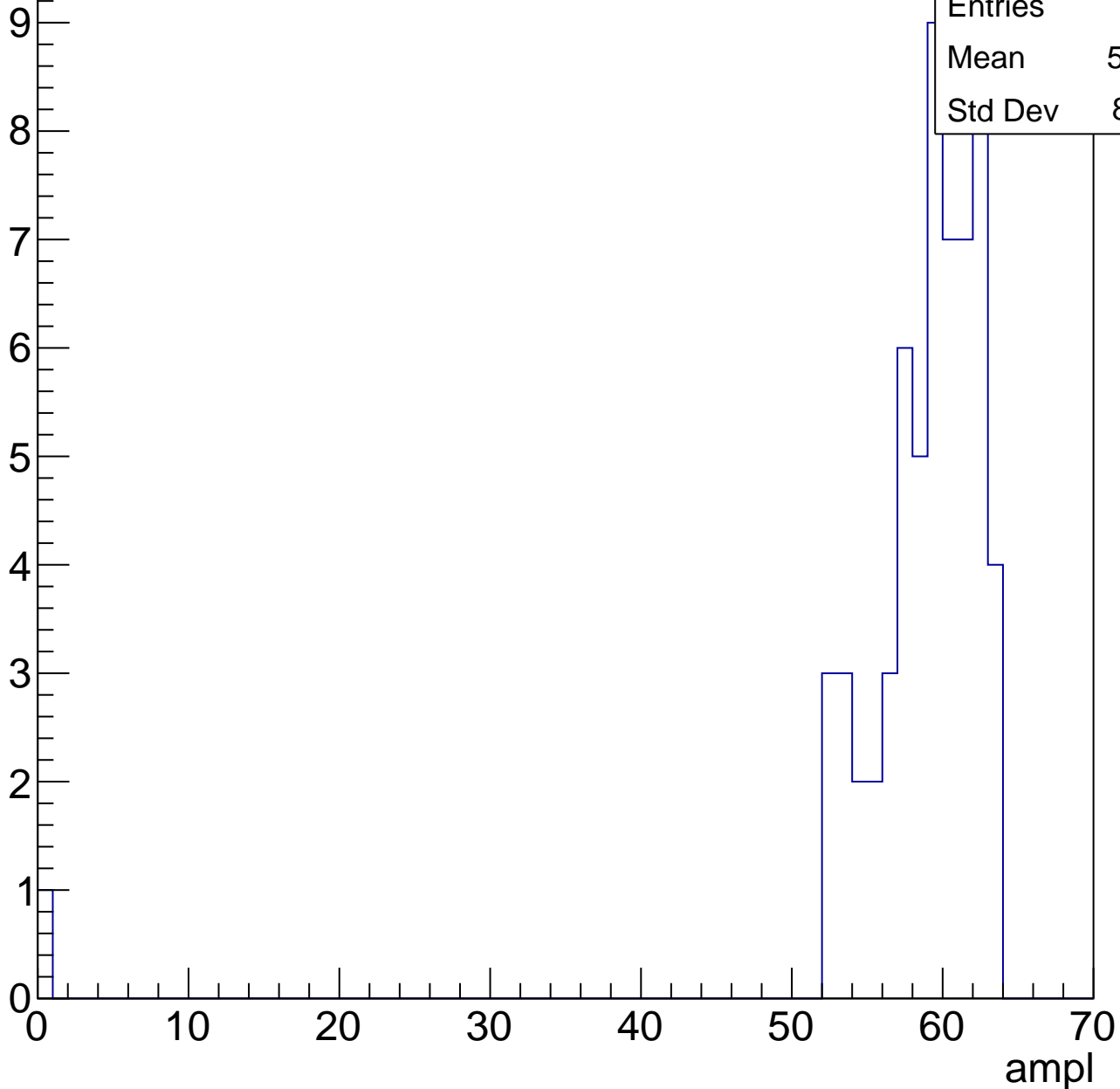


B1L101S, U17-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

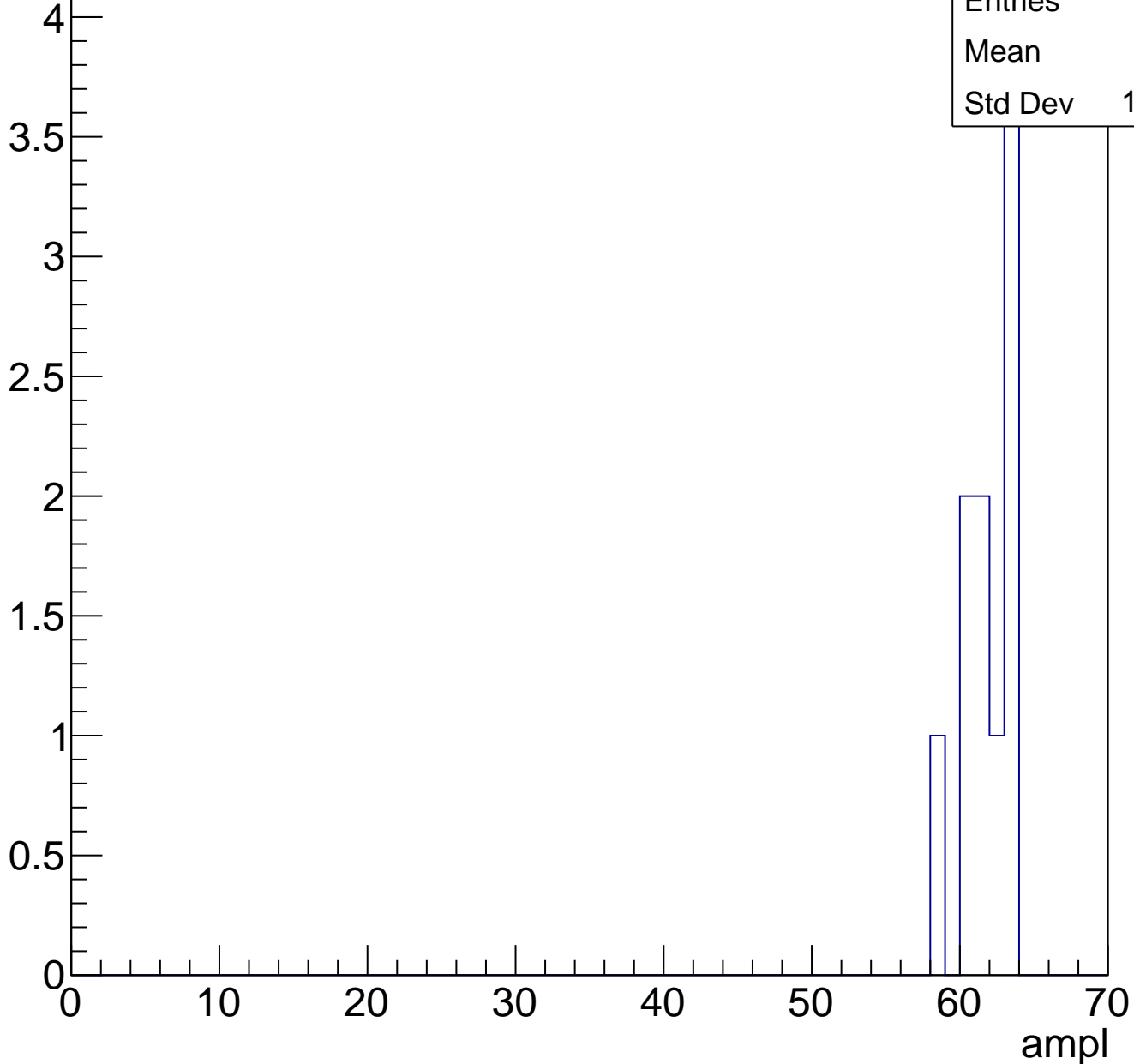
Entries	61
Mean	57.72
Std Dev	8.051



B1L101S, U17-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch73, adc0

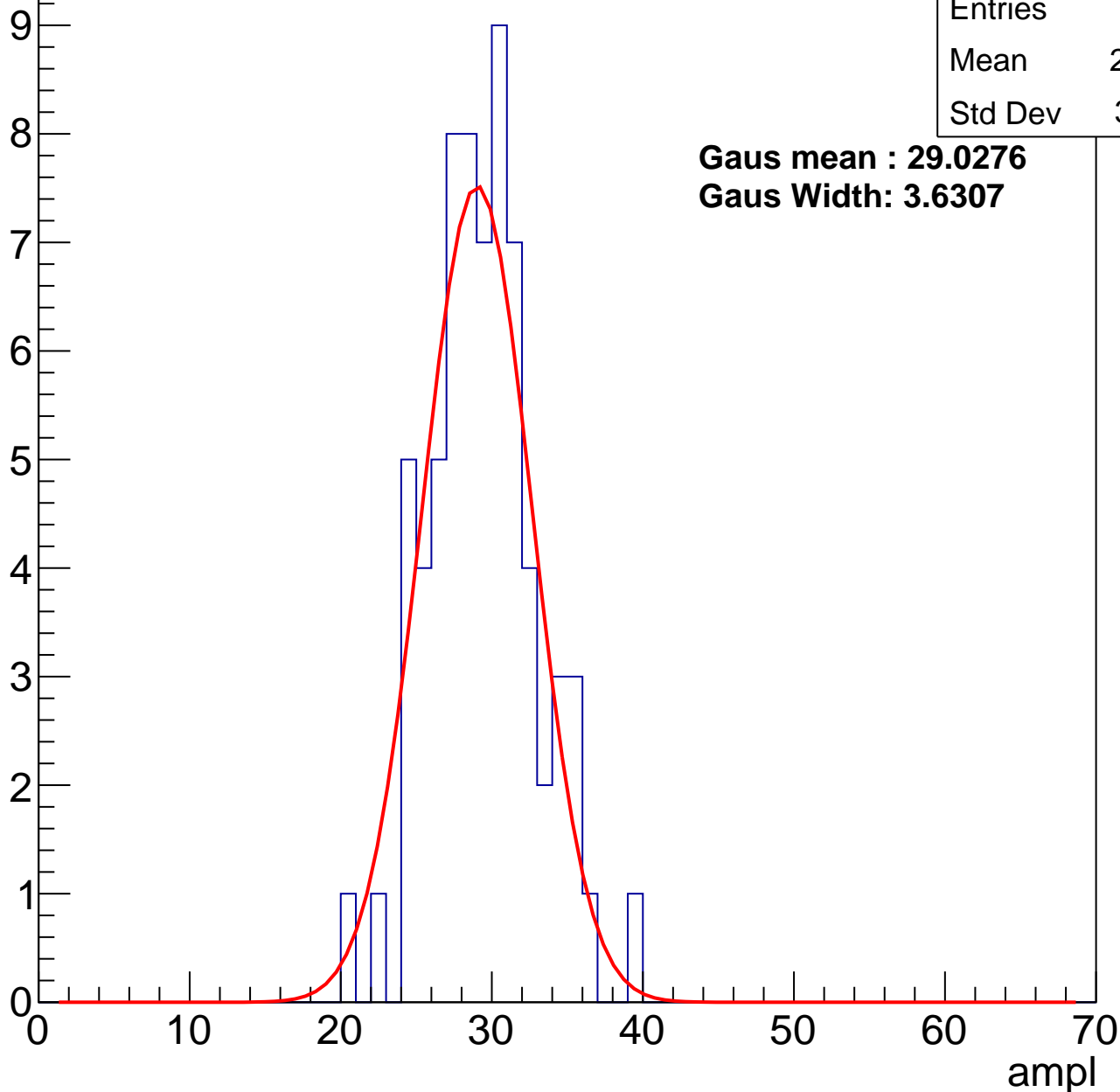
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.96
Std Dev	3.491

Gaus mean : 29.0276

Gaus Width: 3.6307



B1L101S, U17-ch73, adc1

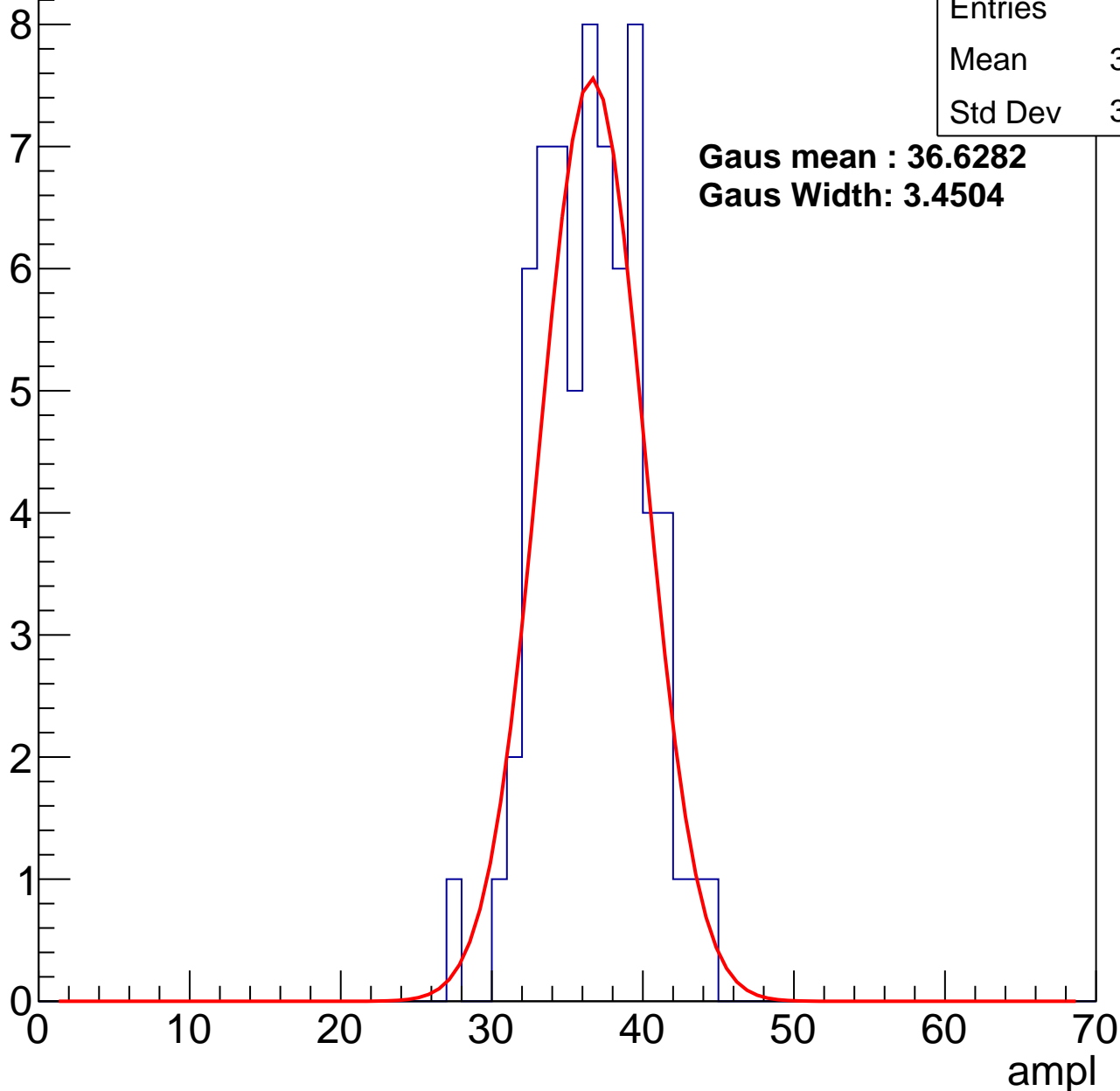
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.16
Std Dev	3.348

Gaus mean : 36.6282

Gaus Width: 3.4504



B1L101S, U17-ch73, adc2

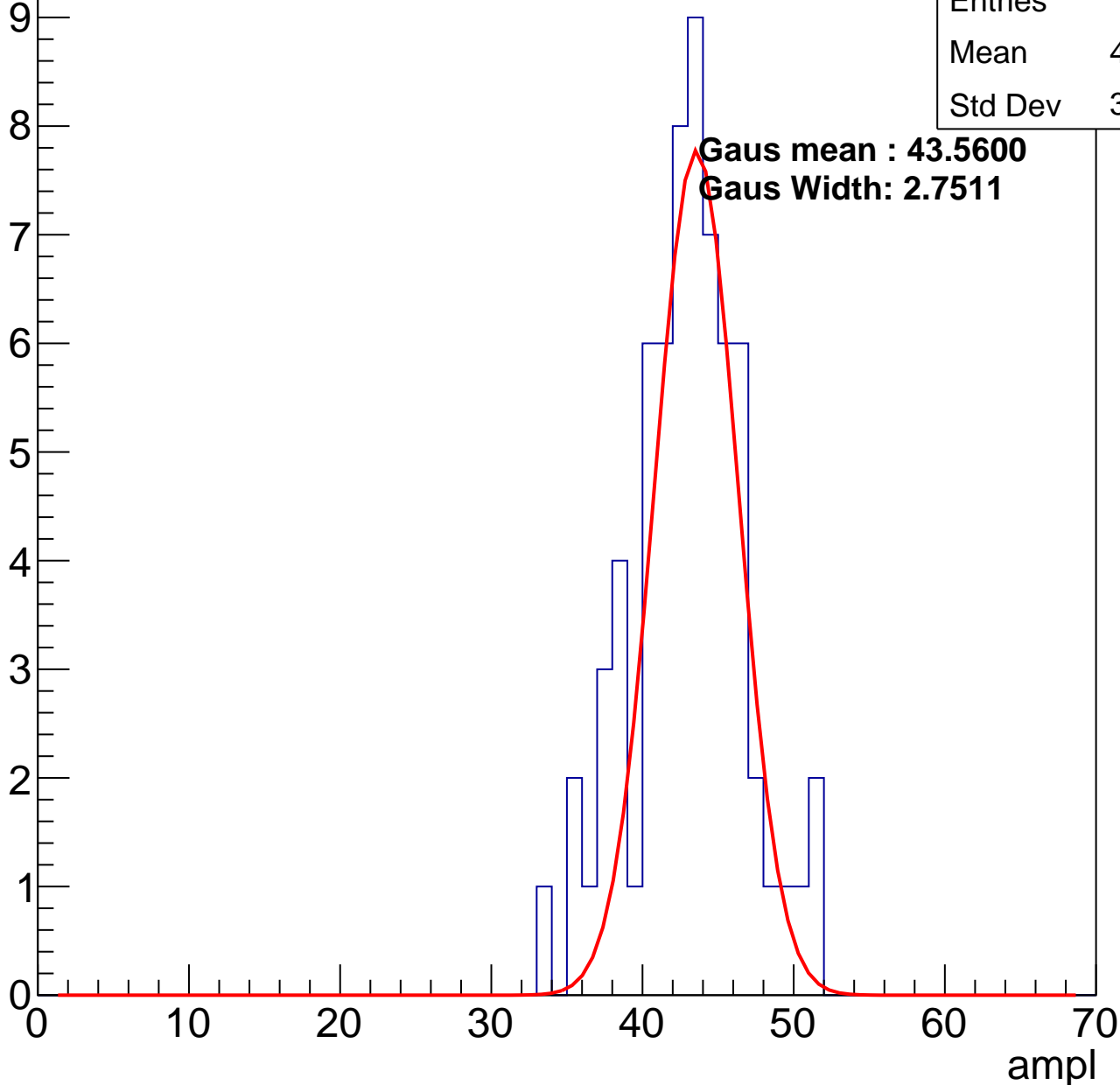
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.49
Std Dev	3.715

Gaus mean : 43.5600

Gaus Width: 2.7511

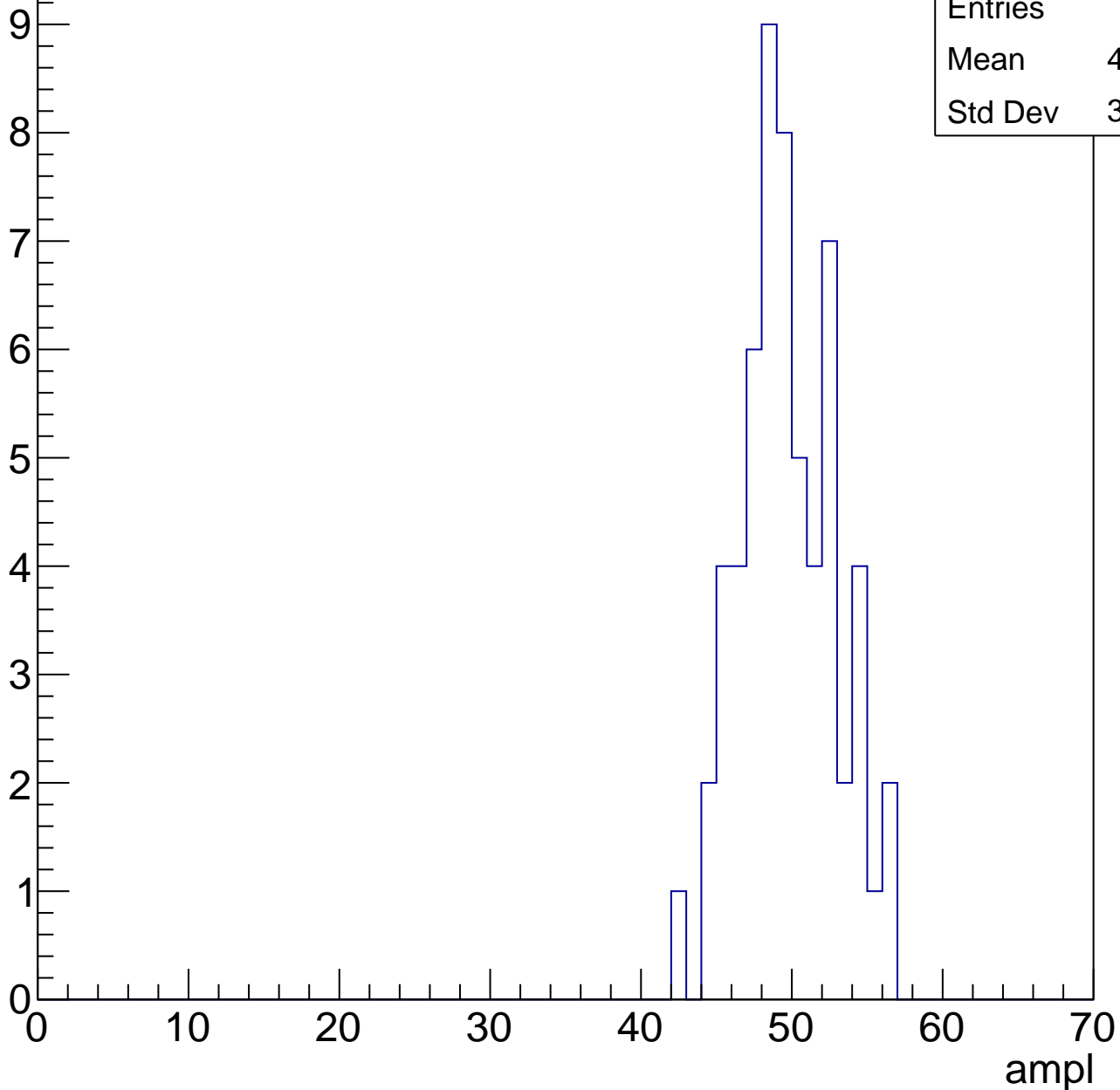


B1L101S, U17-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.27
Std Dev	3.129

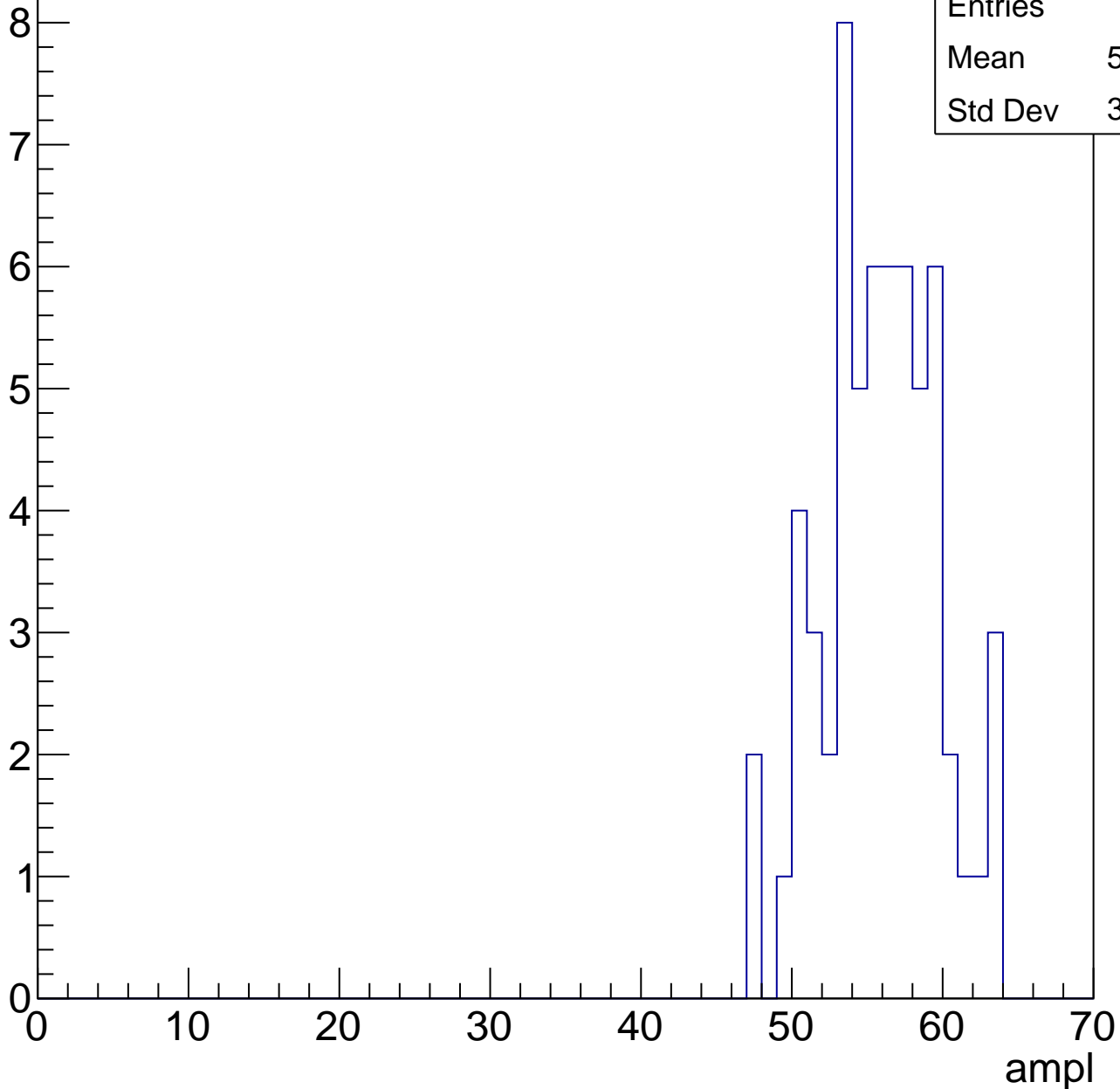


B1L101S, U17-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.38
Std Dev	3.747

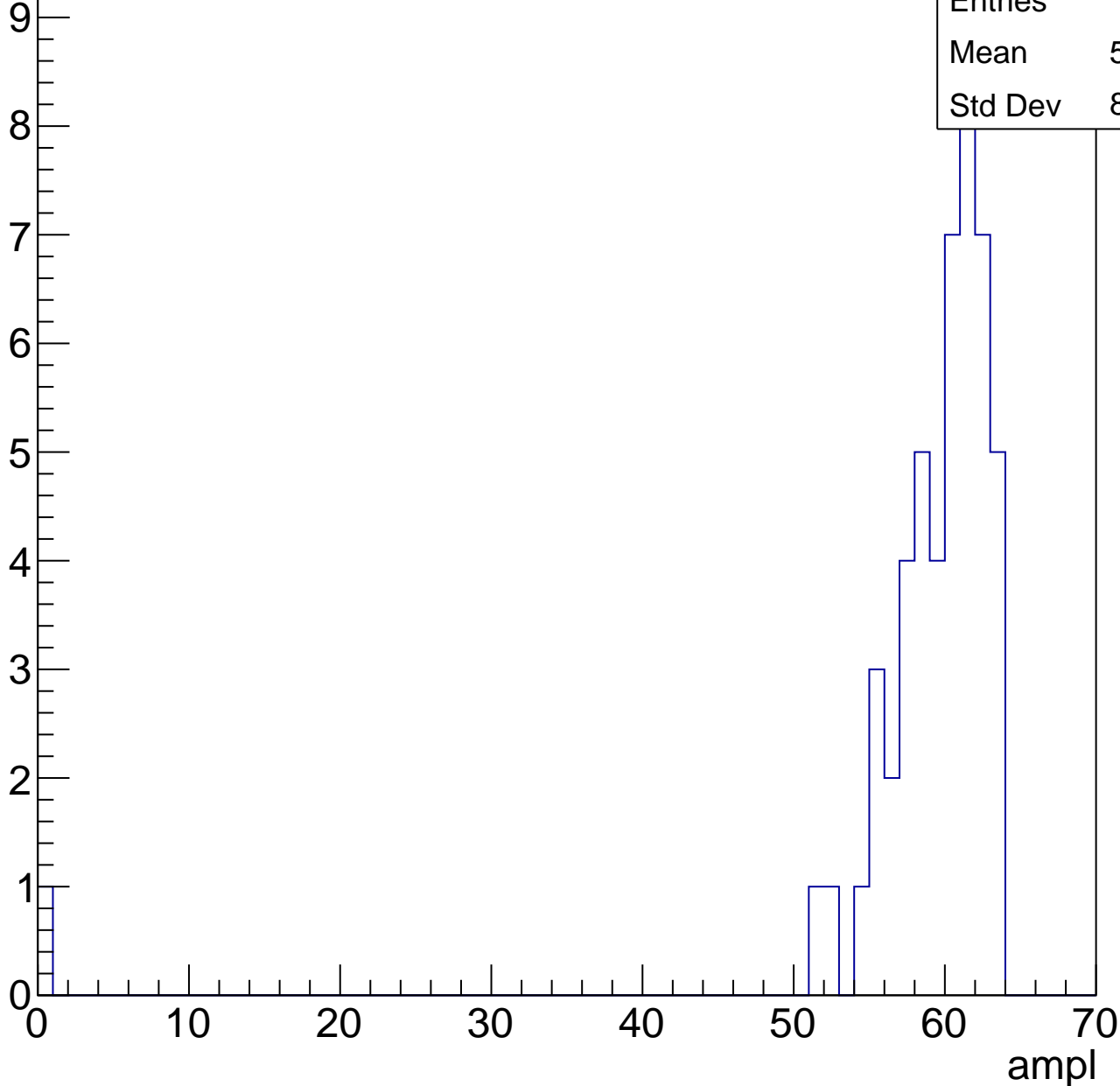


B1L101S, U17-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.12
Std Dev	8.779



B1L101S, U17-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch74, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	28.18
Std Dev	3.495

Gaus mean : 28.1138

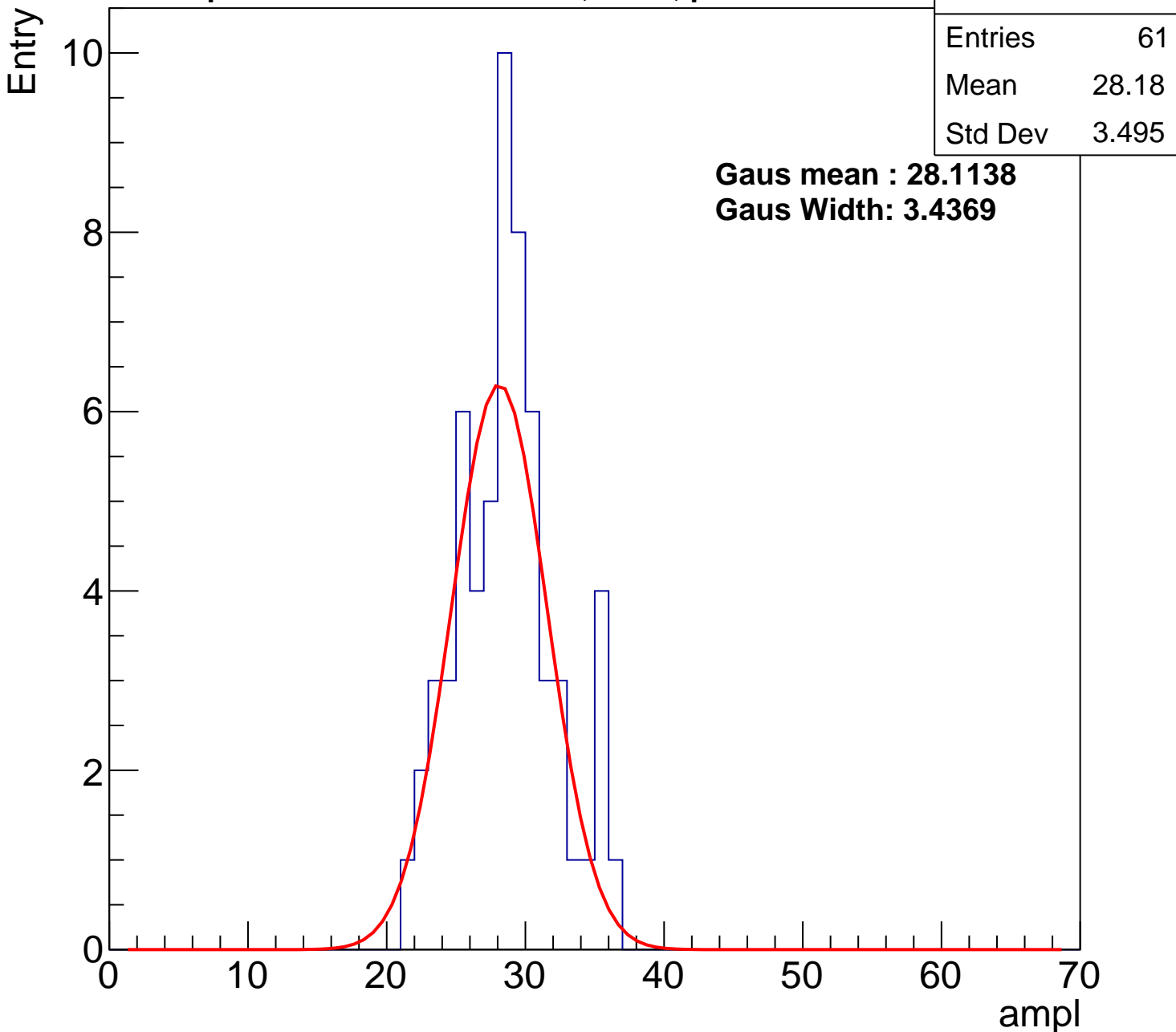
Gaus Width: 3.4369

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch74, adc1

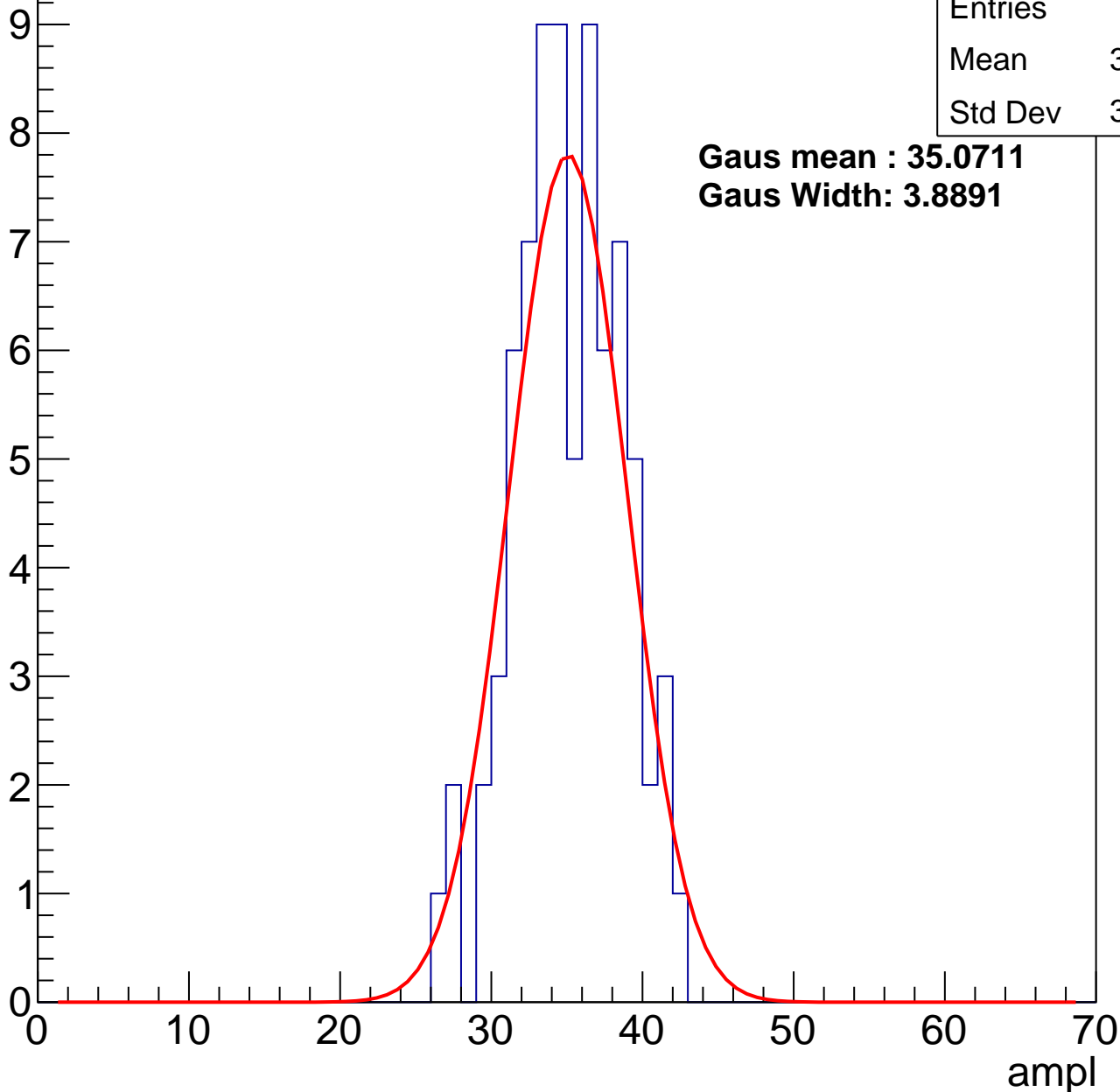
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	34.65
Std Dev	3.482

Gaus mean : 35.0711

Gaus Width: 3.8891



B1L101S, U17-ch74, adc2

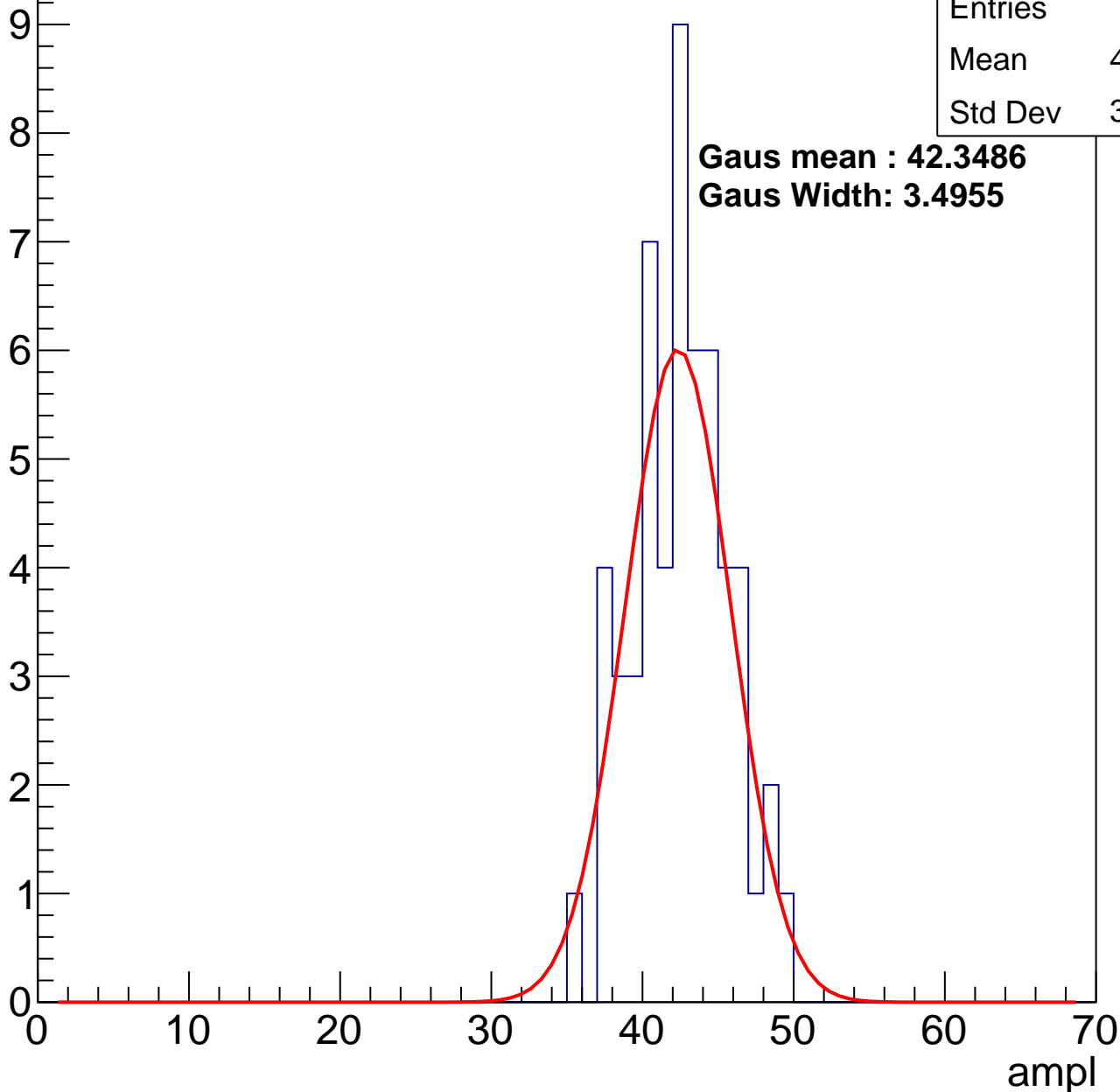
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.07
Std Dev	3.109

Gaus mean : 42.3486

Gaus Width: 3.4955

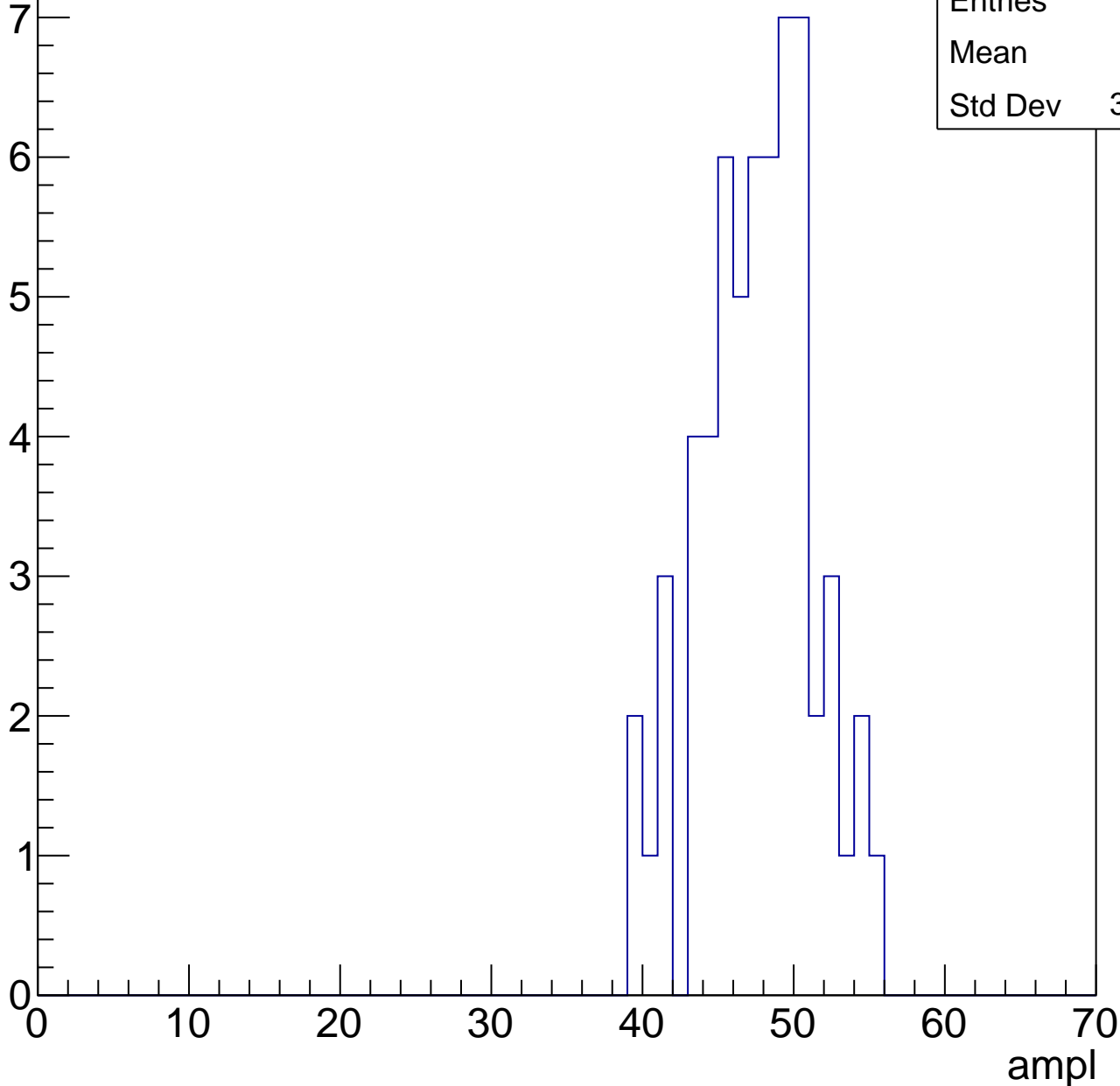


B1L101S, U17-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	47.1
Std Dev	3.686

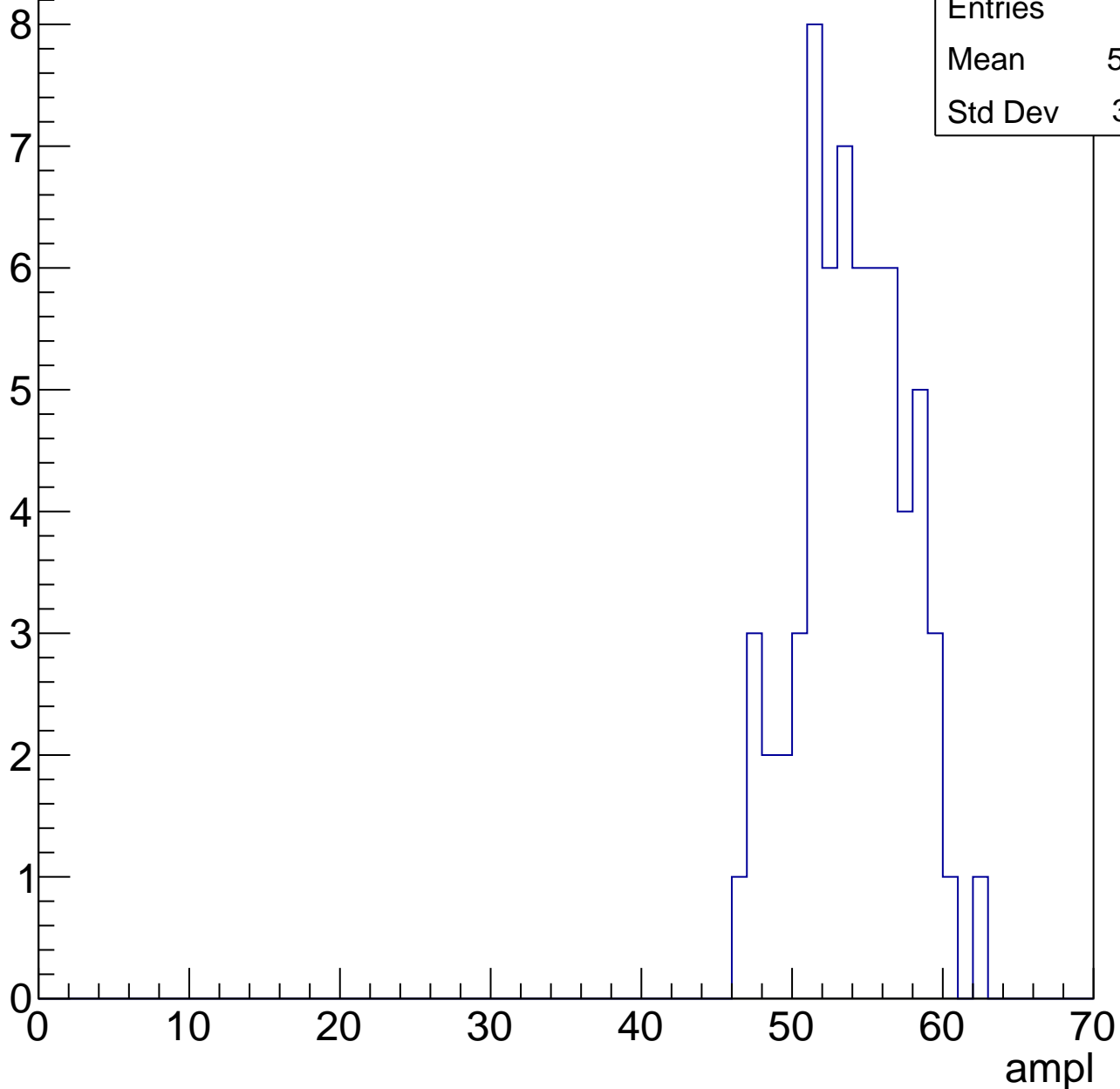


B1L101S, U17-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

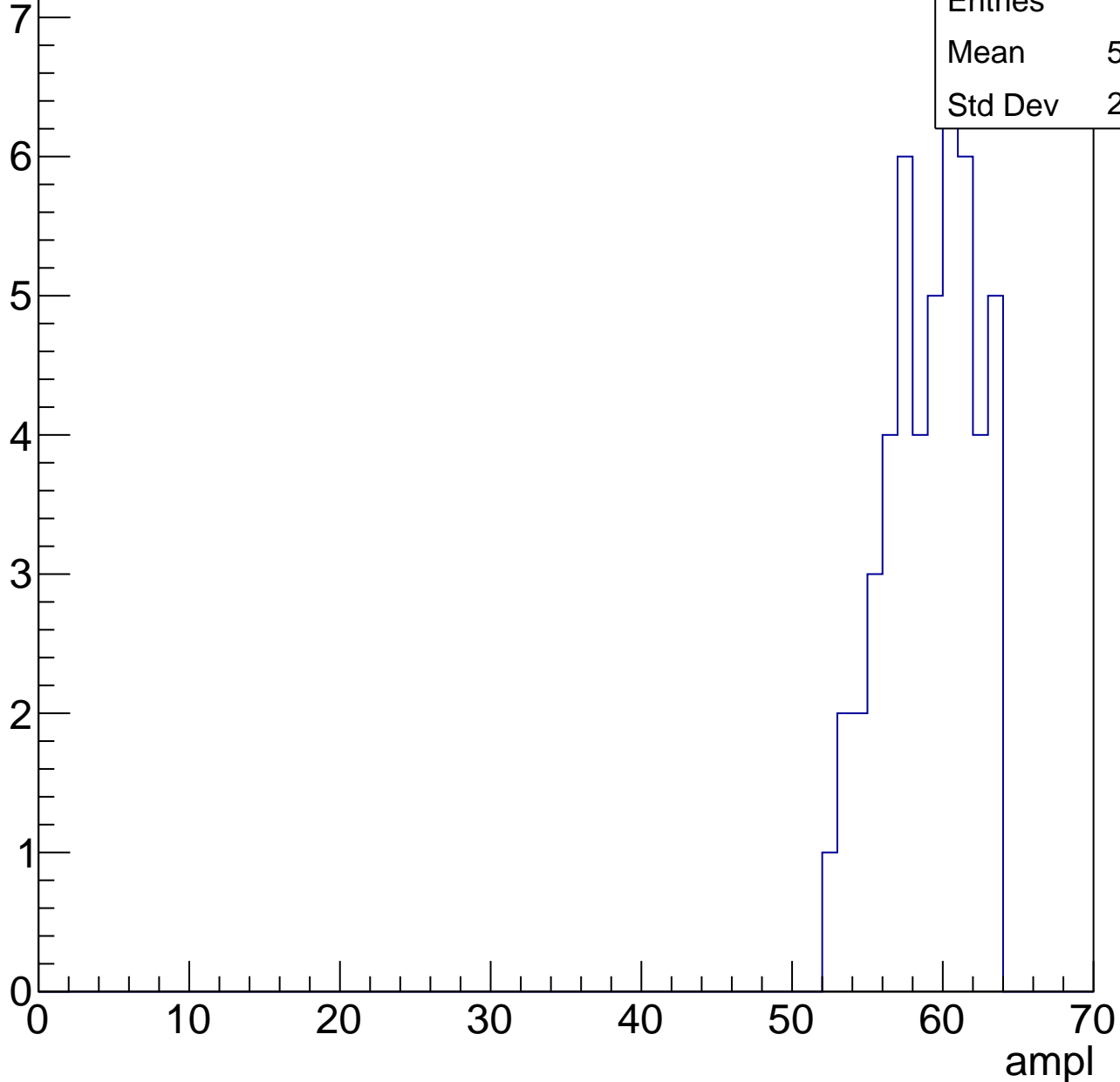
Entries	64
Mean	53.58
Std Dev	3.521



B1L101S, U17-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



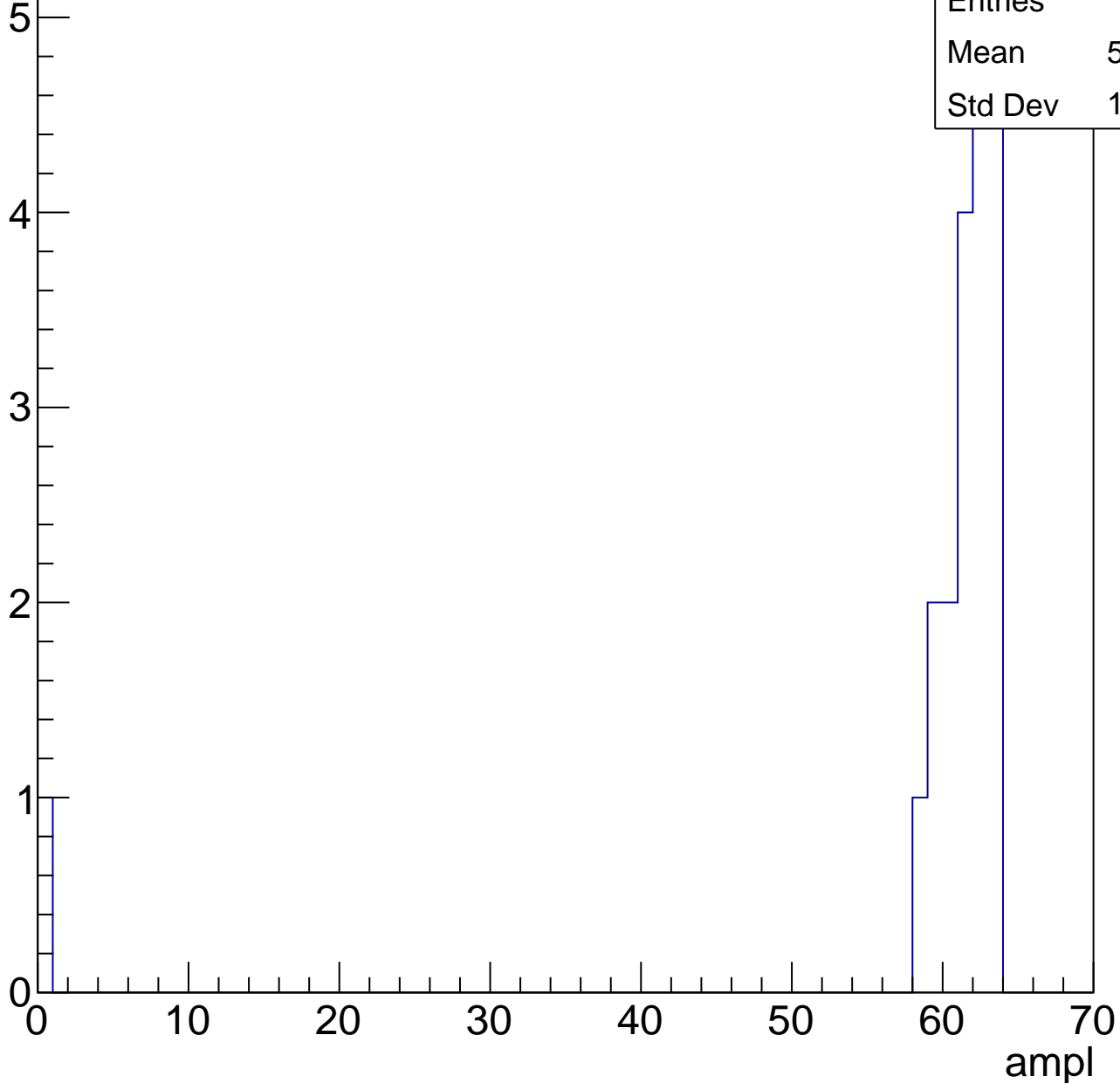
Entries	49
Mean	58.63
Std Dev	2.939

B1L101S, U17-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	58.25
Std Dev	13.44



B1L101S, U17-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch75, adc0

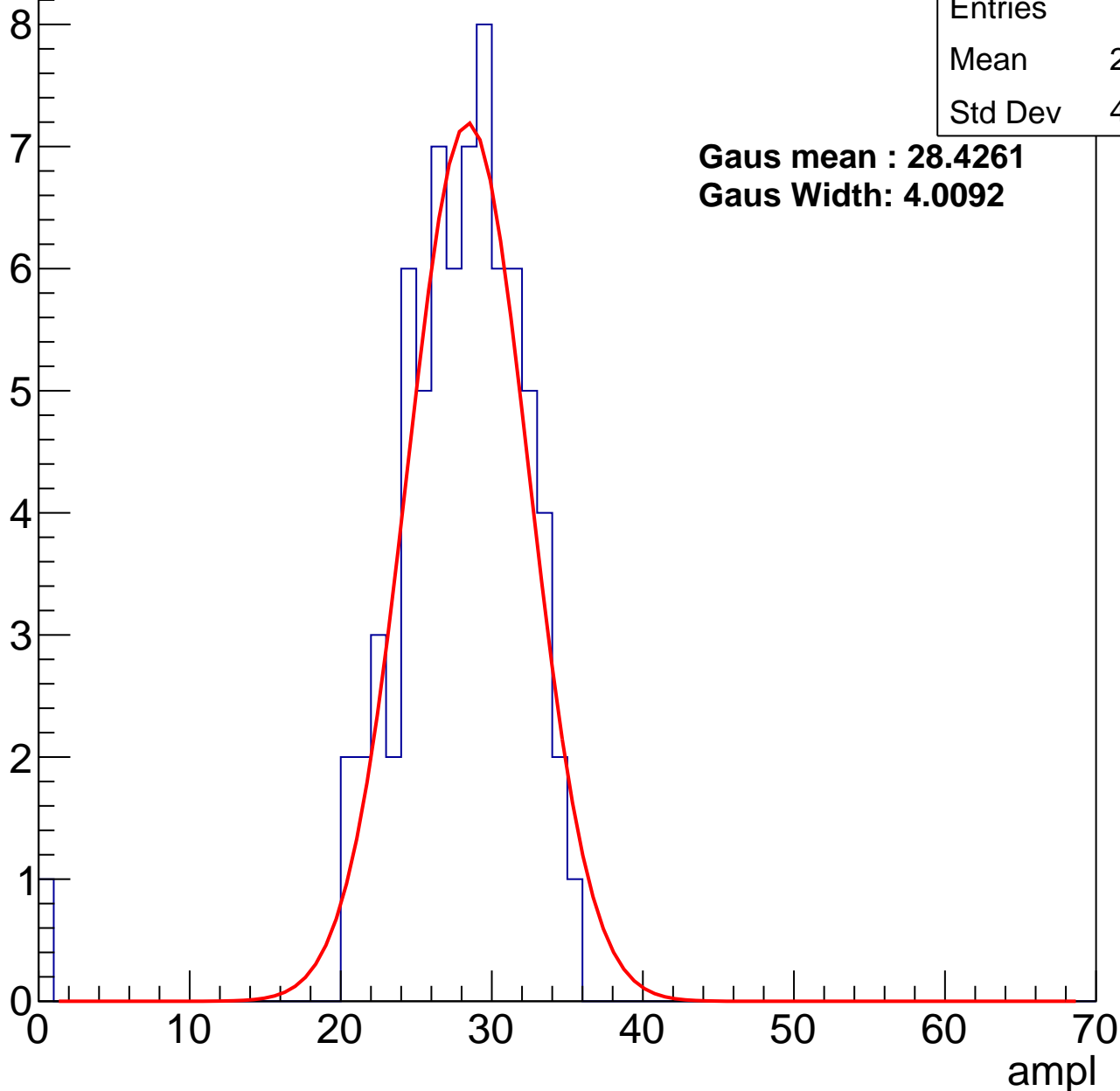
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.34
Std Dev	4.824

Gaus mean : 28.4261

Gaus Width: 4.0092



B1L101S, U17-ch75, adc1

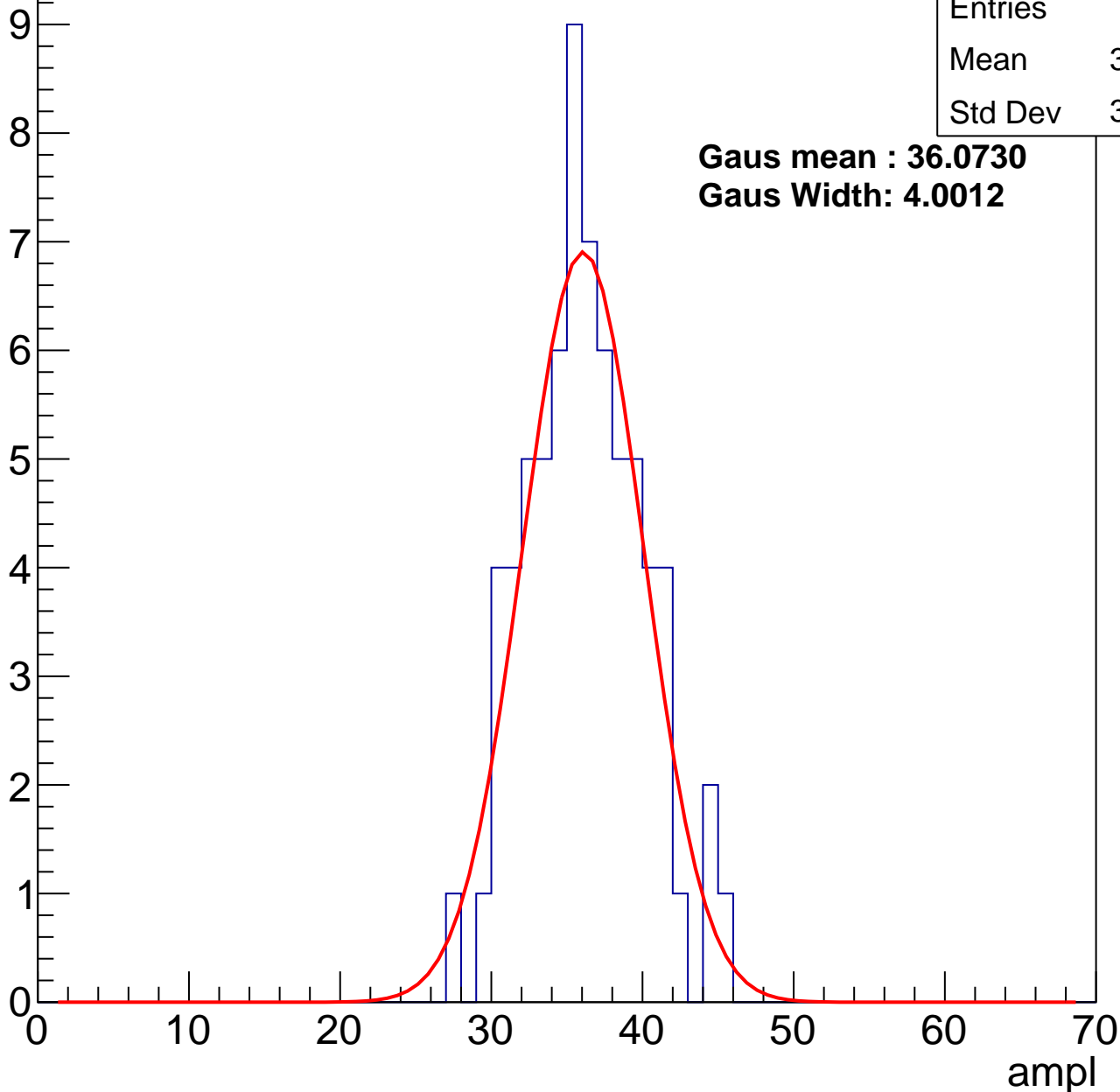
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.74
Std Dev	3.794

Gaus mean : 36.0730

Gaus Width: 4.0012



B1L101S, U17-ch75, adc2

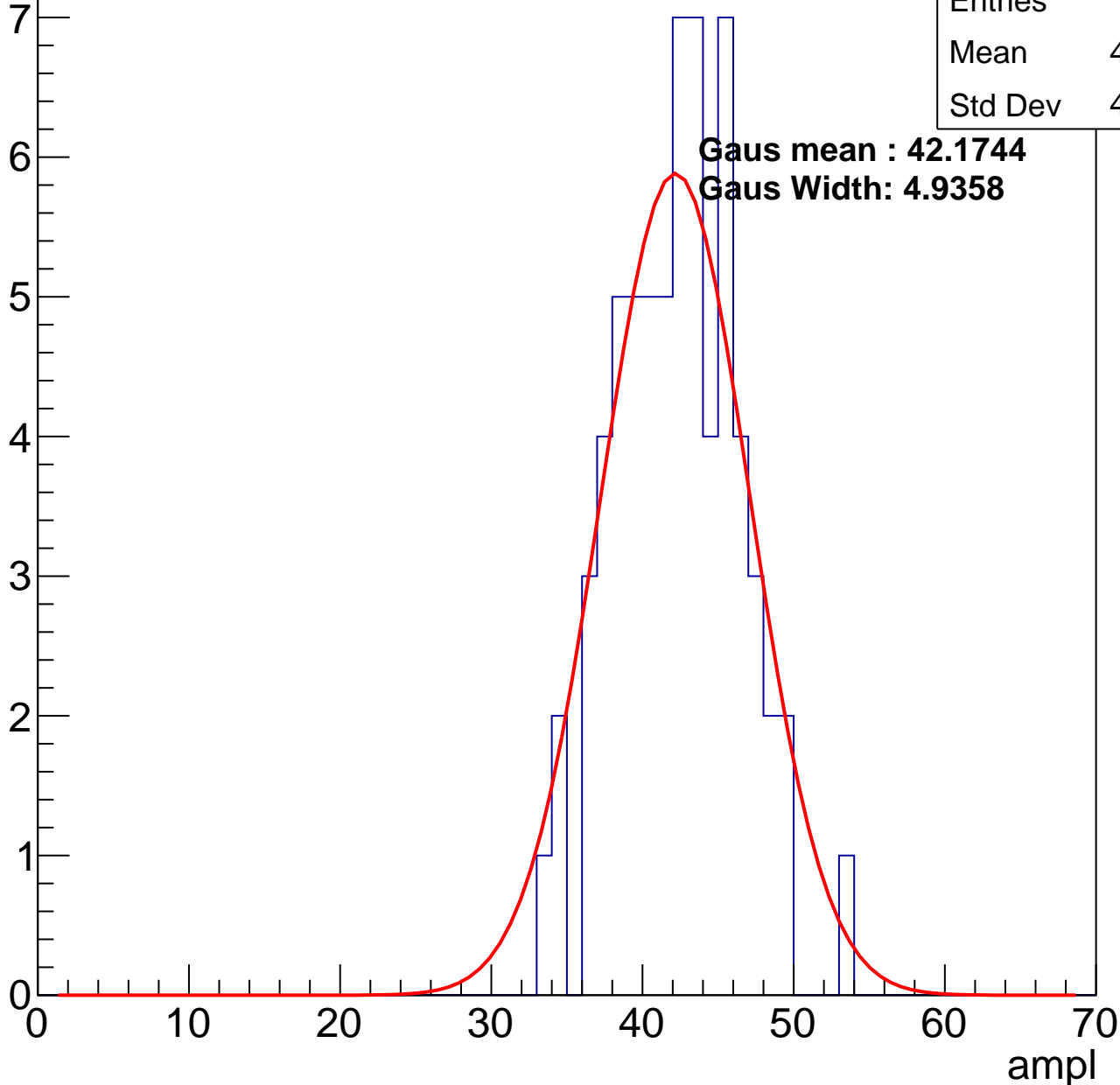
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	41.87
Std Dev	4.018

Gaus mean : 42.1744

Gaus Width: 4.9358

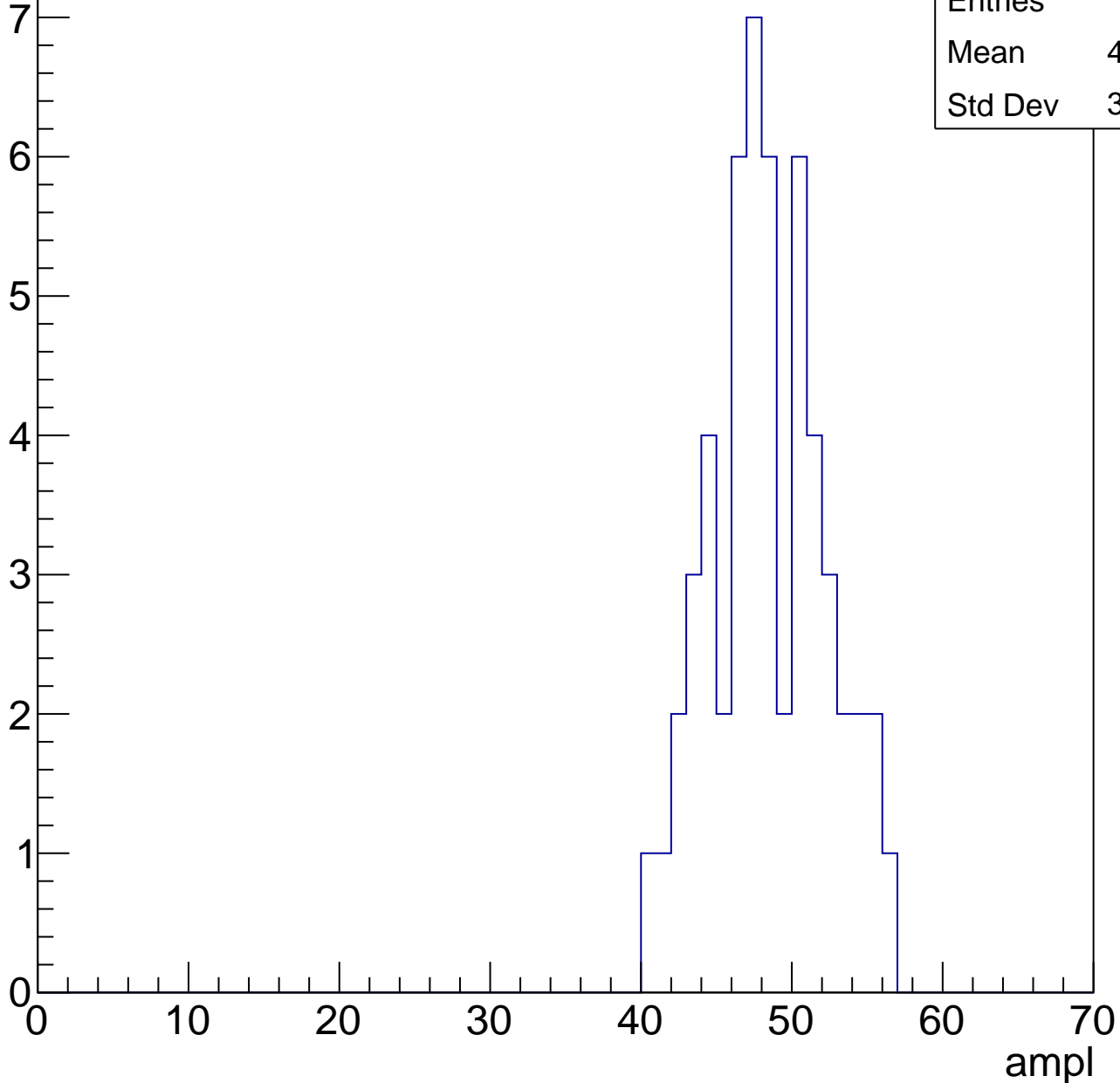


B1L101S, U17-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	47.98
Std Dev	3.769

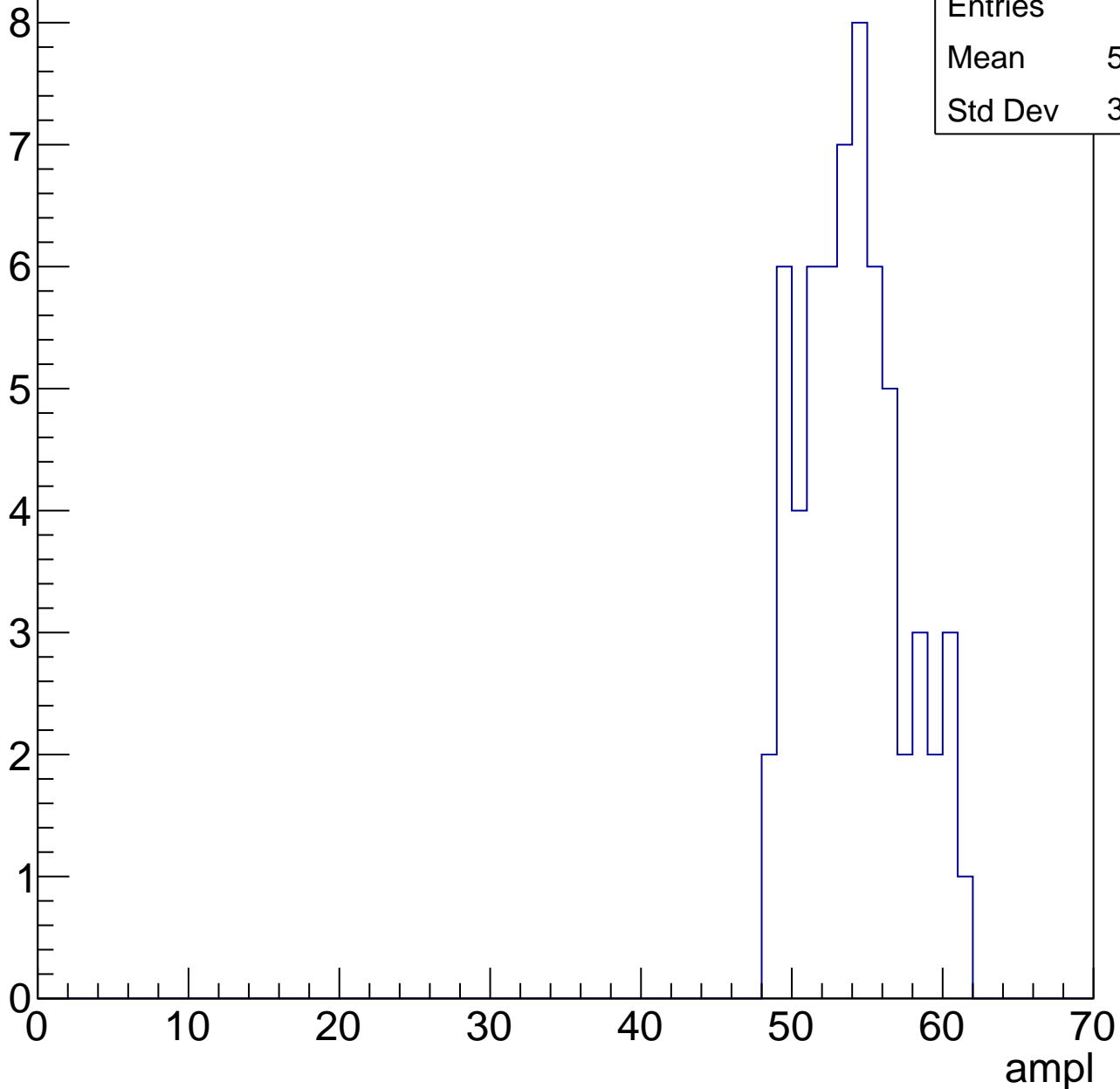


B1L101S, U17-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	53.57
Std Dev	3.287



B1L101S, U17-ch75, adc5

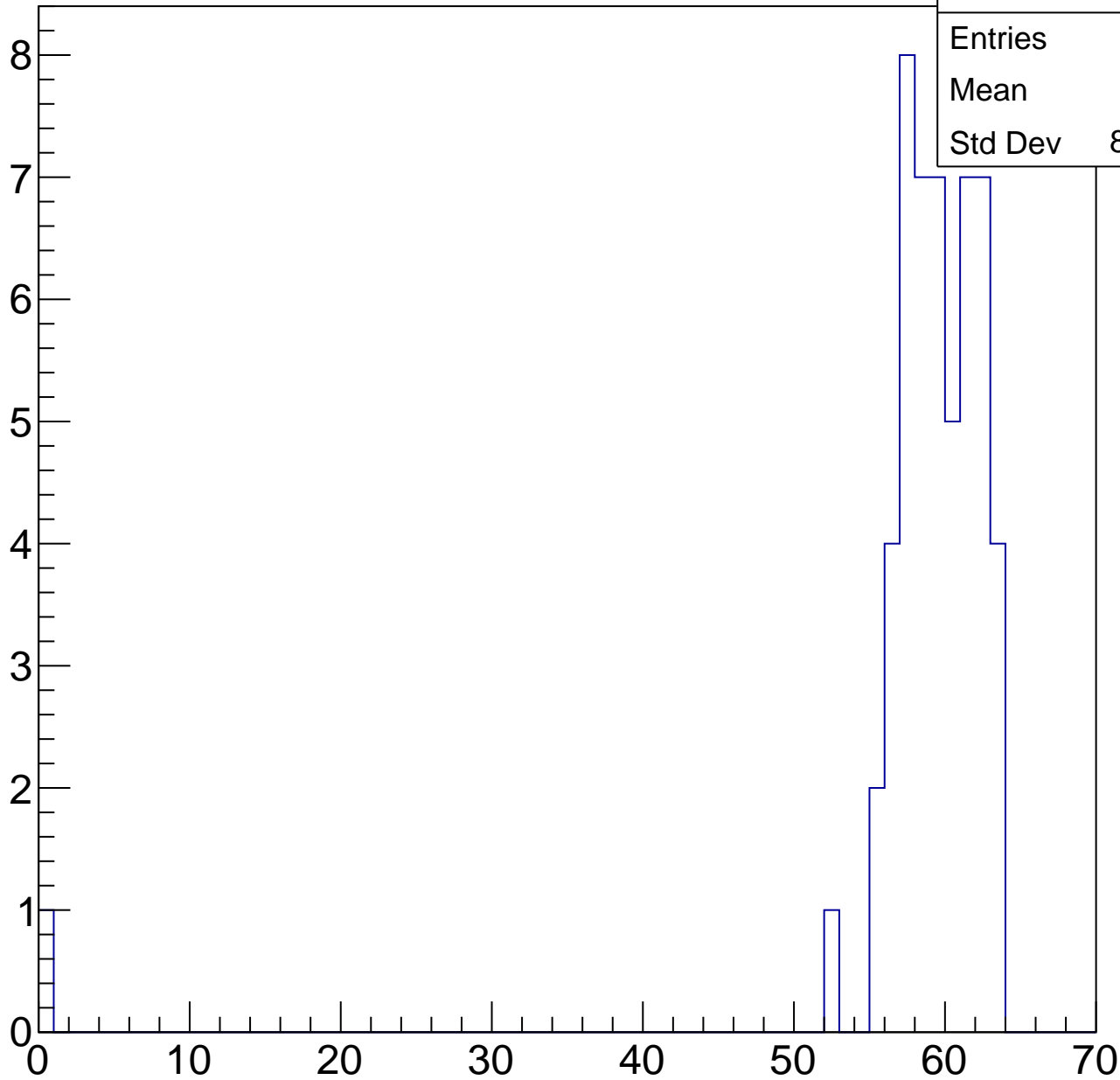
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58
Std Dev	8.405

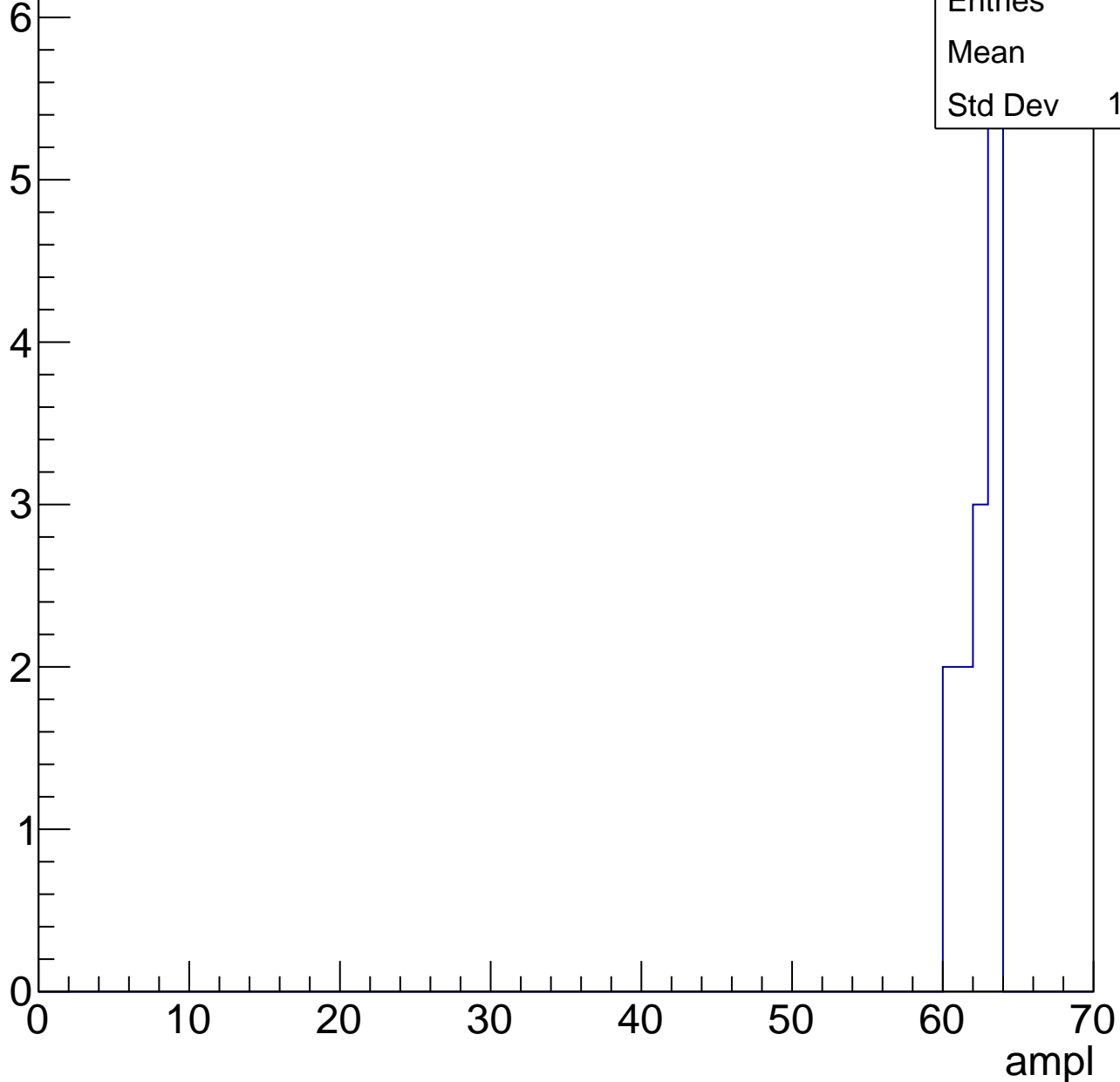
ampl



B1L101S, U17-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

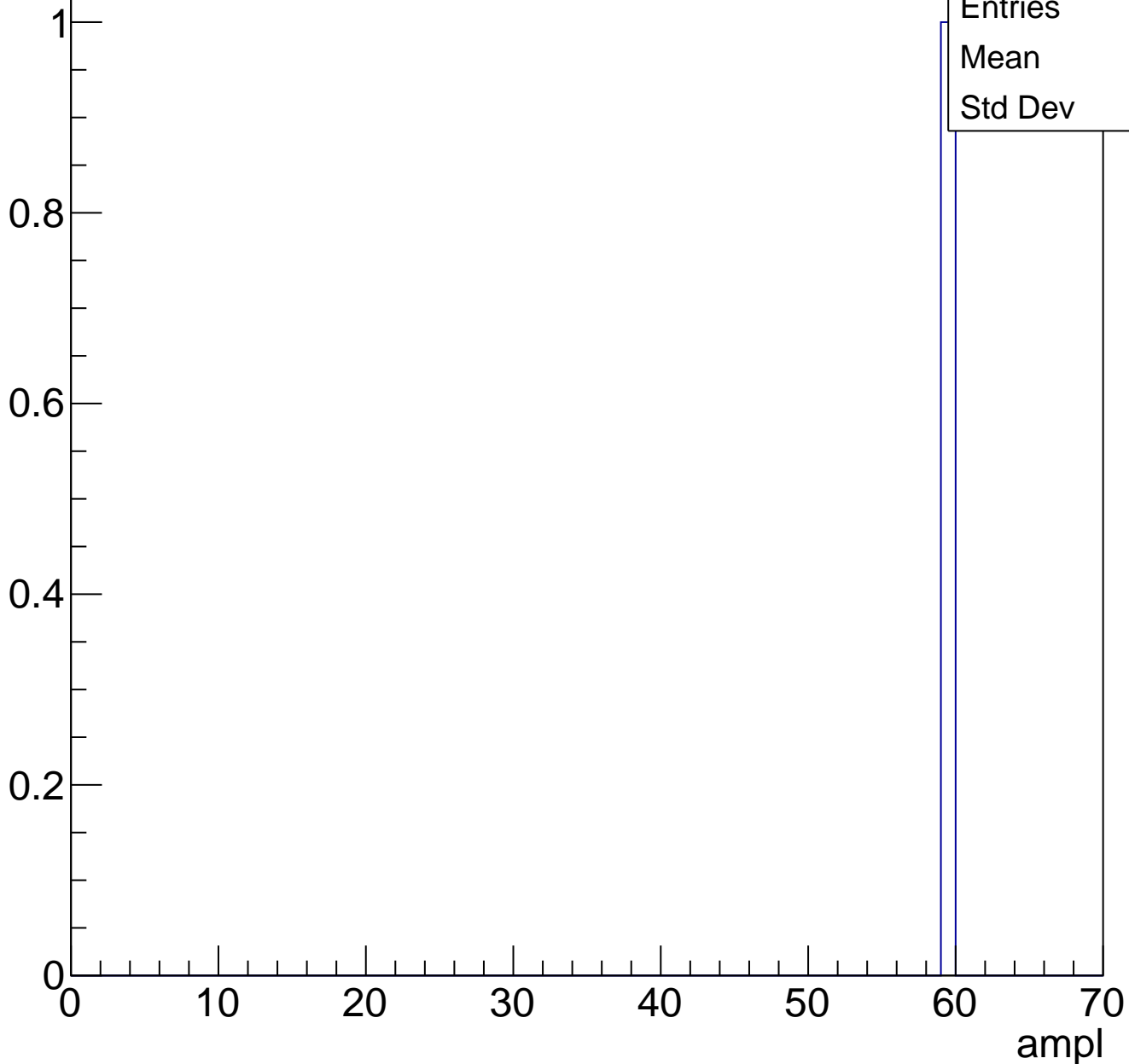


Entries	13
Mean	62
Std Dev	1.109

B1L101S, U17-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch76, adc0

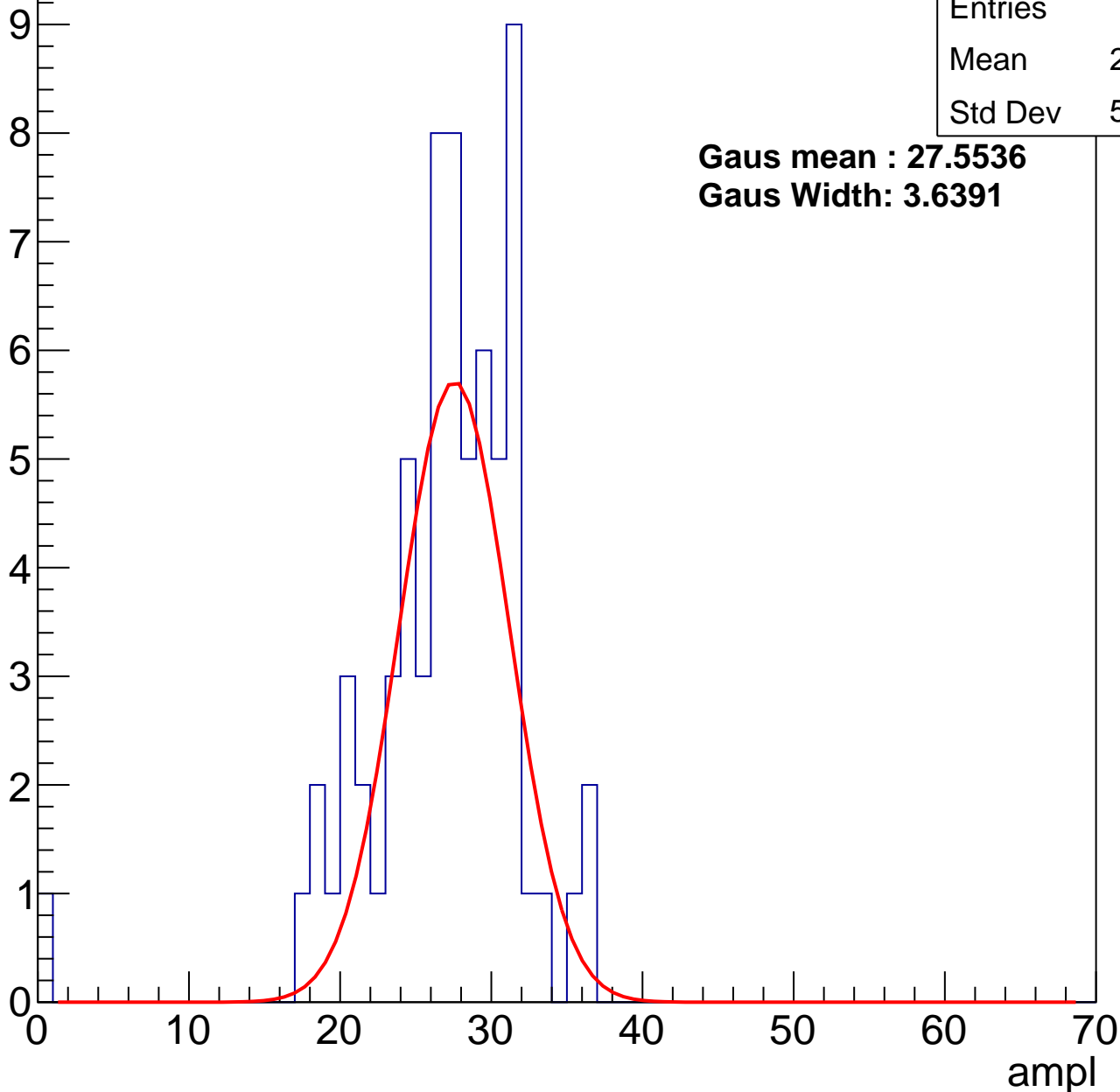
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	26.46
Std Dev	5.282

Gaus mean : 27.5536

Gaus Width: 3.6391



B1L101S, U17-ch76, adc1

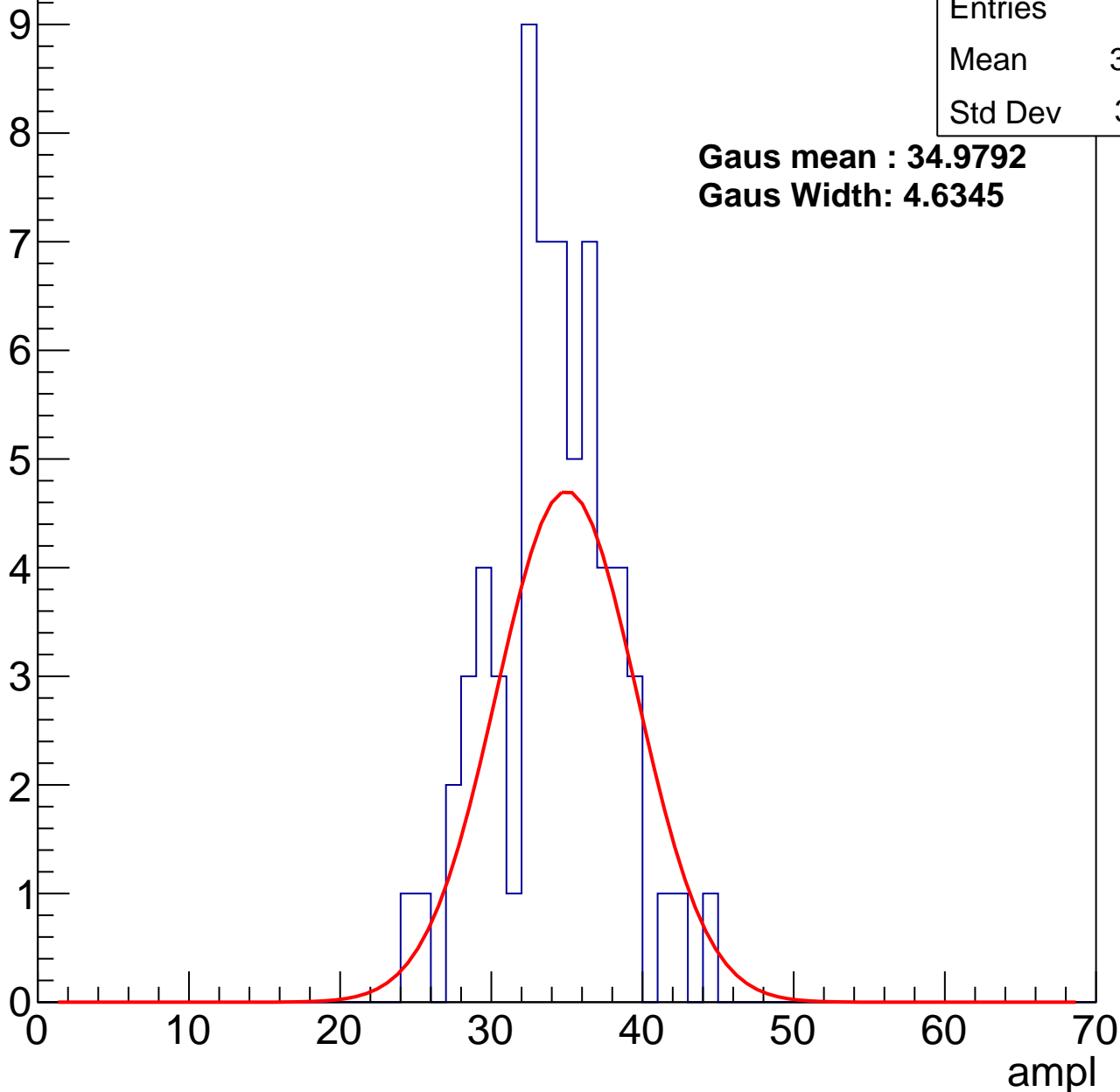
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	33.62
Std Dev	3.951

Gaus mean : 34.9792

Gaus Width: 4.6345



B1L101S, U17-ch76, adc2

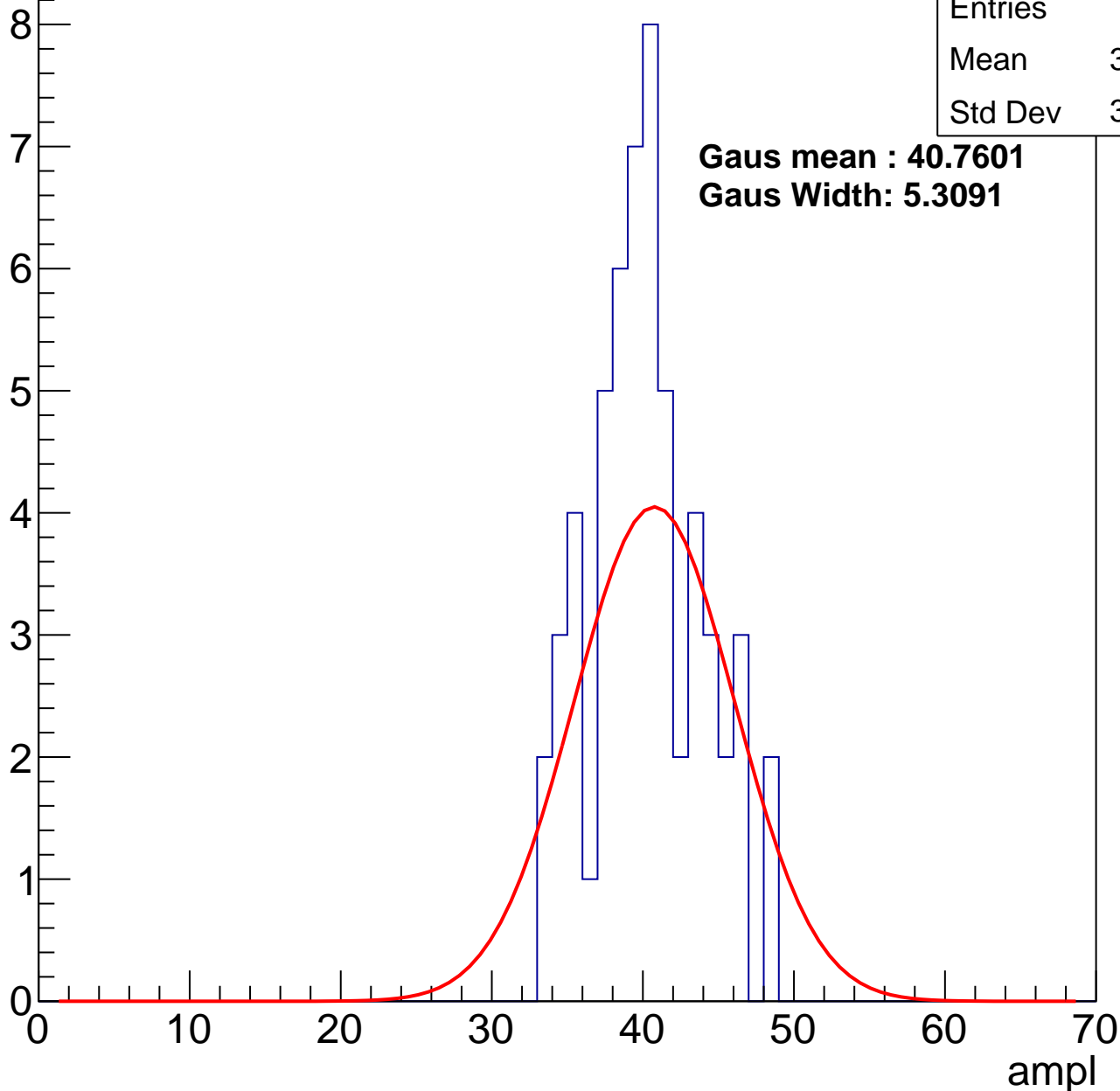
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	39.77
Std Dev	3.695

Gaus mean : 40.7601

Gaus Width: 5.3091

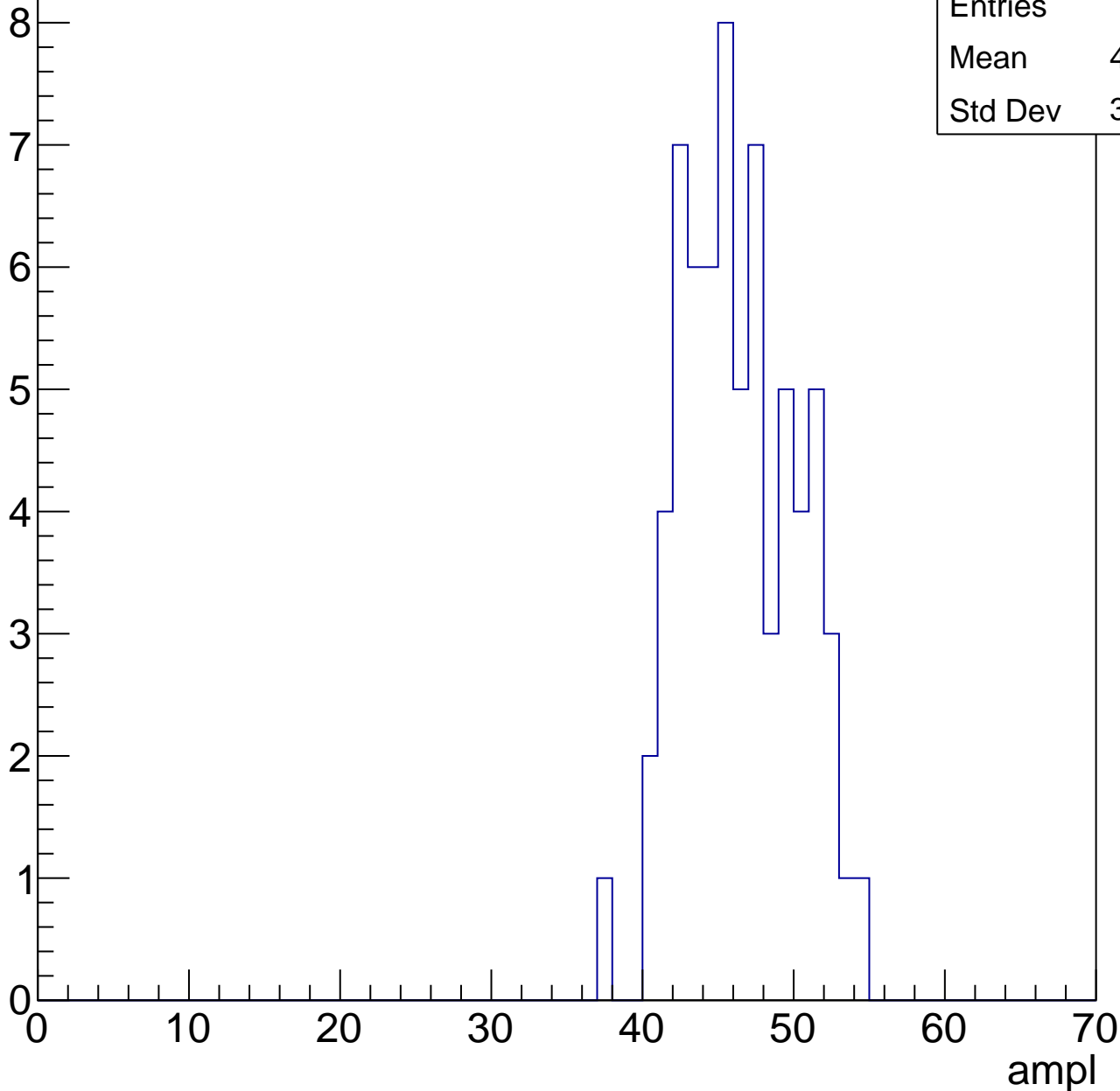


B1L101S, U17-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	45.93
Std Dev	3.687

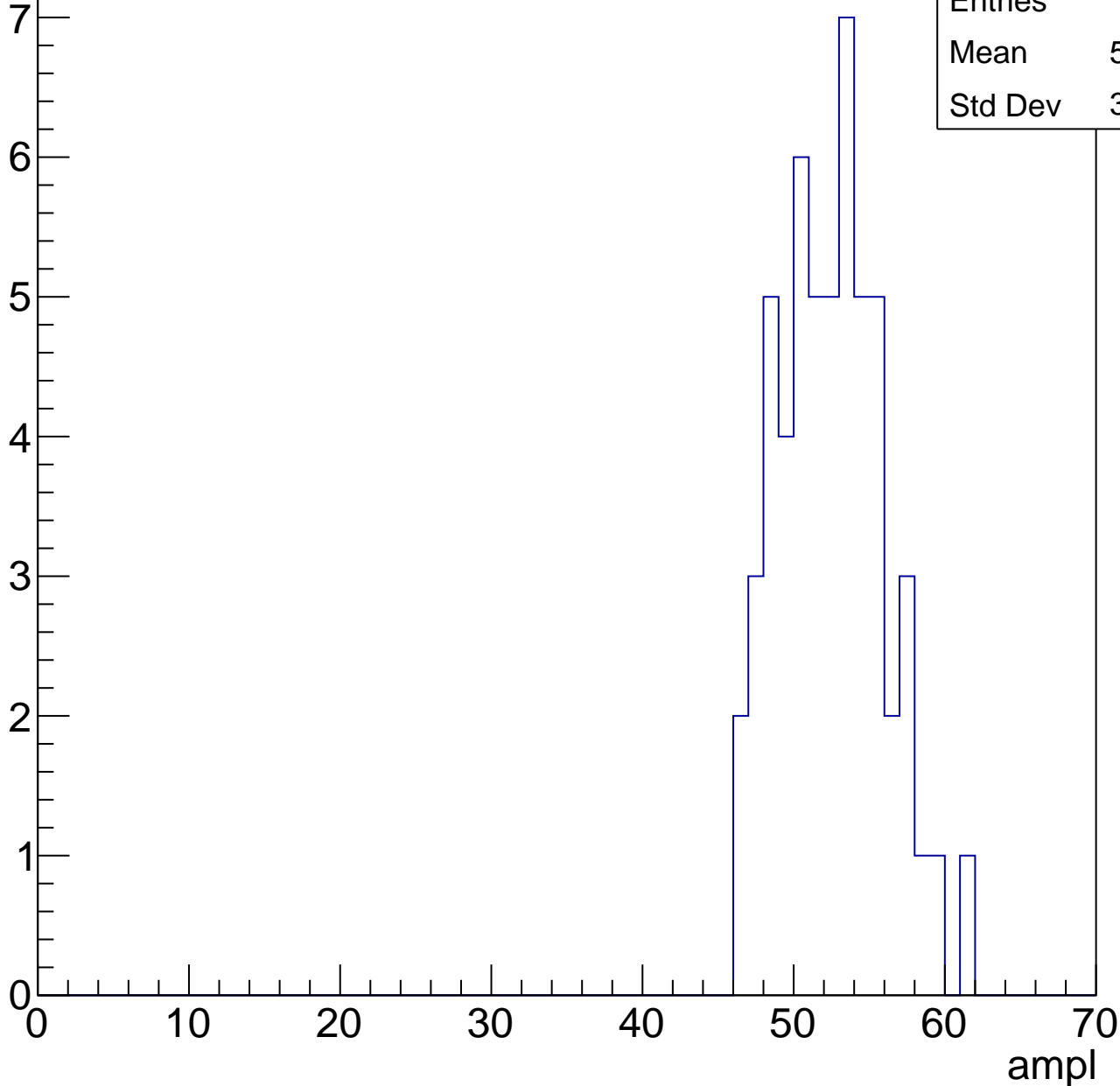


B1L101S, U17-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

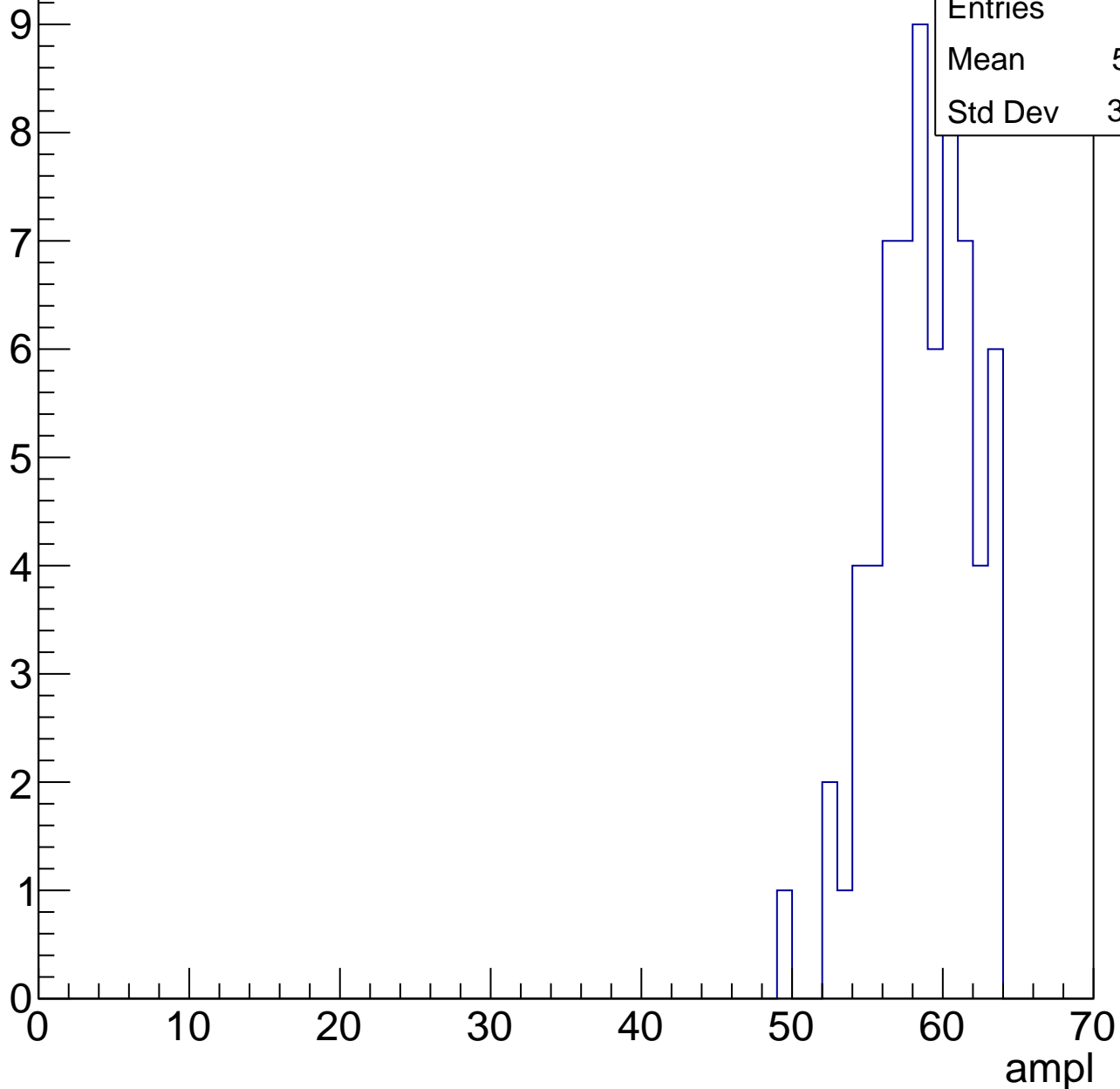
Entries	55
Mean	52.02
Std Dev	3.403



B1L101S, U17-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

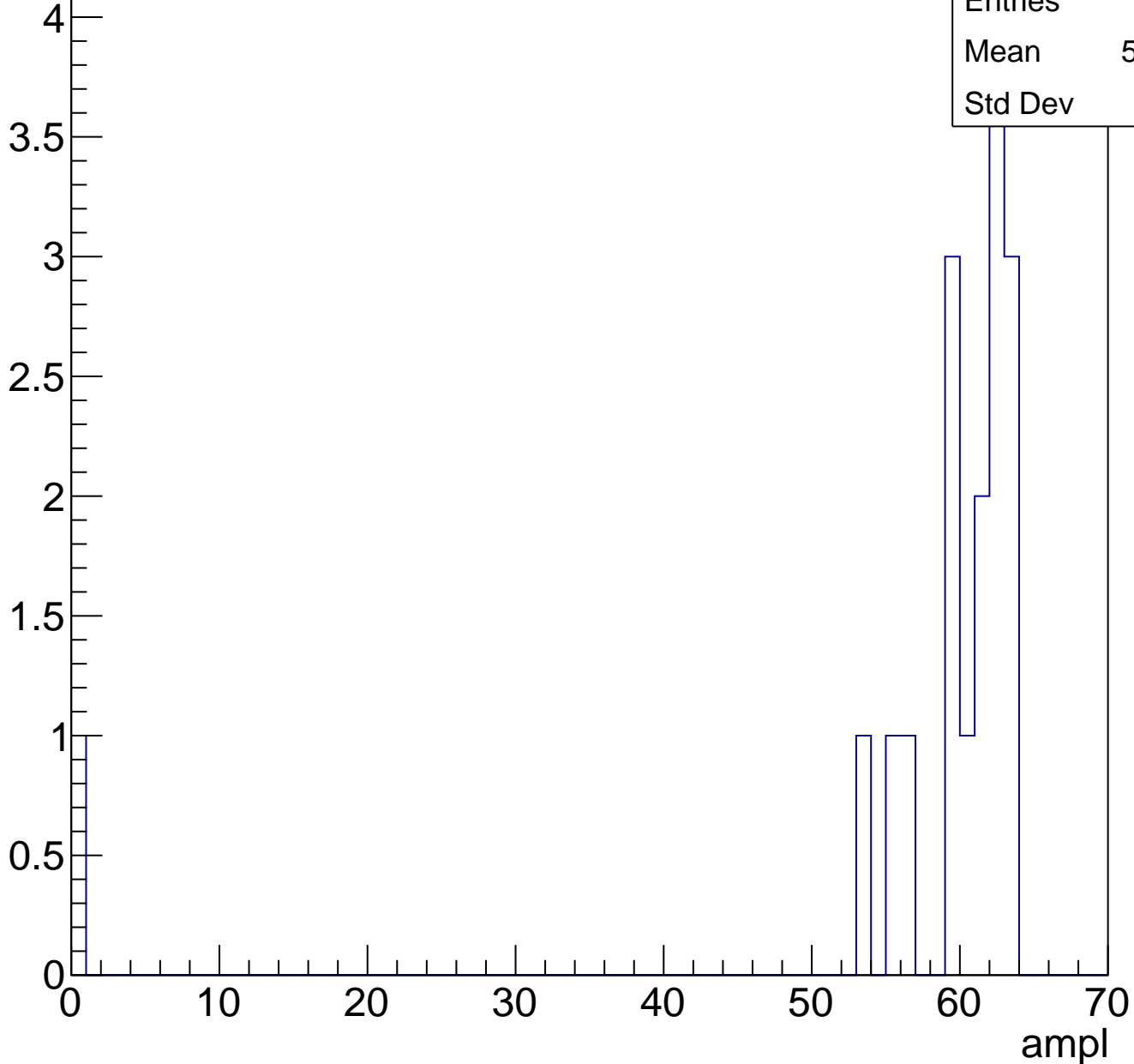


Entries	66
Mean	58.21
Std Dev	3.077

B1L101S, U17-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

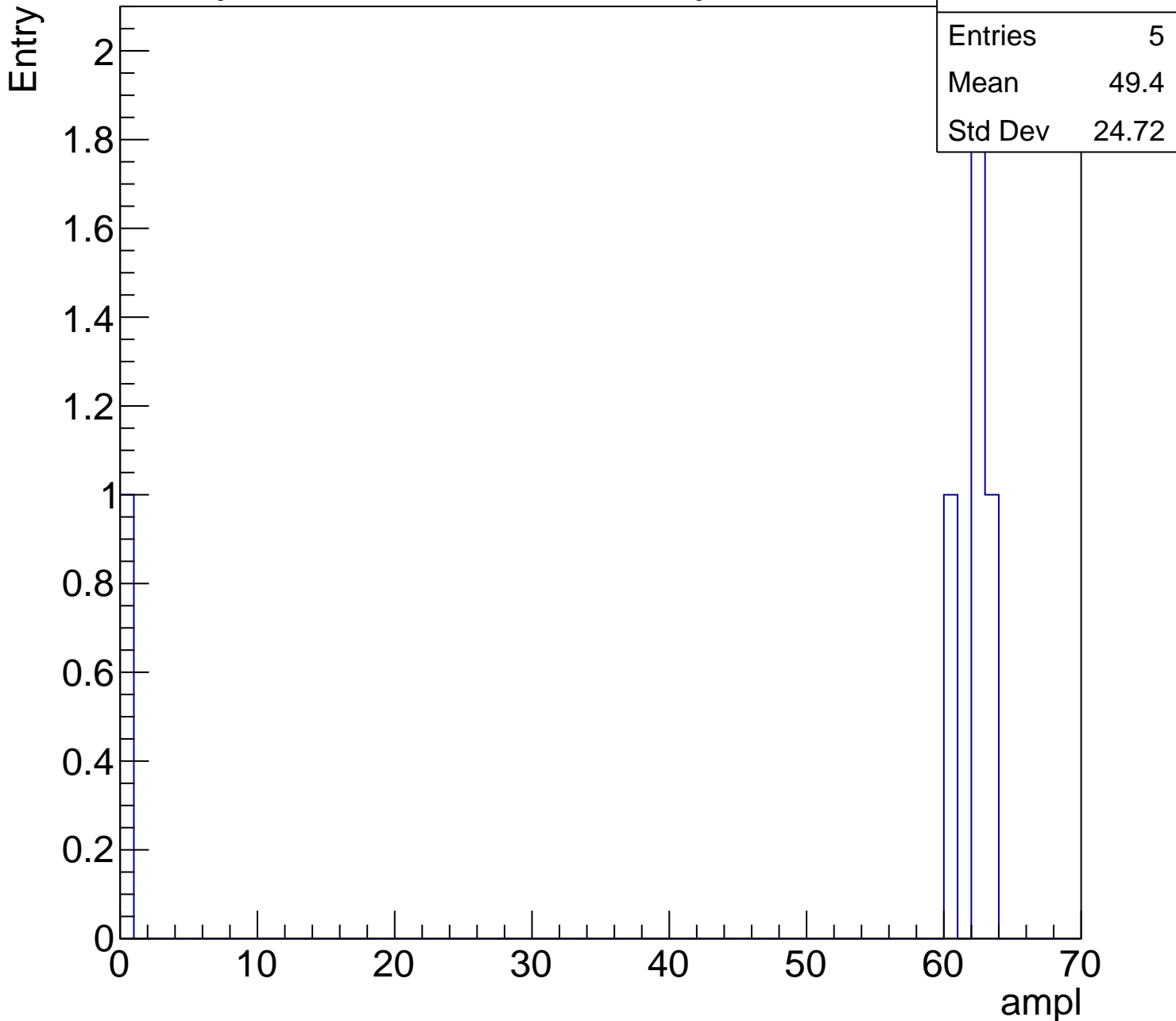
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.72

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch77, adc0

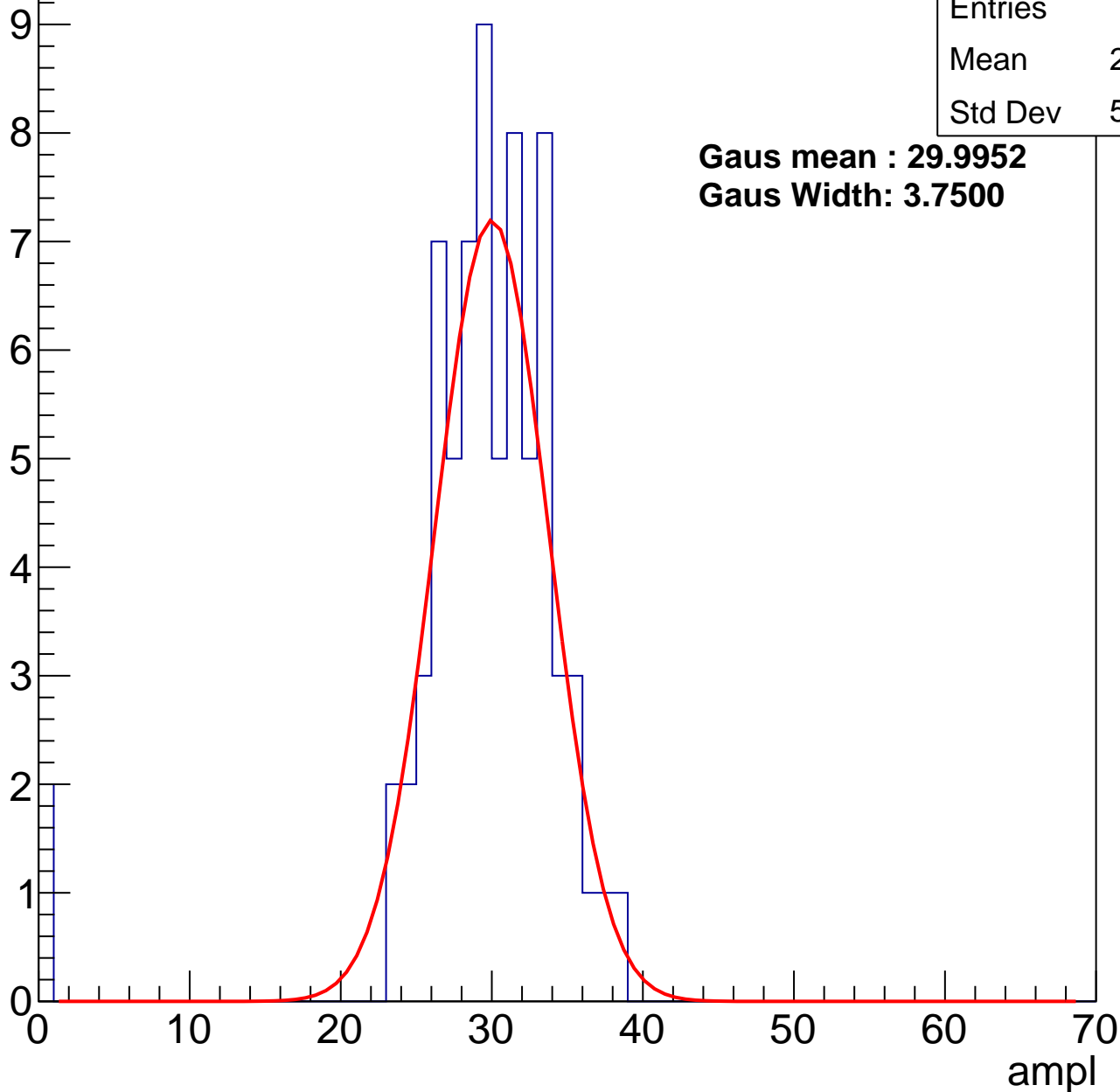
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.93
Std Dev	5.926

Gaus mean : 29.9952

Gaus Width: 3.7500



B1L101S, U17-ch77, adc1

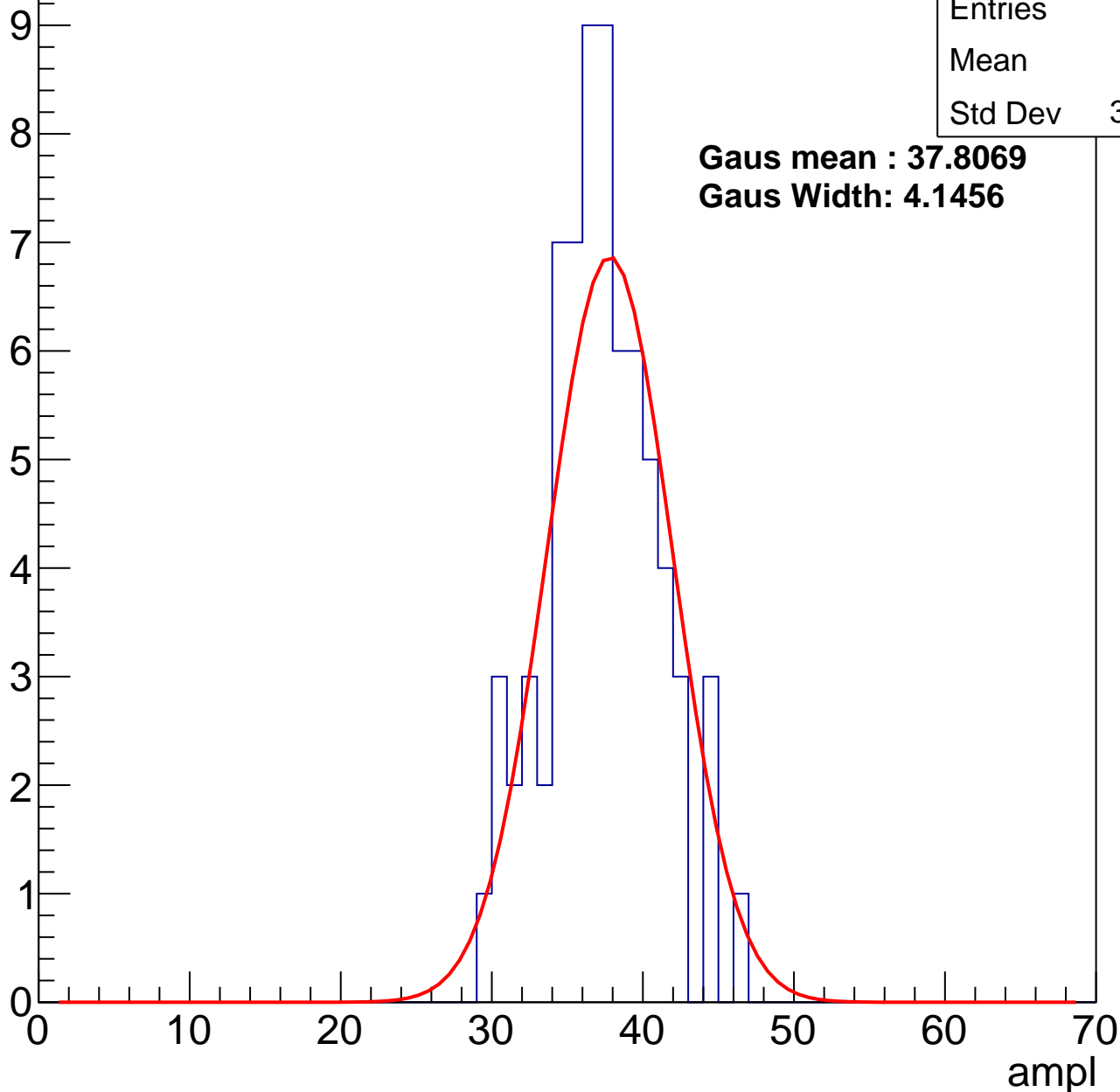
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.8
Std Dev	3.618

Gaus mean : 37.8069

Gaus Width: 4.1456



B1L101S, U17-ch77, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	43.7
Std Dev	3.71

Gaus mean : 43.3060

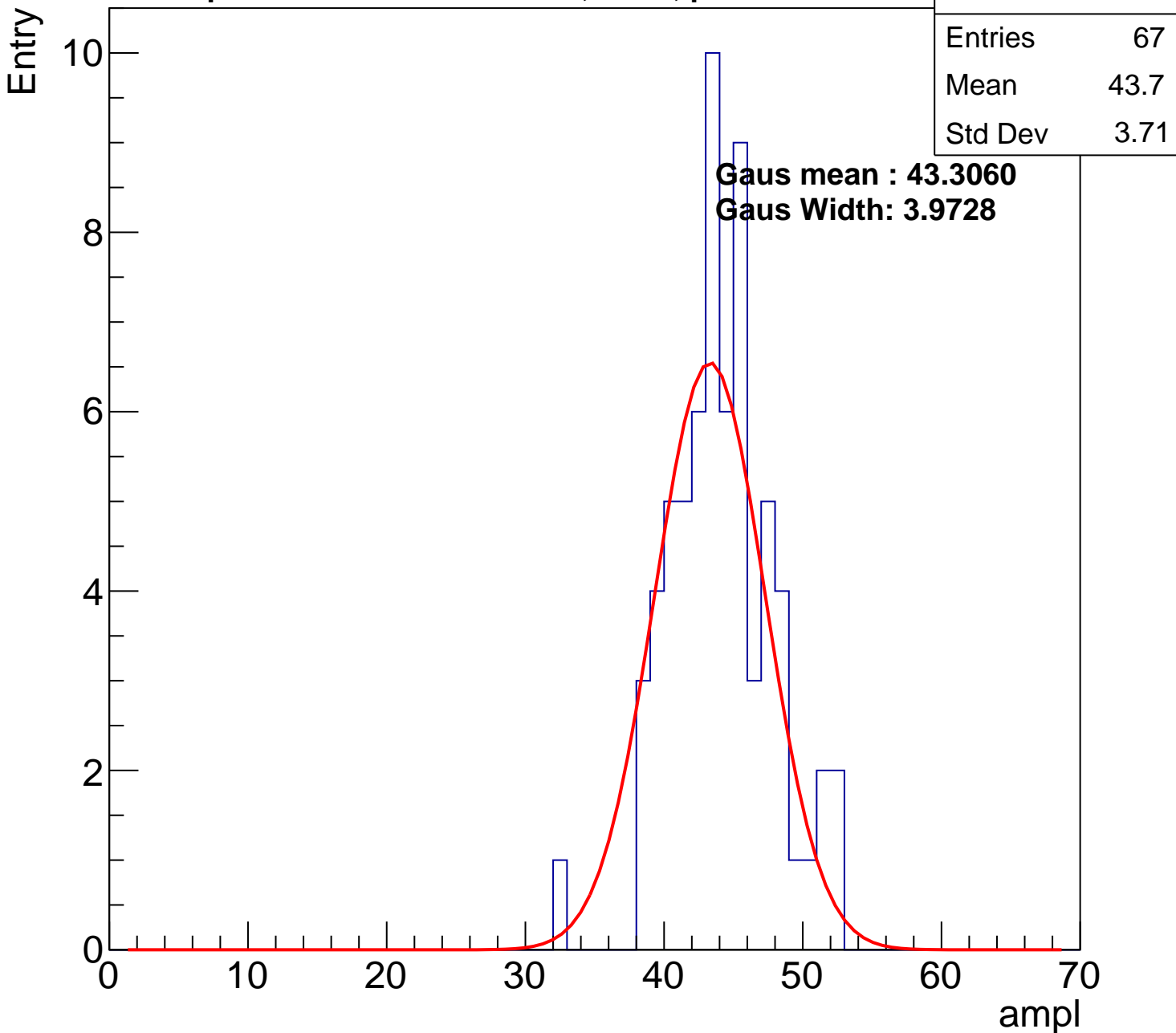
Gaus Width: 3.9728

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

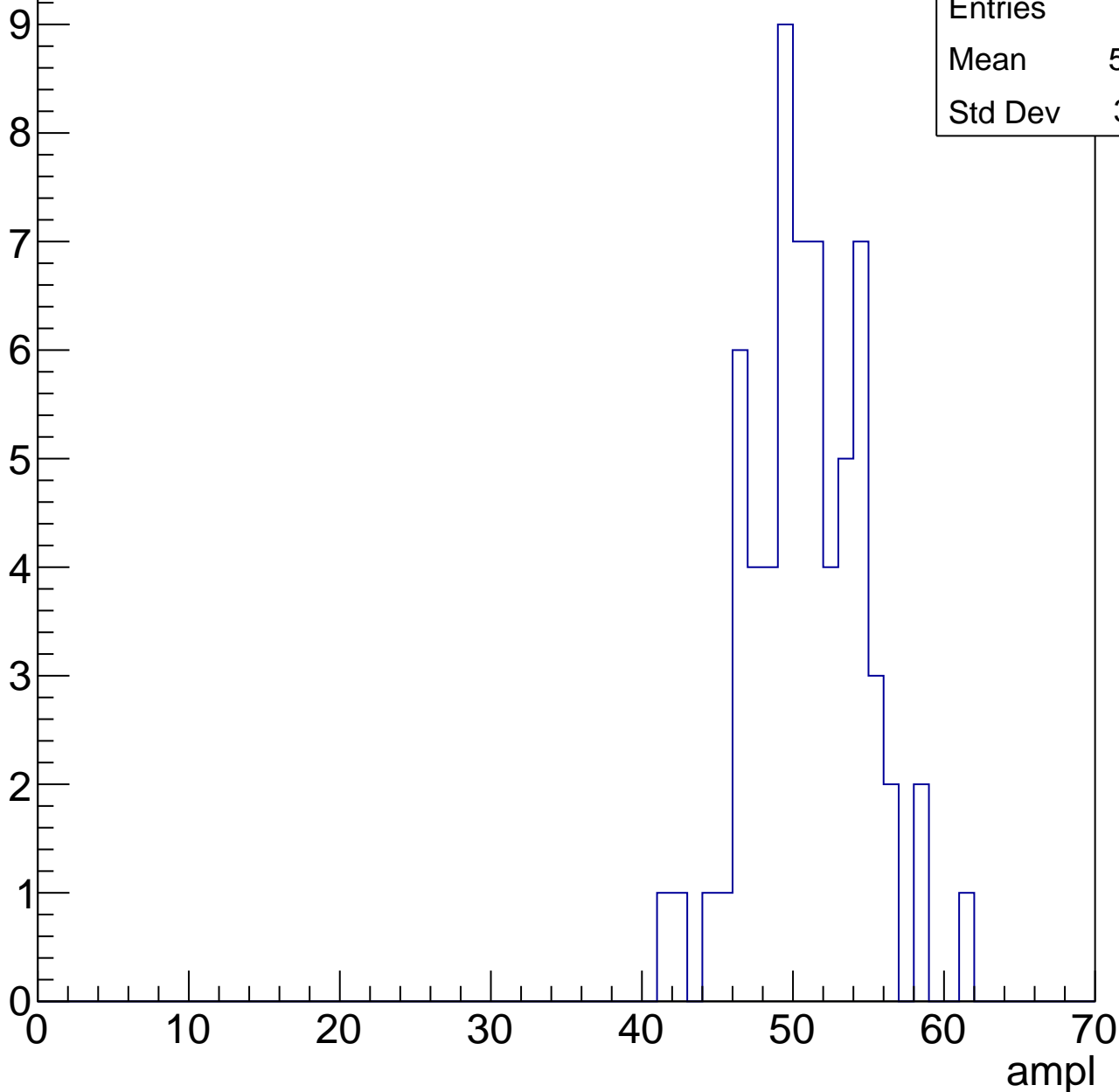


B1L101S, U17-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.48
Std Dev	3.771



B1L101S, U17-ch77, adc4

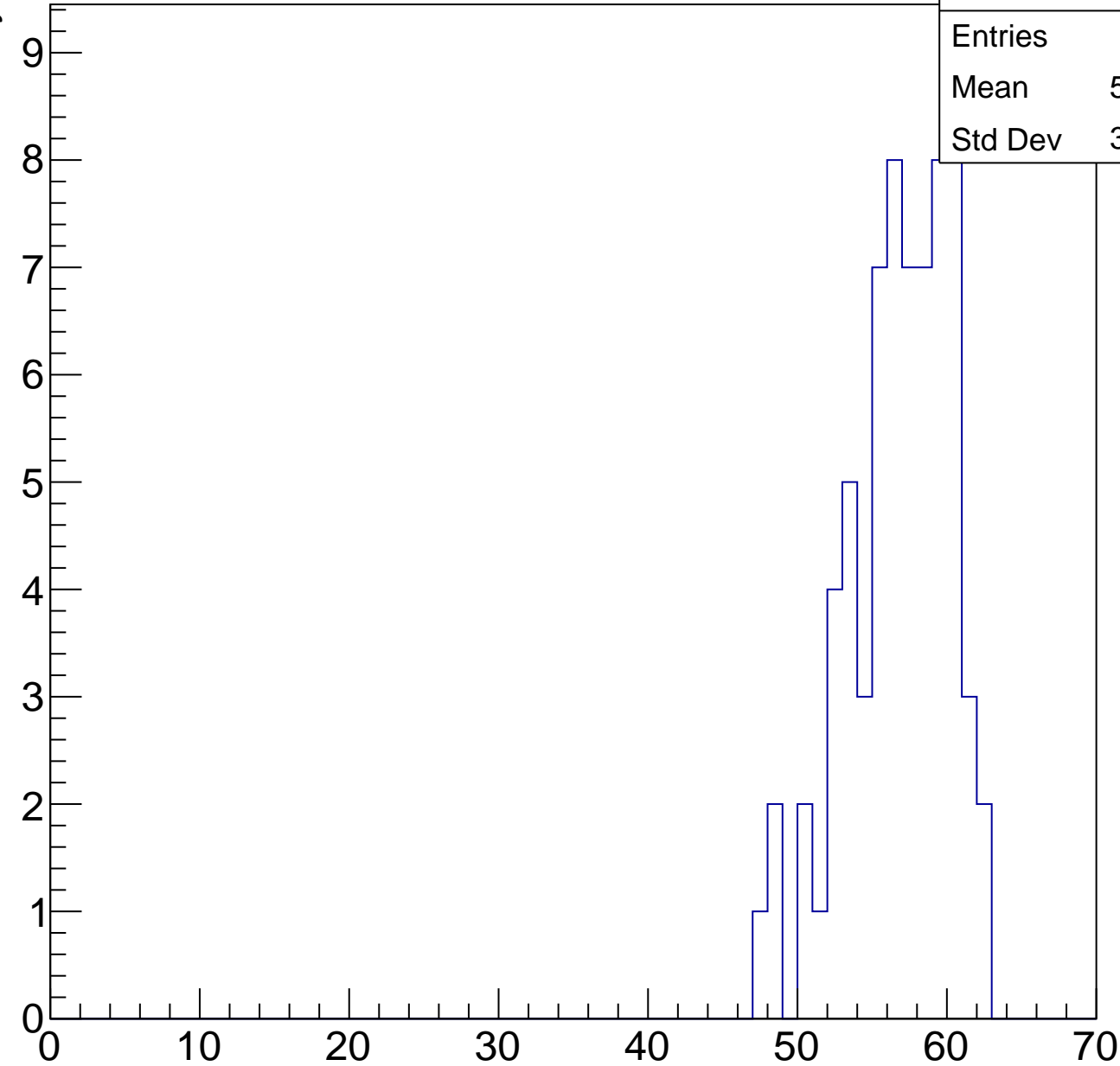
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	56.32
Std Dev	3.458

ampl

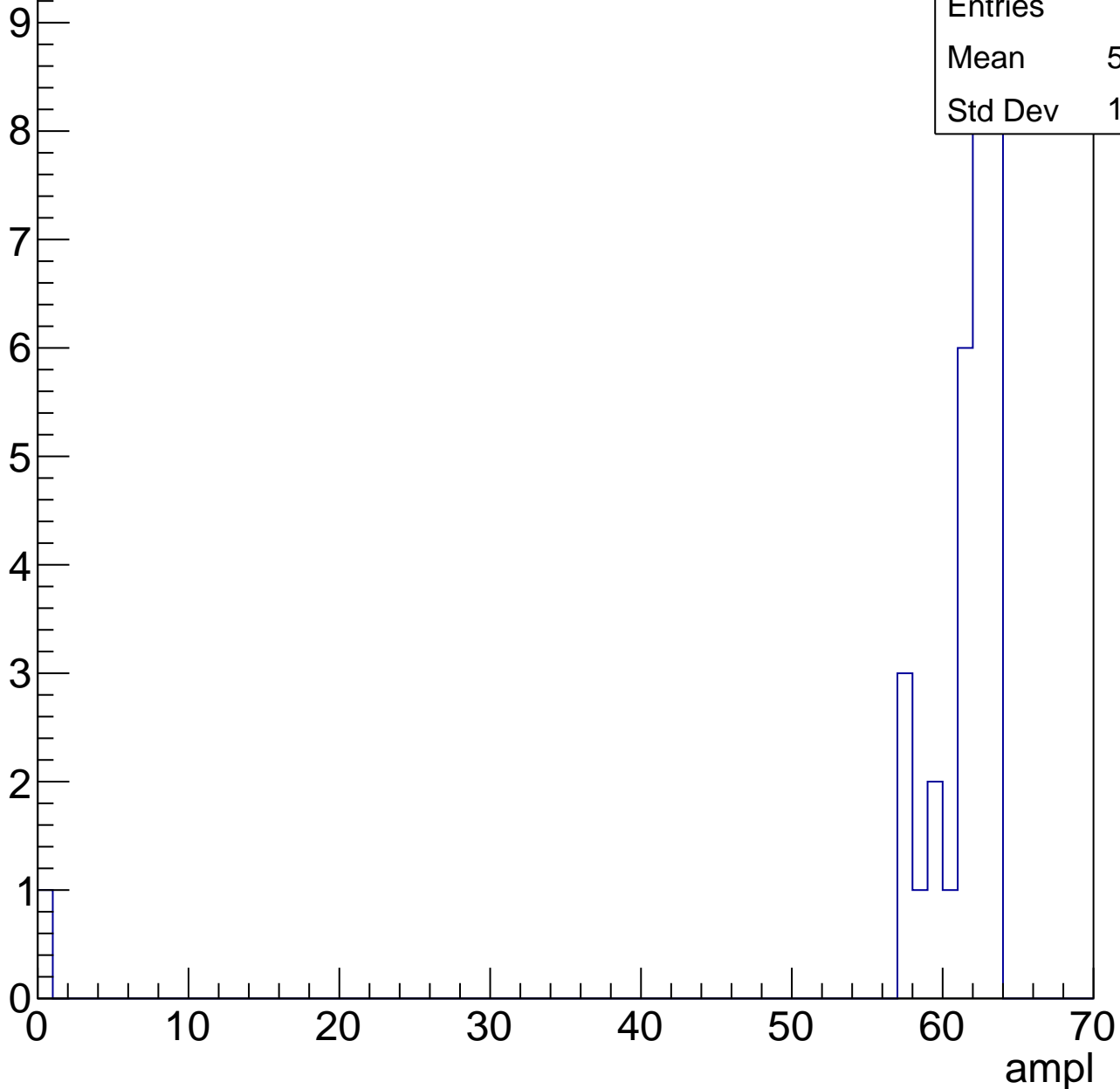


B1L101S, U17-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	59.23
Std Dev	10.97



B1L101S, U17-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U17-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch78, adc0

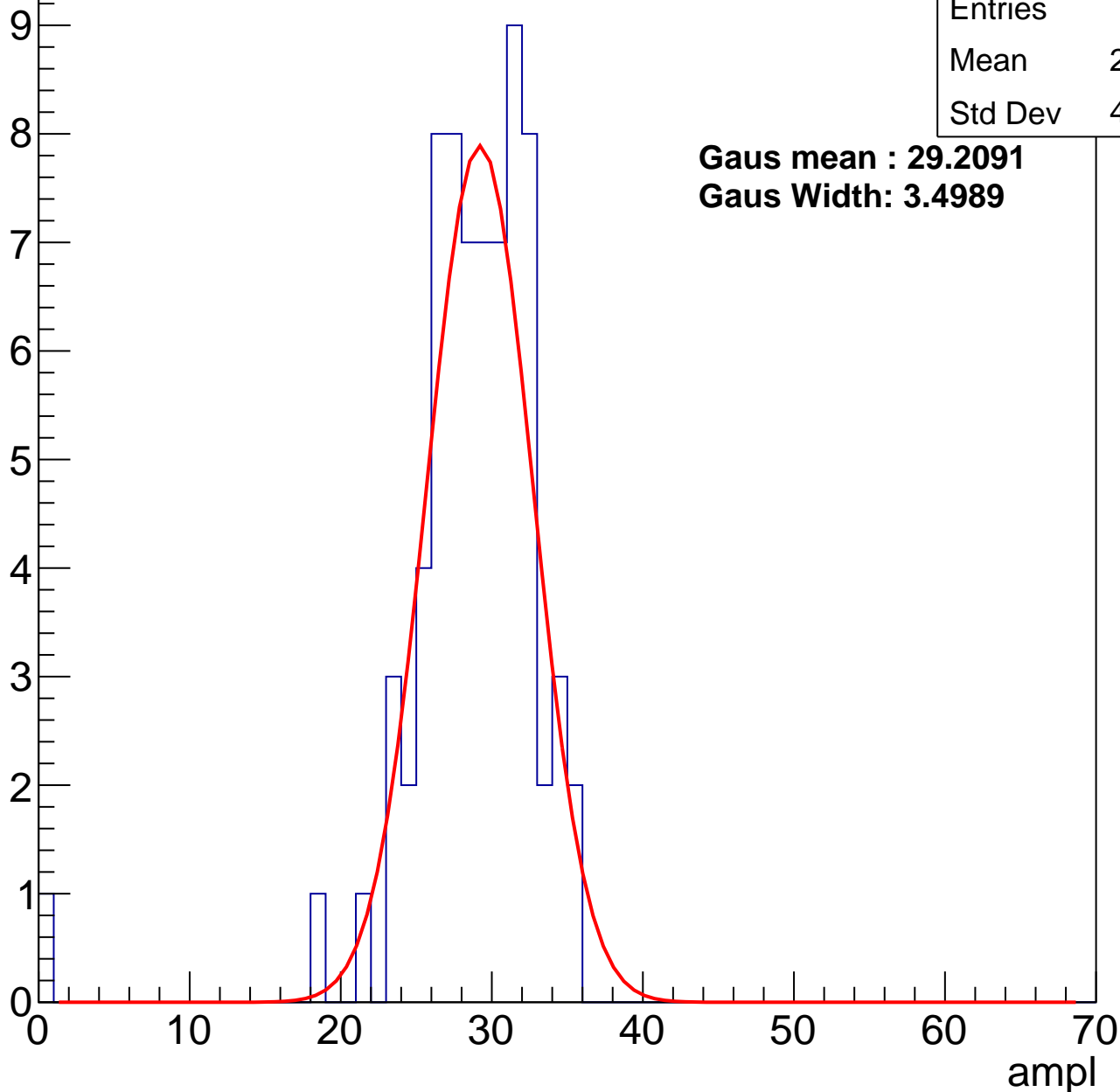
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.25
Std Dev	4.699

Gaus mean : 29.2091

Gaus Width: 3.4989



B1L101S, U17-ch78, adc1

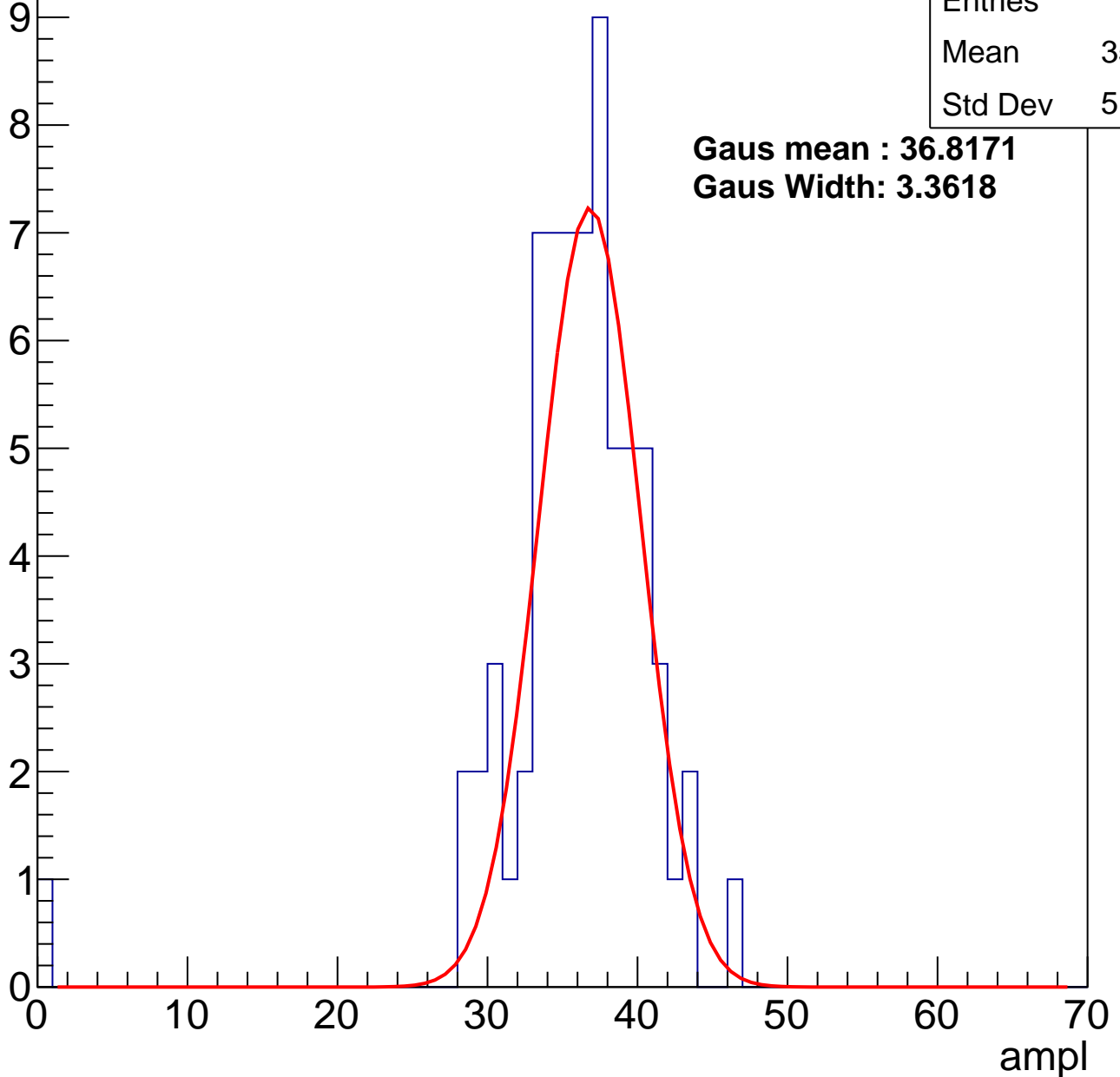
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.43
Std Dev	5.638

Gaus mean : 36.8171

Gaus Width: 3.3618



B1L101S, U17-ch78, adc2

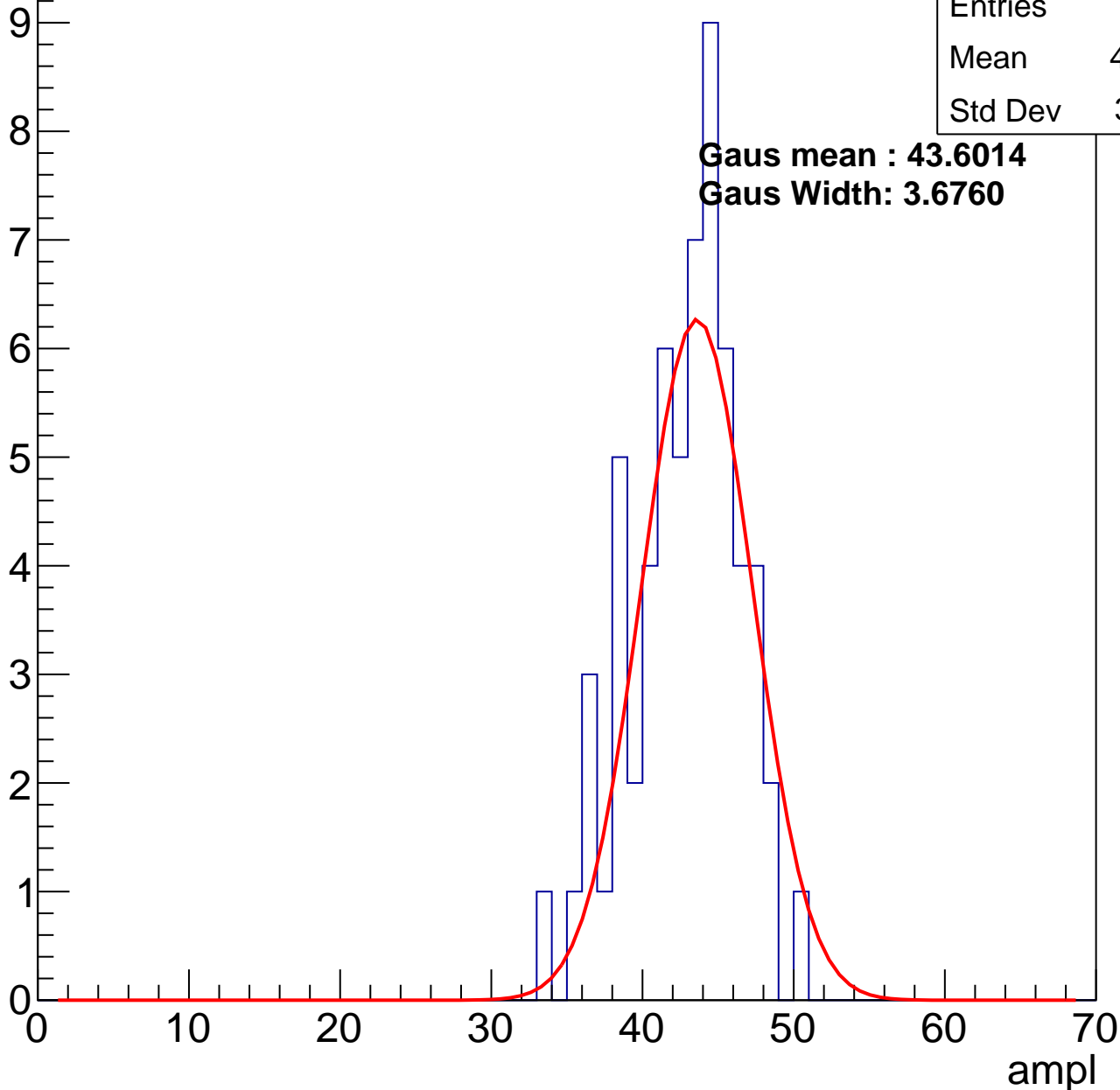
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.33
Std Dev	3.561

Gaus mean : 43.6014

Gaus Width: 3.6760

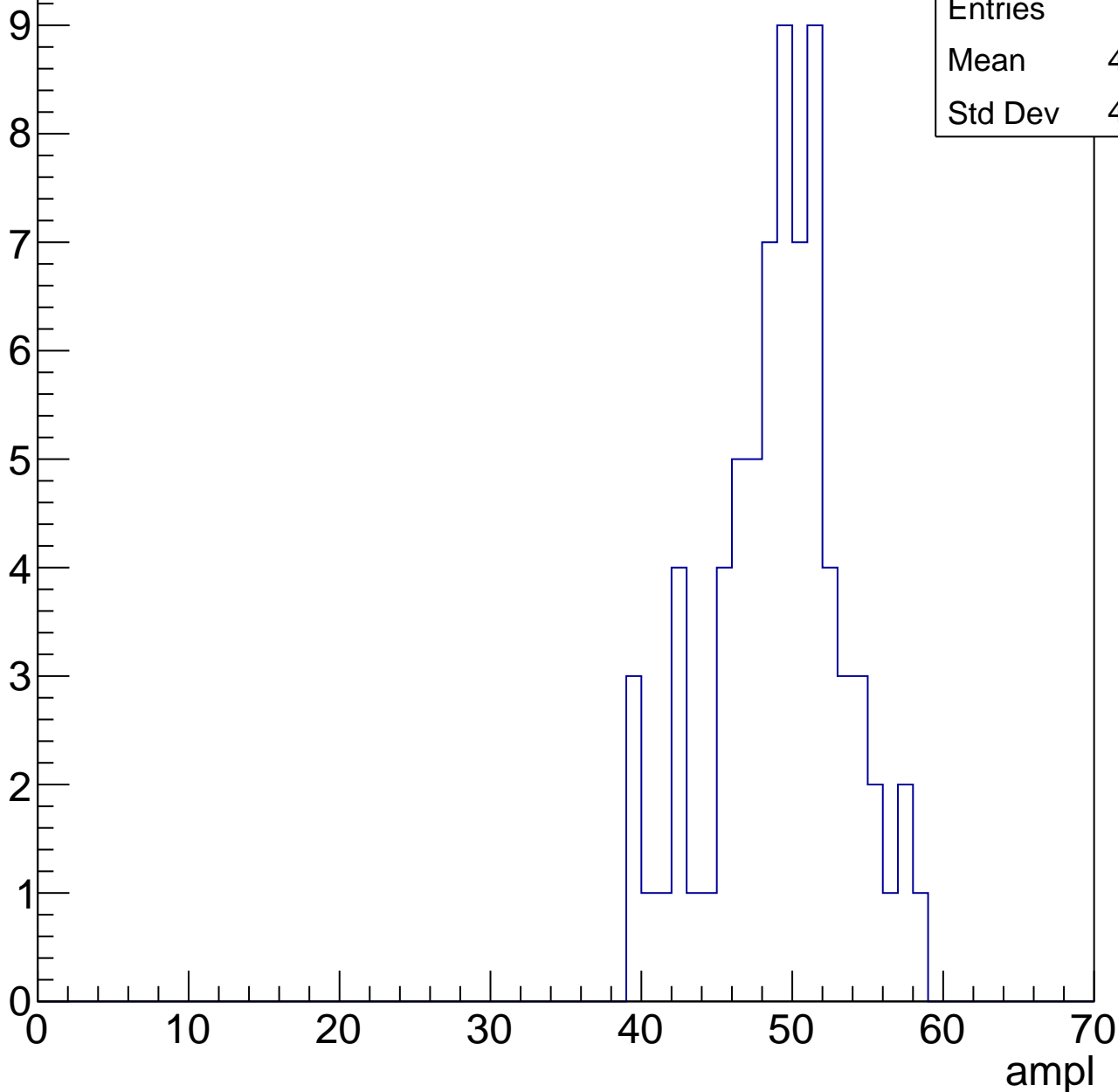


B1L101S, U17-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

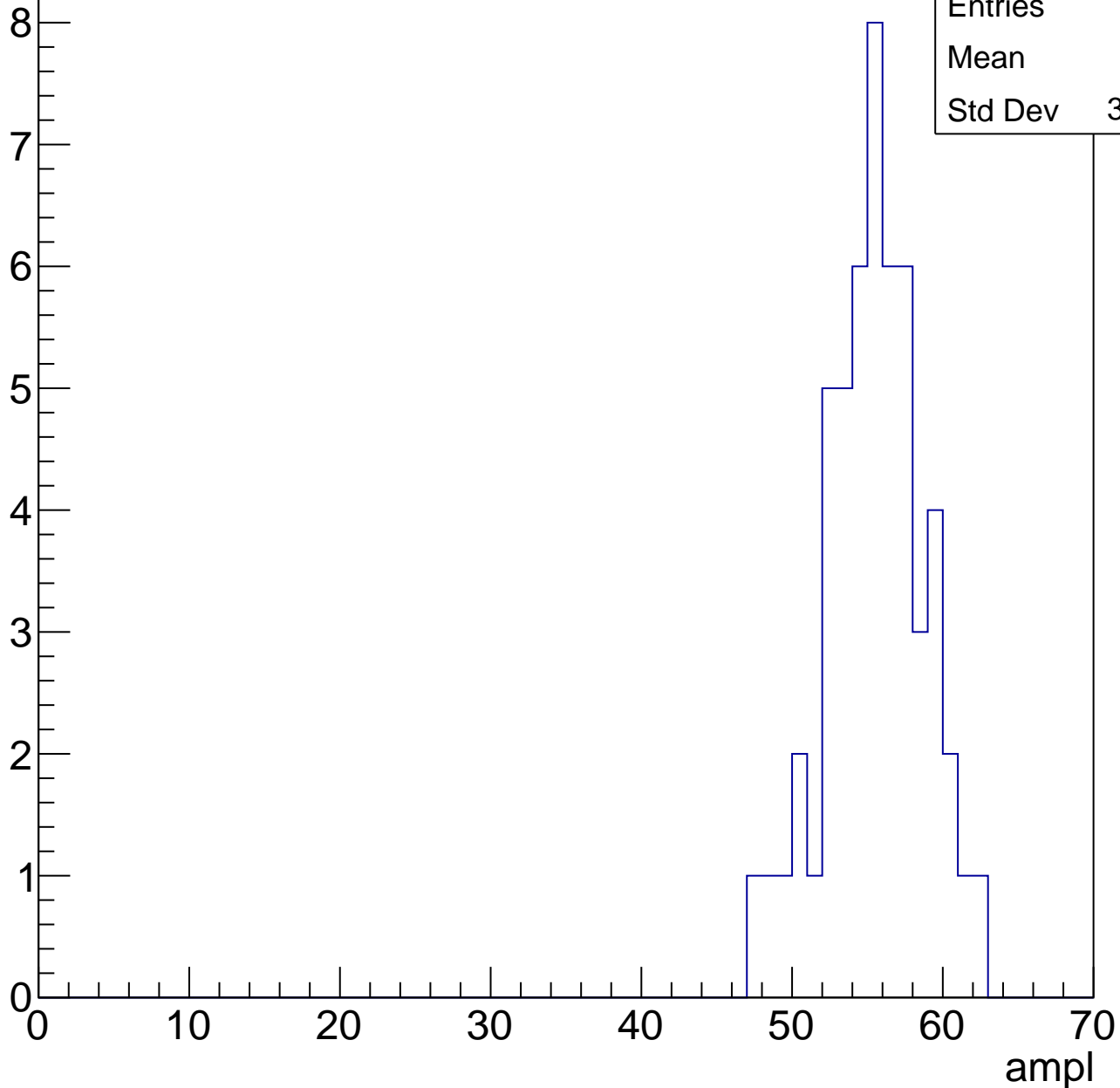
Entries	73
Mean	48.64
Std Dev	4.317



B1L101S, U17-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

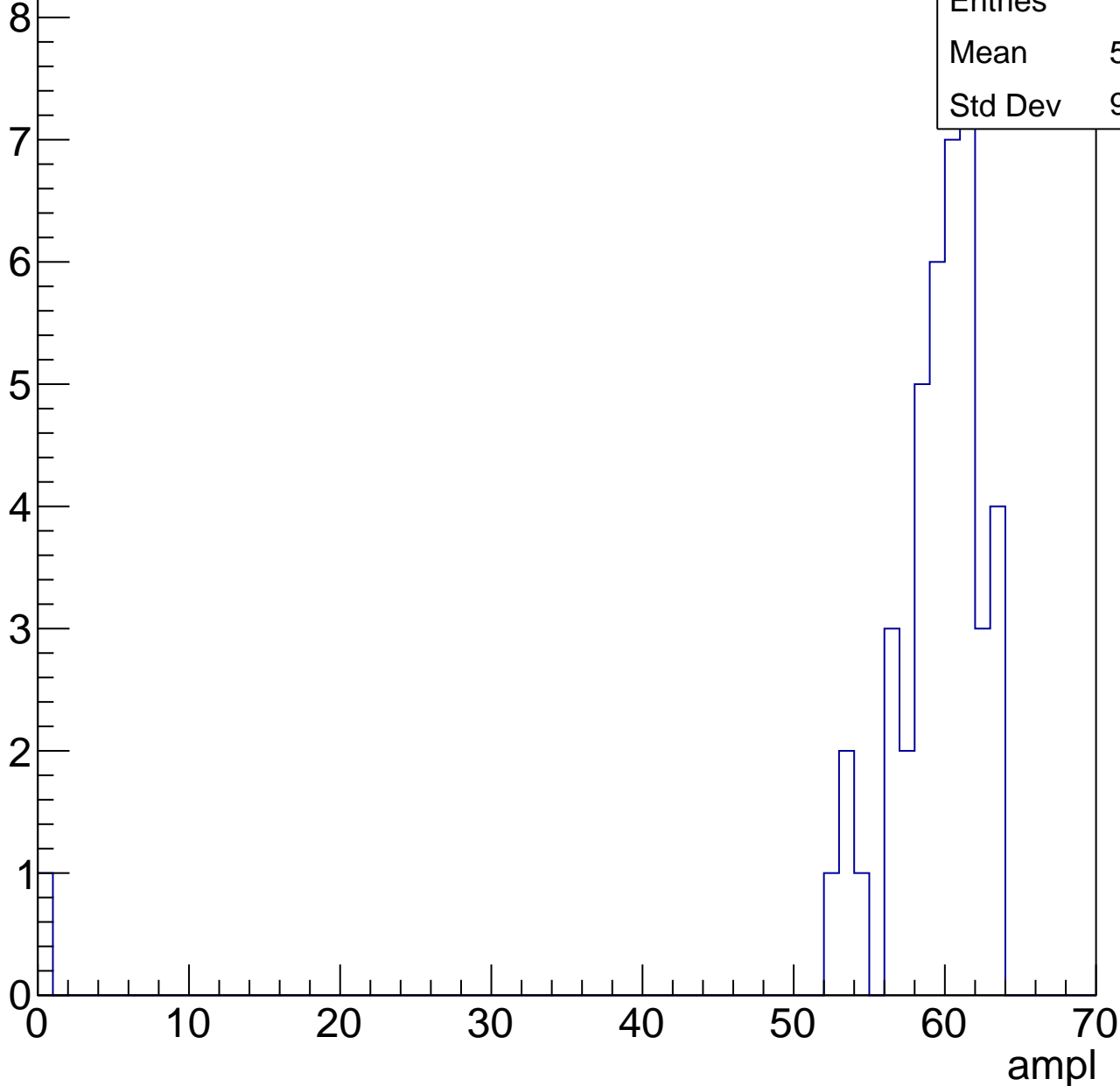


B1L101S, U17-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	57.77
Std Dev	9.316



B1L101S, U17-ch78, adc6

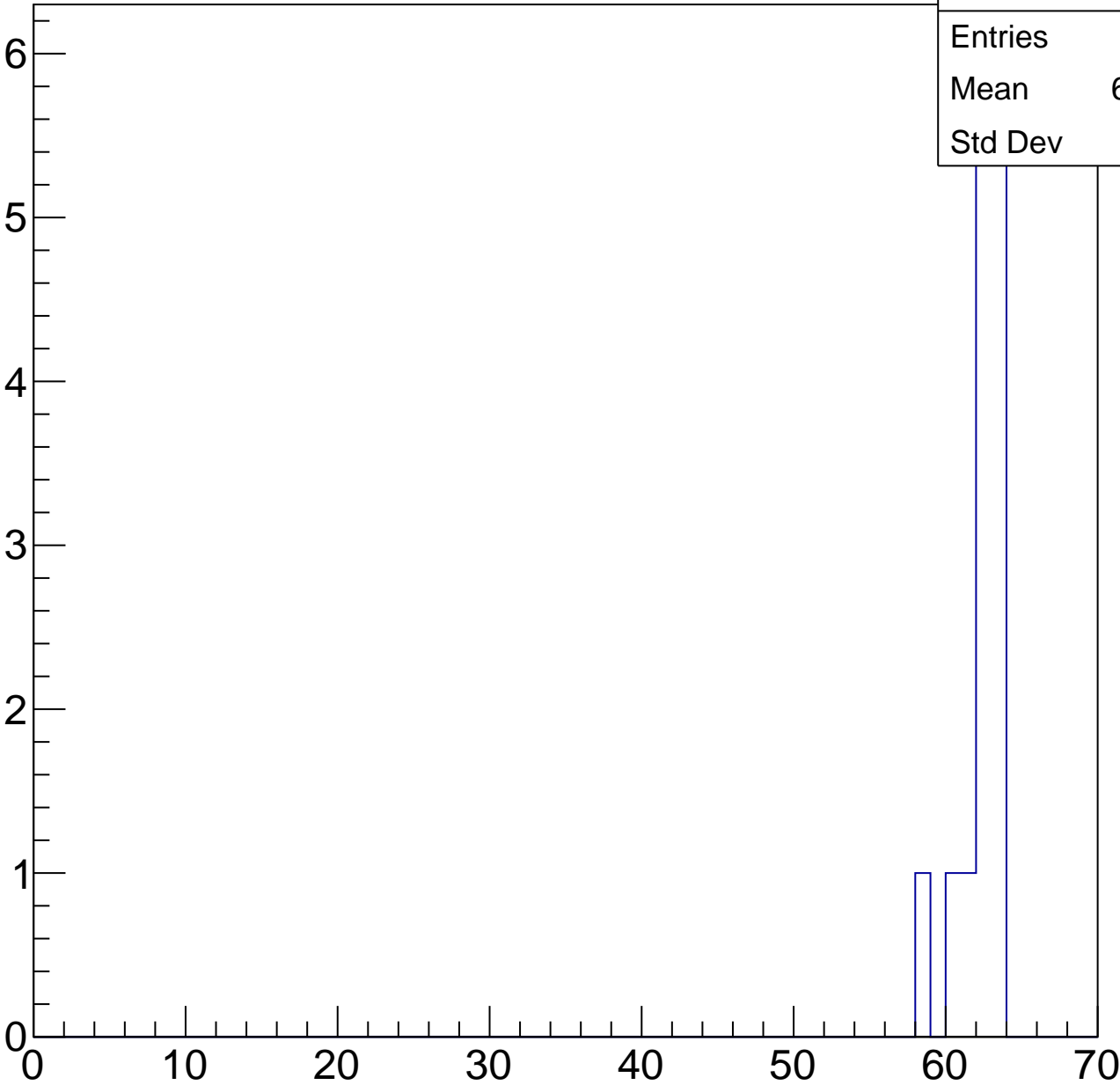
calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	15
Mean	61.93
Std Dev	1.34

ampl



B1L101S, U17-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch79, adc0

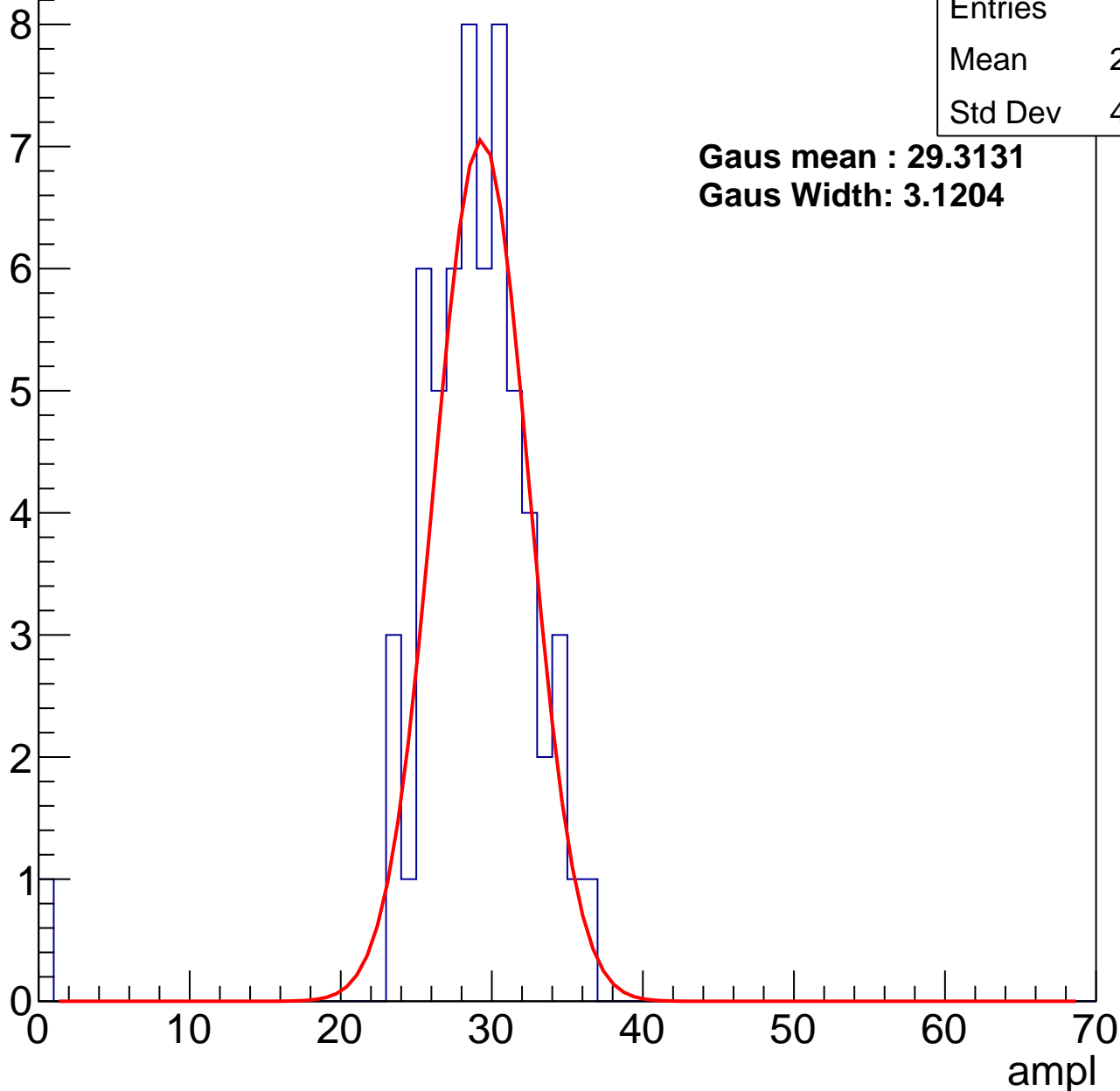
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.25
Std Dev	4.777

Gaus mean : 29.3131

Gaus Width: 3.1204



B1L101S, U17-ch79, adc1

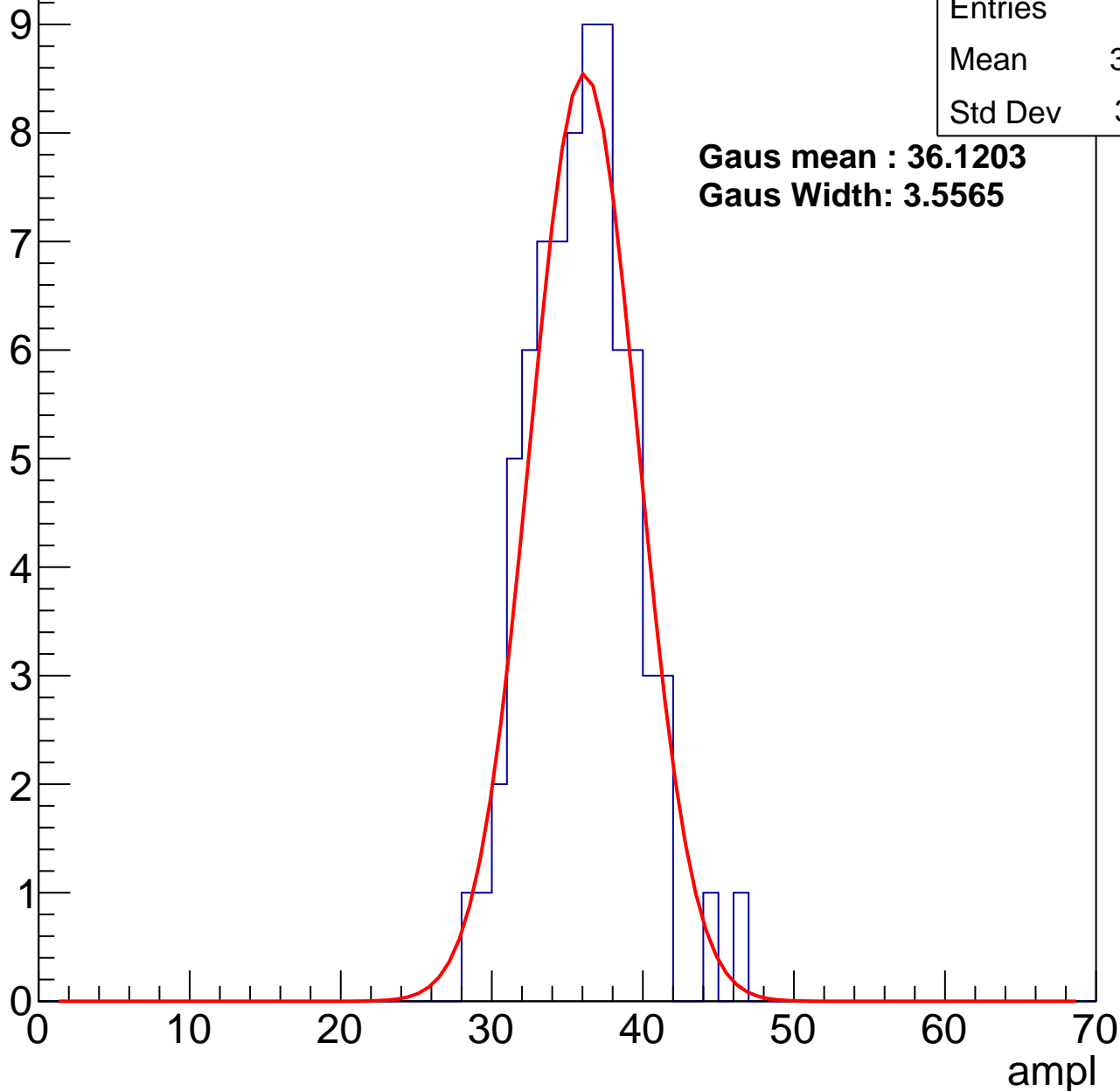
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.53
Std Dev	3.391

Gaus mean : 36.1203

Gaus Width: 3.5565



B1L101S, U17-ch79, adc2

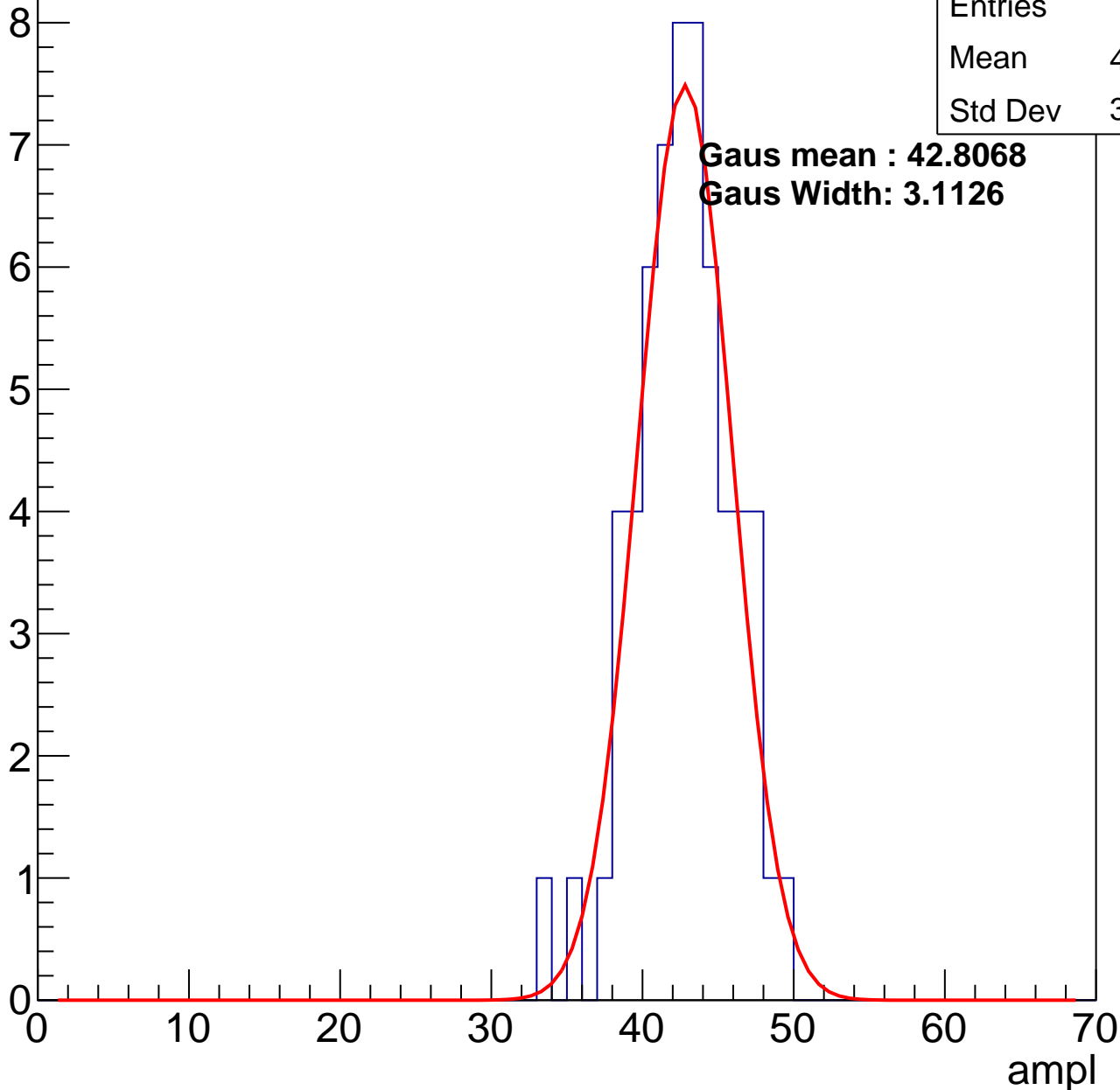
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.22
Std Dev	3.163

Gaus mean : 42.8068

Gaus Width: 3.1126

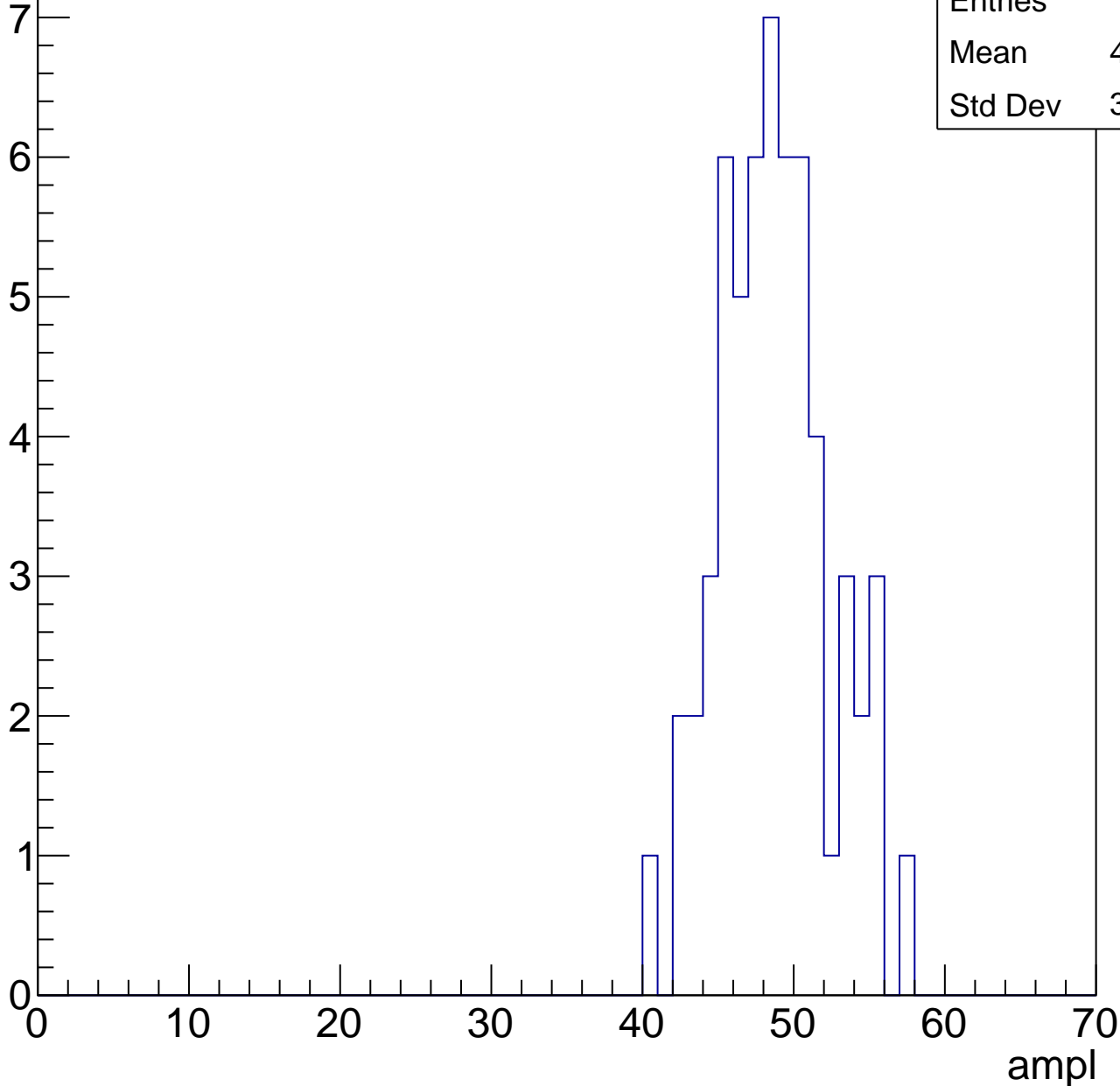


B1L101S, U17-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

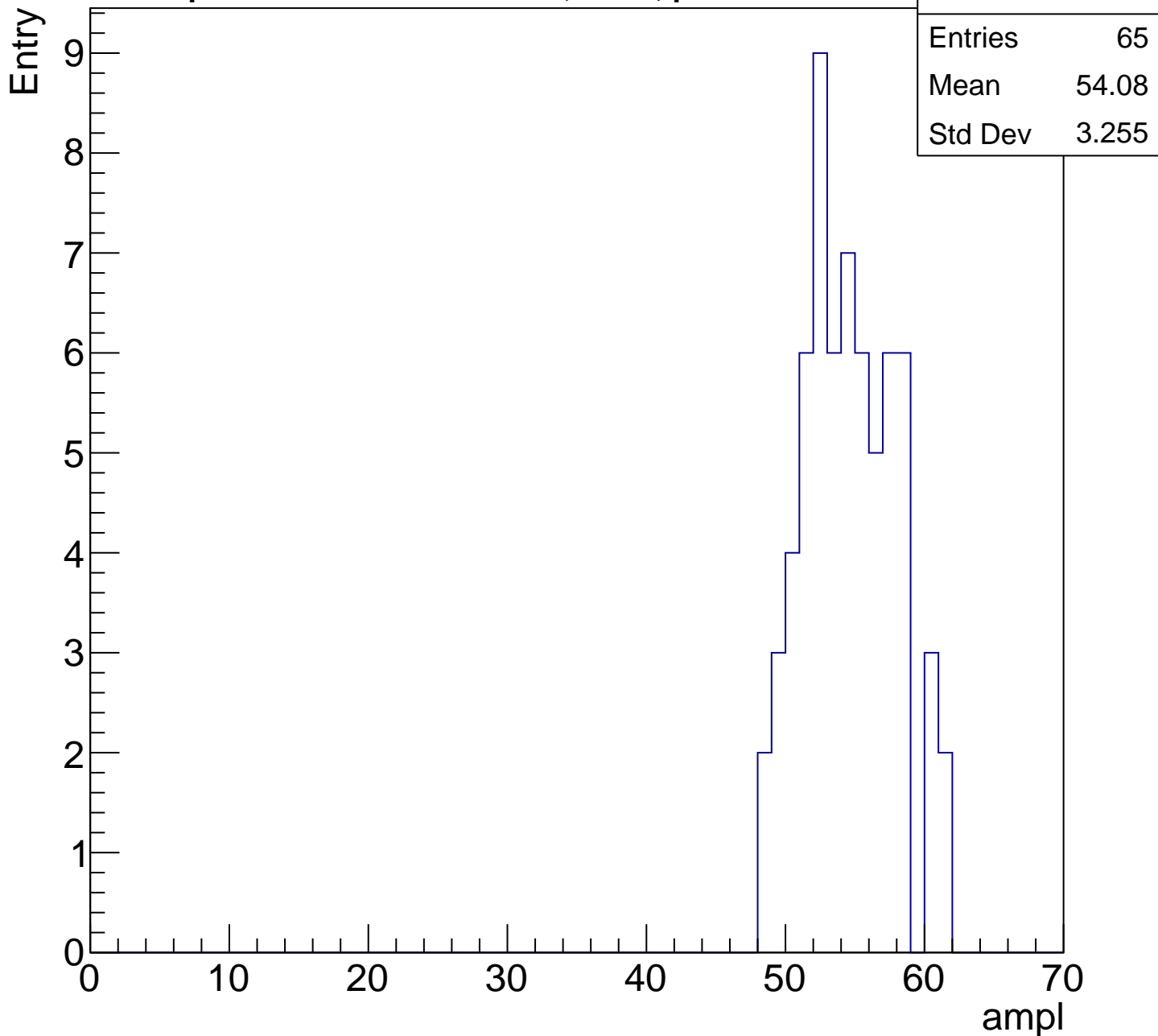
Entry

Entries	58
Mean	48.26
Std Dev	3.637



B1L101S, U17-ch79, adc4

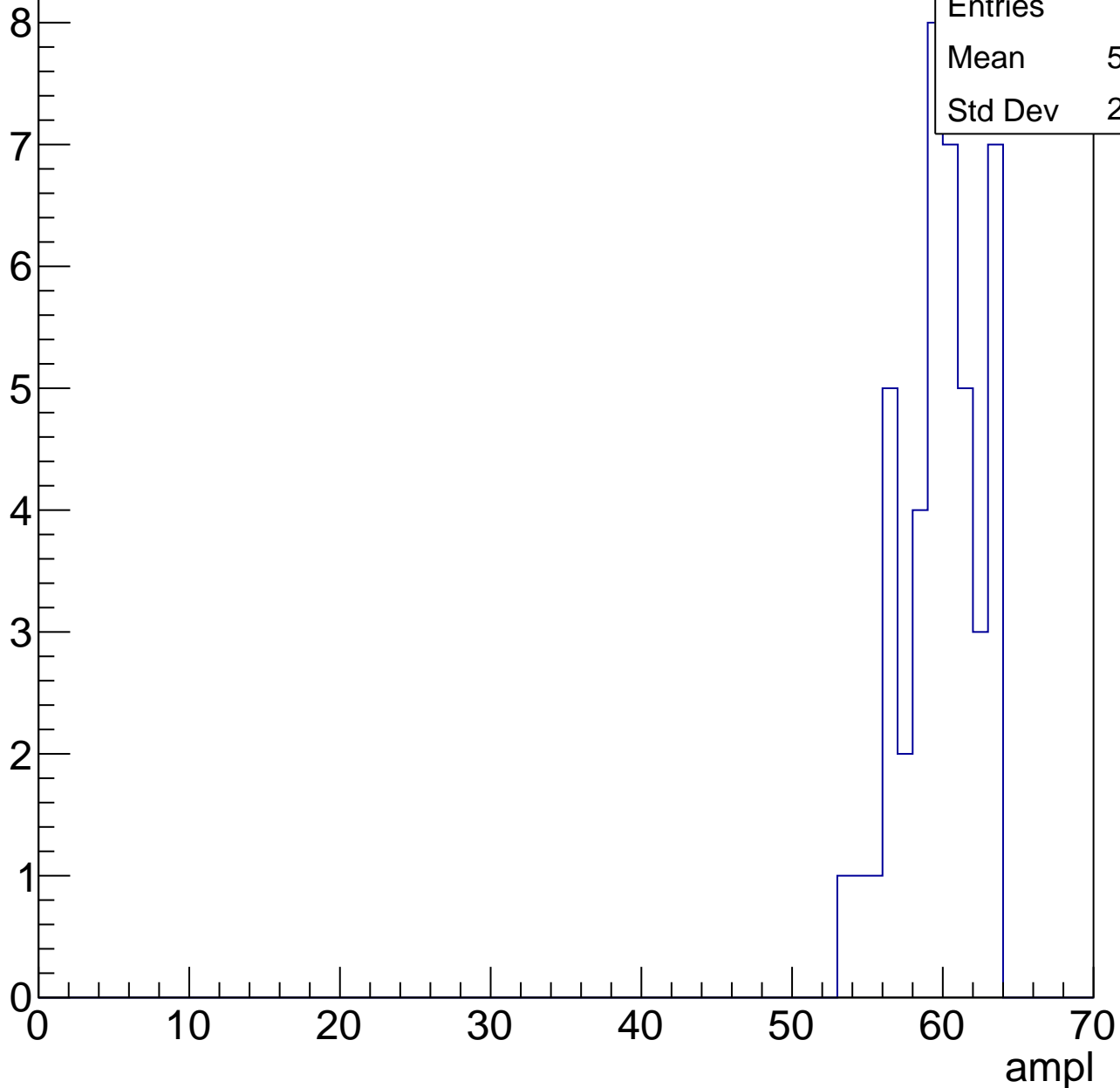
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

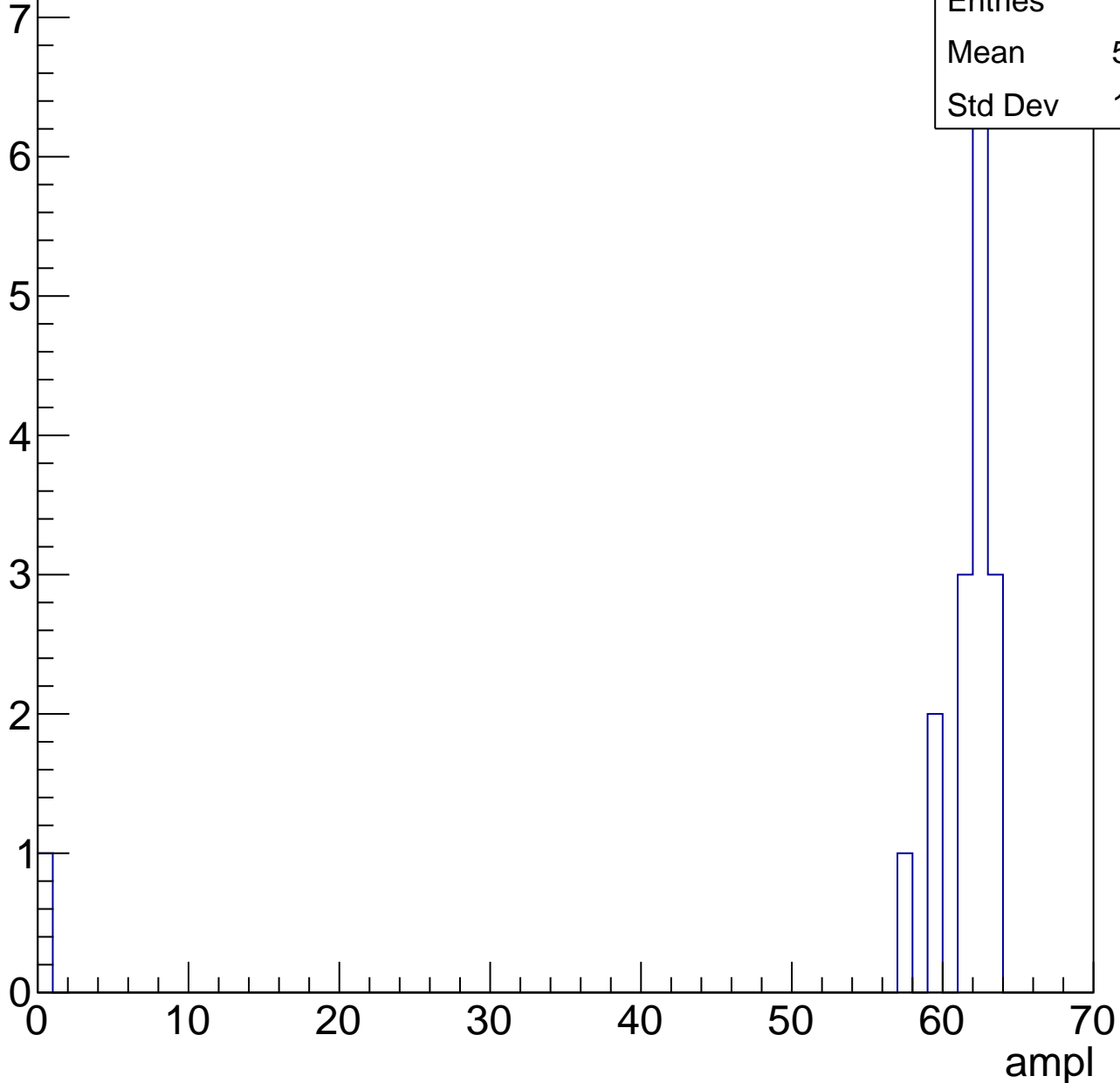


B1L101S, U17-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.71
Std Dev	14.51



B1L101S, U17-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch80, adc0

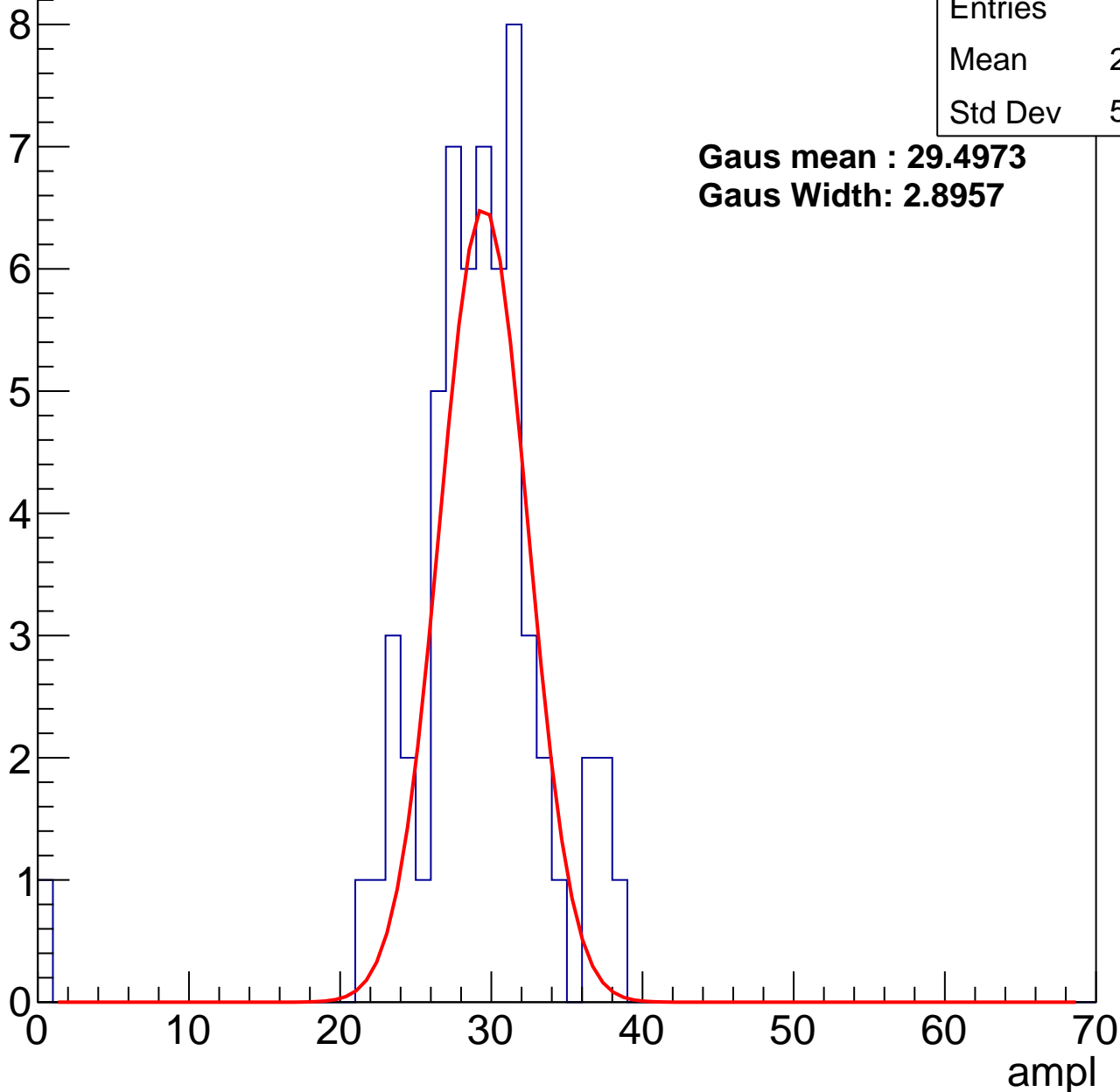
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.53
Std Dev	5.244

Gaus mean : 29.4973

Gaus Width: 2.8957



B1L101S, U17-ch80, adc1

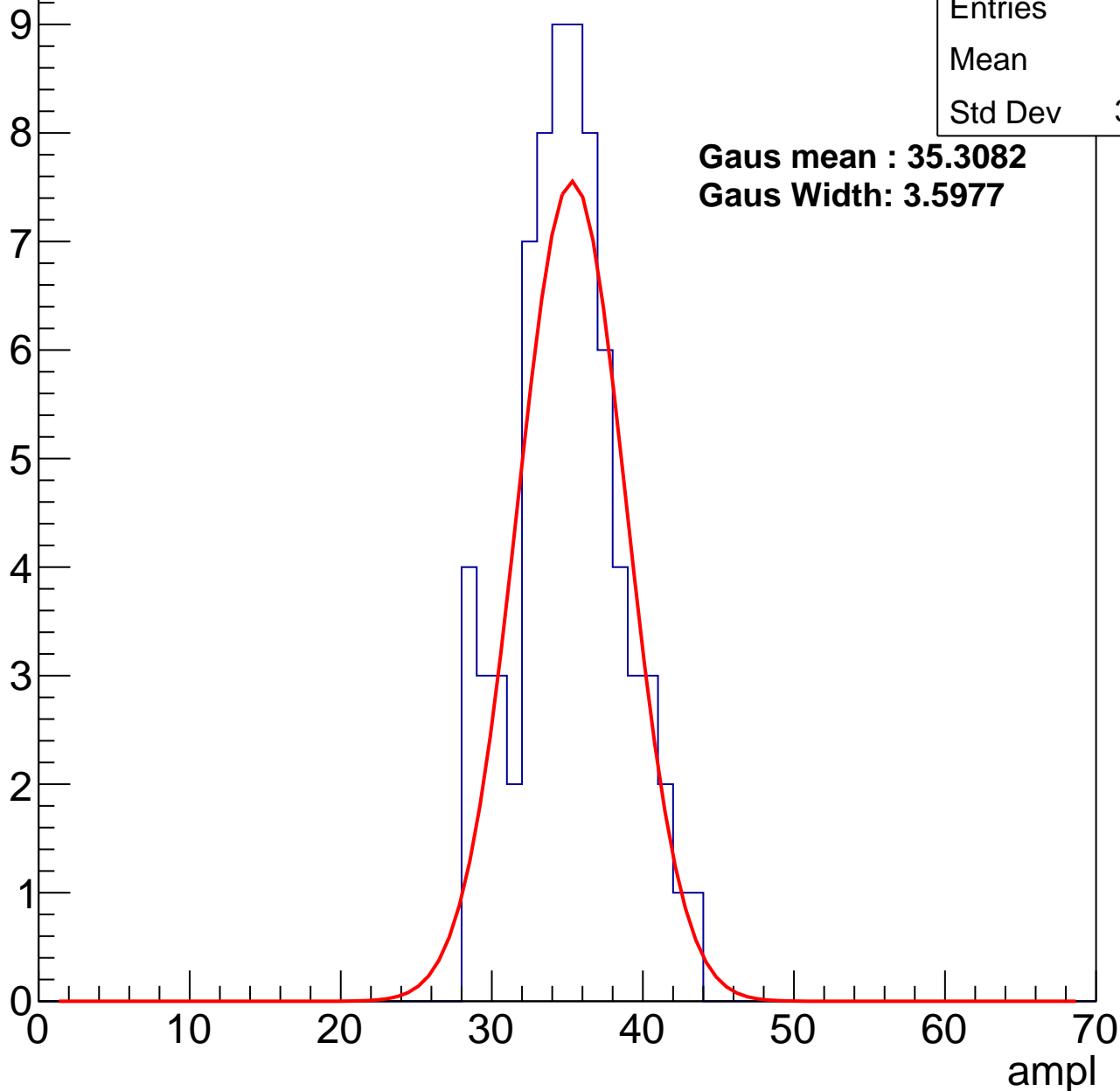
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	34.6
Std Dev	3.471

Gaus mean : 35.3082

Gaus Width: 3.5977



B1L101S, U17-ch80, adc2

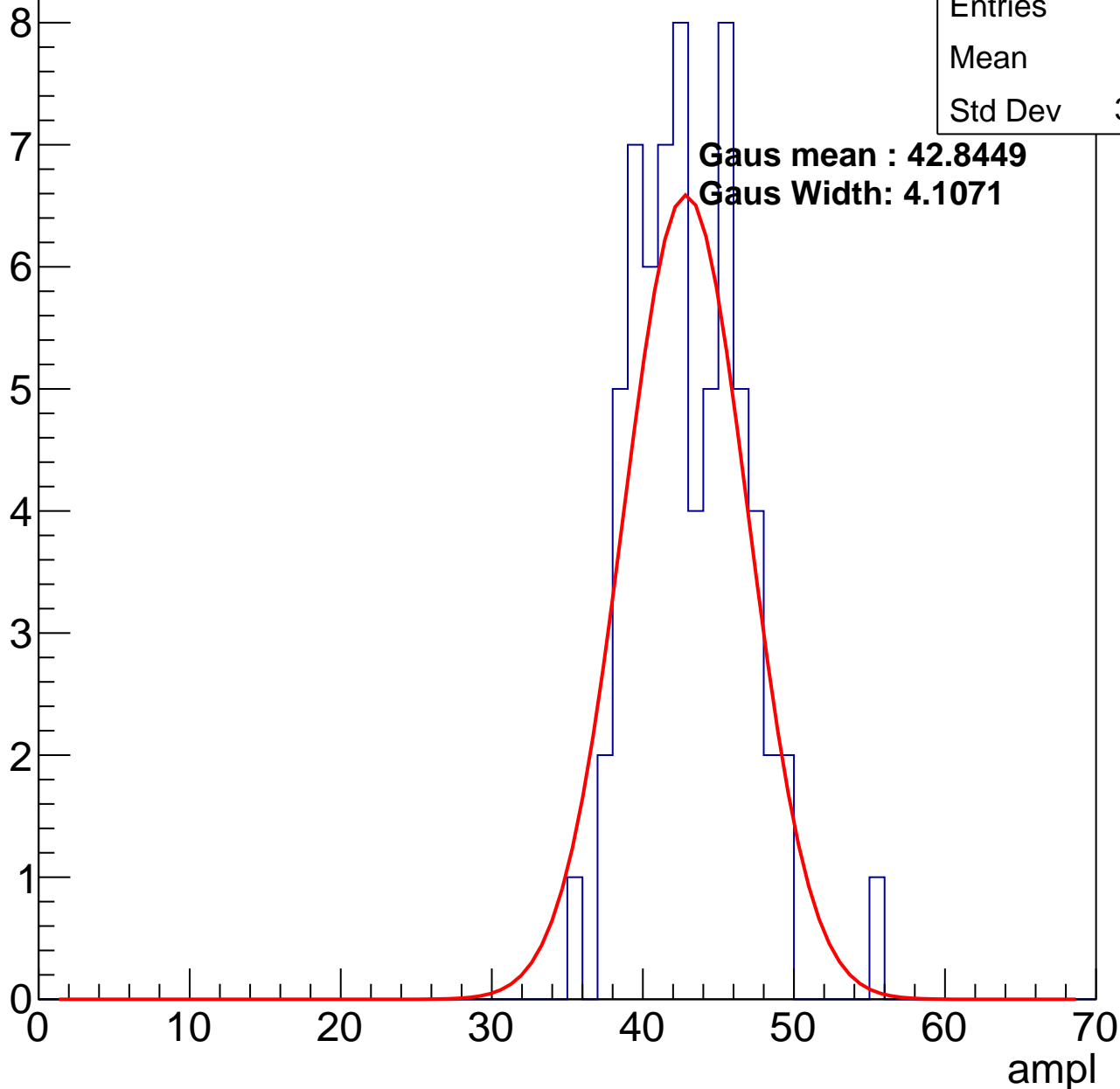
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.6
Std Dev	3.591

Gaus mean : 42.8449

Gaus Width: 4.1071

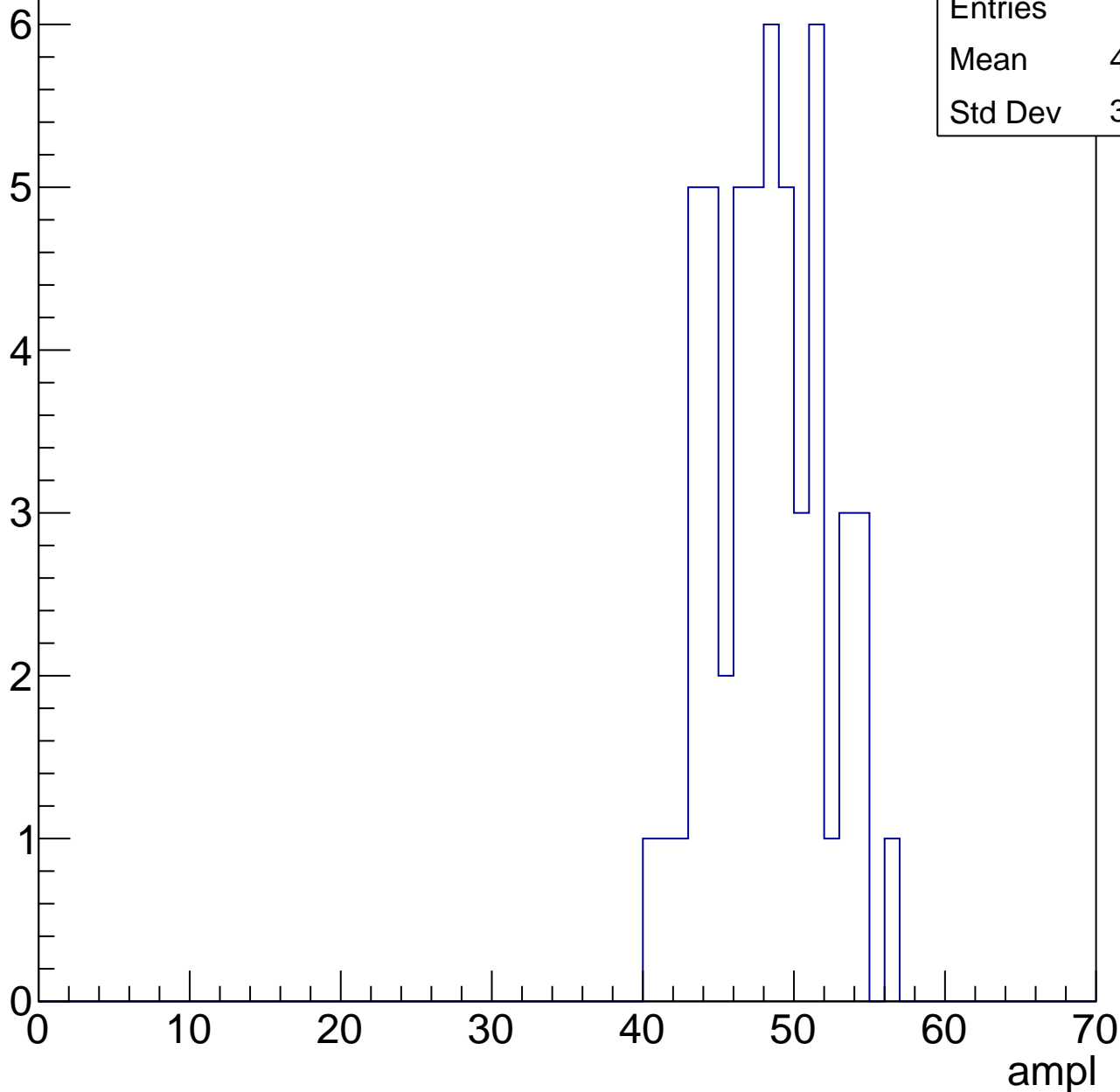


B1L101S, U17-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	47.75
Std Dev	3.706

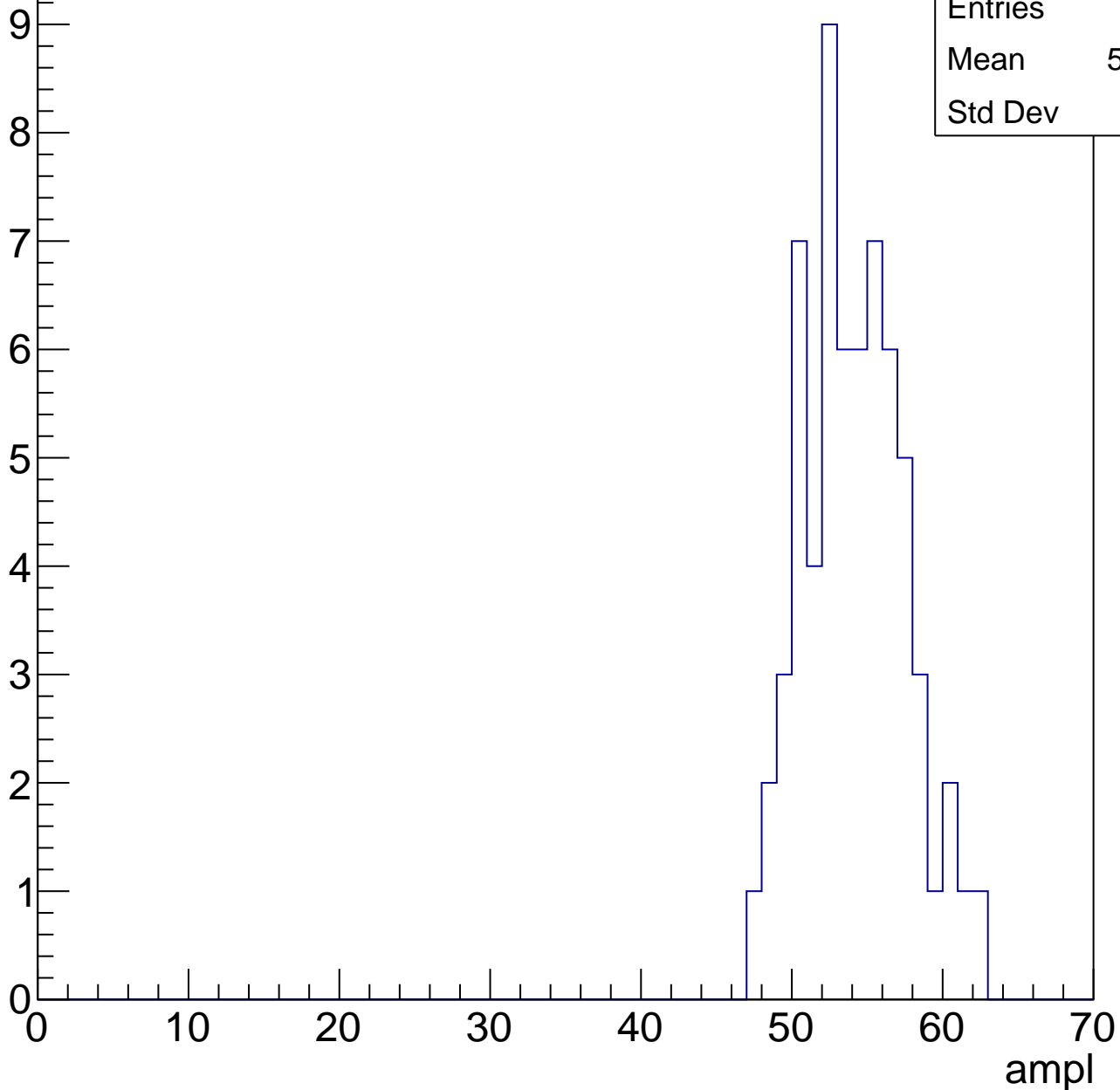


B1L101S, U17-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	53.69
Std Dev	3.34



B1L101S, U17-ch80, adc5

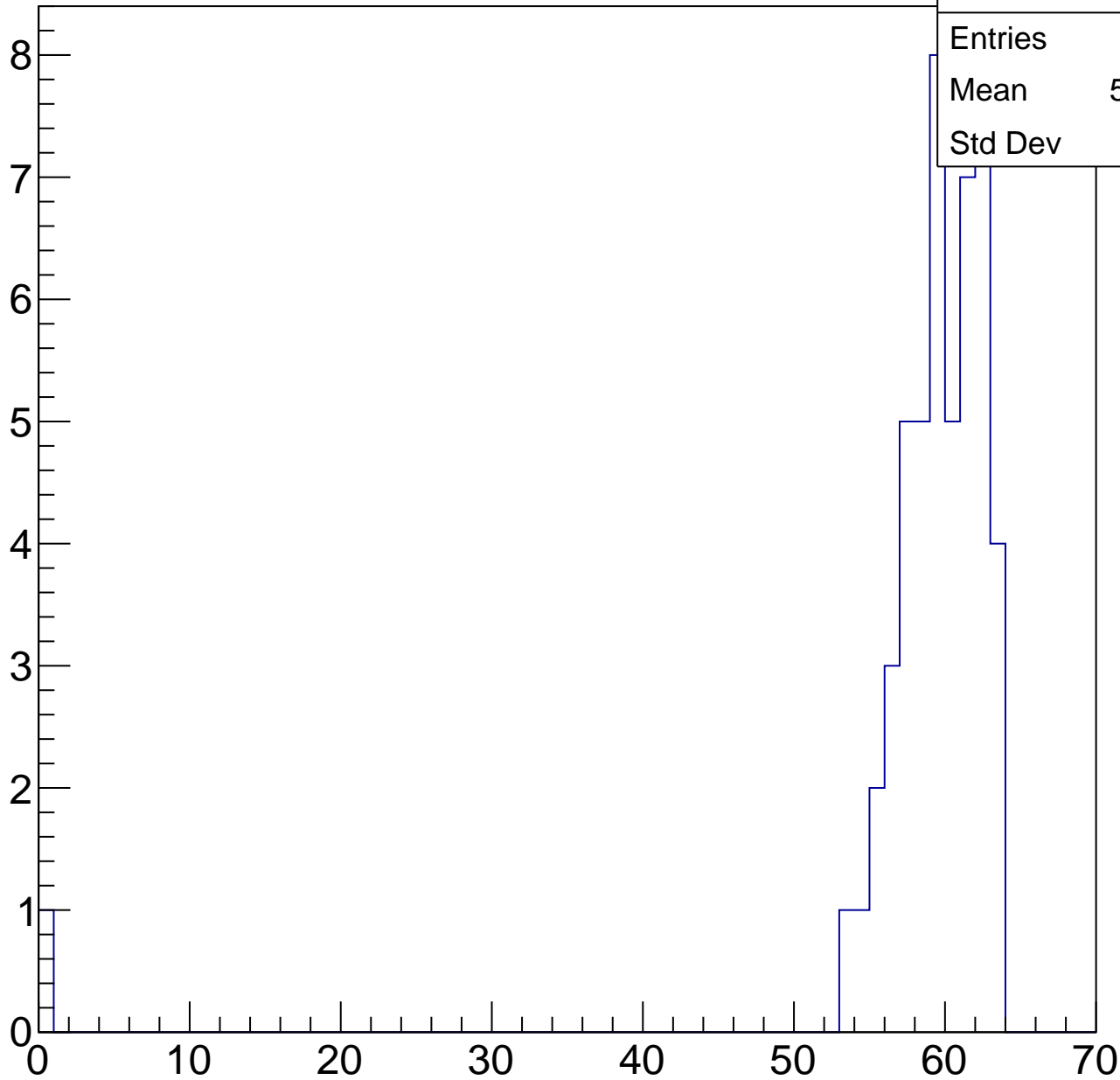
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.14
Std Dev	8.67

ampl

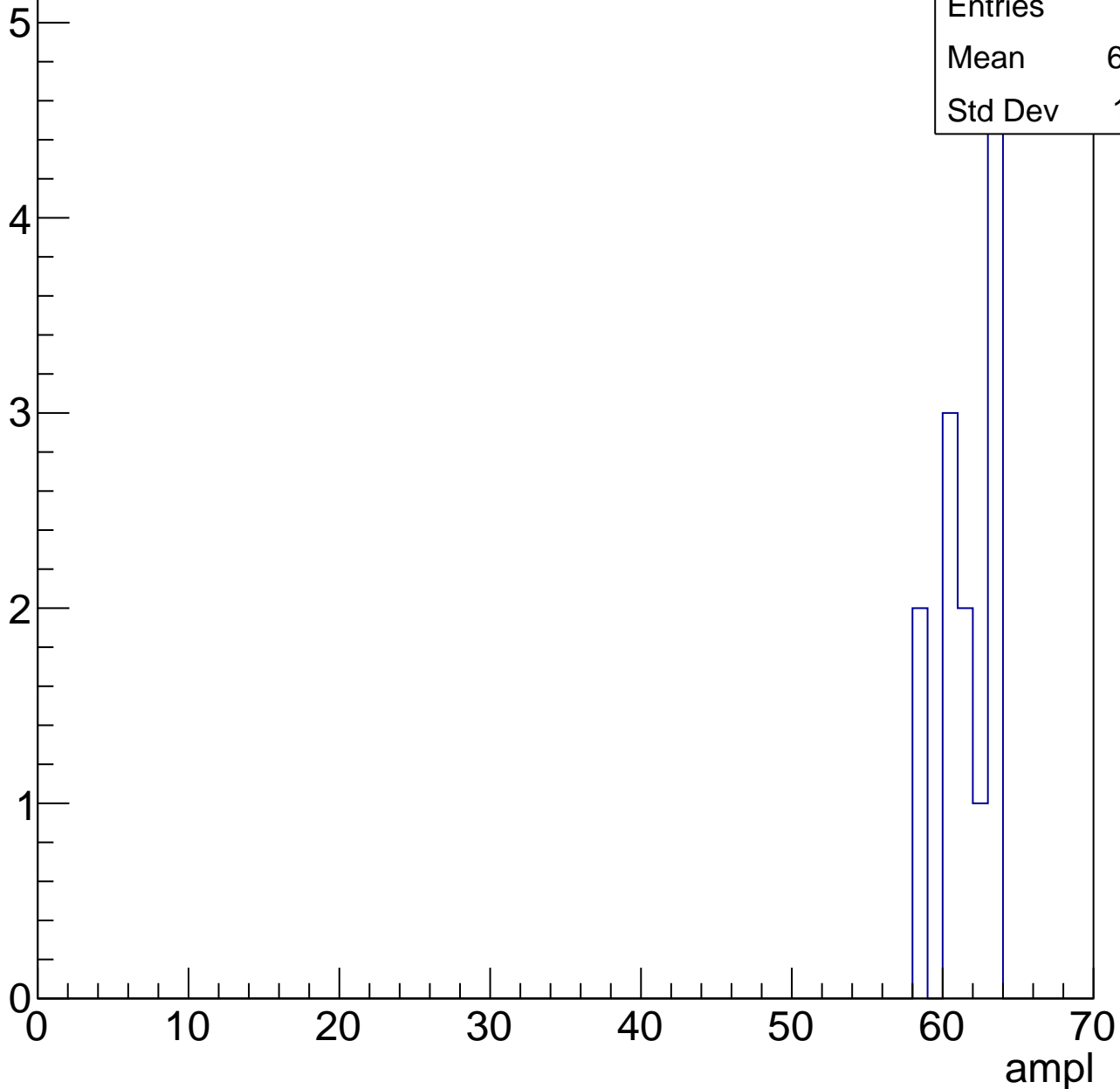


B1L101S, U17-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

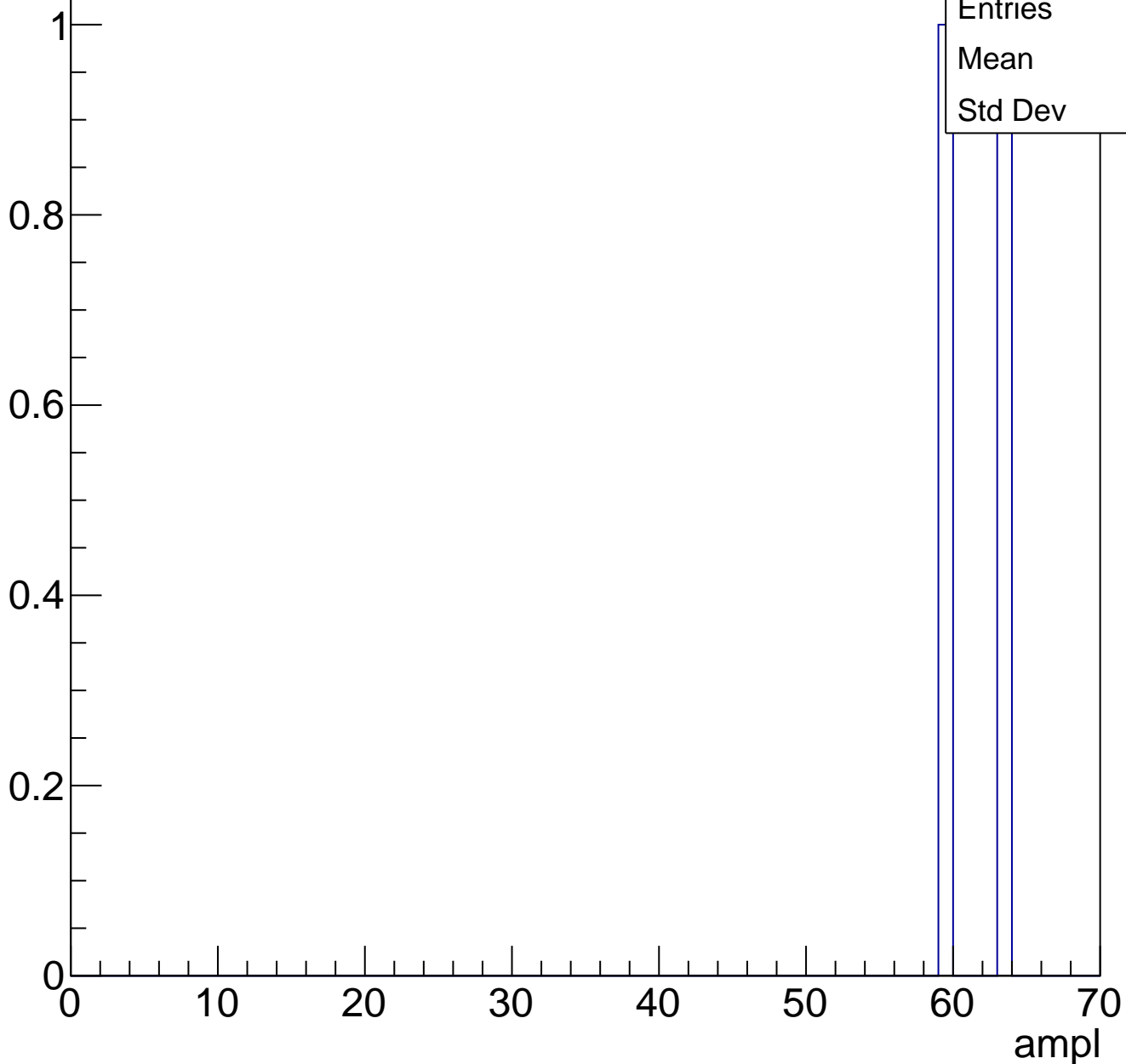
Entries	13
Mean	61.15
Std Dev	1.791



B1L101S, U17-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch81, adc0

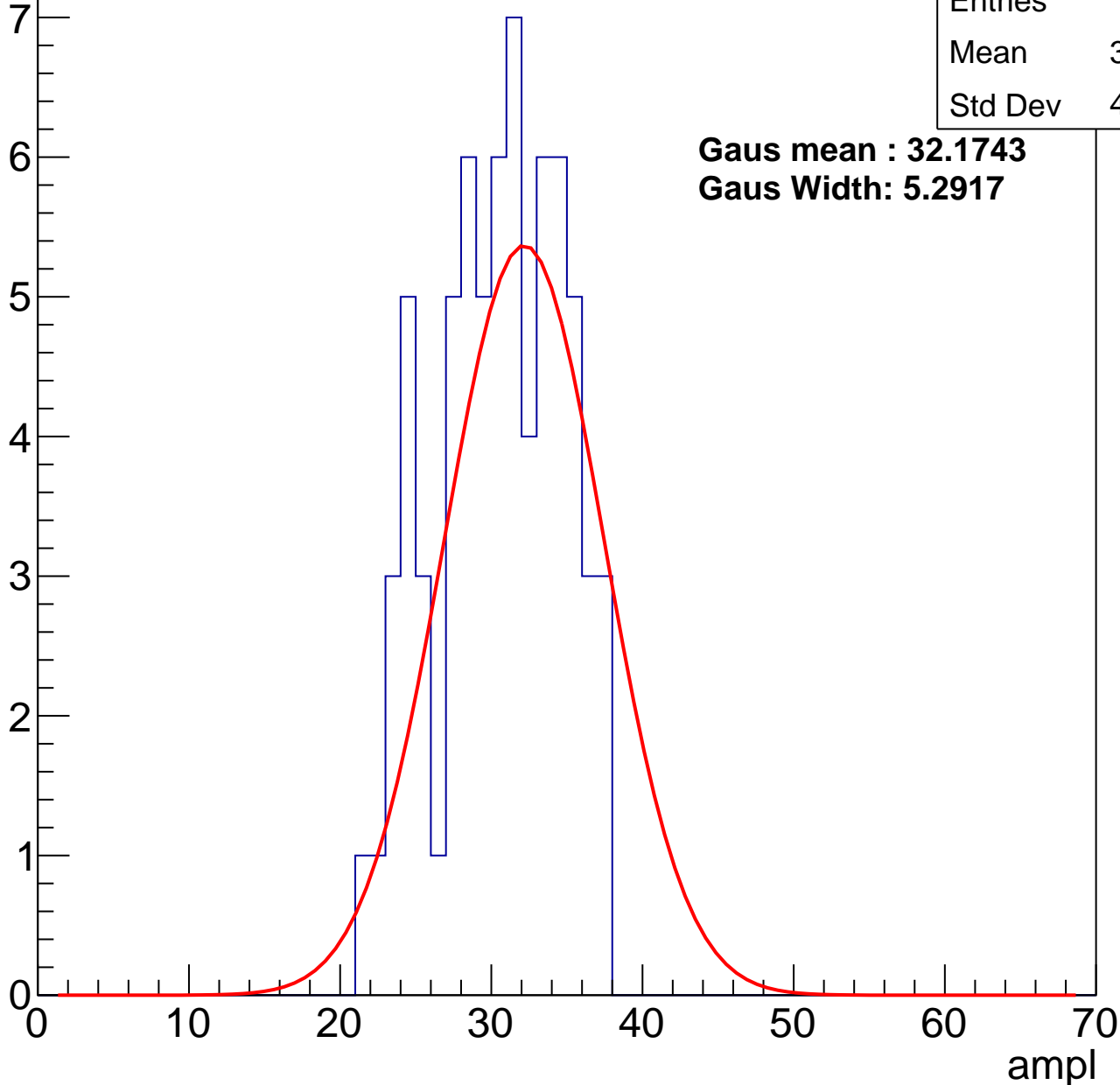
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.03
Std Dev	4.123

Gaus mean : 32.1743

Gaus Width: 5.2917



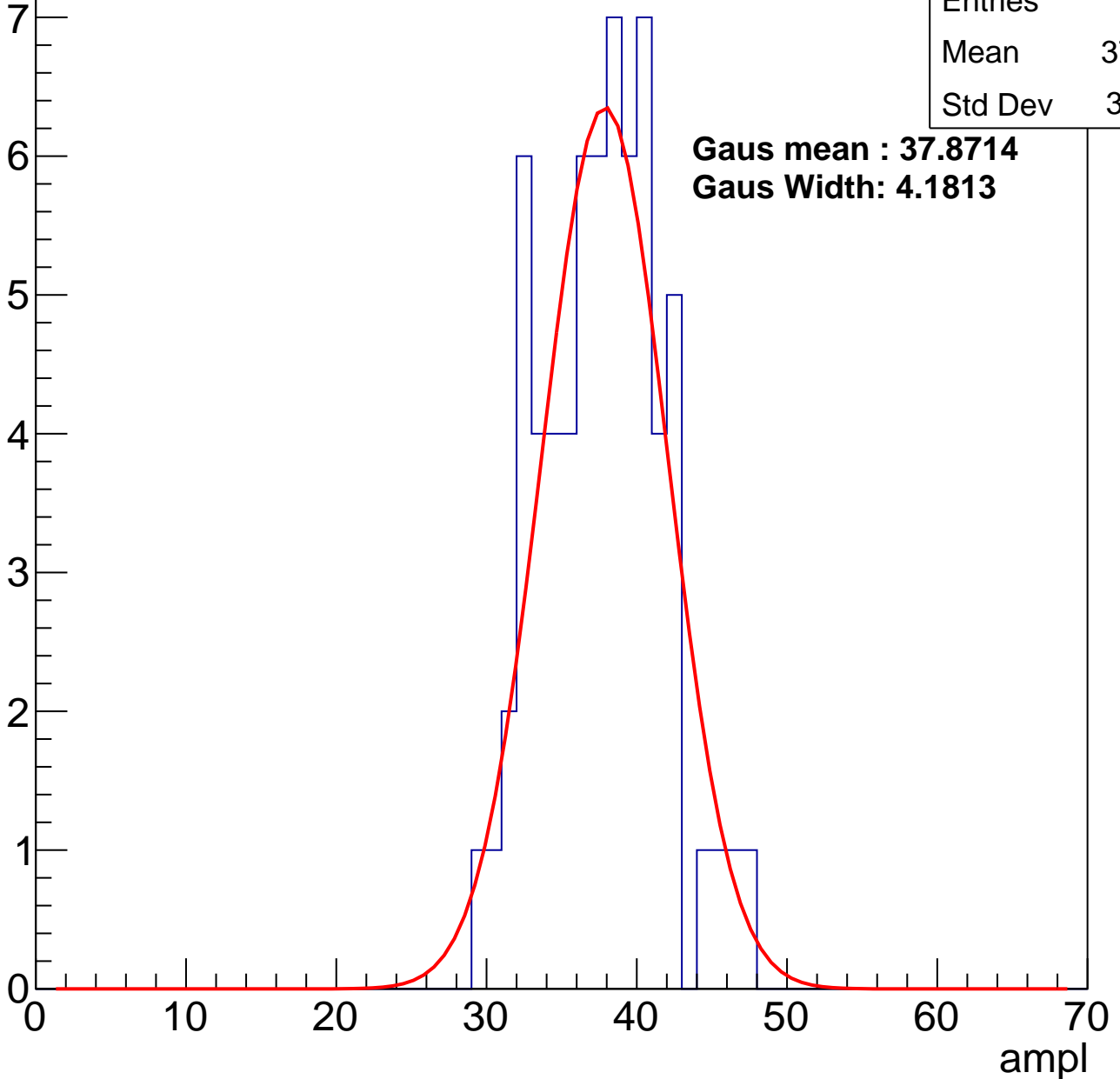
B1L101S, U17-ch81, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.24
Std Dev	3.921

Gaus mean : 37.8714
Gaus Width: 4.1813

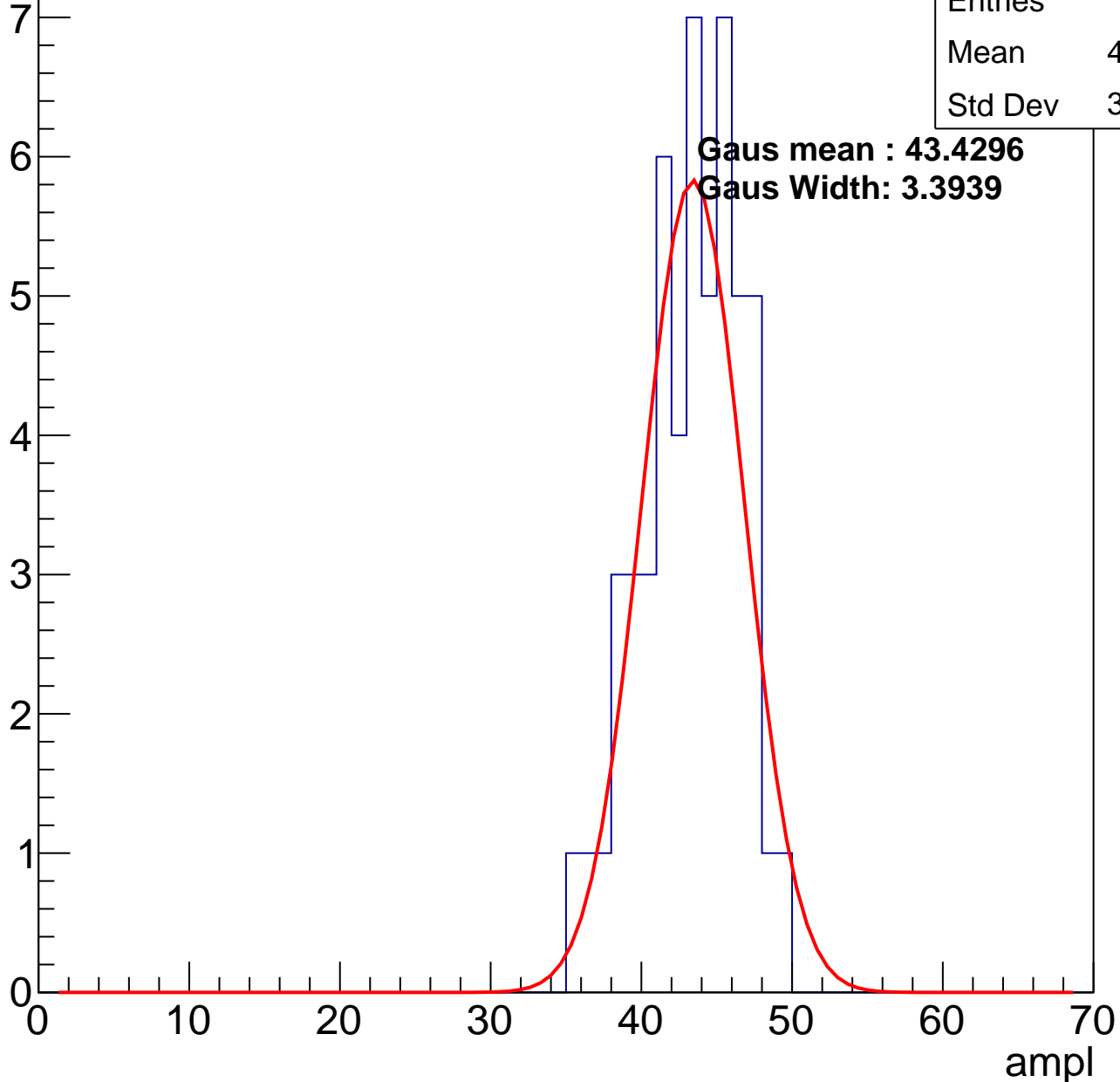


B1L101S, U17-ch81, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.85
Std Dev	3.206



B1L101S, U17-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	49.53
Std Dev	3.838

Entry

10

8

6

4

2

0

0

10

20

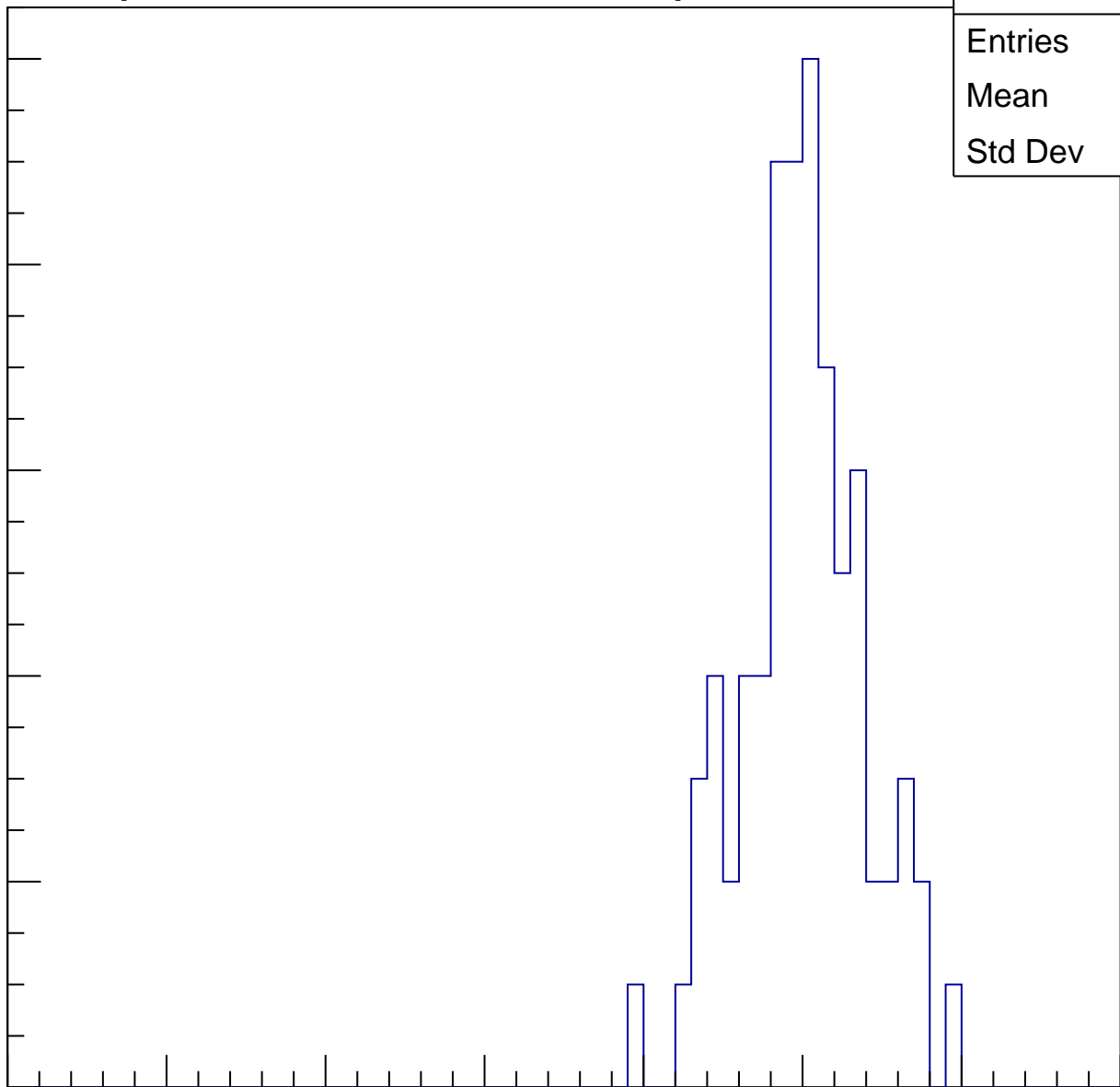
30

40

50

60

ampl

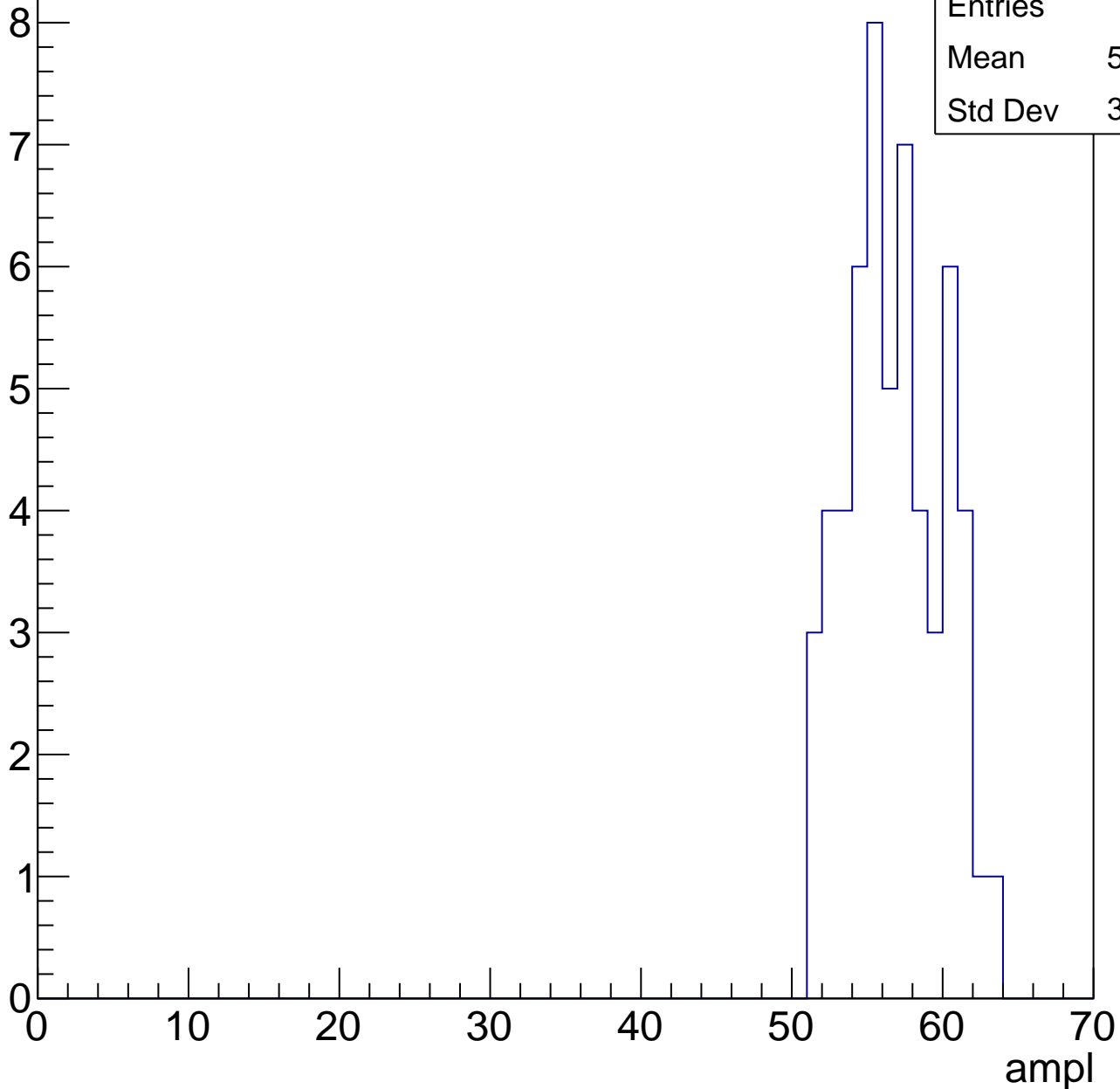


B1L101S, U17-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.32
Std Dev	3.083

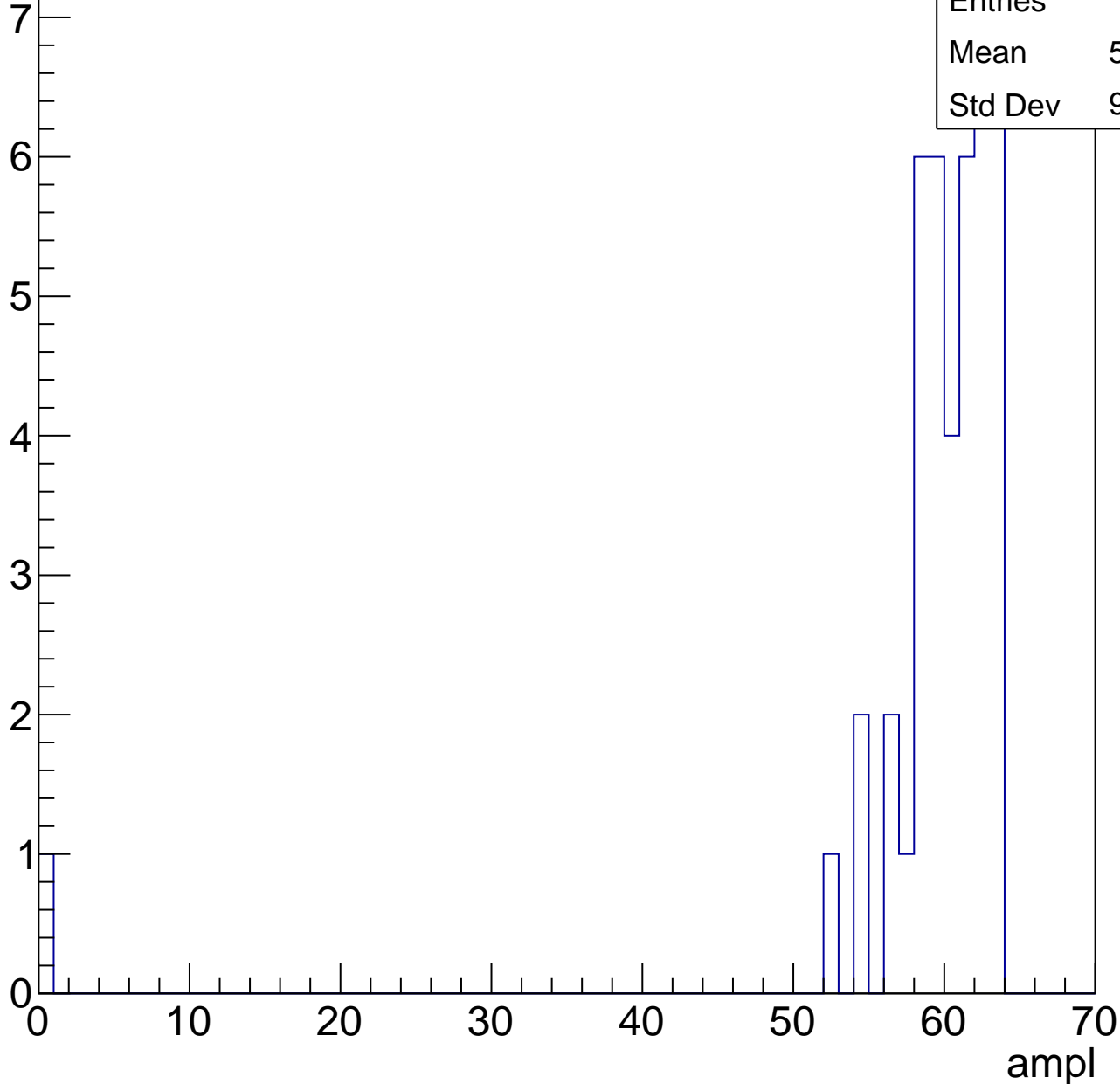


B1L101S, U17-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.42
Std Dev	9.396



B1L101S, U17-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch82, adc0

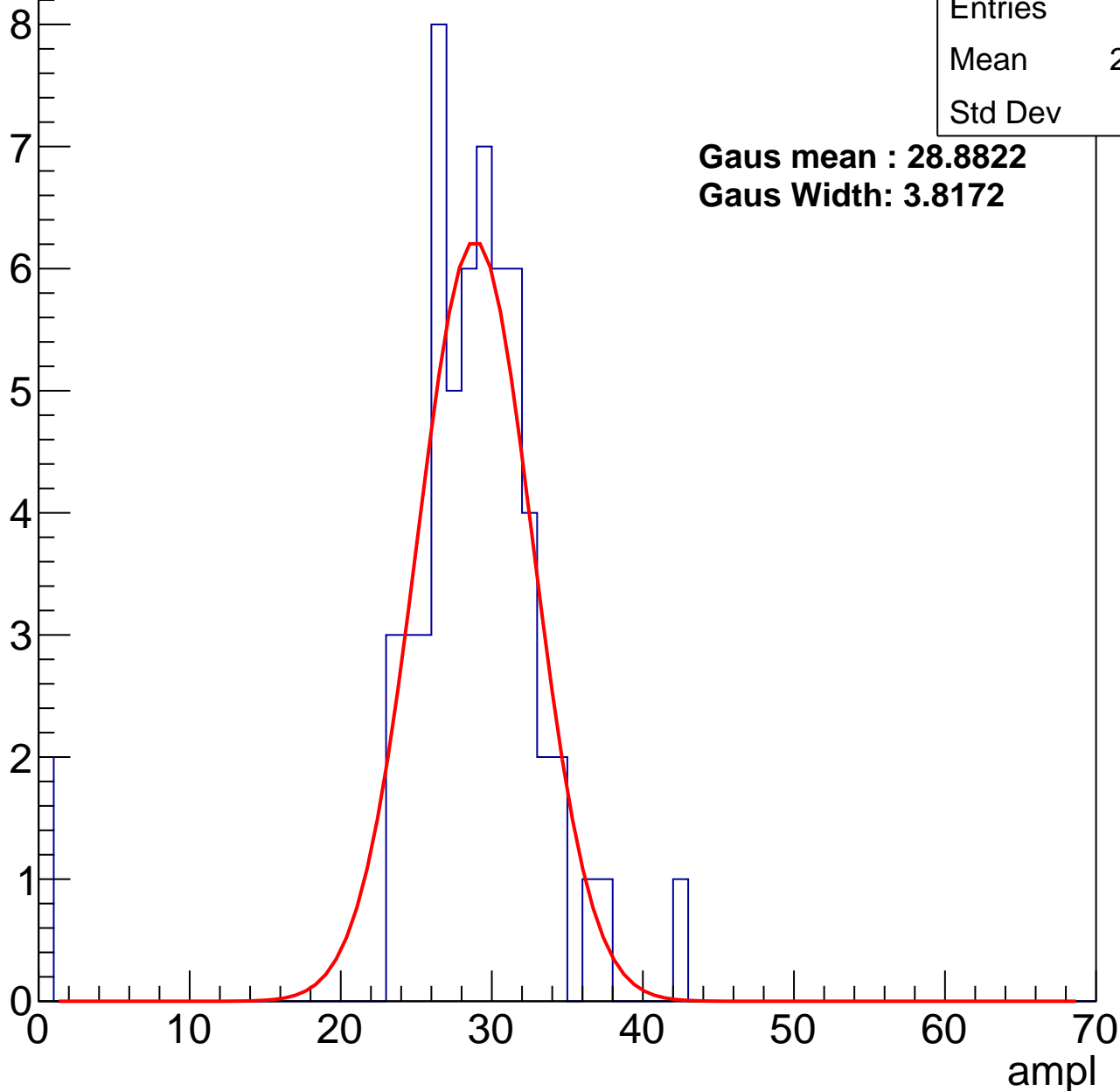
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	27.88
Std Dev	6.28

Gaus mean : 28.8822

Gaus Width: 3.8172



B1L101S, U17-ch82, adc1

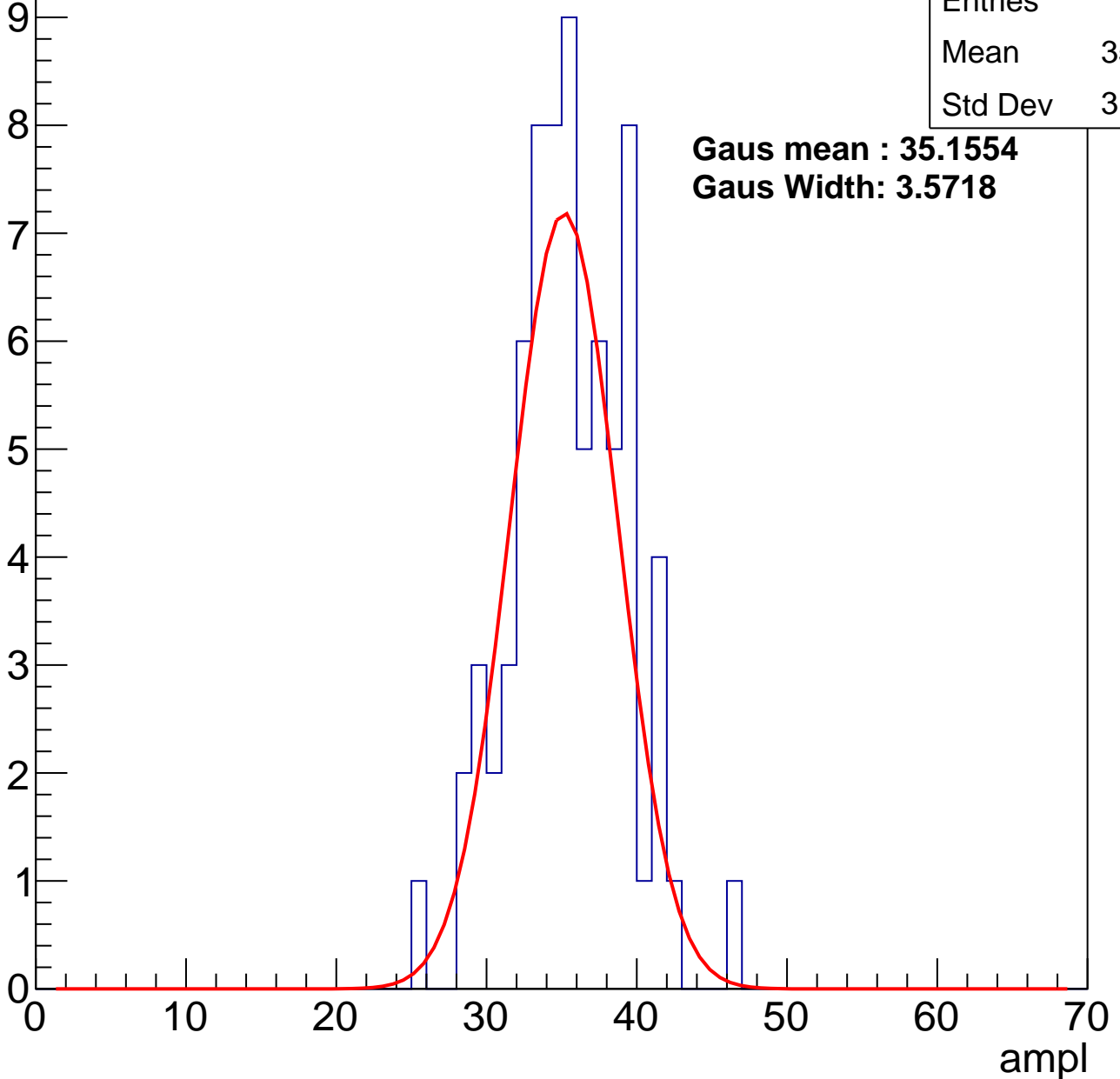
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.07
Std Dev	3.772

Gaus mean : 35.1554

Gaus Width: 3.5718



B1L101S, U17-ch82, adc2

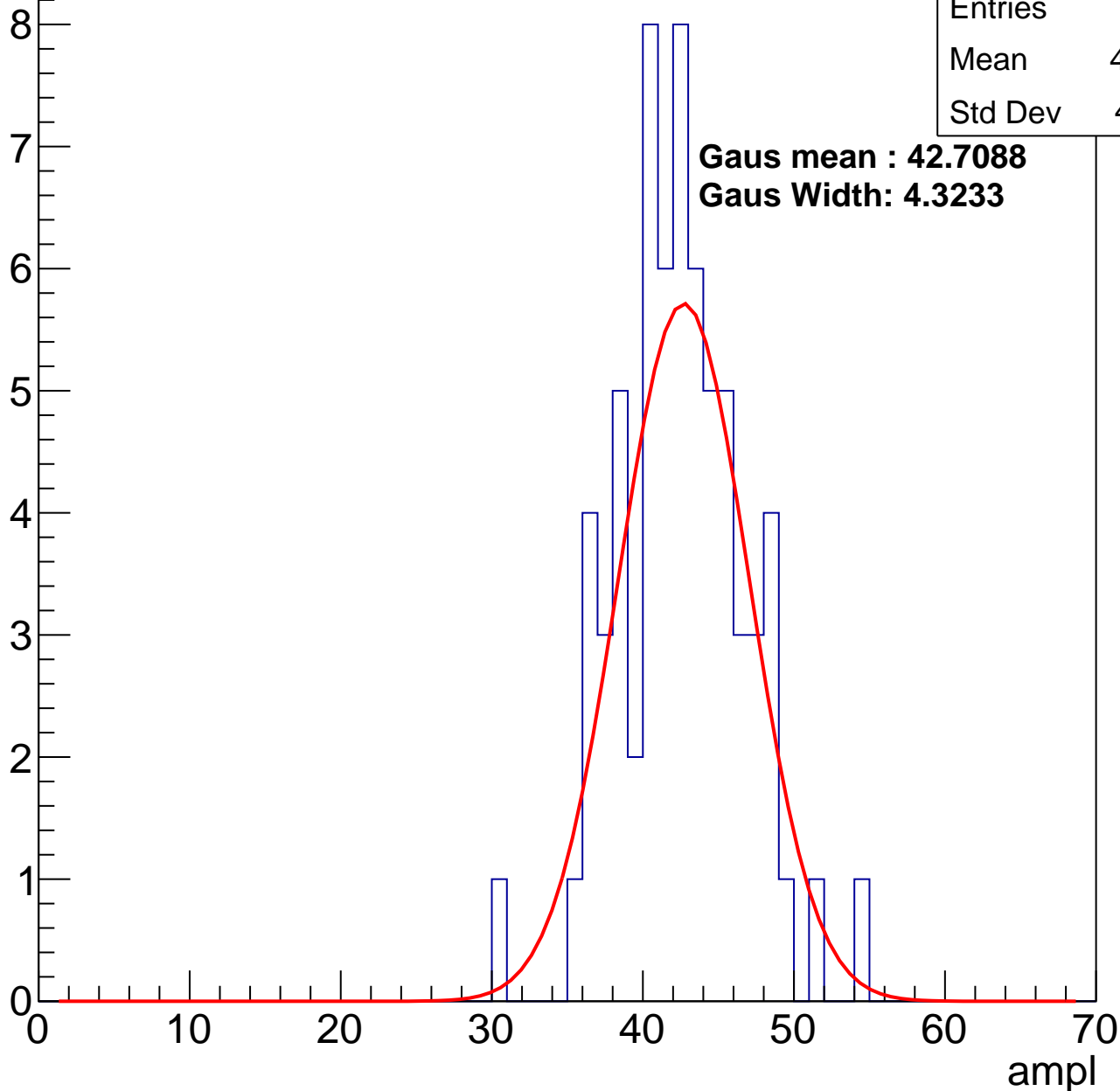
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.06
Std Dev	4.161

Gaus mean : 42.7088

Gaus Width: 4.3233

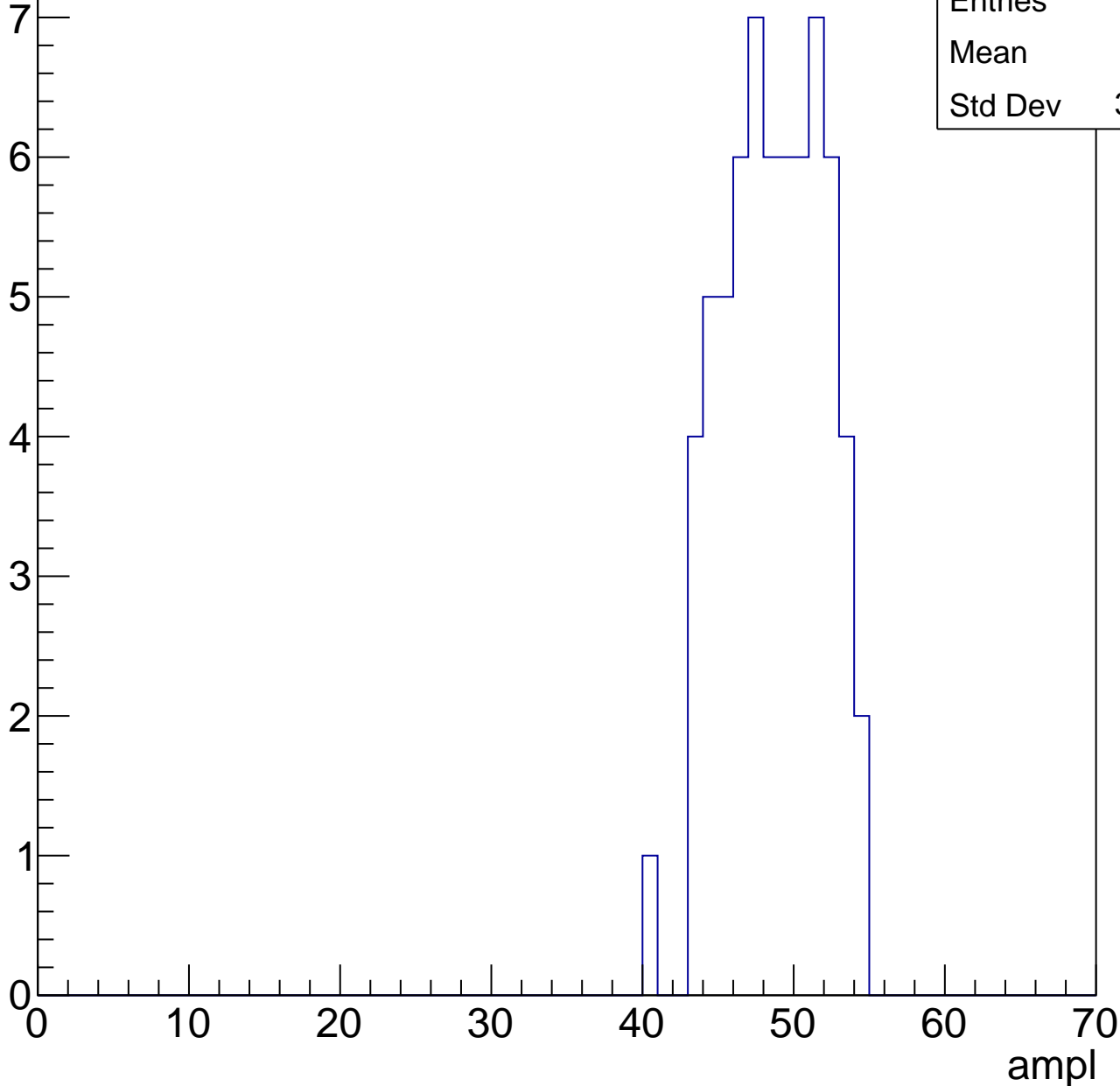


B1L101S, U17-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.2
Std Dev	3.231

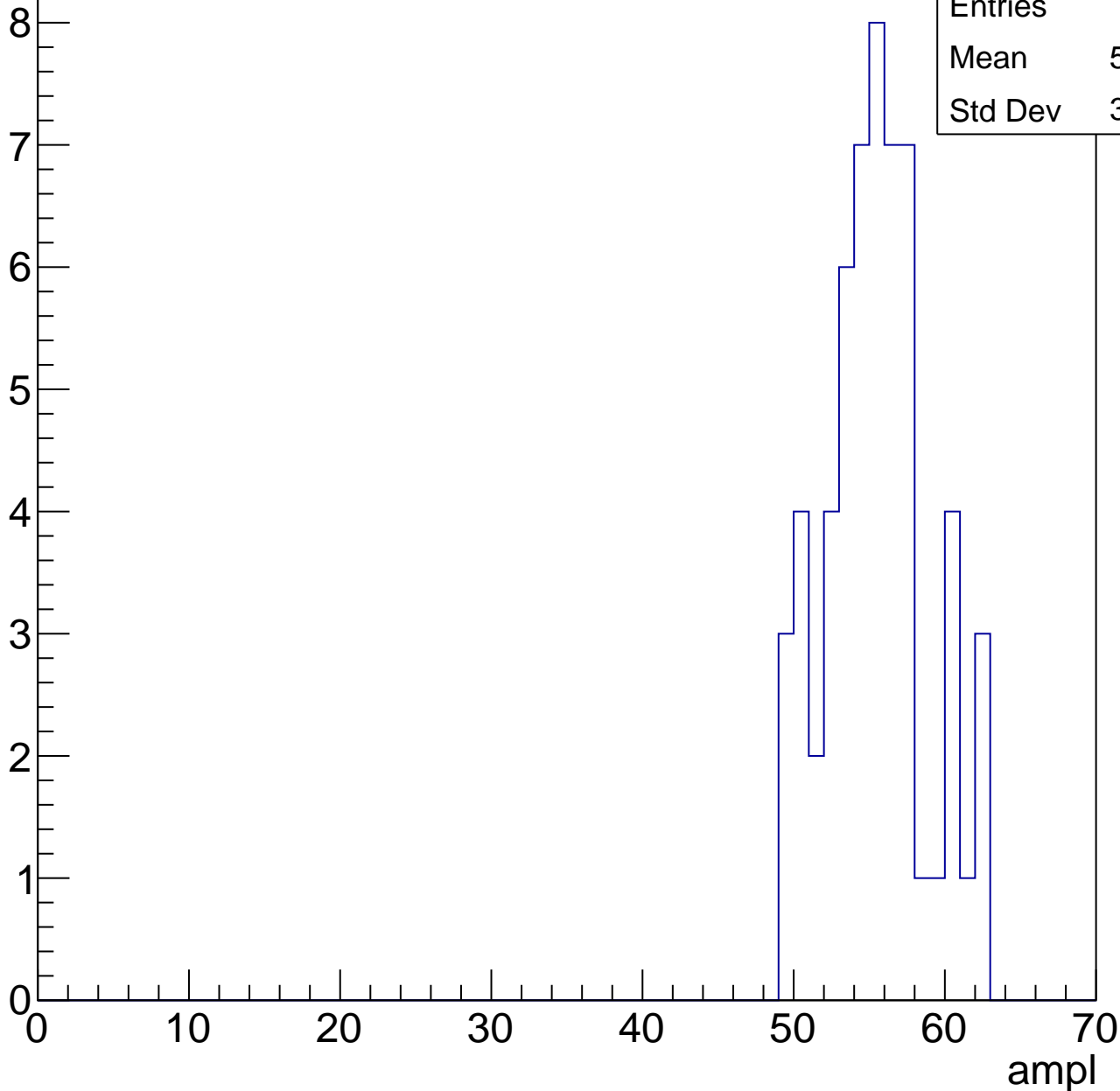


B1L101S, U17-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

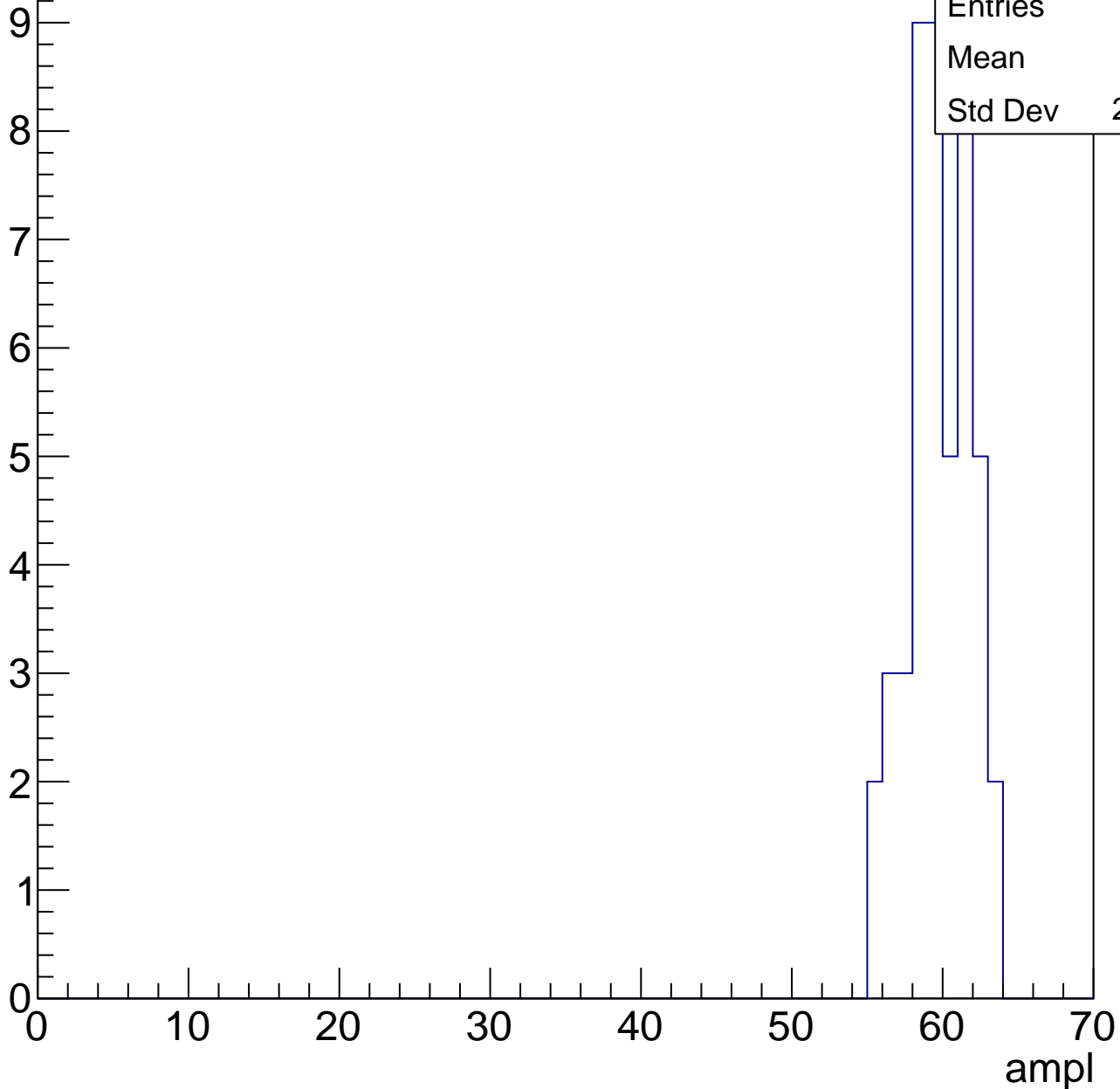
Entries	58
Mean	54.97
Std Dev	3.347



B1L101S, U17-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

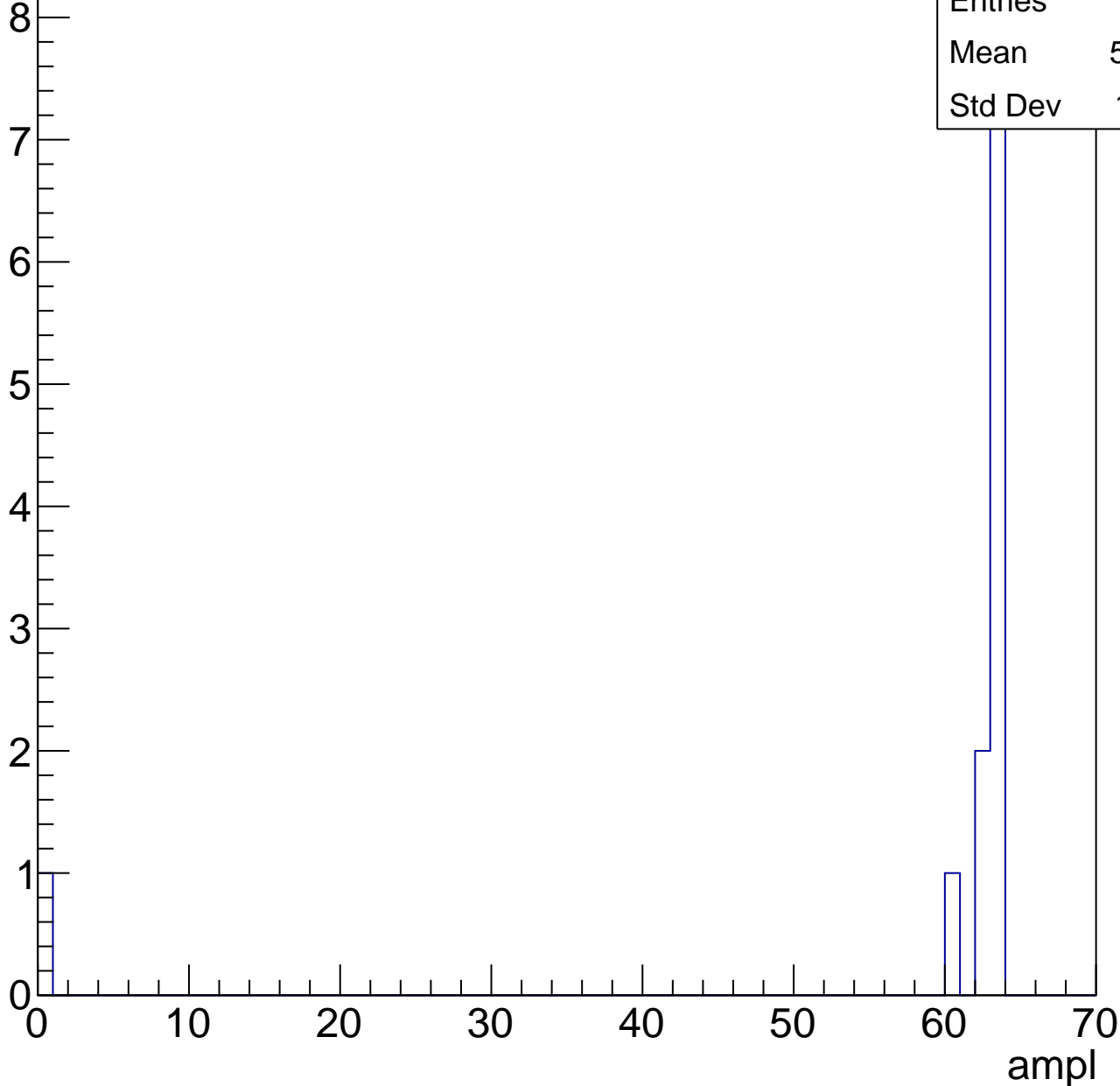


B1L101S, U17-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

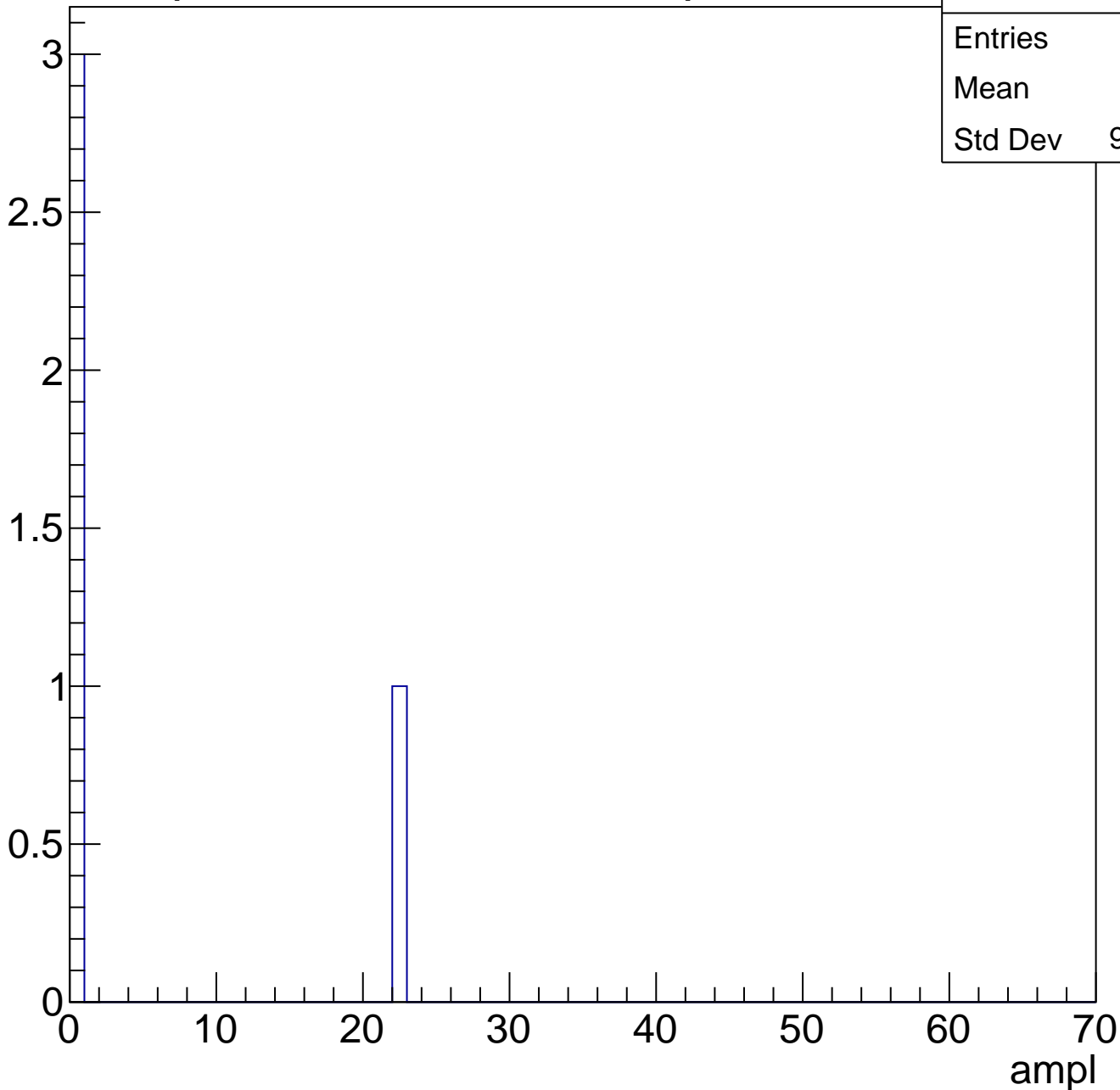
Entries	12
Mean	57.33
Std Dev	17.31



B1L101S, U17-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch83, adc0

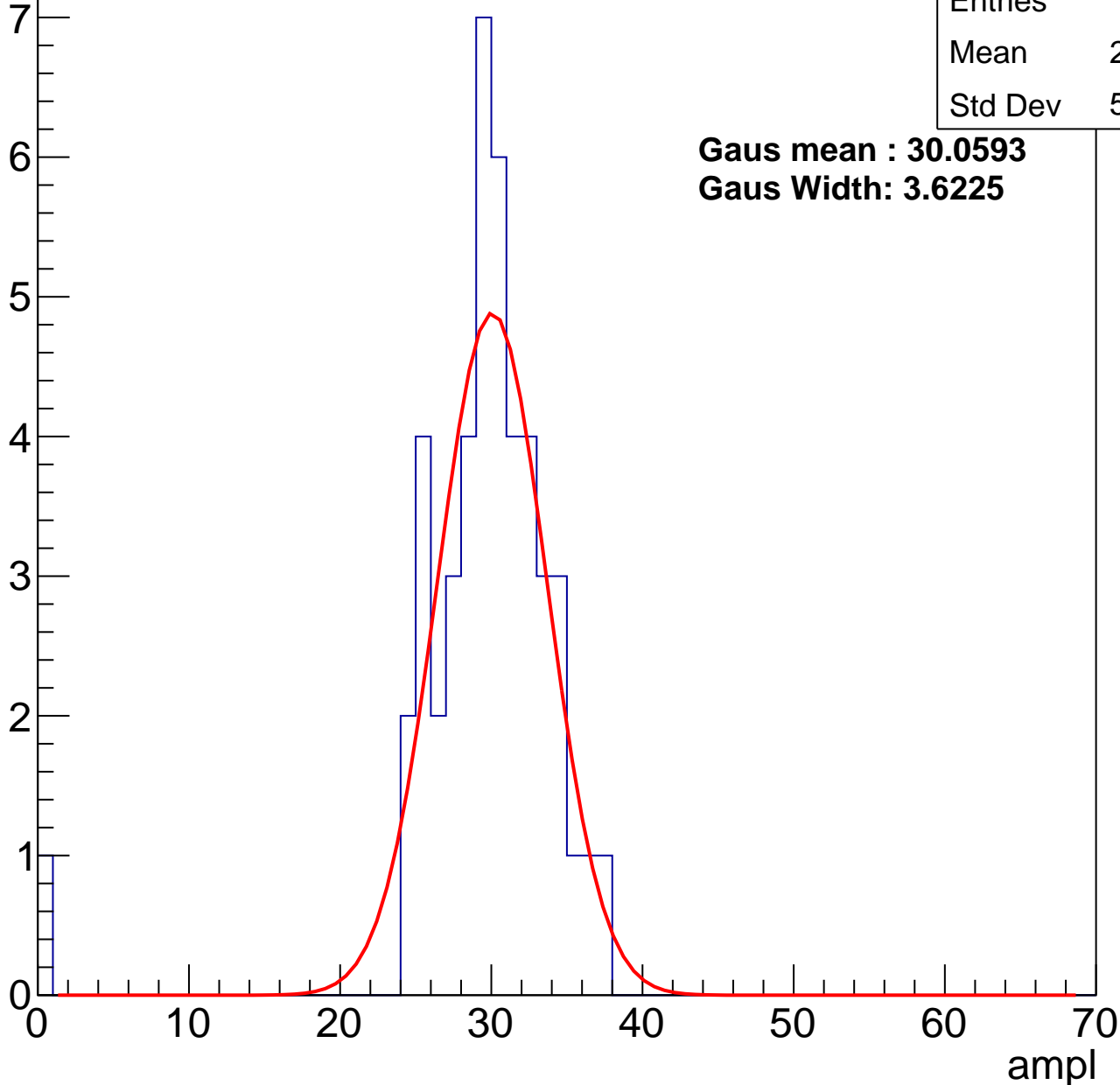
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	29.07
Std Dev	5.354

Gaus mean : 30.0593

Gaus Width: 3.6225



B1L101S, U17-ch83, adc1

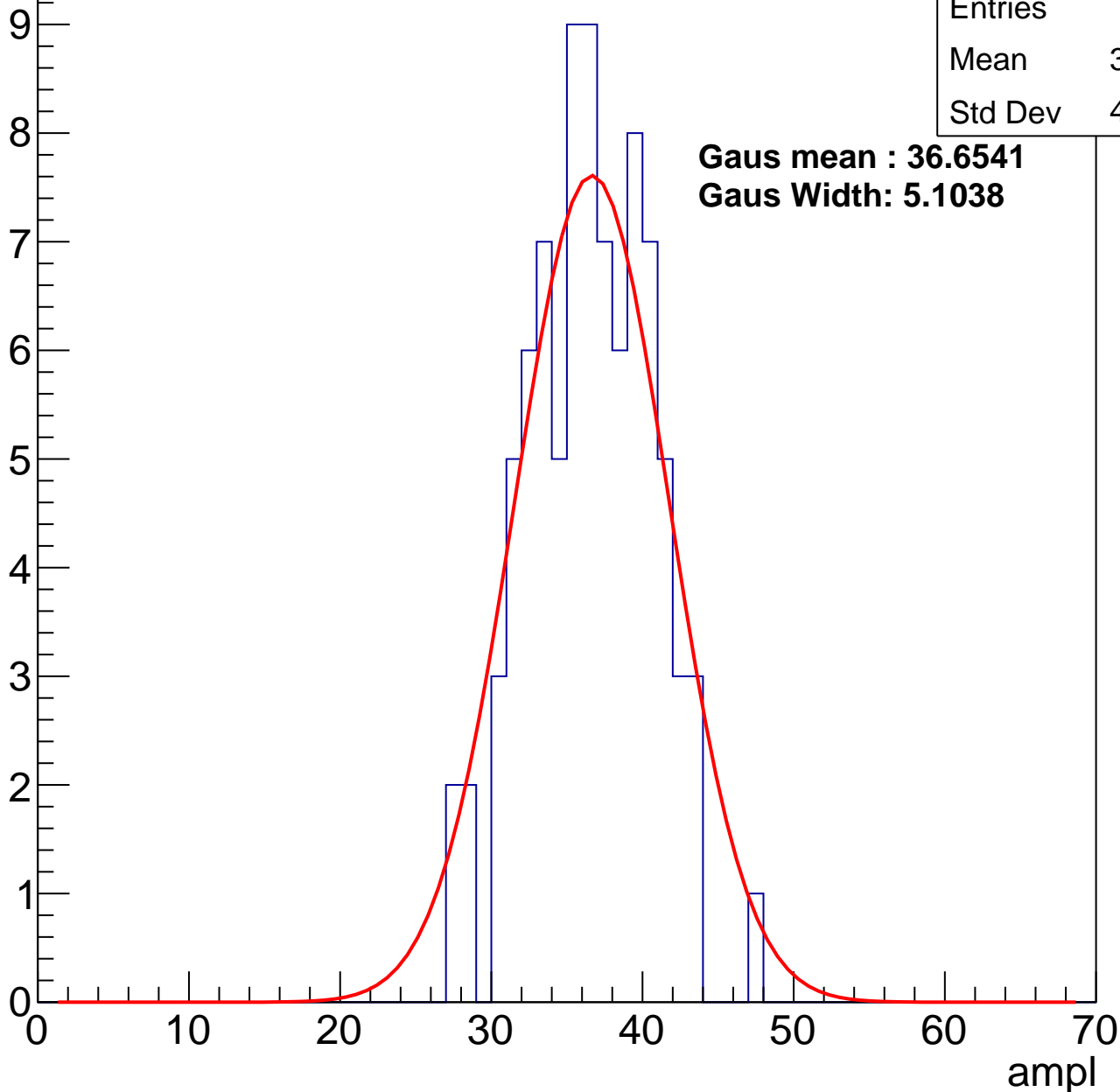
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	36.06
Std Dev	4.026

Gaus mean : 36.6541

Gaus Width: 5.1038



B1L101S, U17-ch83, adc2

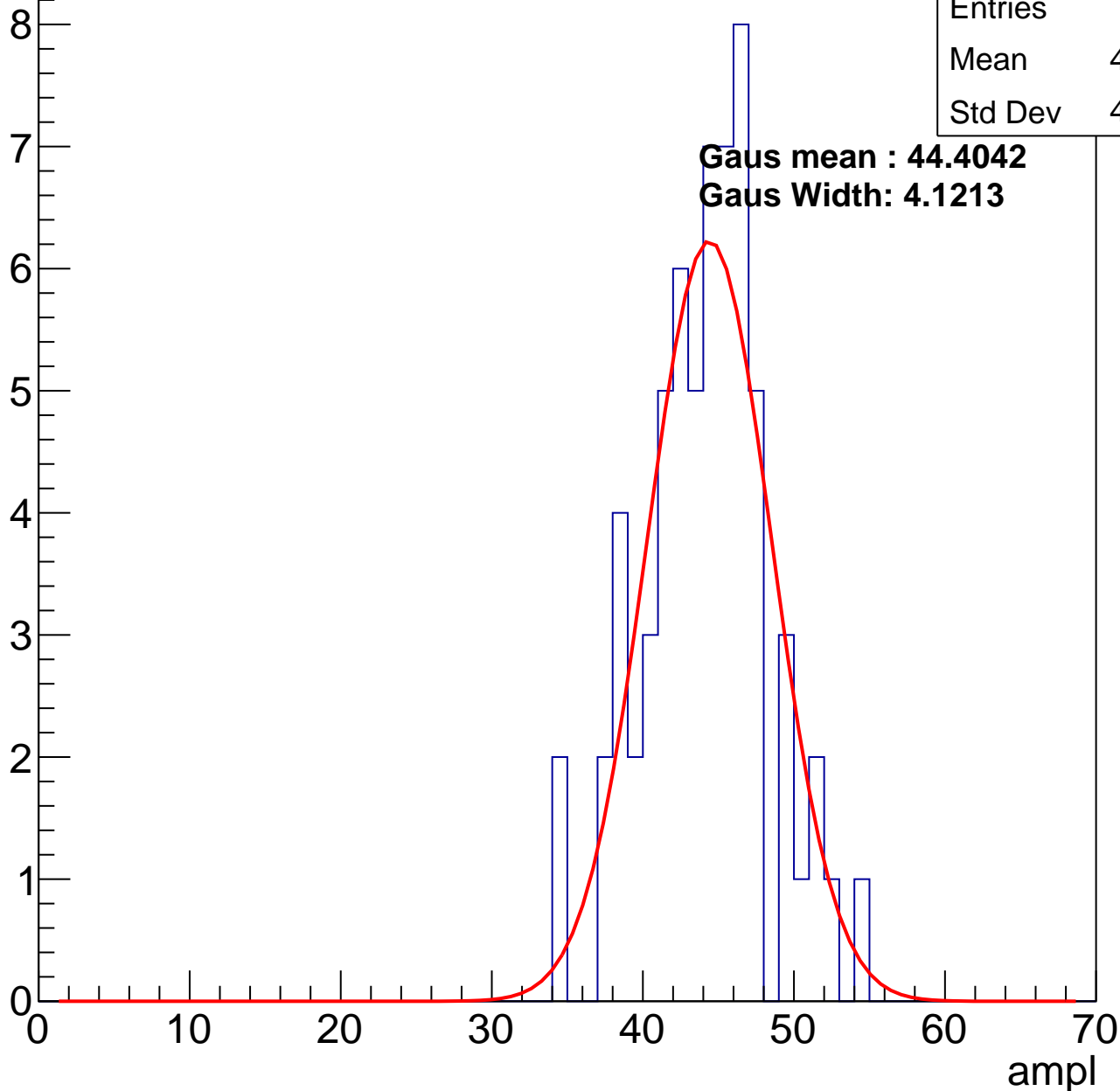
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.67
Std Dev	4.078

Gaus mean : 44.4042

Gaus Width: 4.1213



B1L101S, U17-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

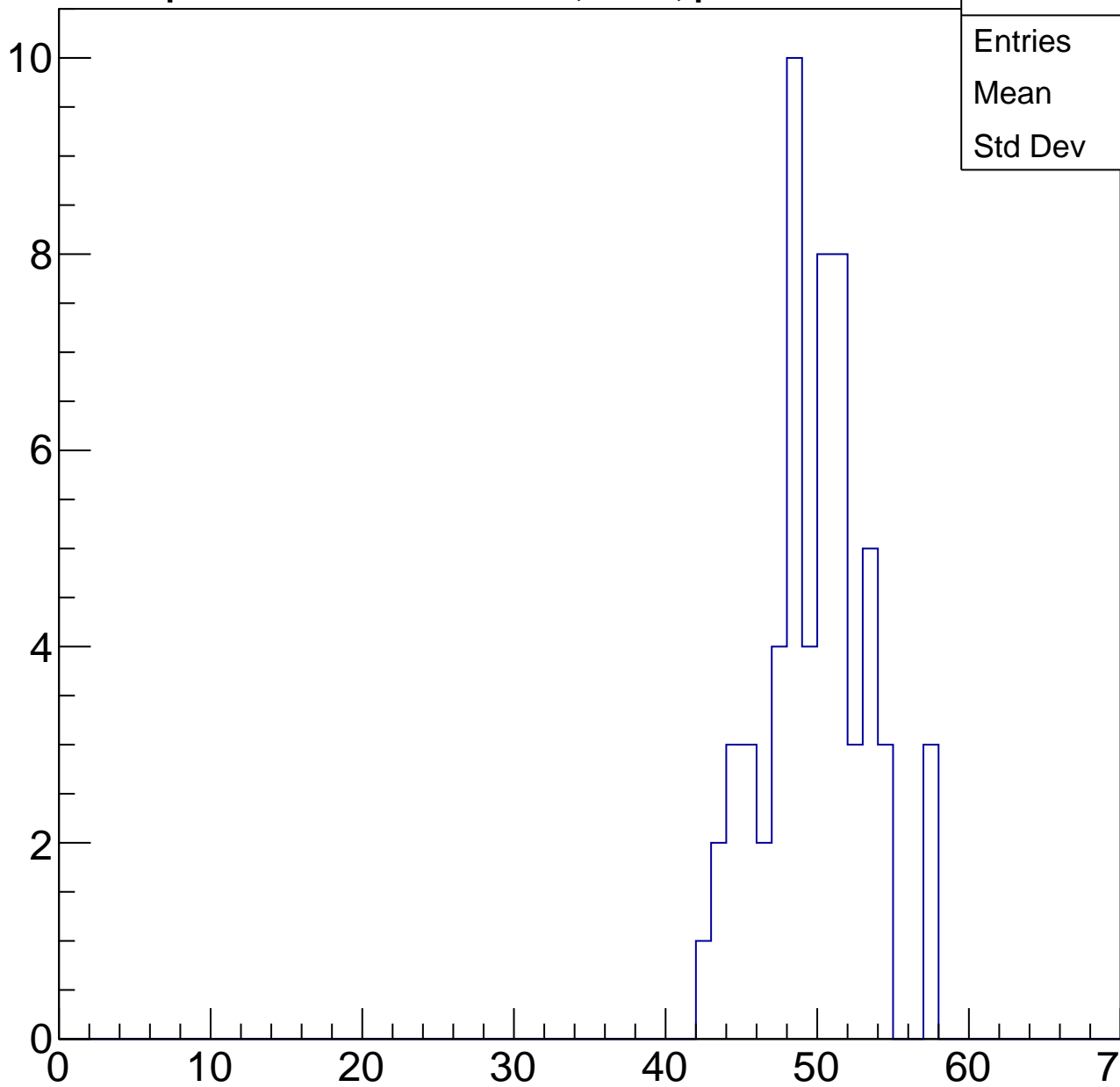
Entries	59
Mean	49.37
Std Dev	3.429

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

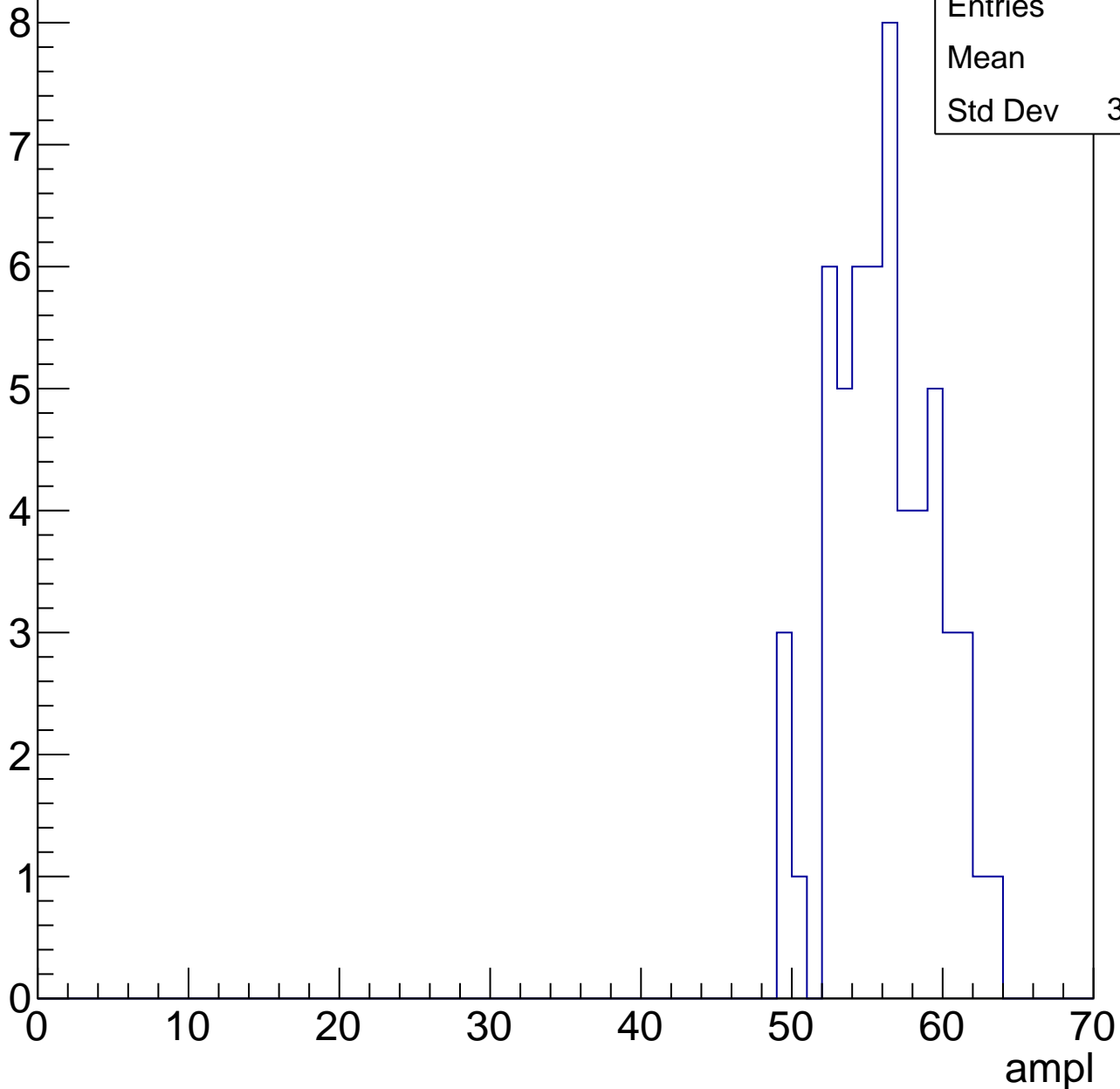


B1L101S, U17-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

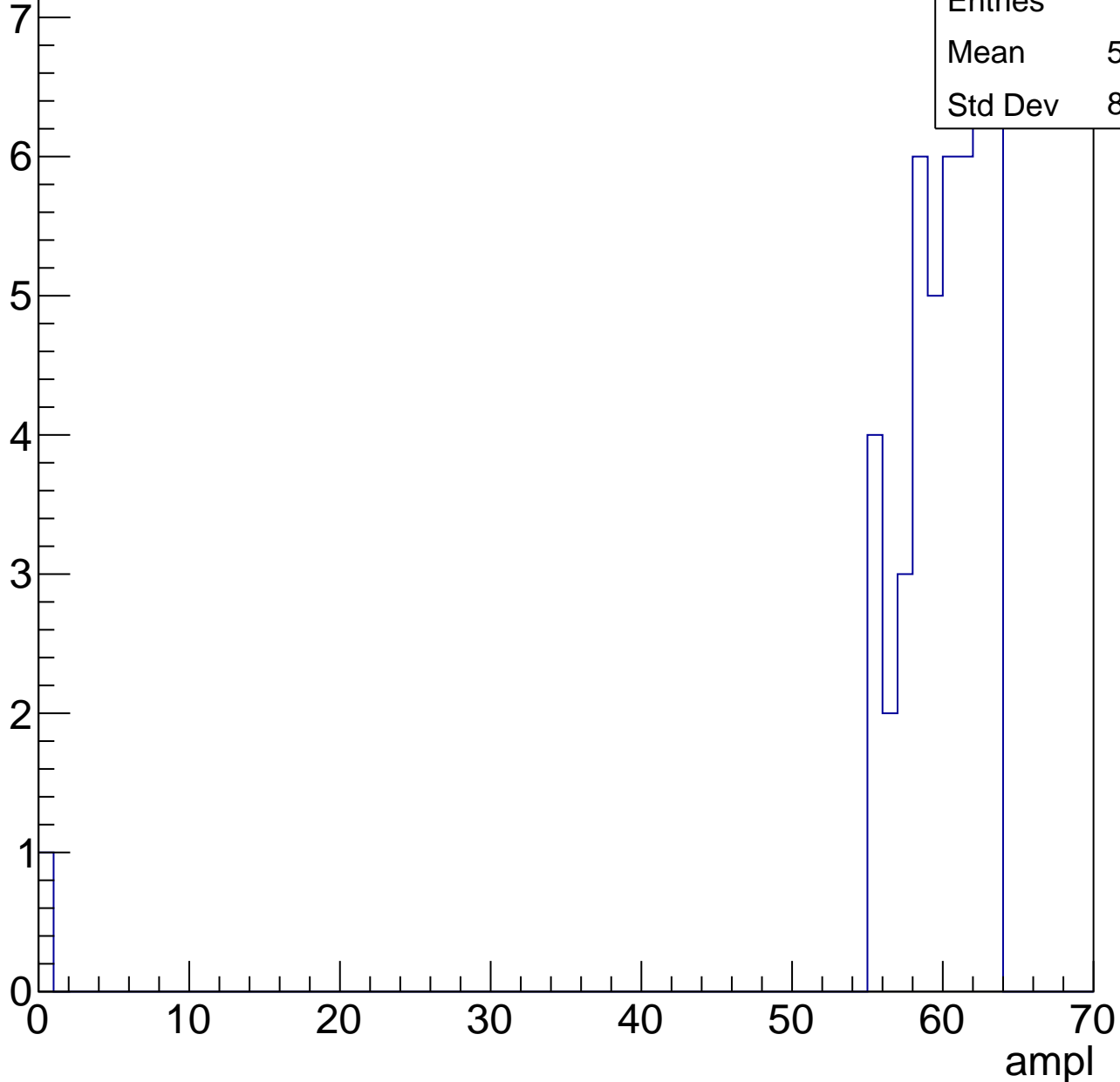
Entries	56
Mean	55.7
Std Dev	3.332



B1L101S, U17-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

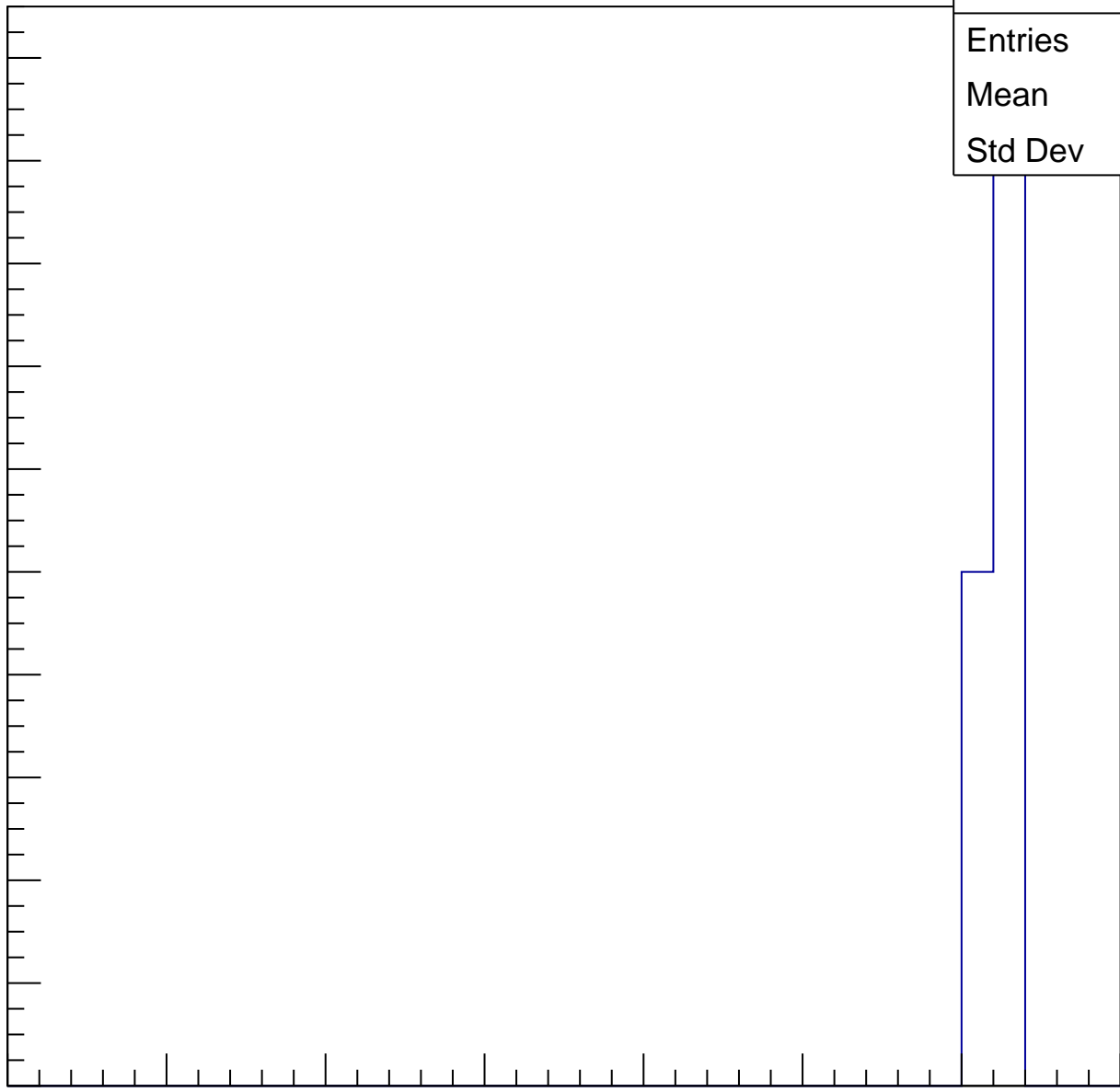
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L101S, U17-ch84, adc0

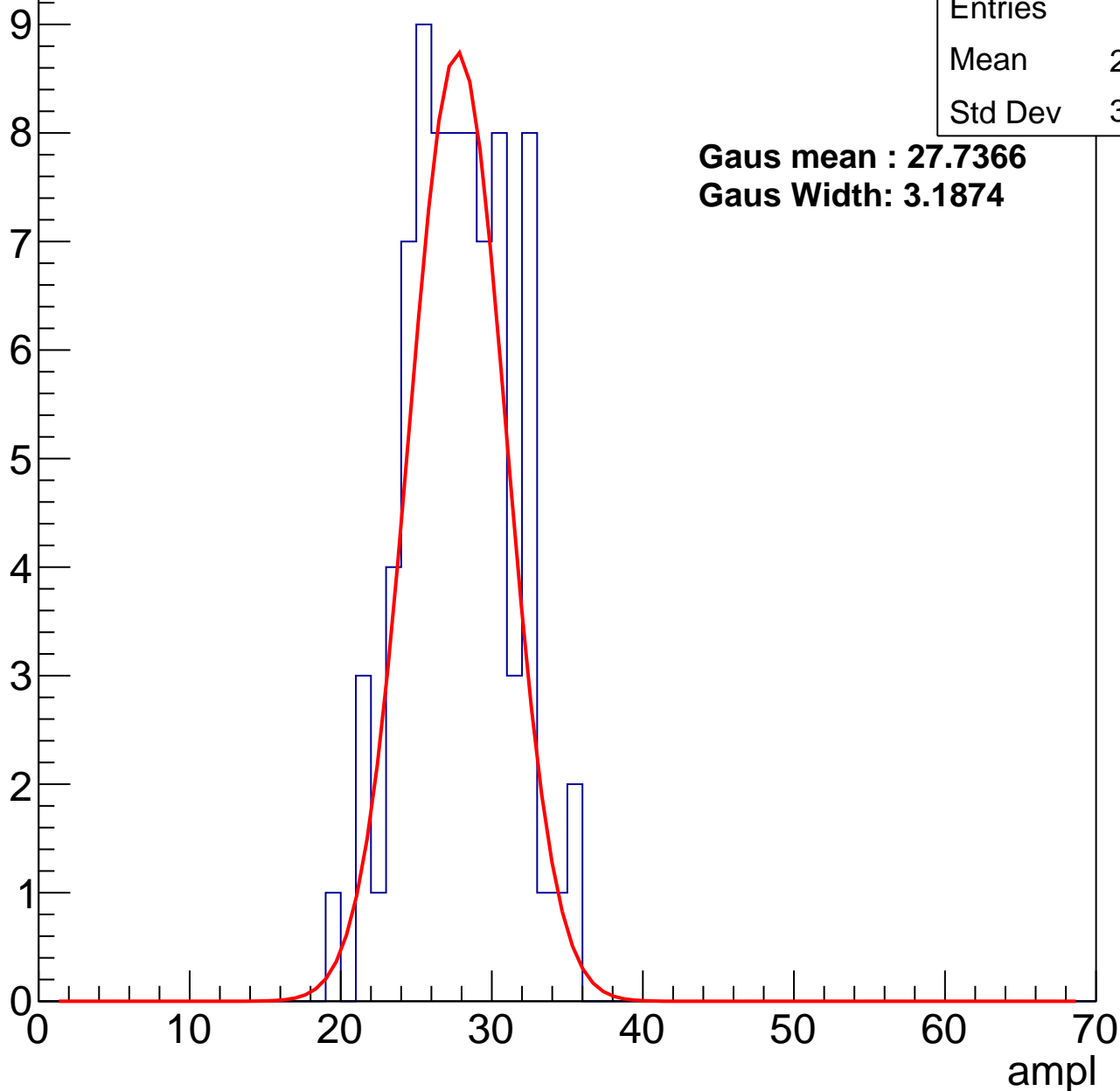
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	27.42
Std Dev	3.419

Gaus mean : 27.7366

Gaus Width: 3.1874



B1L101S, U17-ch84, adc1

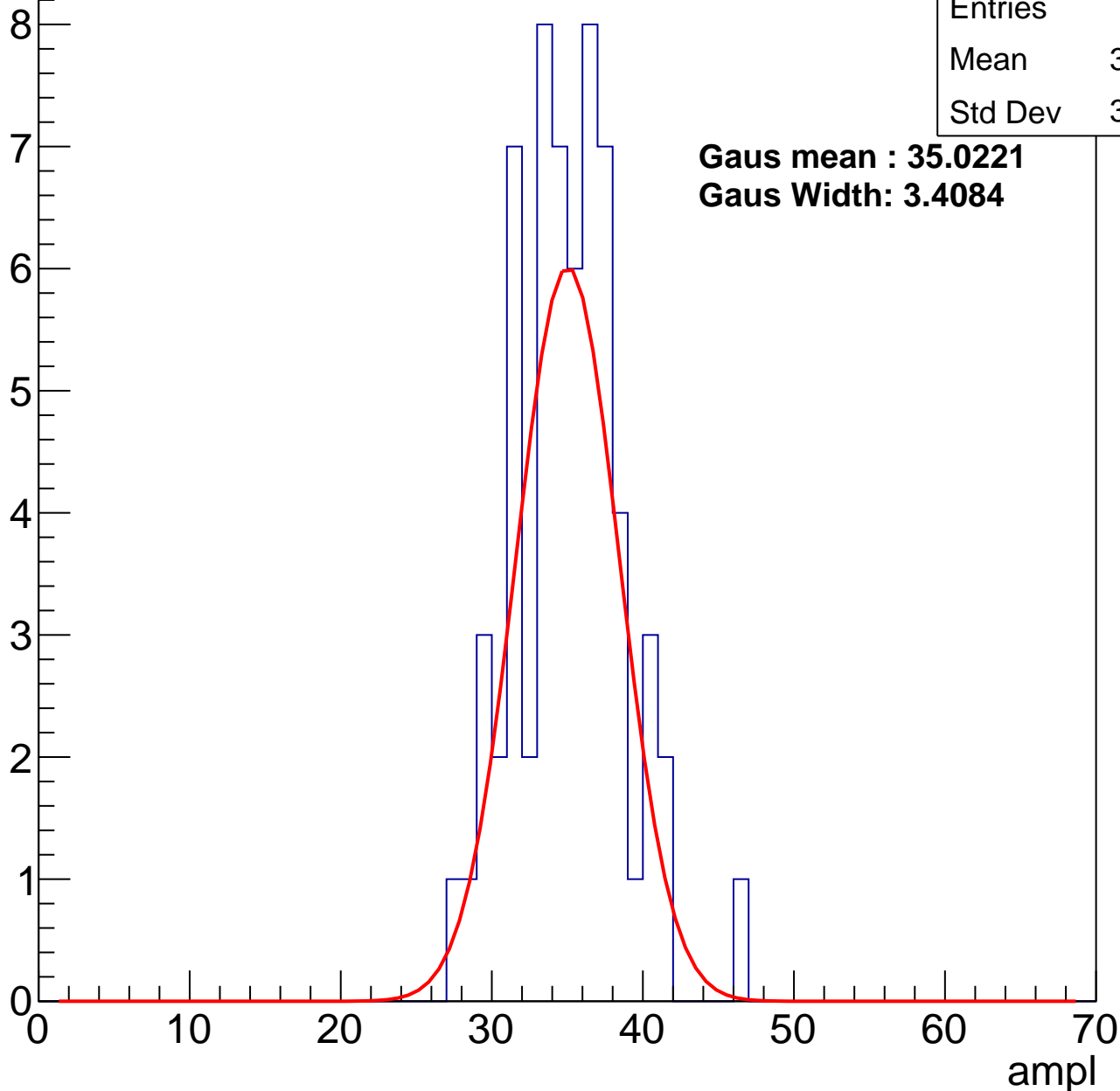
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	34.62
Std Dev	3.534

Gaus mean : 35.0221

Gaus Width: 3.4084



B1L101S, U17-ch84, adc2

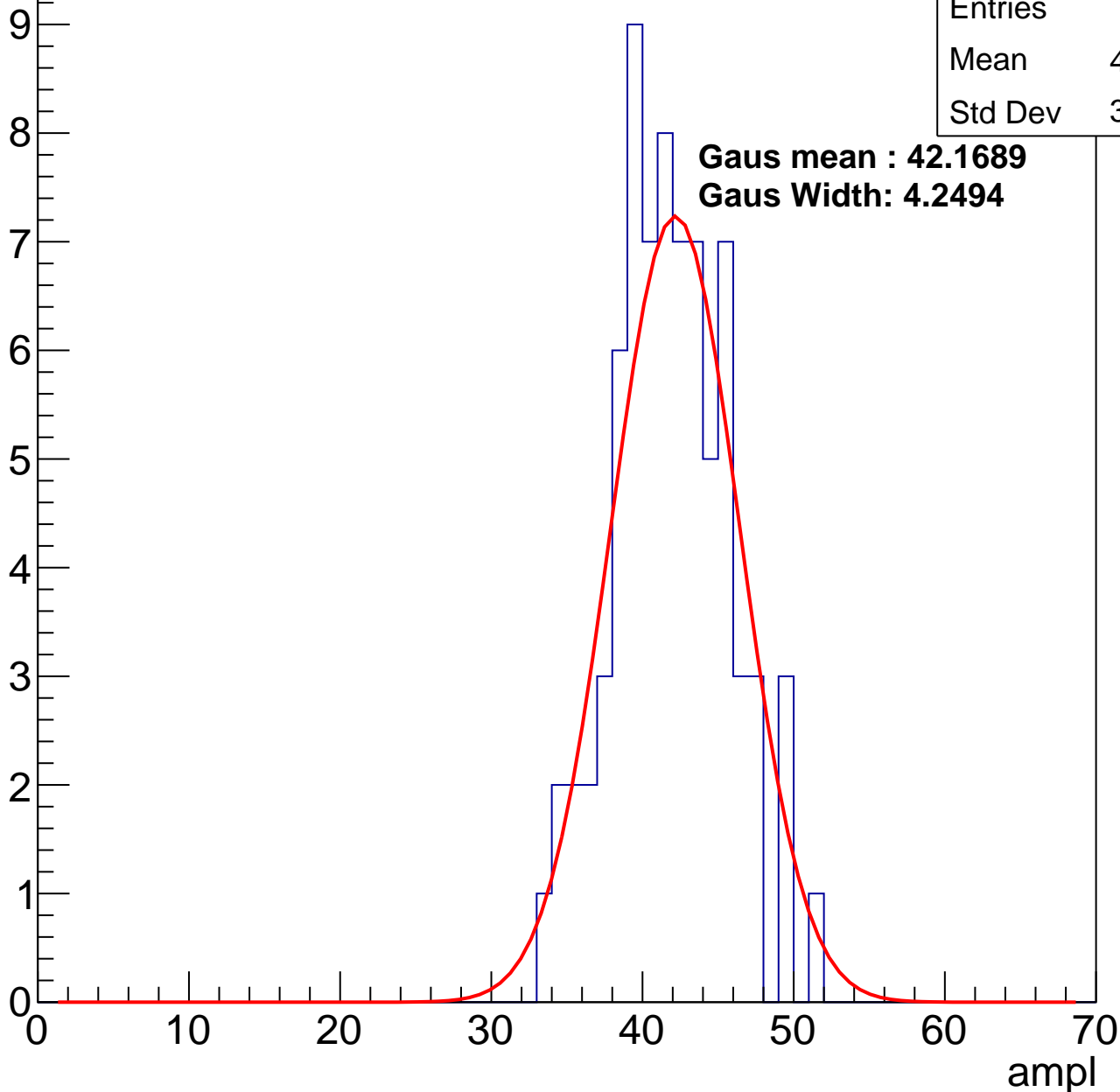
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	41.42
Std Dev	3.774

Gaus mean : 42.1689

Gaus Width: 4.2494

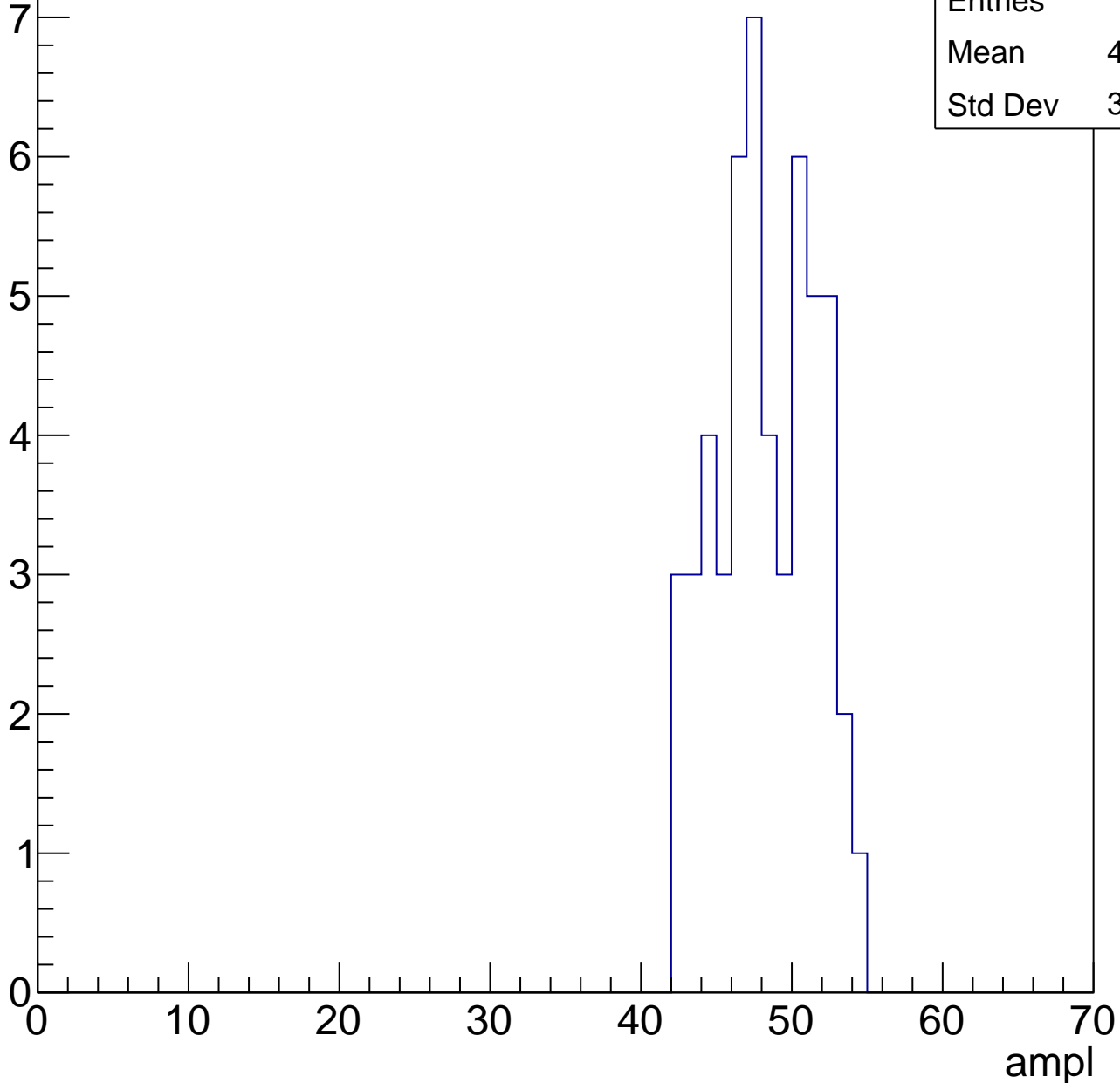


B1L101S, U17-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	47.79
Std Dev	3.225

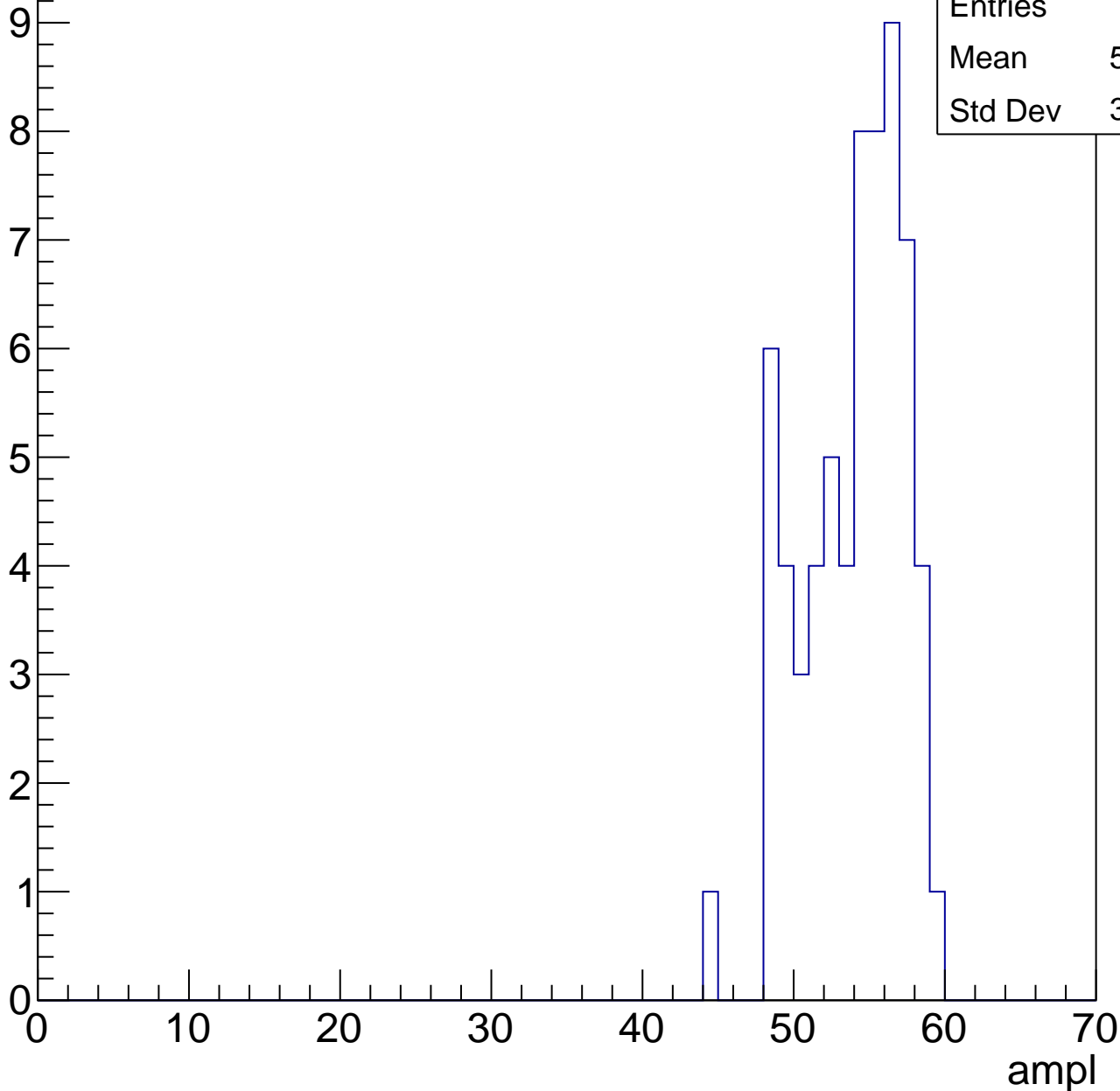


B1L101S, U17-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	53.44
Std Dev	3.307

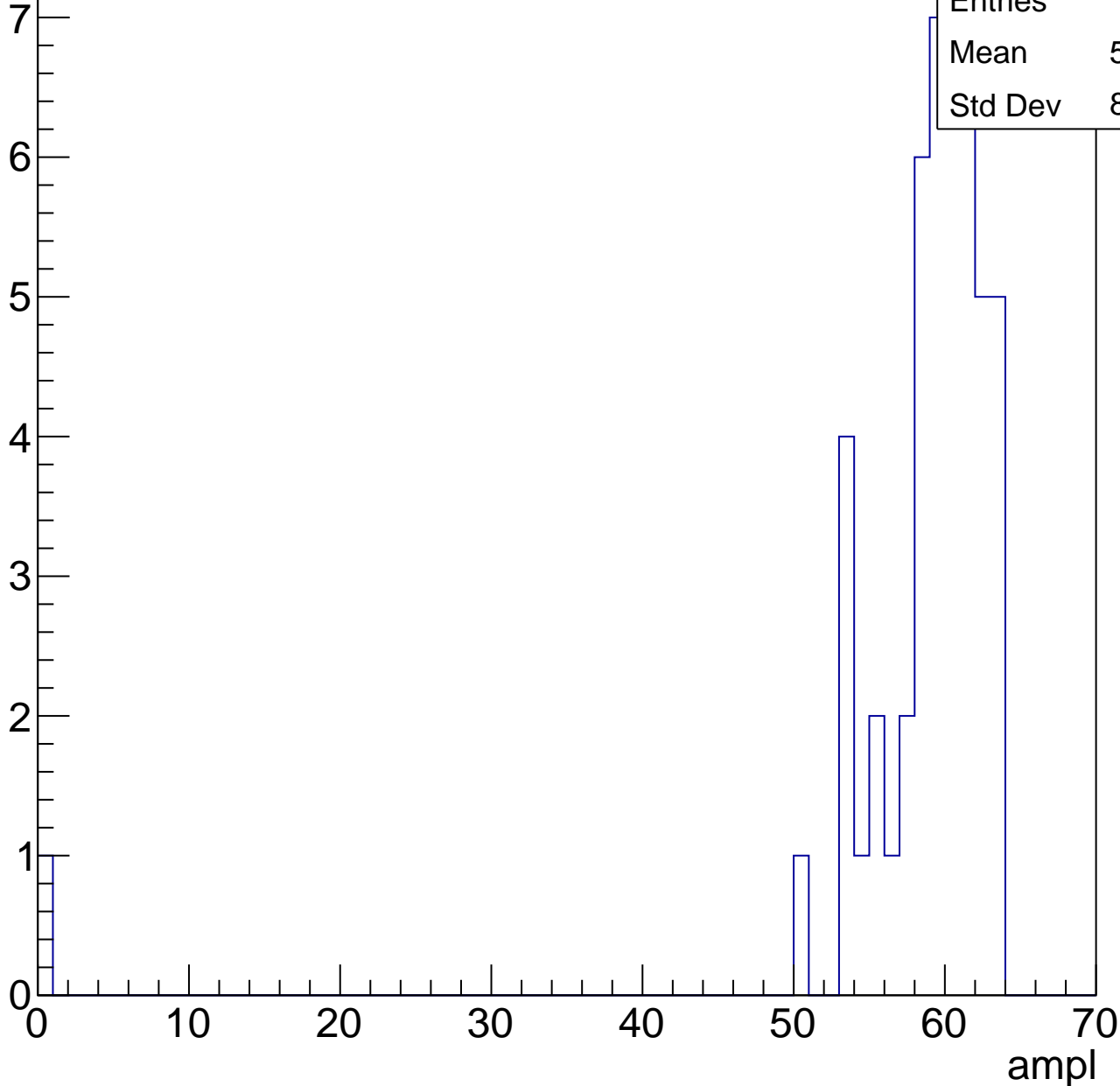


B1L101S, U17-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.73
Std Dev	8.884

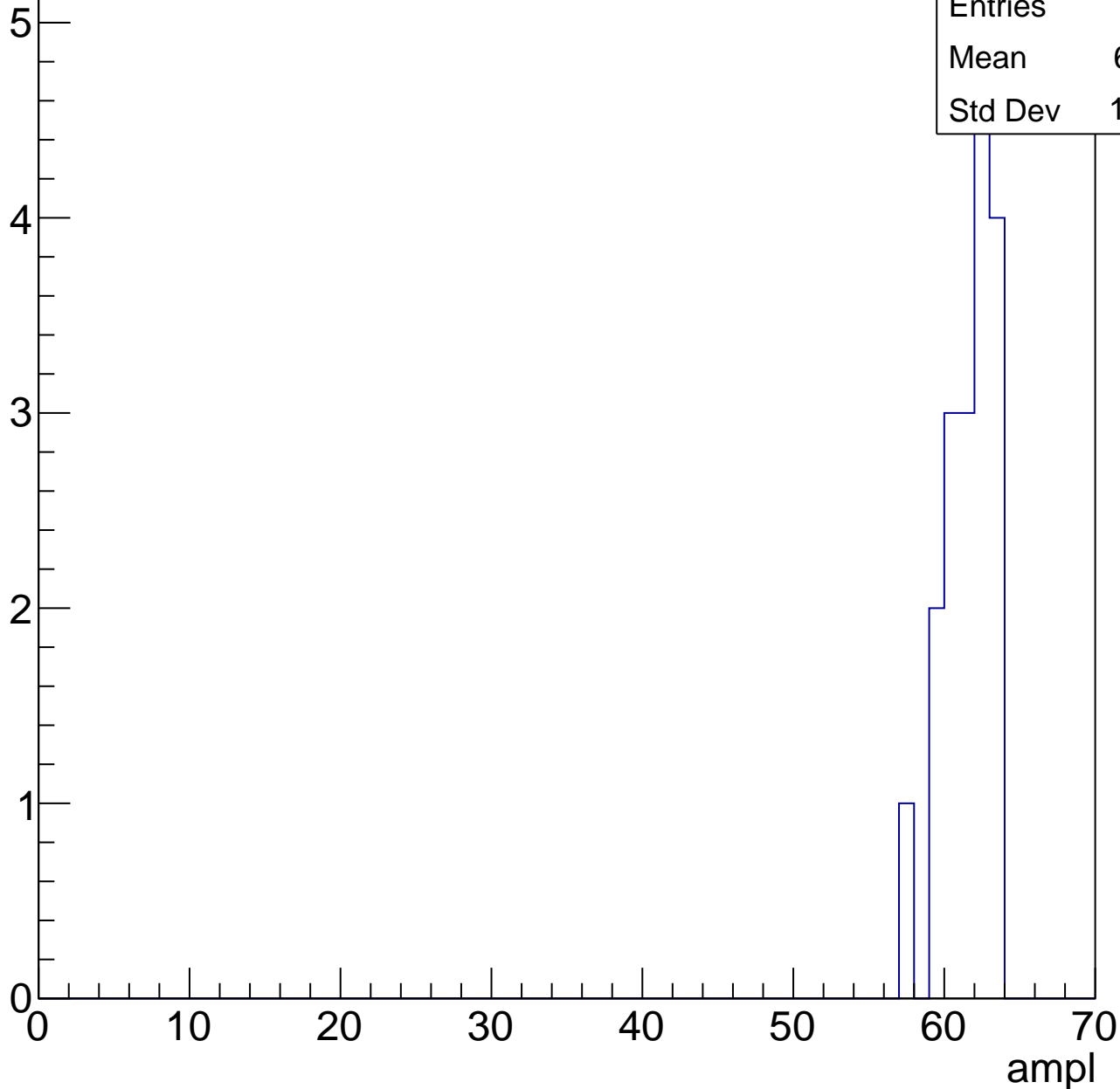


B1L101S, U17-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.11
Std Dev	1.629



B1L101S, U17-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch85, adc0

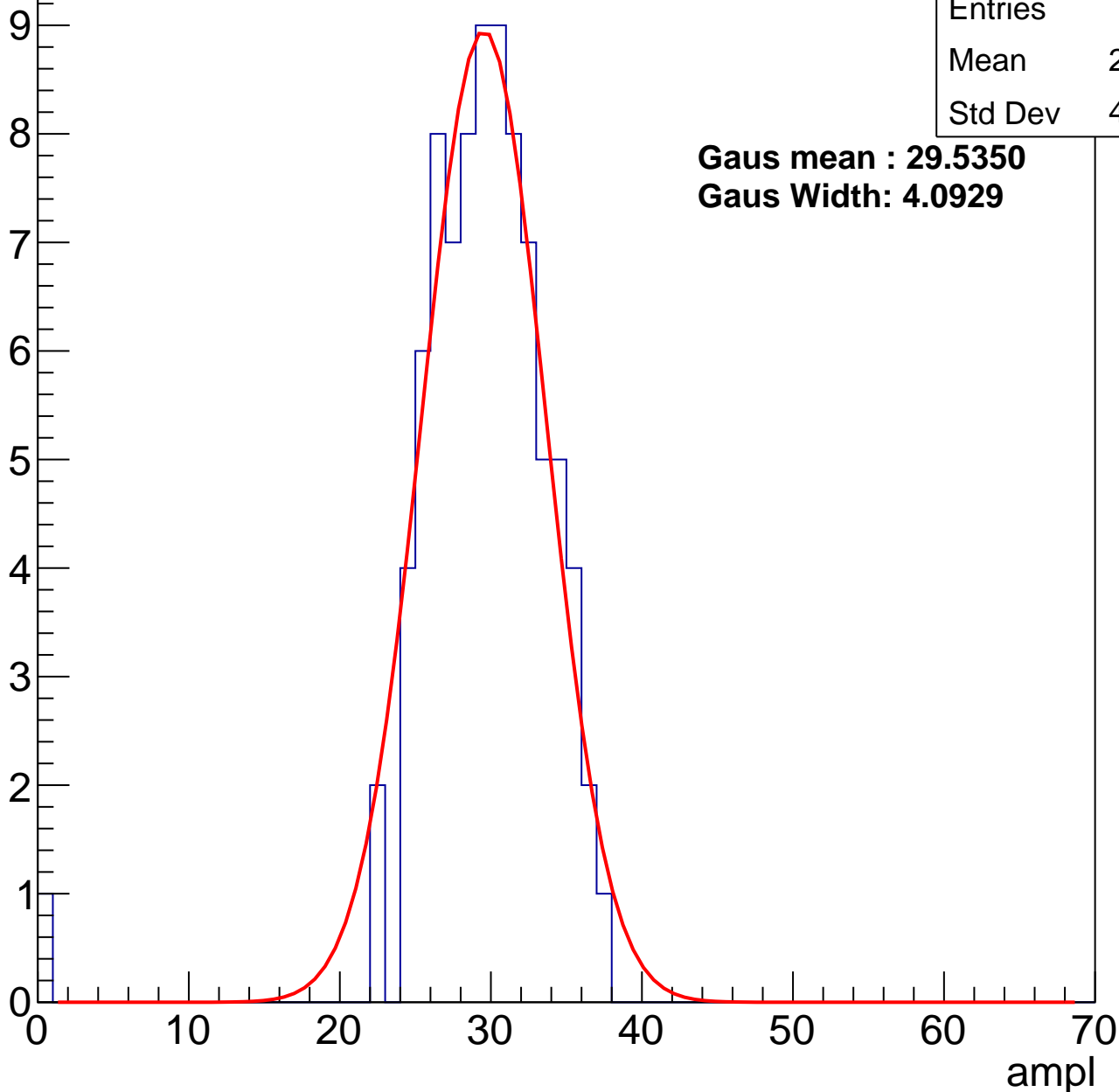
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.05
Std Dev	4.648

Gaus mean : 29.5350

Gaus Width: 4.0929



B1L101S, U17-ch85, adc1

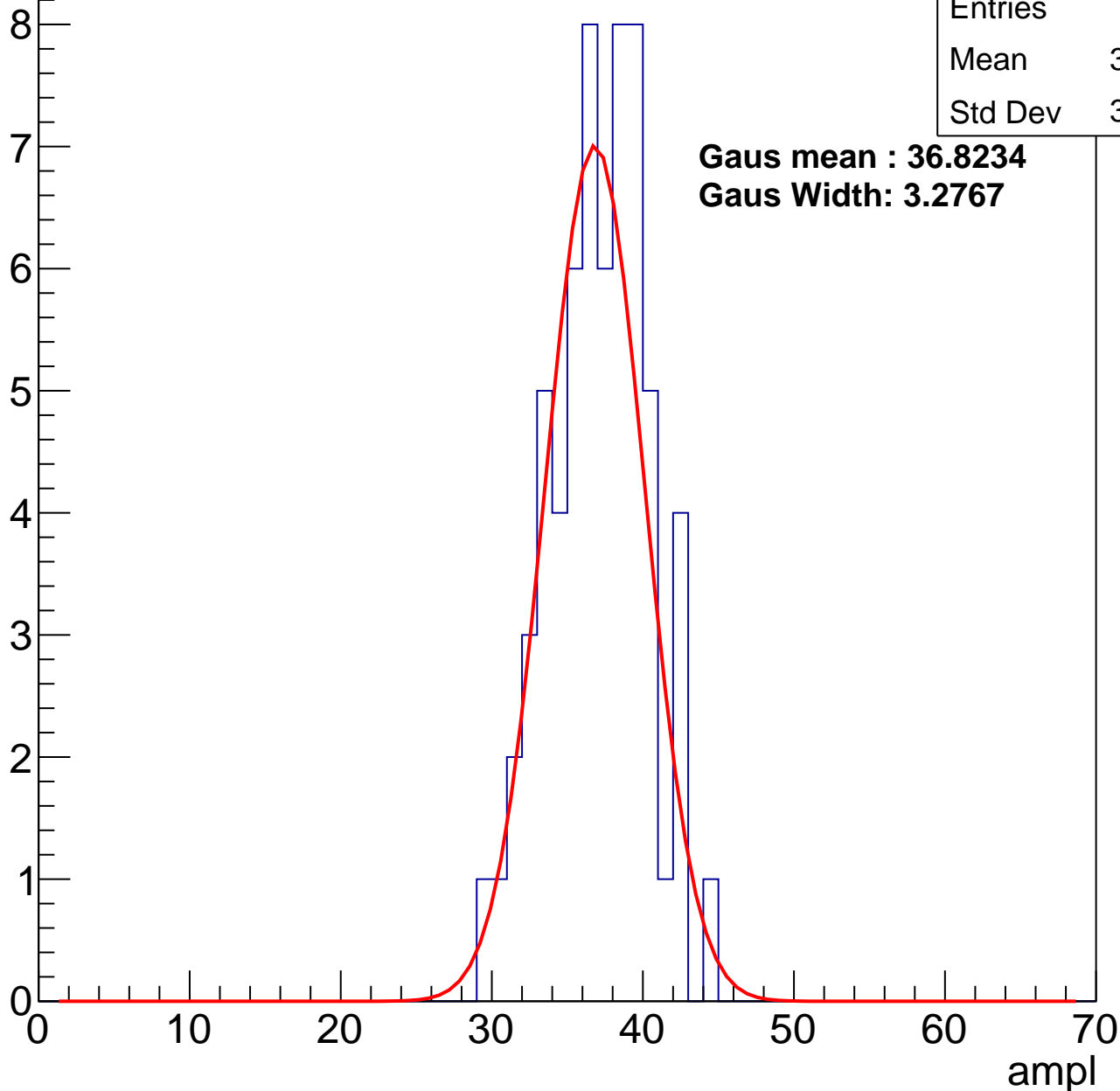
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.62
Std Dev	3.209

Gaus mean : 36.8234

Gaus Width: 3.2767



B1L101S, U17-ch85, adc2

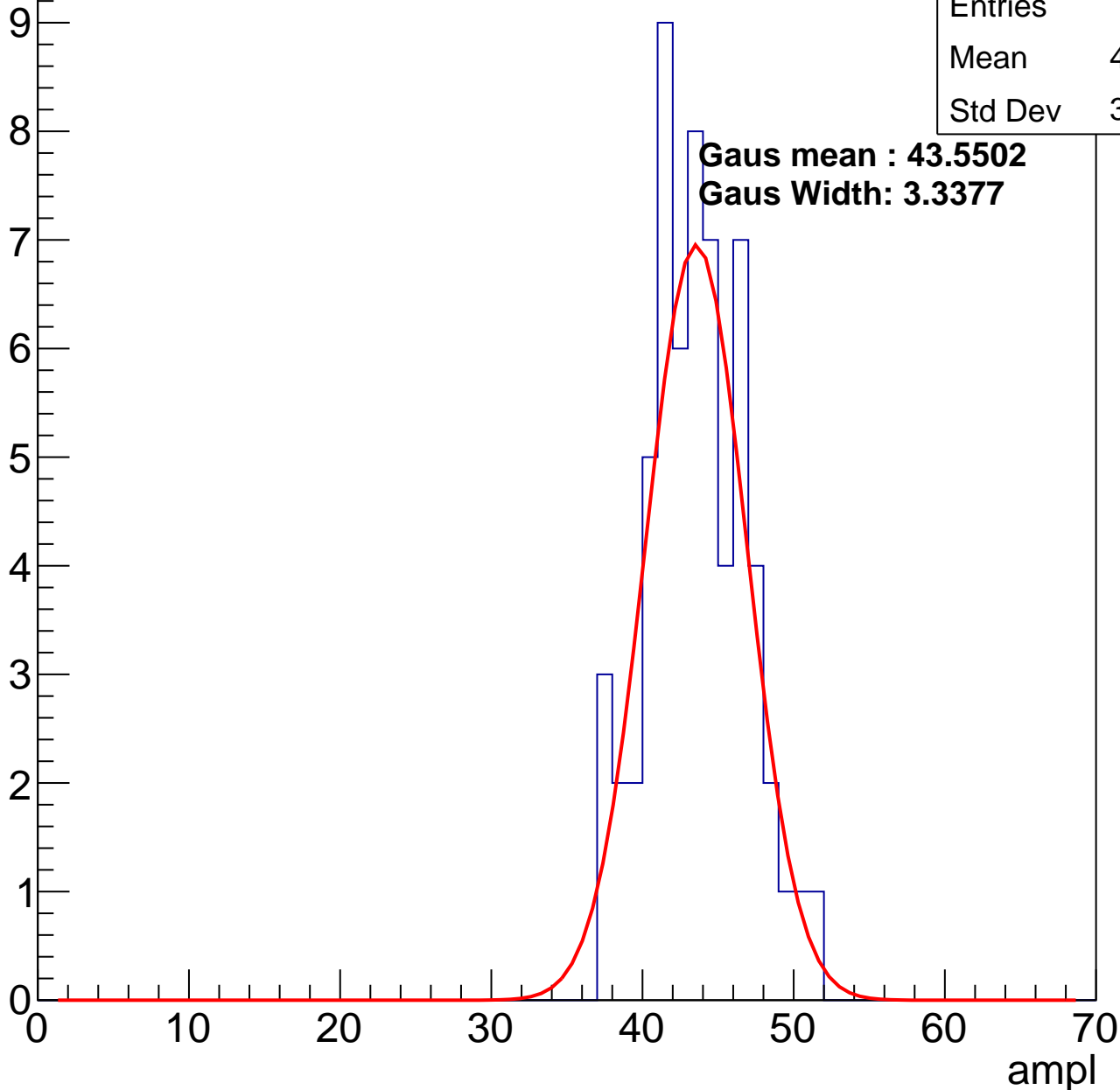
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.13
Std Dev	3.175

Gaus mean : 43.5502

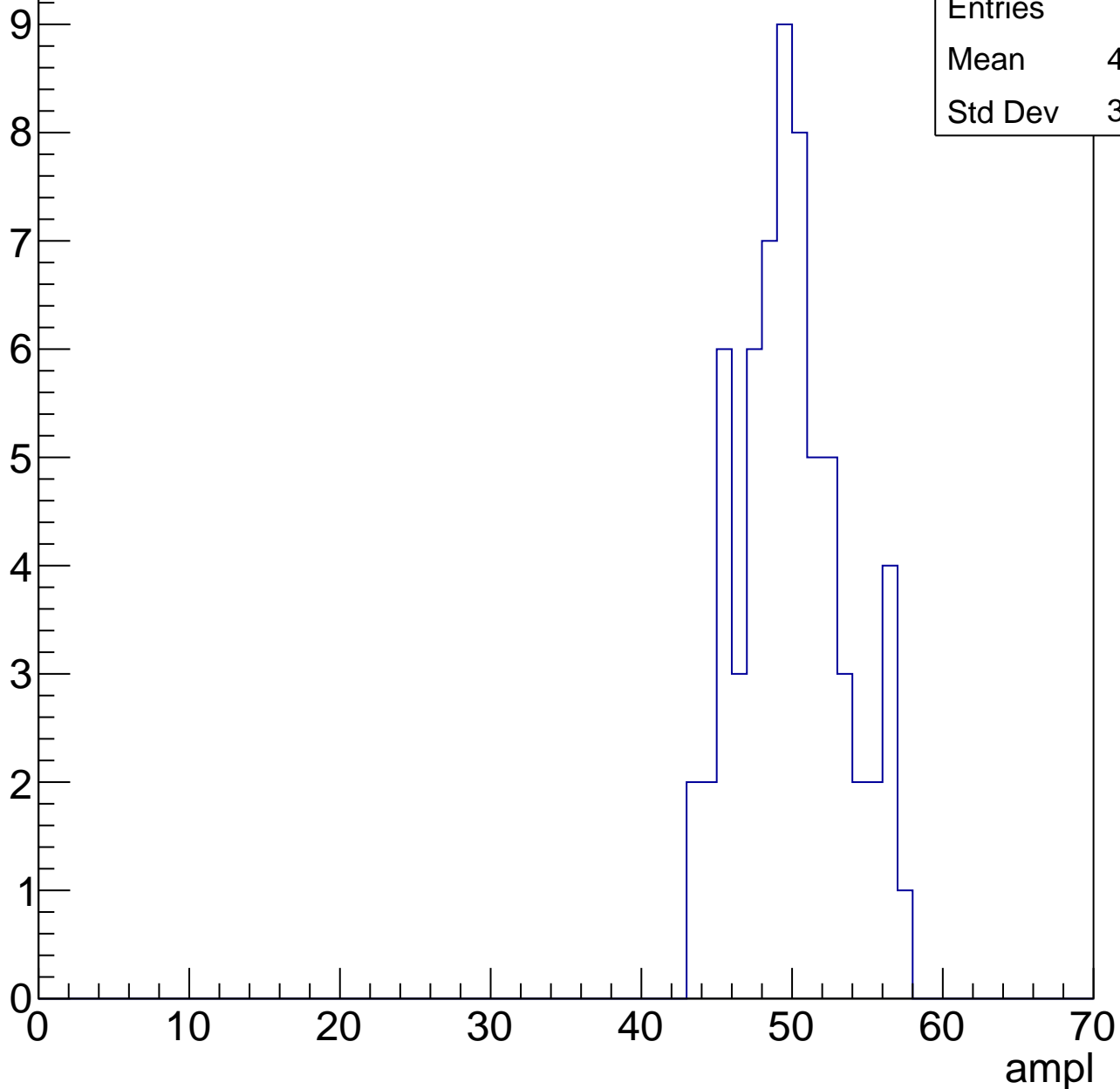
Gaus Width: 3.3377



B1L101S, U17-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



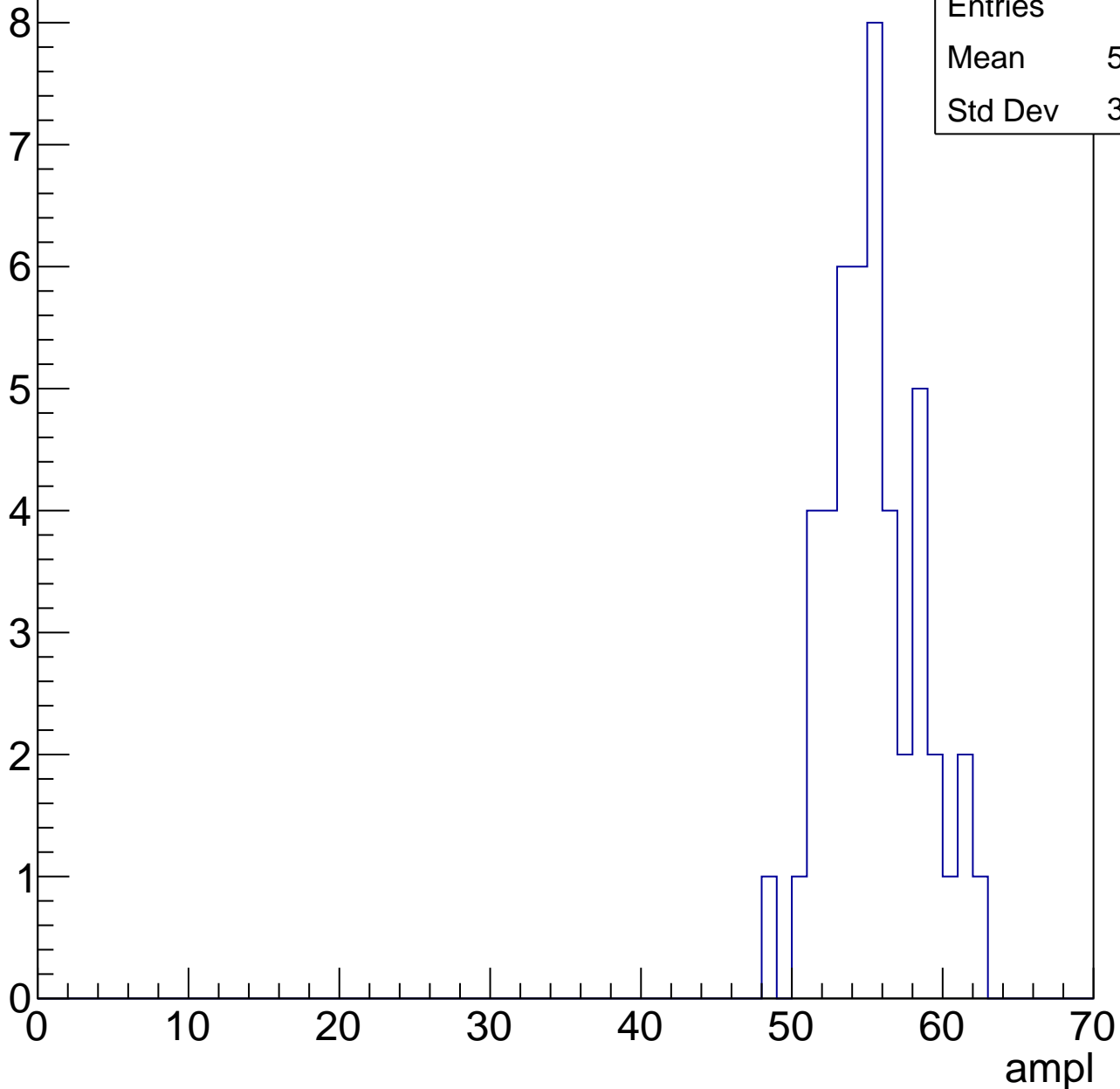
Entries	65
Mean	49.45
Std Dev	3.433

B1L101S, U17-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

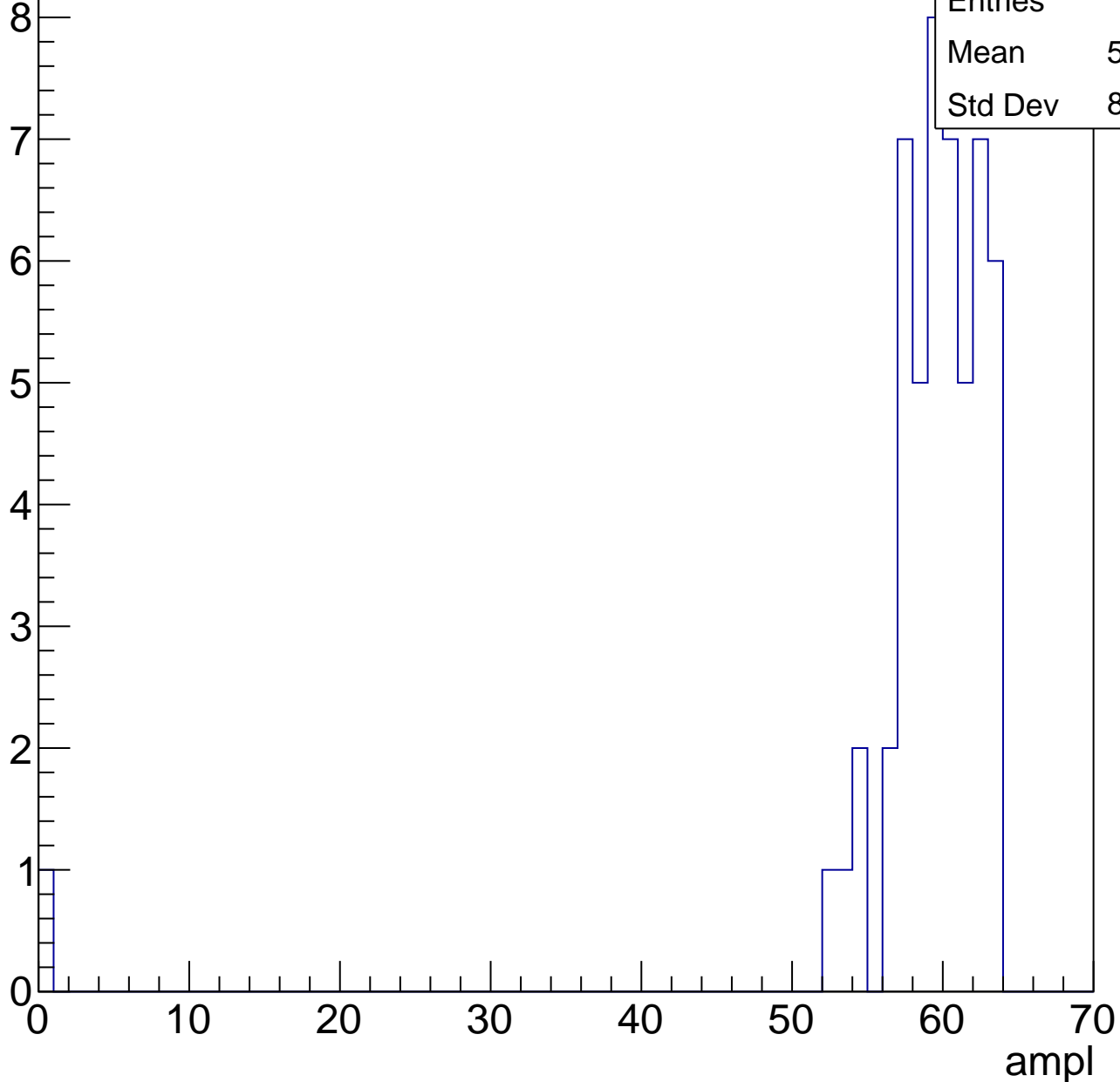
Entries	47
Mean	54.94
Std Dev	3.056



B1L101S, U17-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

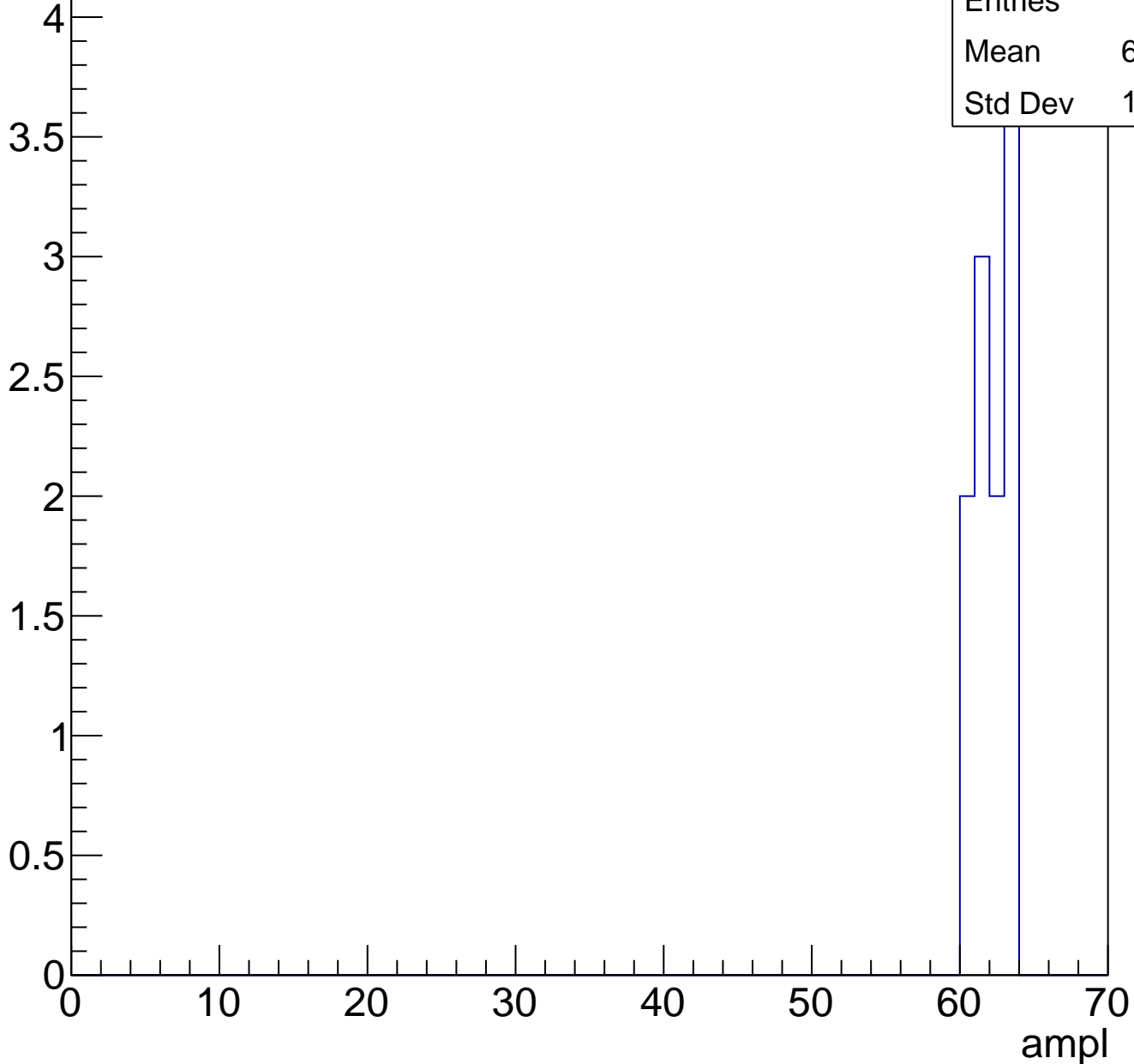
Entry



B1L101S, U17-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	11
Mean	61.73
Std Dev	1.135

B1L101S, U17-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L101S, U17-ch86, adc0

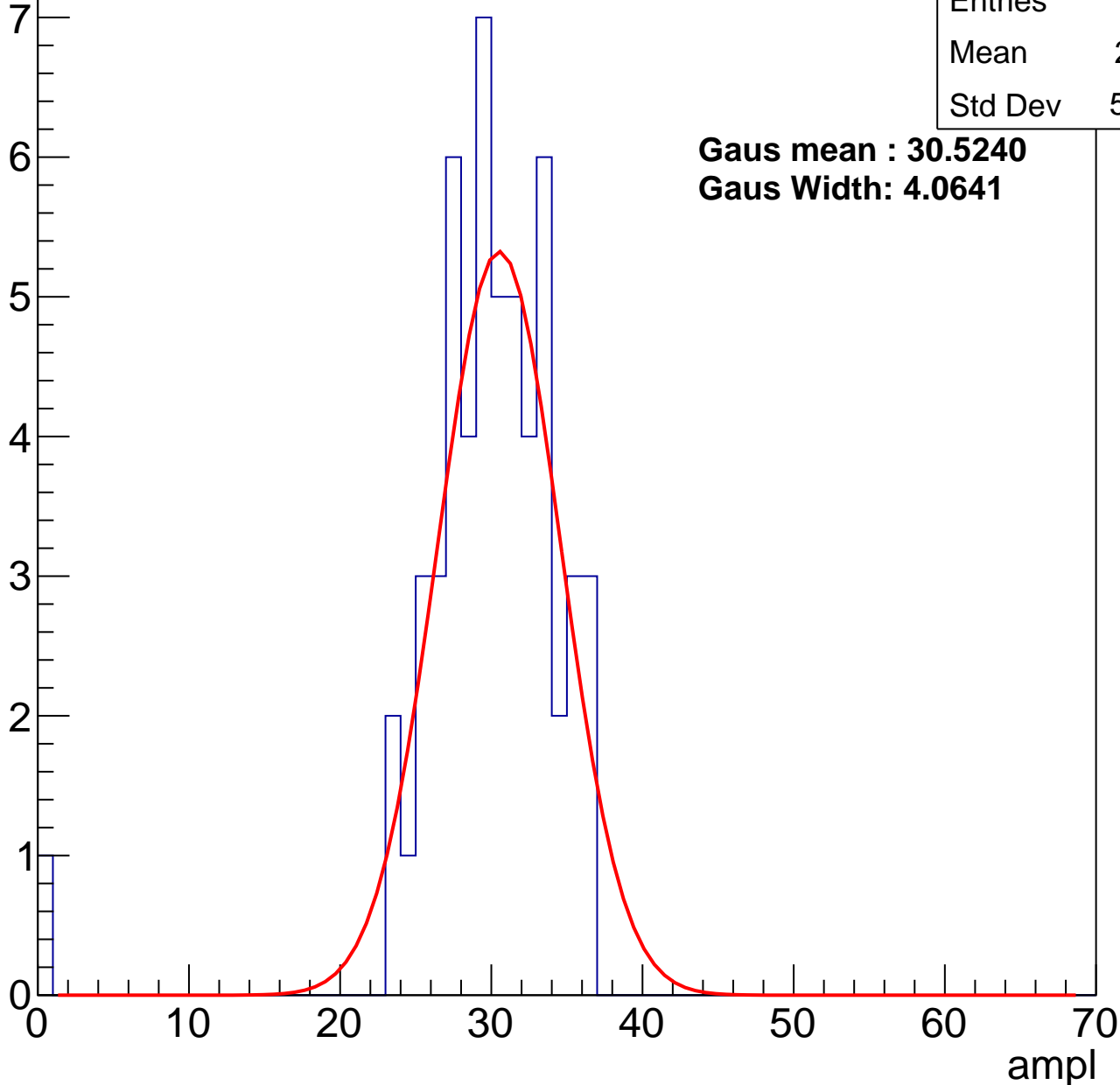
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	29.31
Std Dev	5.218

Gaus mean : 30.5240

Gaus Width: 4.0641



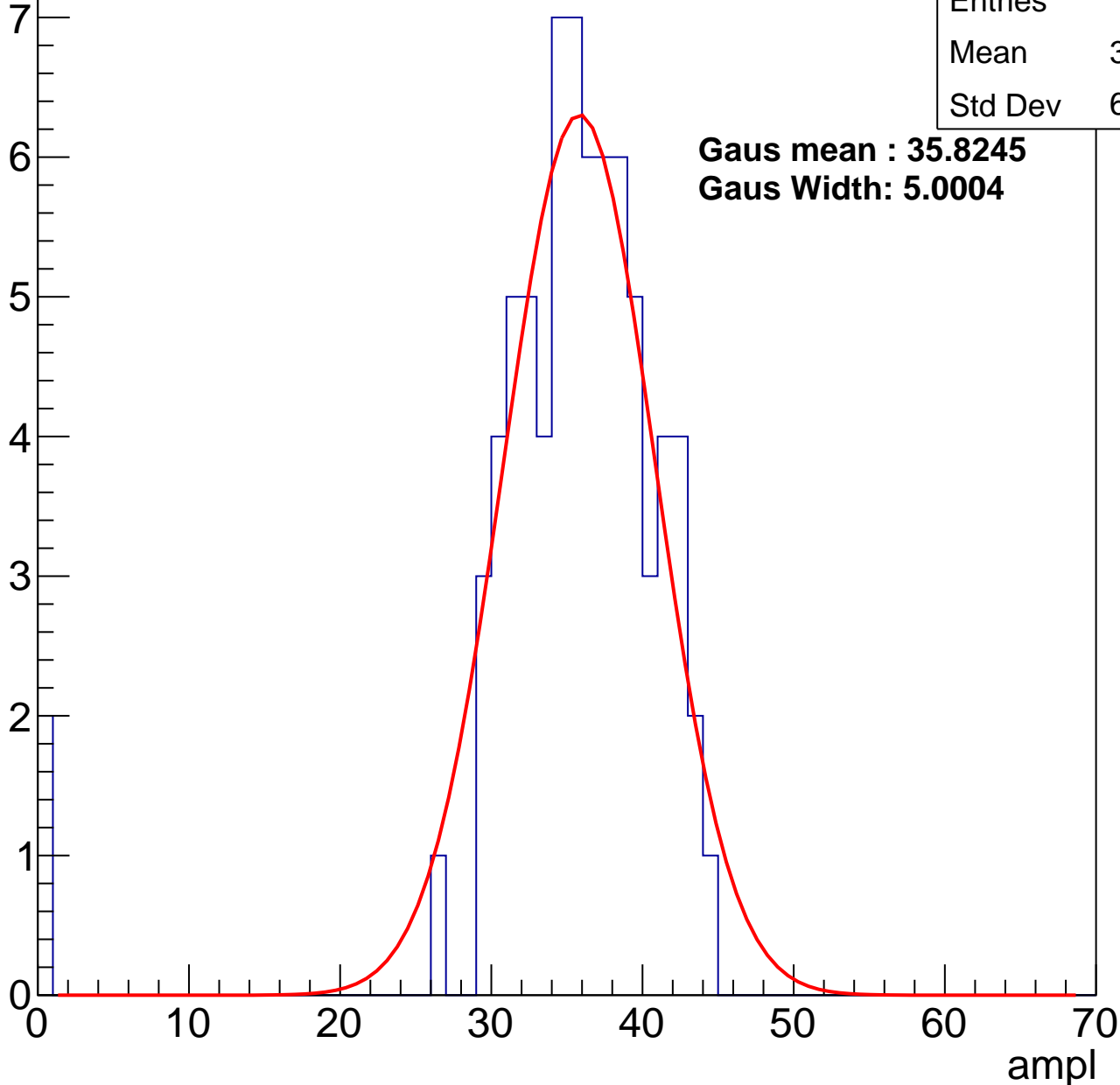
B1L101S, U17-ch86, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	34.75
Std Dev	6.995

Gaus mean : 35.8245
Gaus Width: 5.0004



B1L101S, U17-ch86, adc2

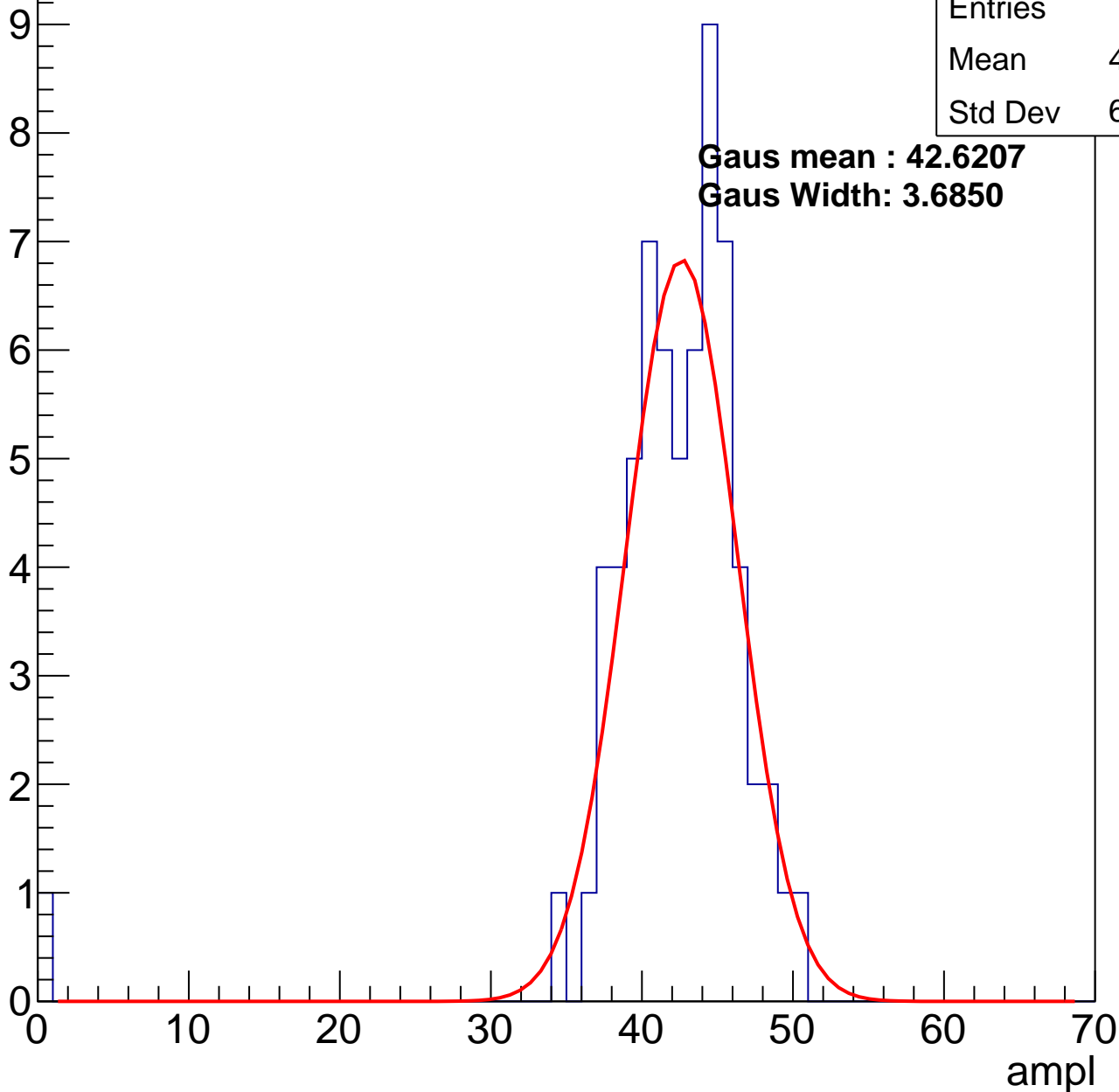
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.56
Std Dev	6.152

Gaus mean : 42.6207

Gaus Width: 3.6850

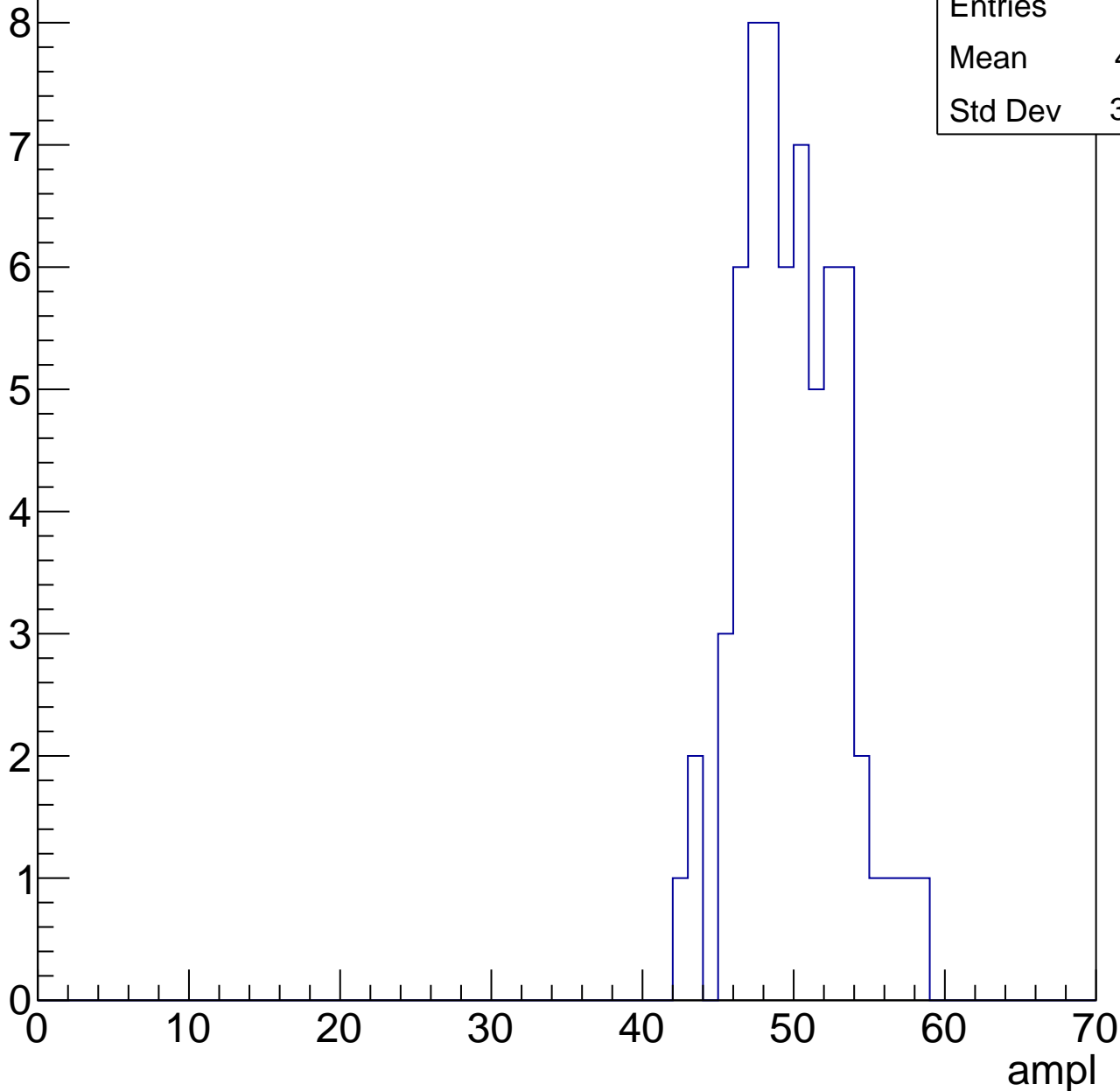


B1L101S, U17-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

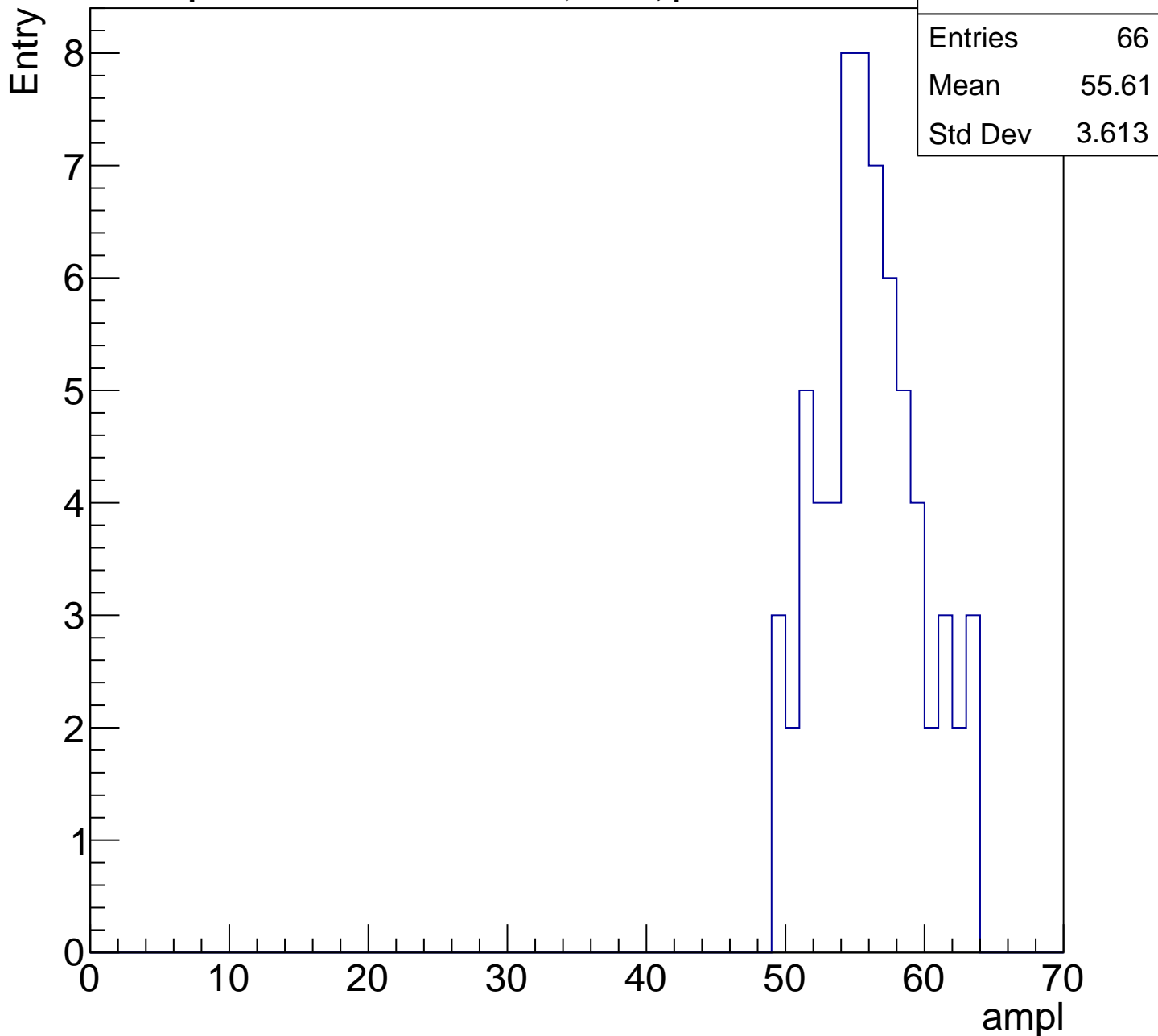
Entry

Entries	64
Mean	49.41
Std Dev	3.329



B1L101S, U17-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

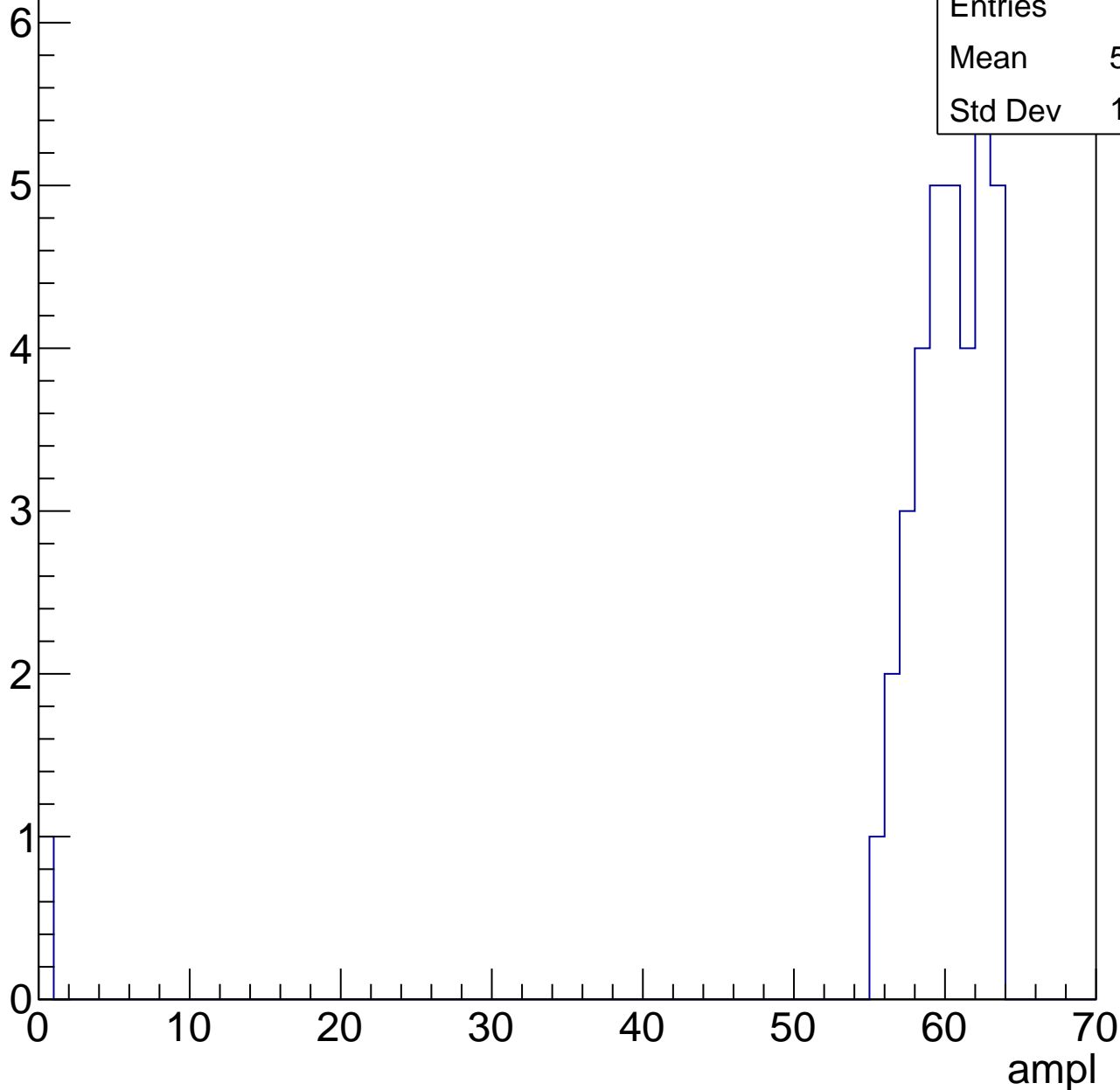


B1L101S, U17-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.22
Std Dev	10.09



B1L101S, U17-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

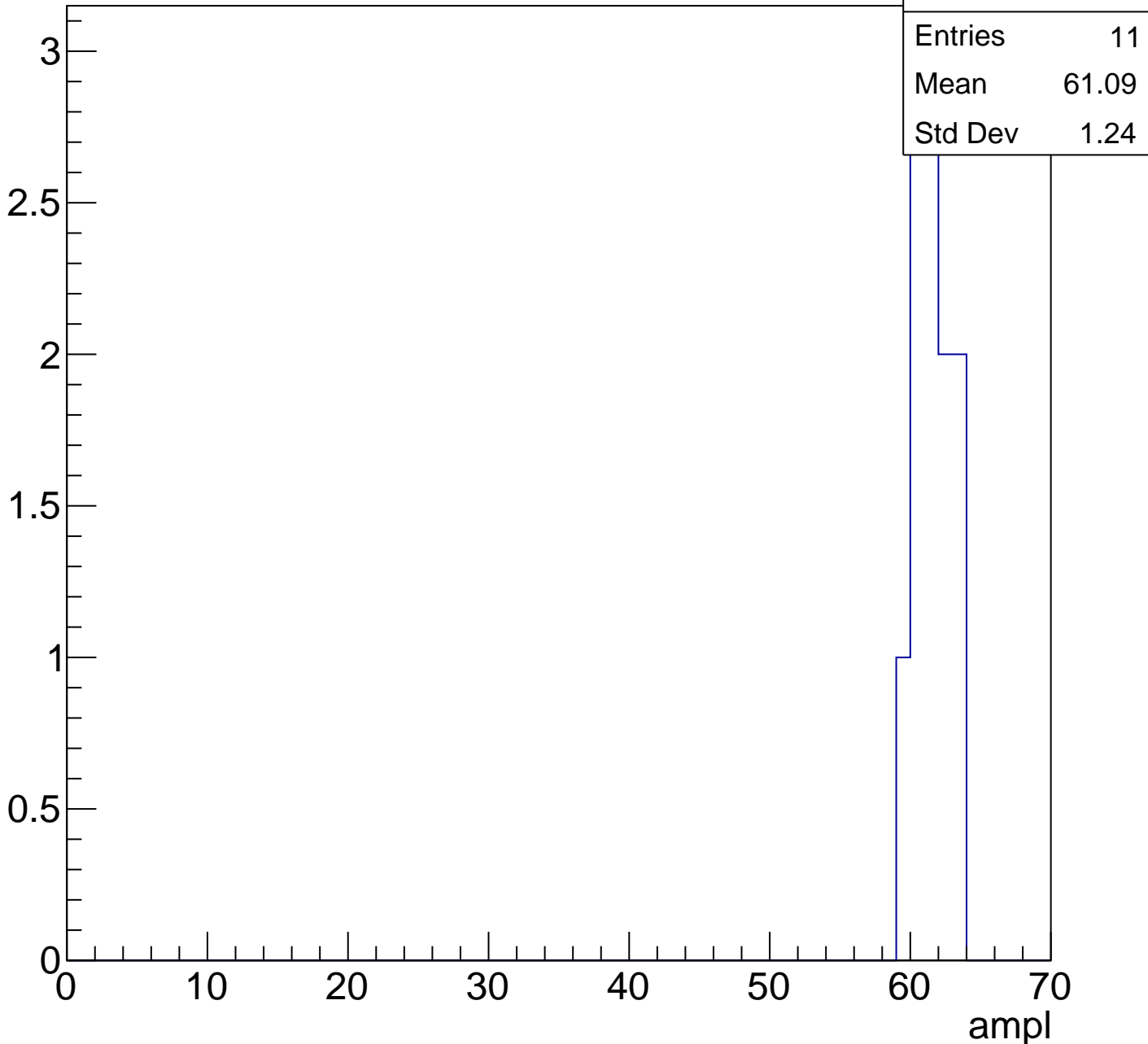
50

60

70

ampl

Entries	11
Mean	61.09
Std Dev	1.24



B1L101S, U17-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch87, adc0

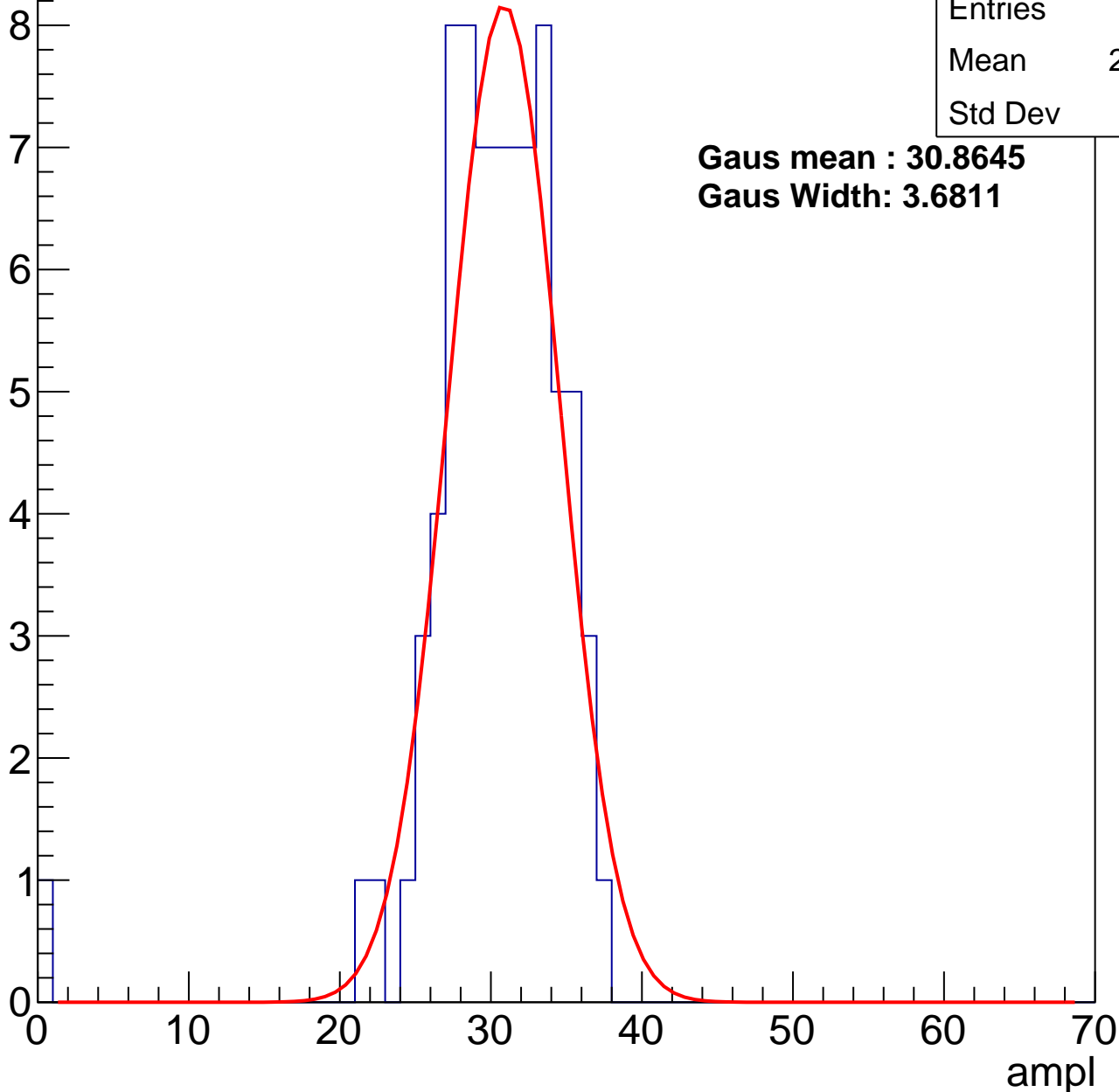
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.79
Std Dev	4.83

Gaus mean : 30.8645

Gaus Width: 3.6811



B1L101S, U17-ch87, adc1

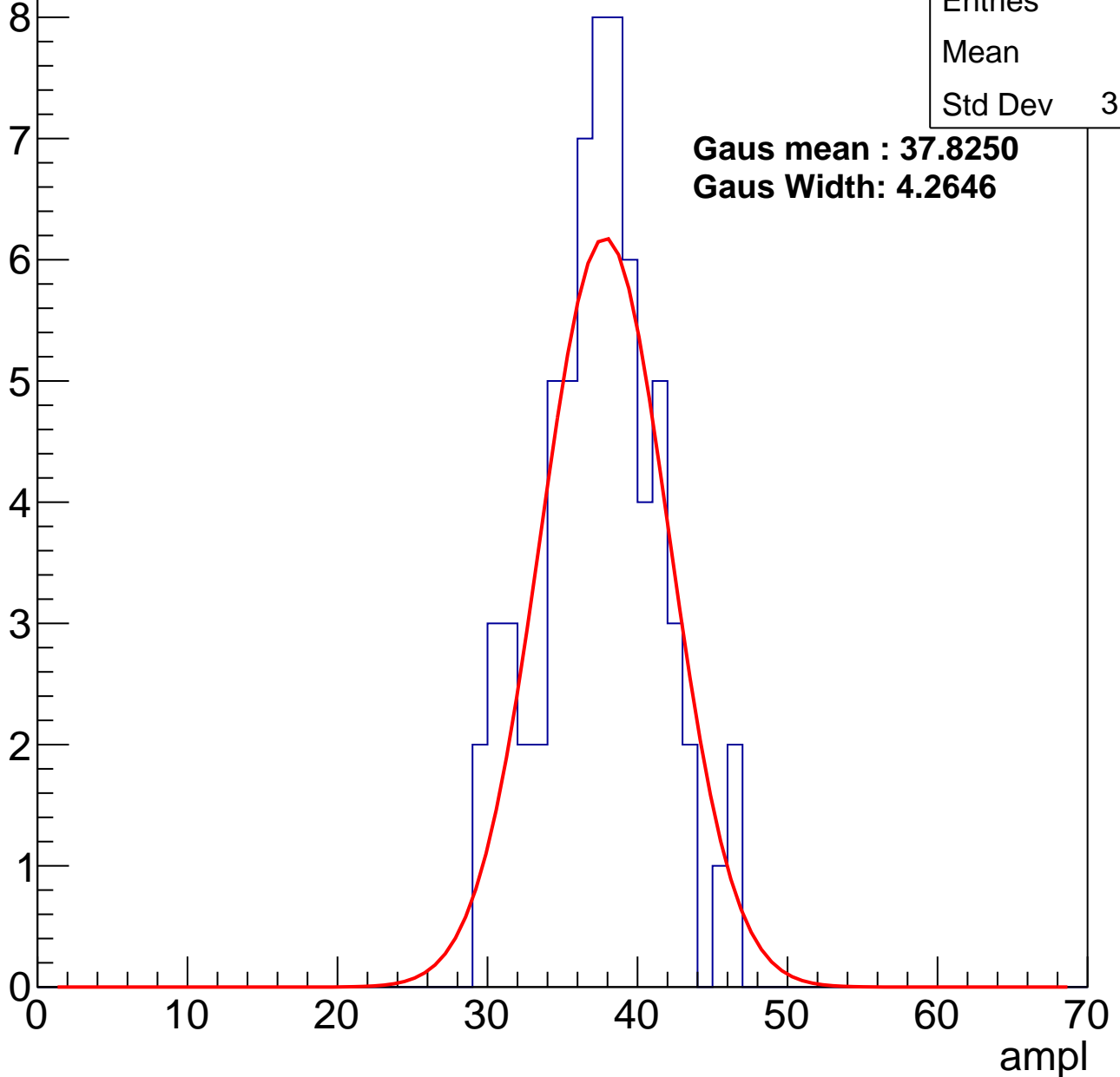
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37
Std Dev	3.944

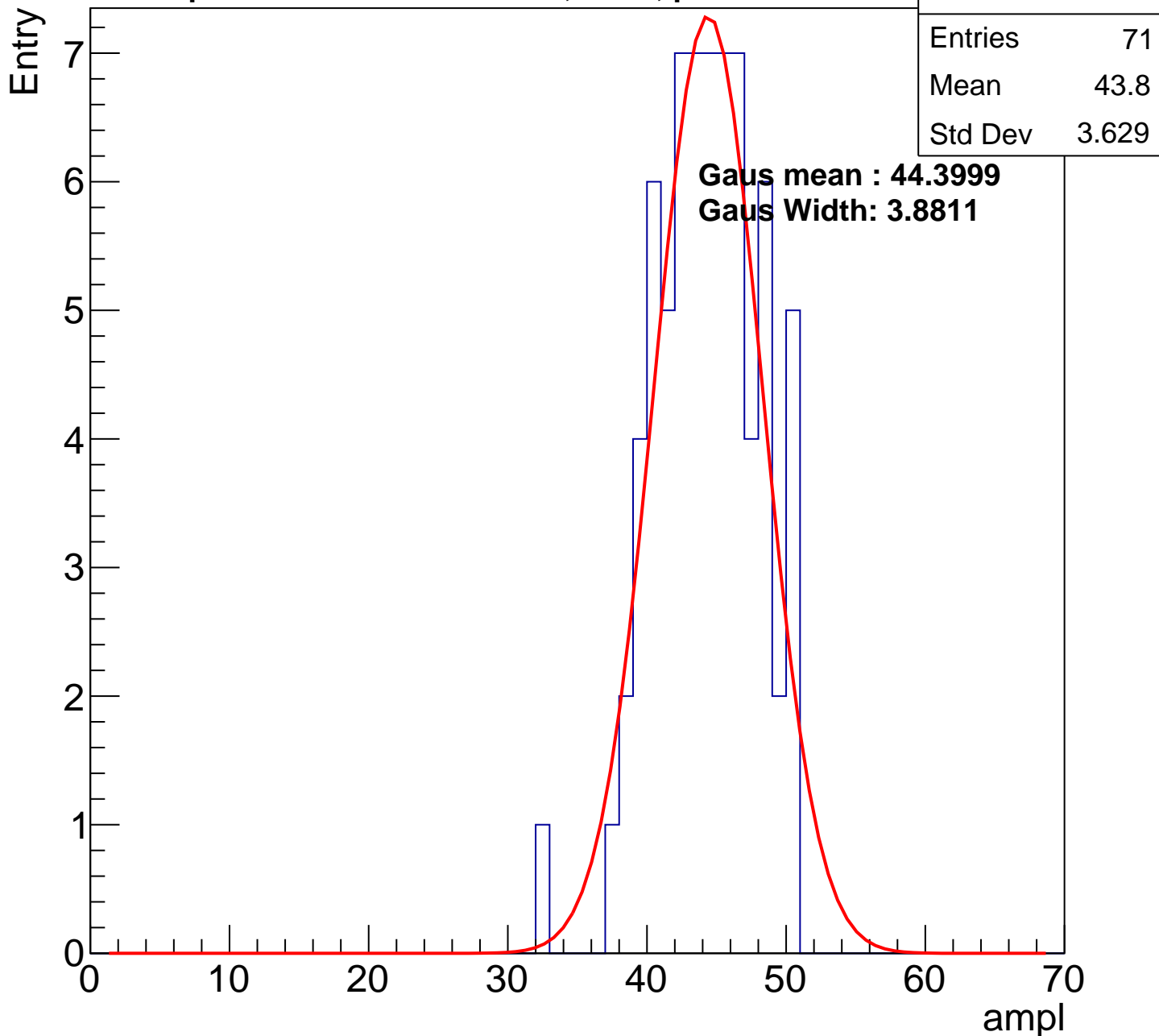
Gaus mean : 37.8250

Gaus Width: 4.2646



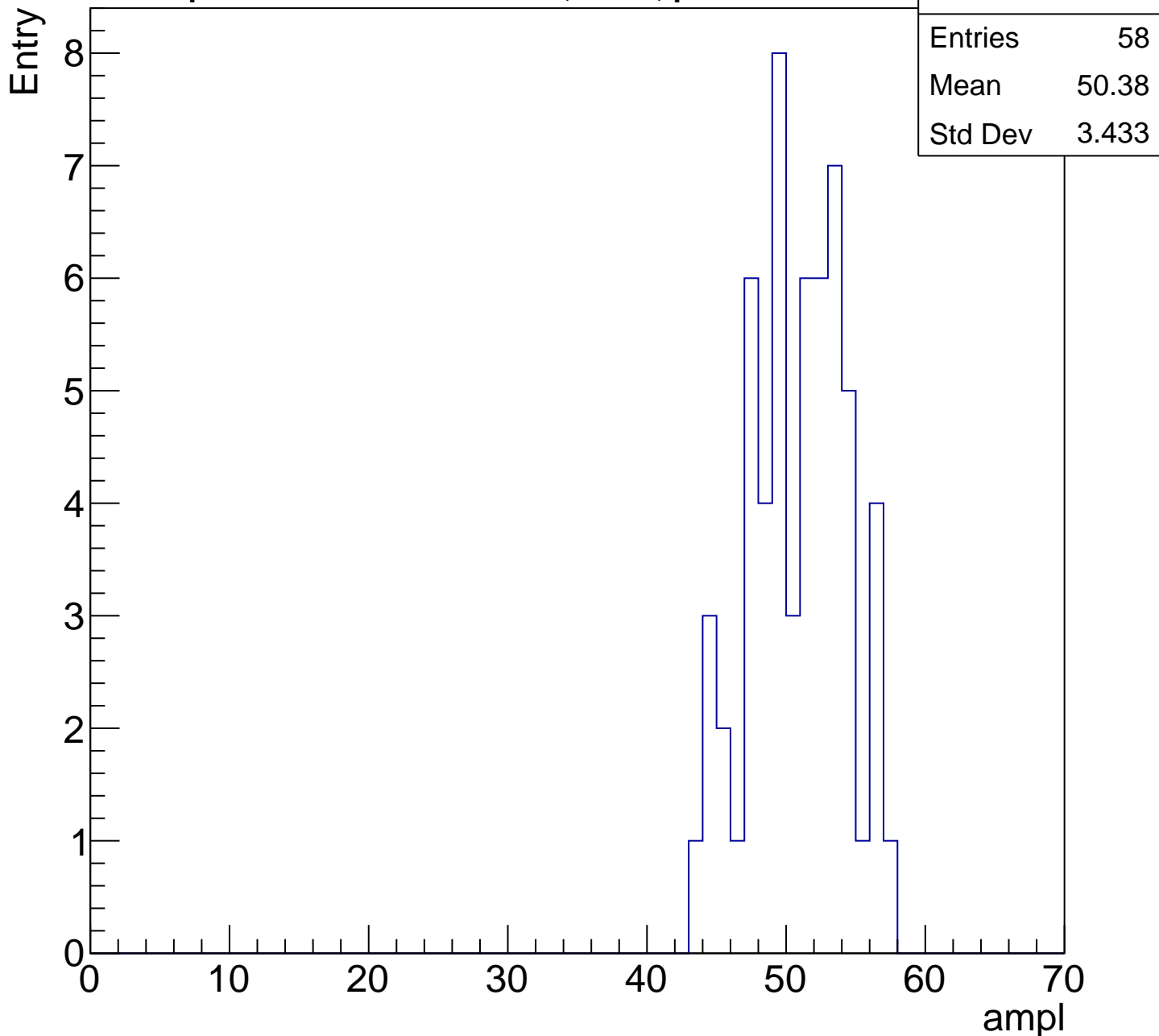
B1L101S, U17-ch87, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U17-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

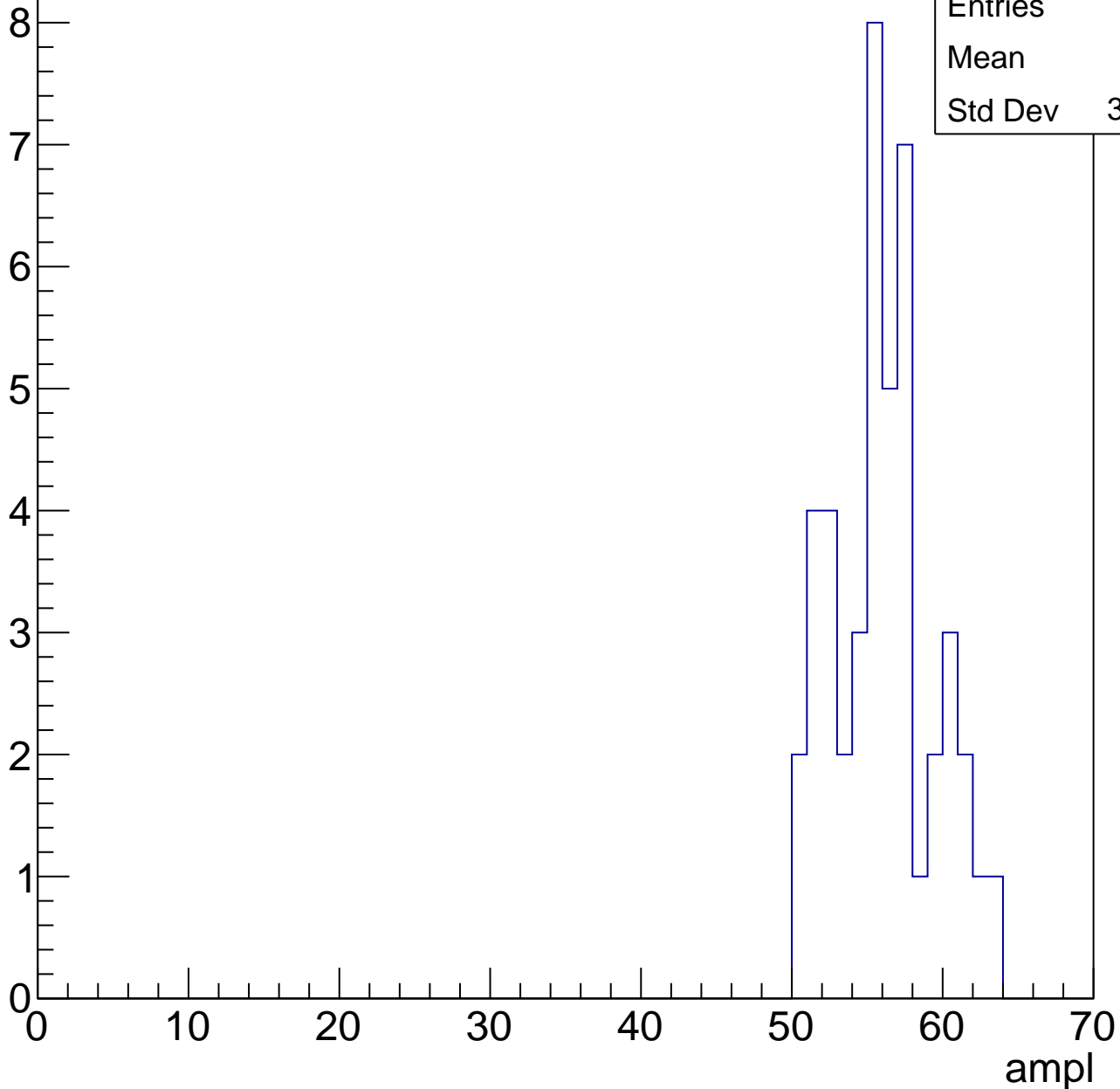


B1L101S, U17-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.6
Std Dev	3.262

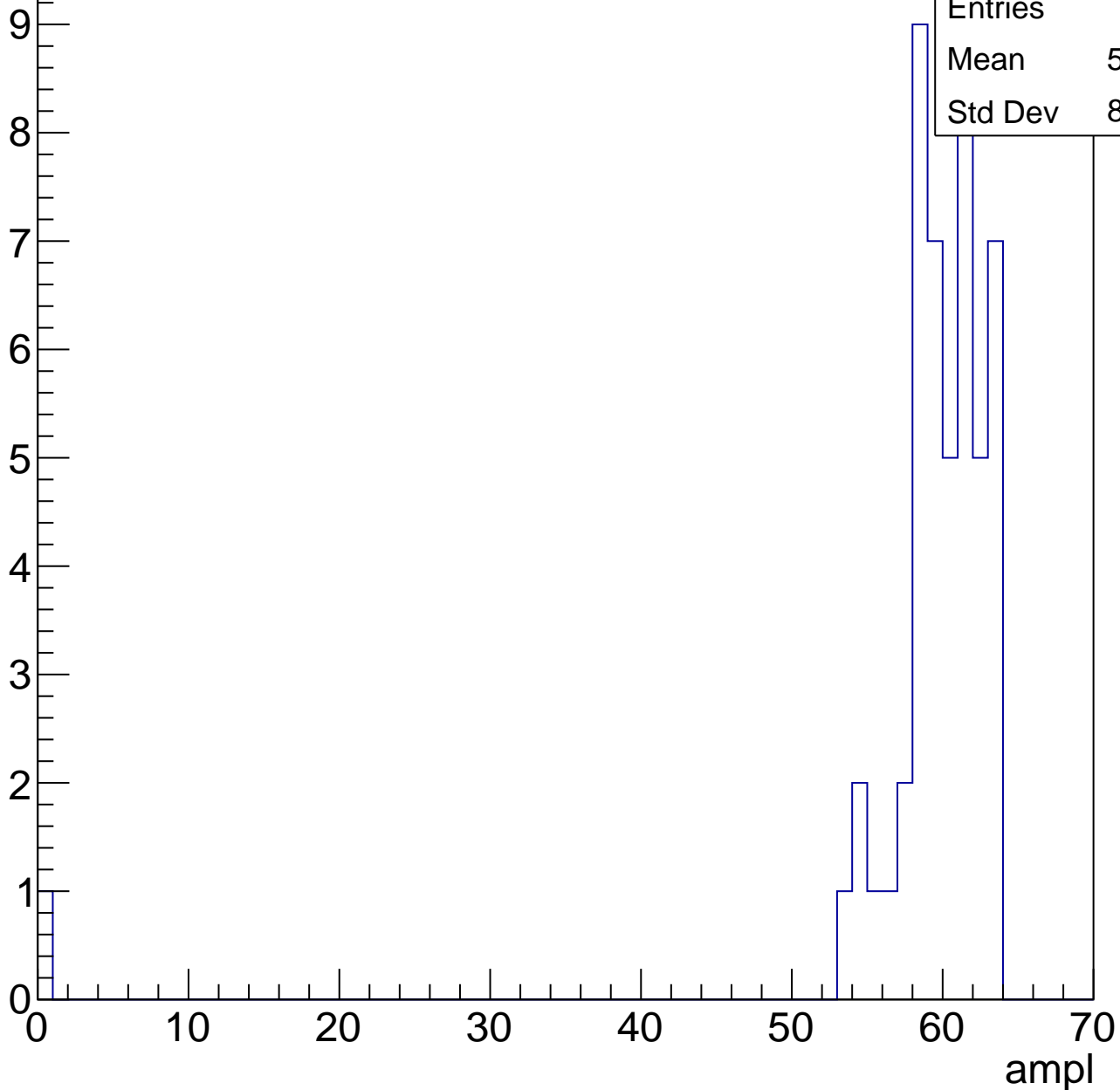


B1L101S, U17-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

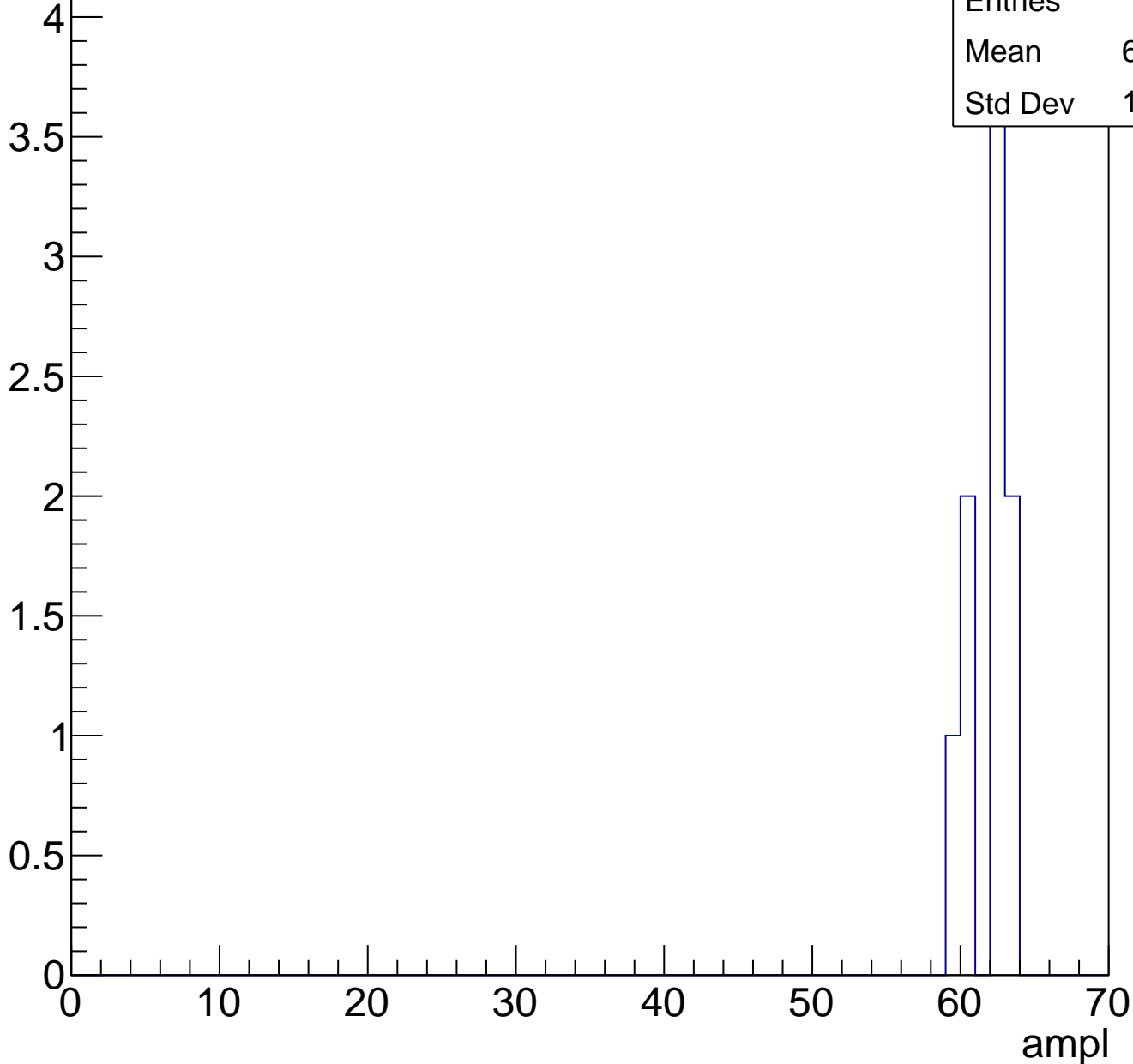
Entries	49
Mean	58.37
Std Dev	8.787



B1L101S, U17-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch88, adc0

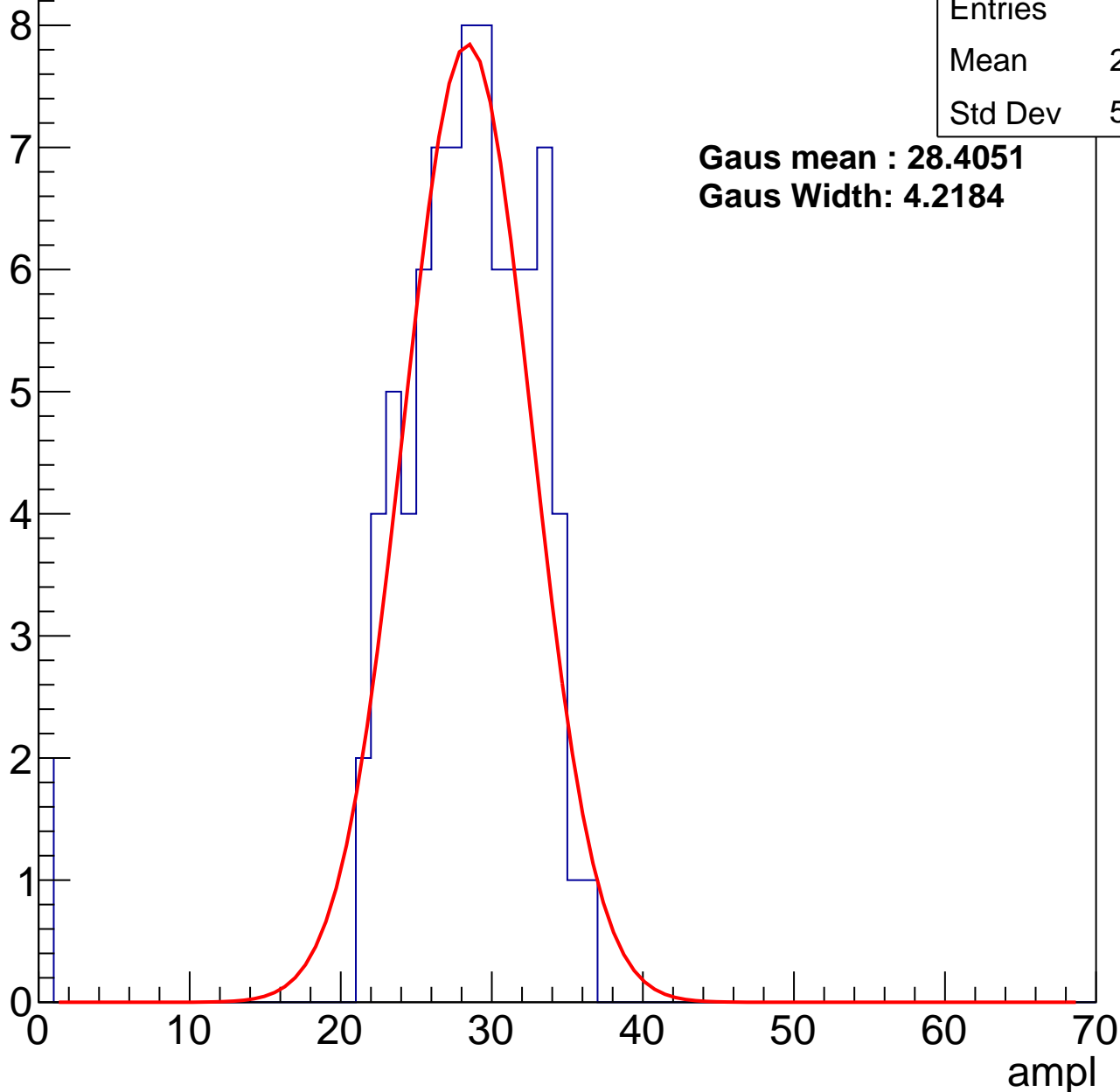
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	27.55
Std Dev	5.653

Gaus mean : 28.4051

Gaus Width: 4.2184



B1L101S, U17-ch88, adc1

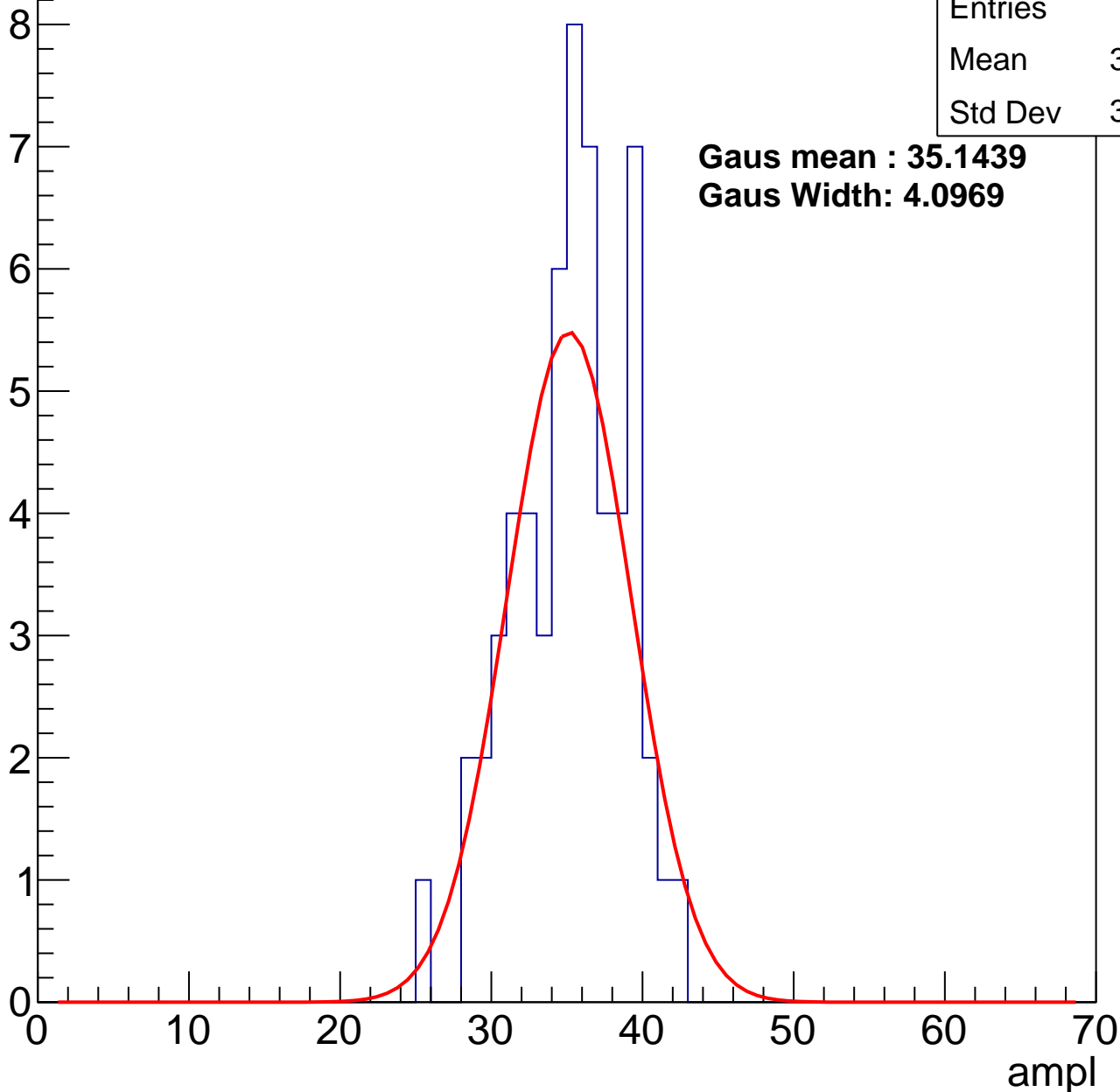
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.78
Std Dev	3.604

Gaus mean : 35.1439

Gaus Width: 4.0969



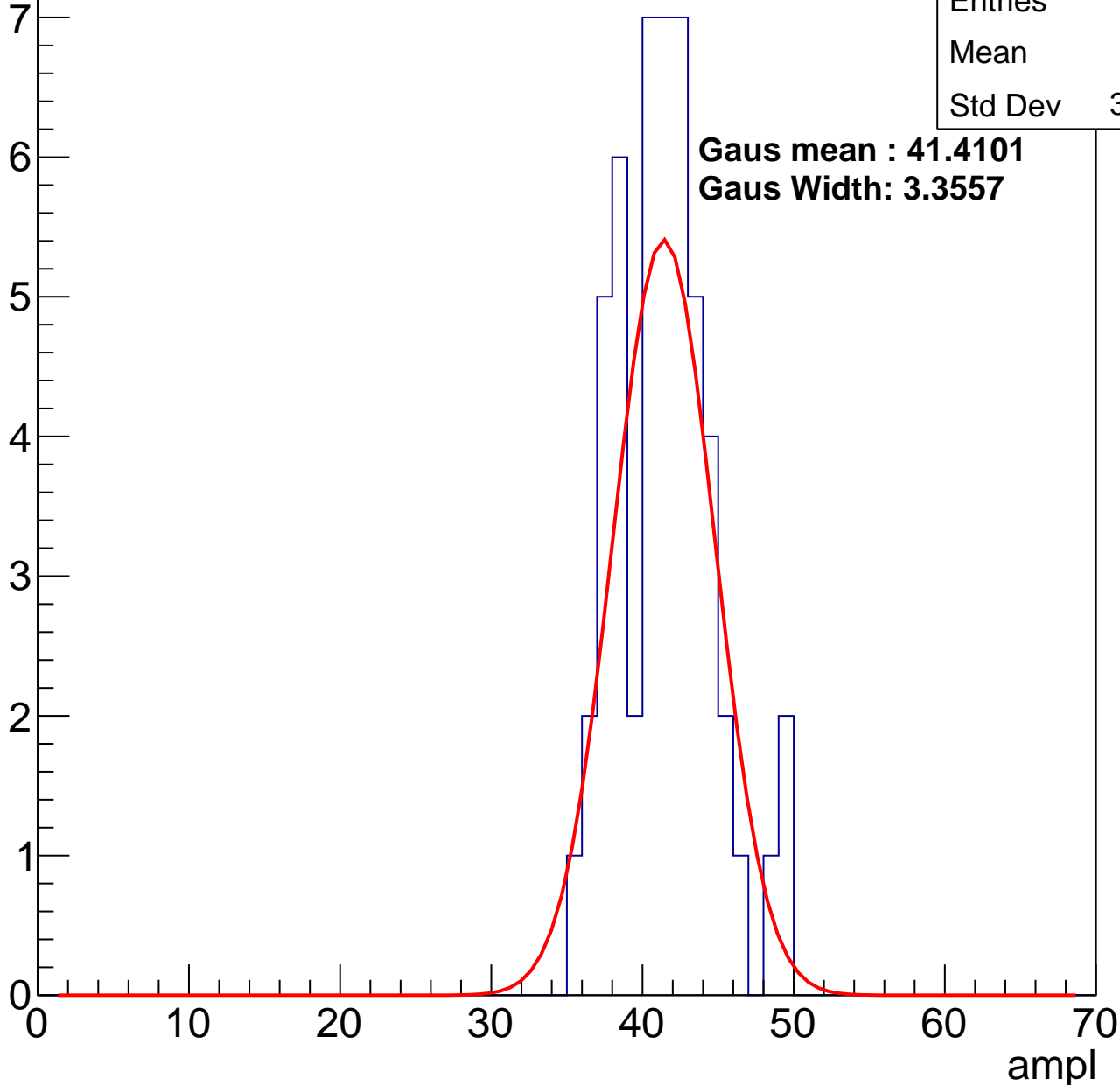
B1L101S, U17-ch88, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41
Std Dev	3.199

Gaus mean : 41.4101
Gaus Width: 3.3557

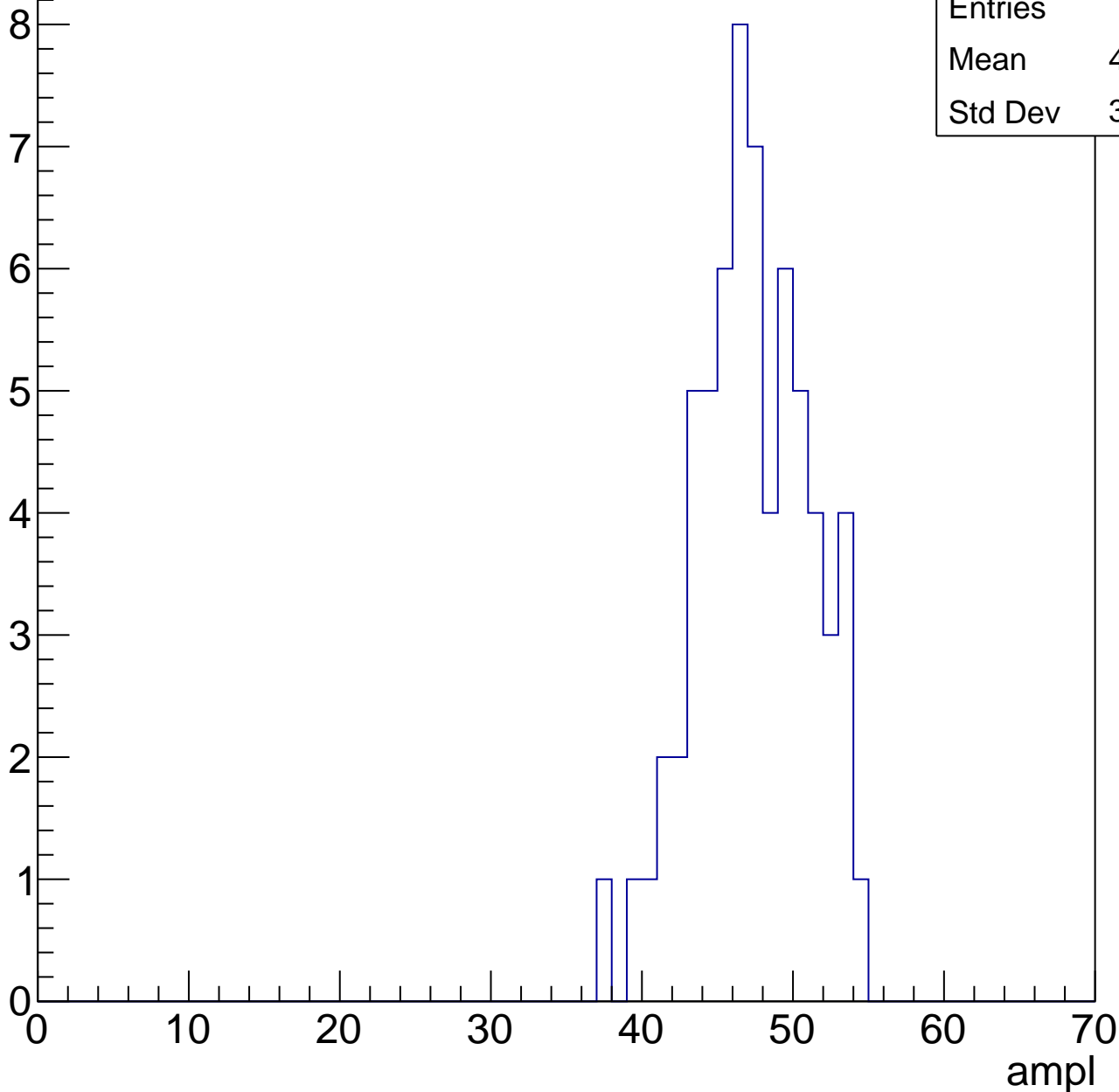


B1L101S, U17-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	46.86
Std Dev	3.712

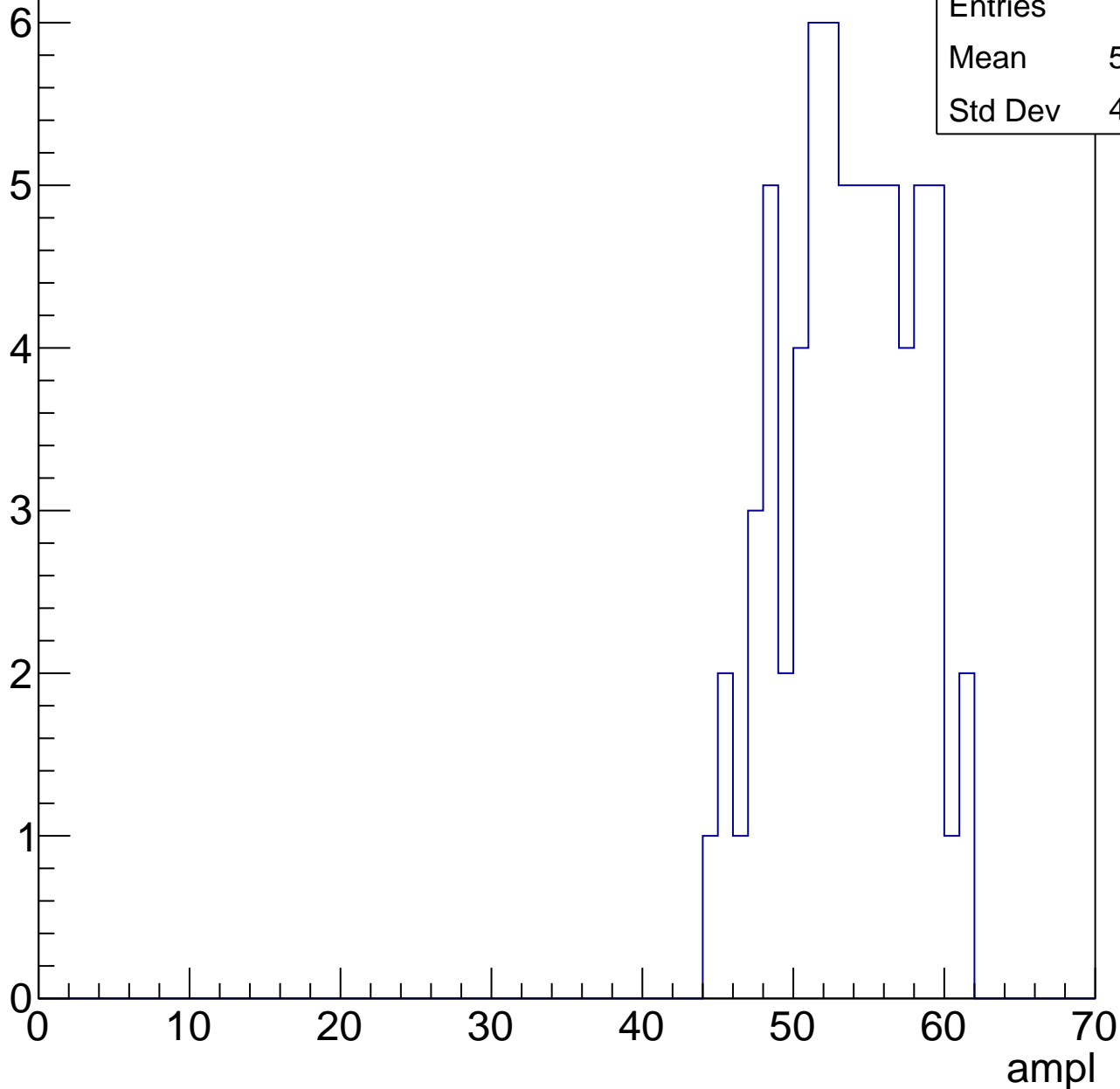


B1L101S, U17-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	53.16
Std Dev	4.238



B1L101S, U17-ch88, adc5

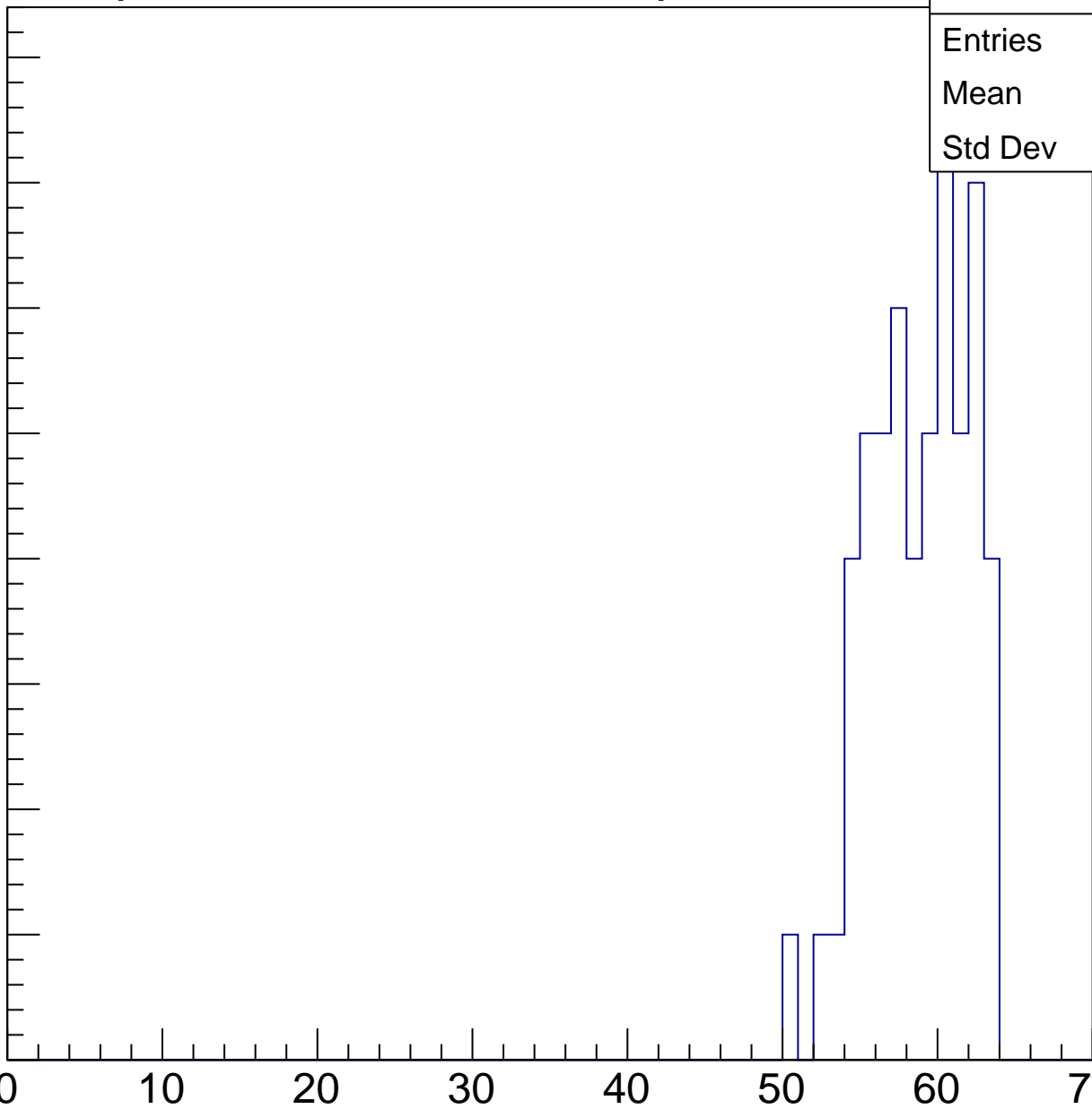
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.32
Std Dev	3.129

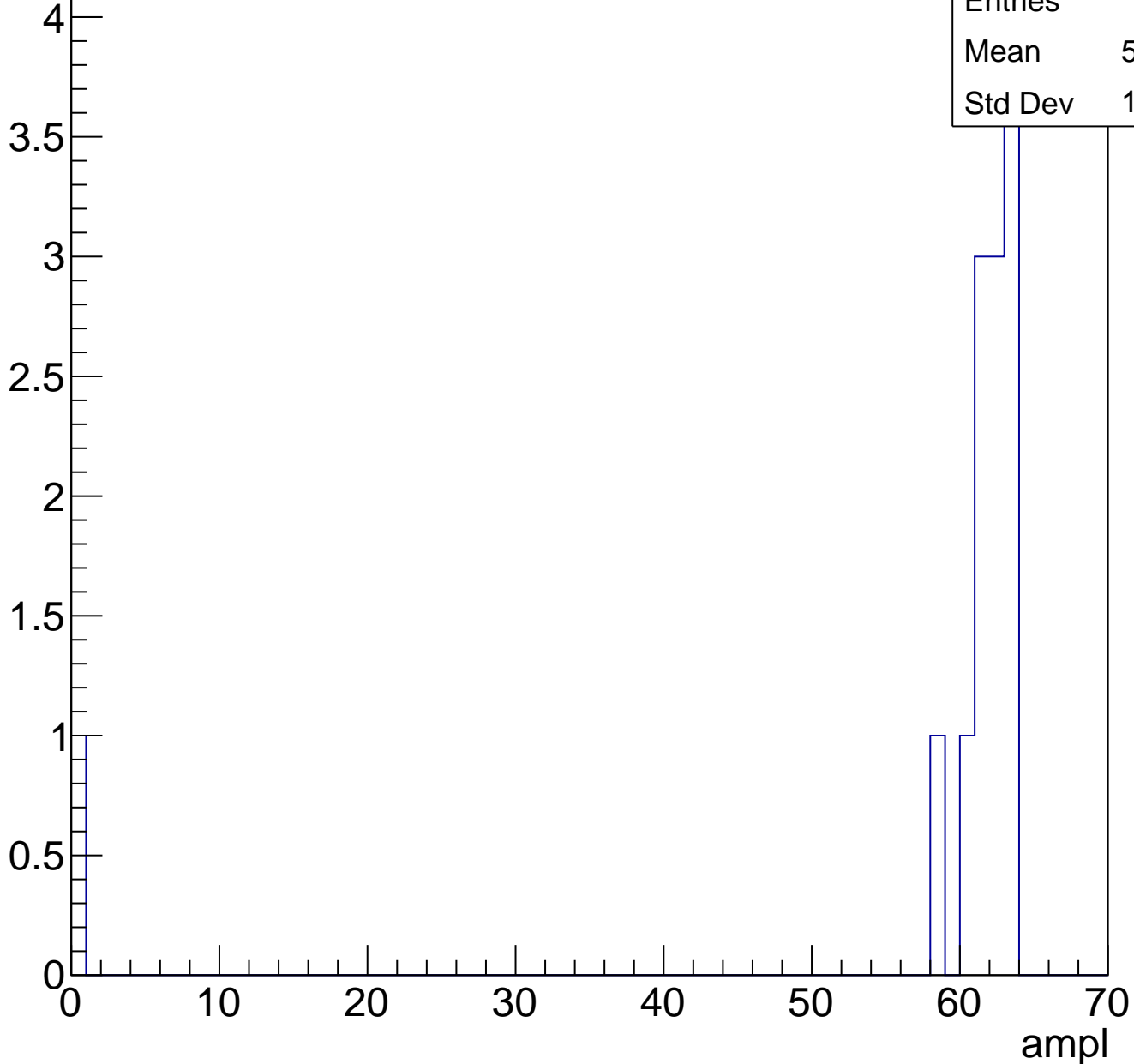
ampl



B1L101S, U17-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

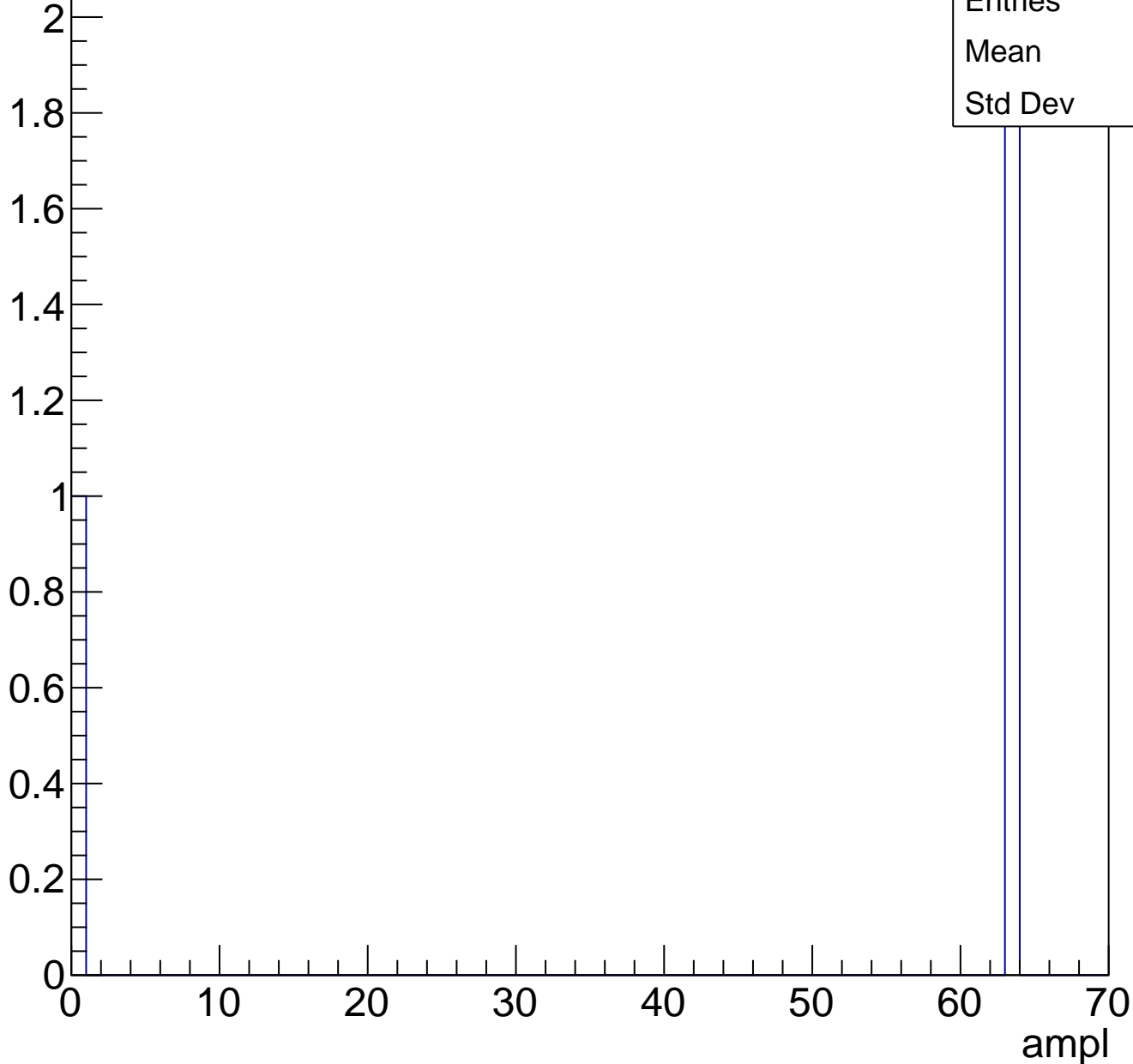
Entry



B1L101S, U17-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch89, adc0

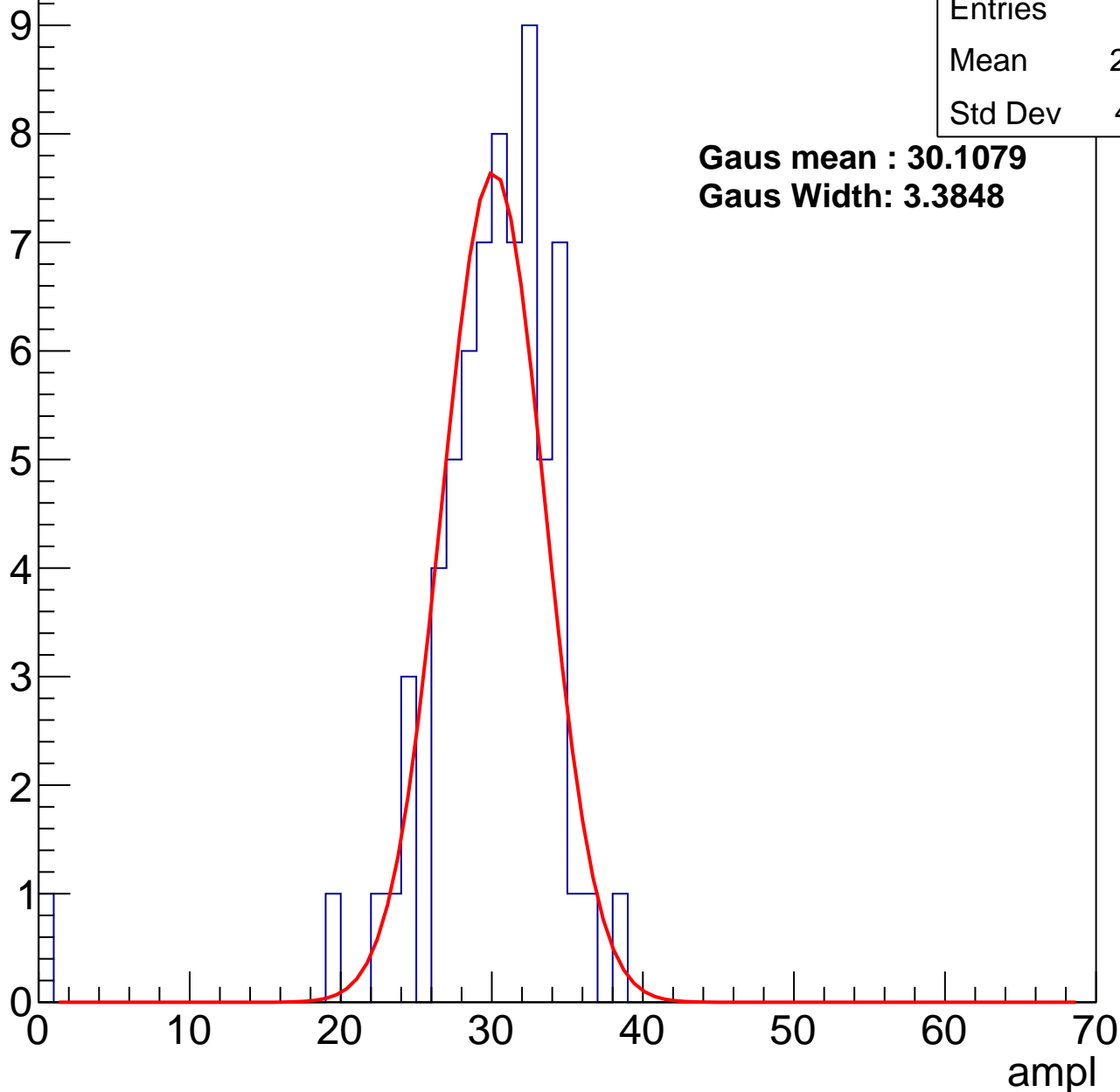
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.46
Std Dev	4.981

Gaus mean : 30.1079

Gaus Width: 3.3848



B1L101S, U17-ch89, adc1

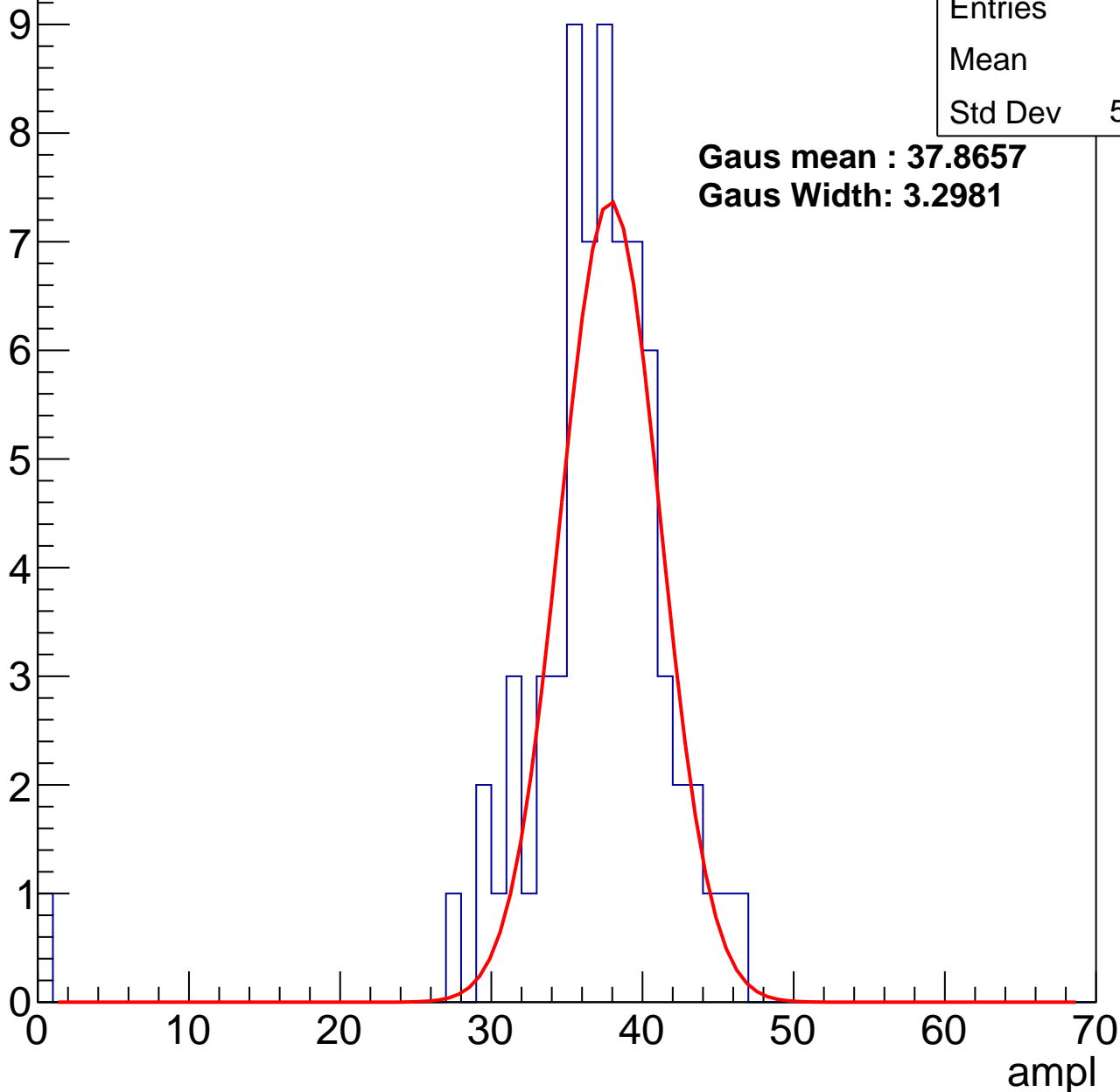
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	36.4
Std Dev	5.763

Gaus mean : 37.8657

Gaus Width: 3.2981



B1L101S, U17-ch89, adc2

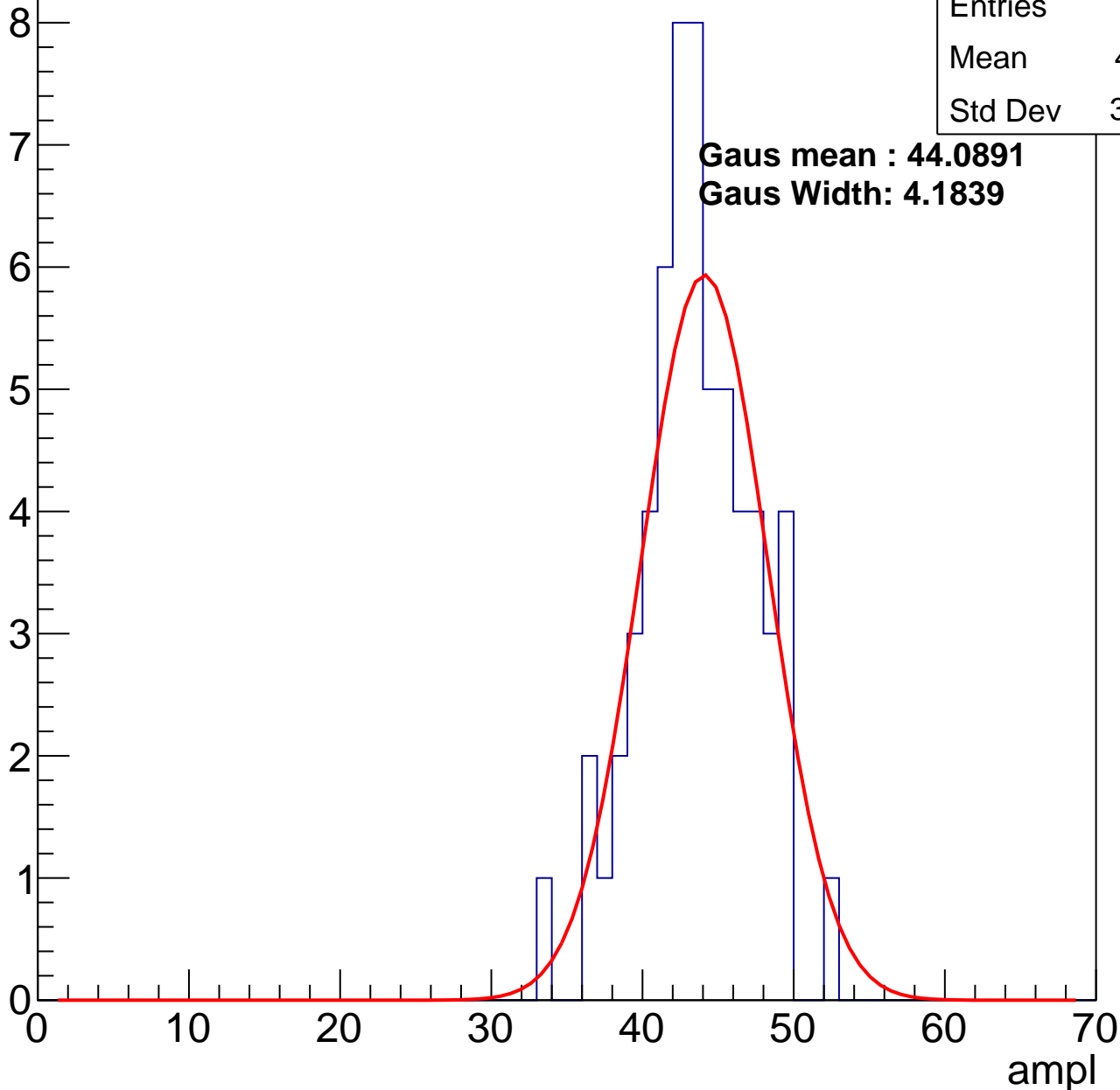
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.11
Std Dev	3.676

Gaus mean : 44.0891

Gaus Width: 4.1839

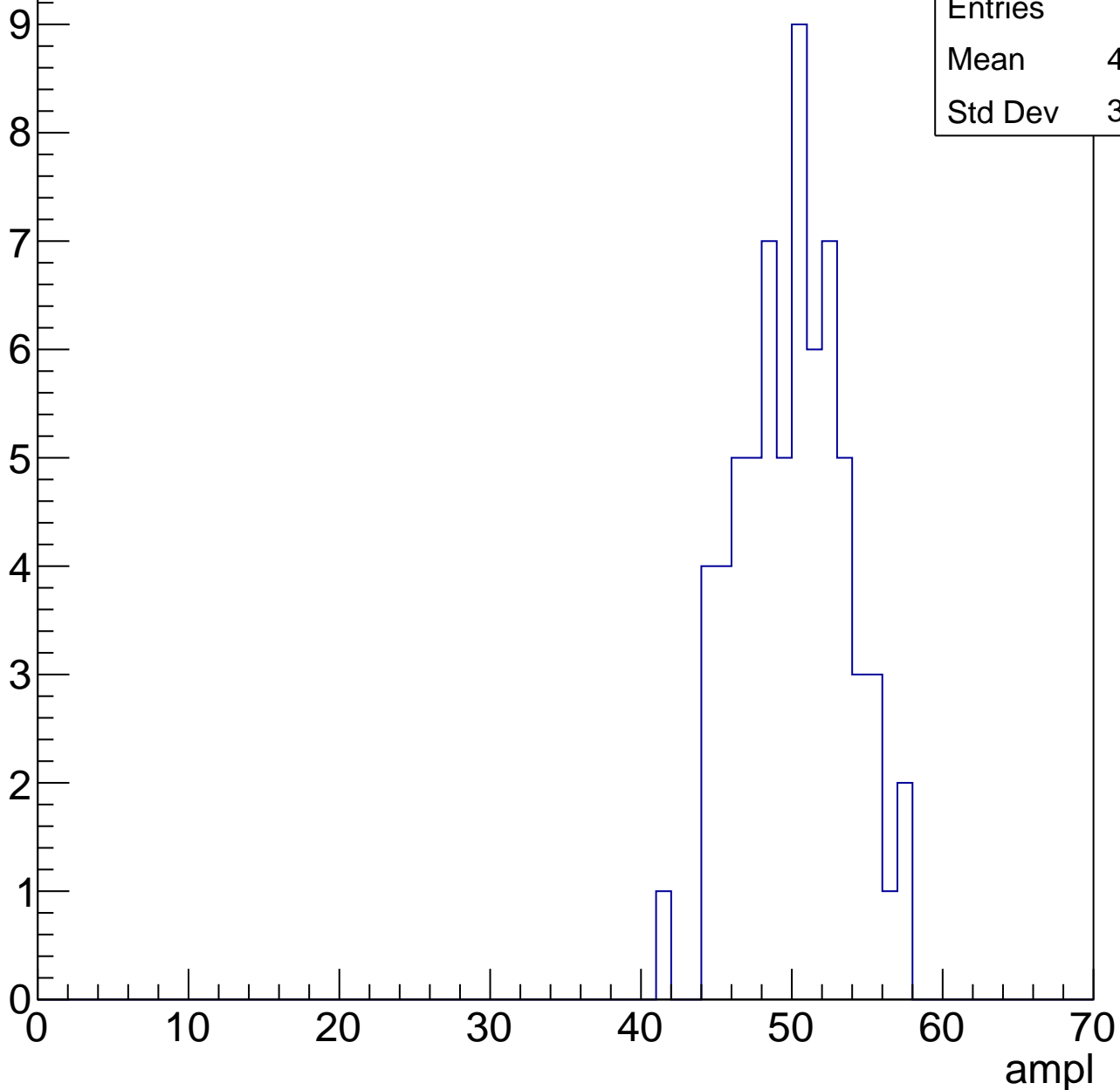


B1L101S, U17-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.63
Std Dev	3.476

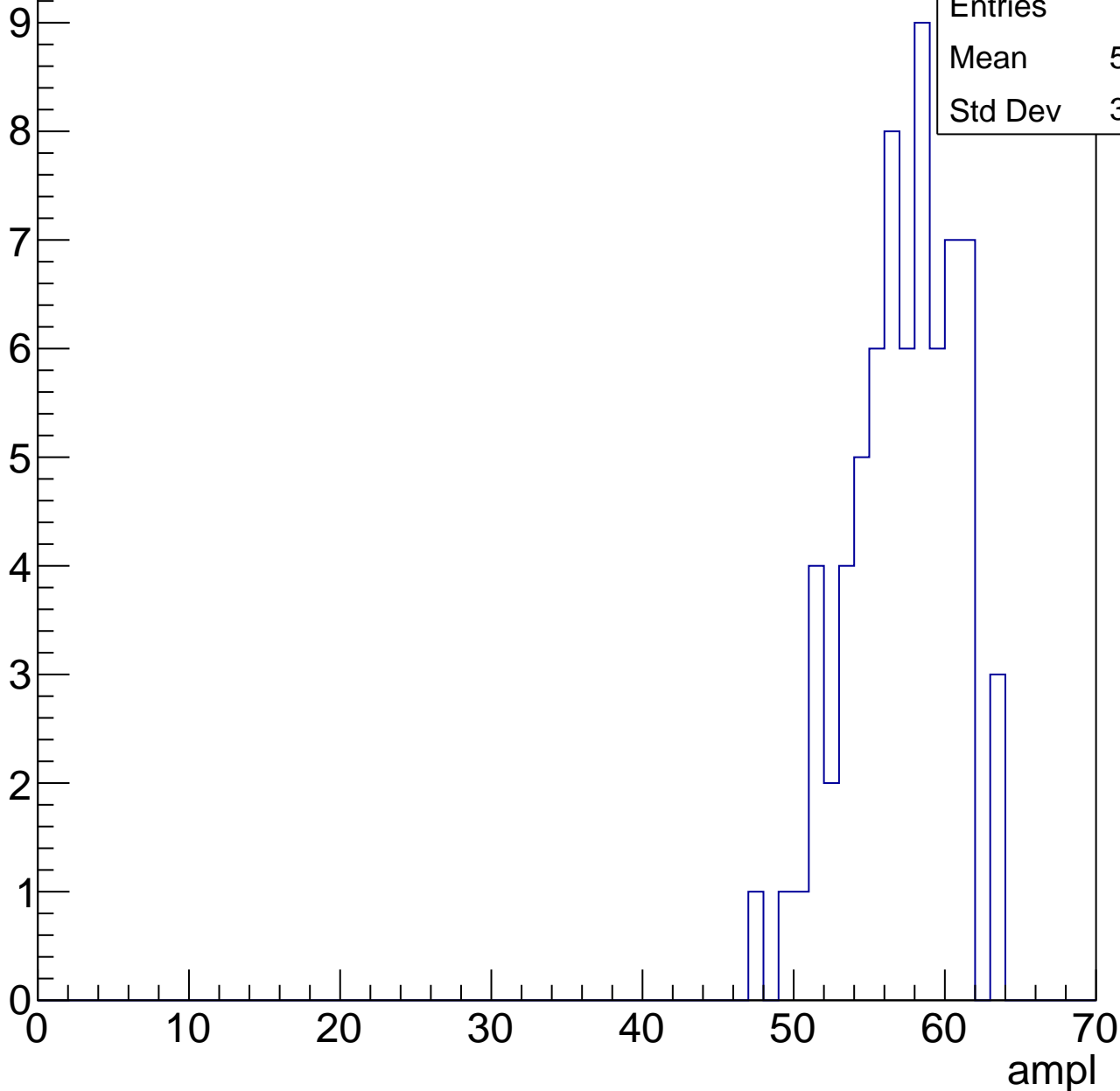


B1L101S, U17-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	56.69
Std Dev	3.499

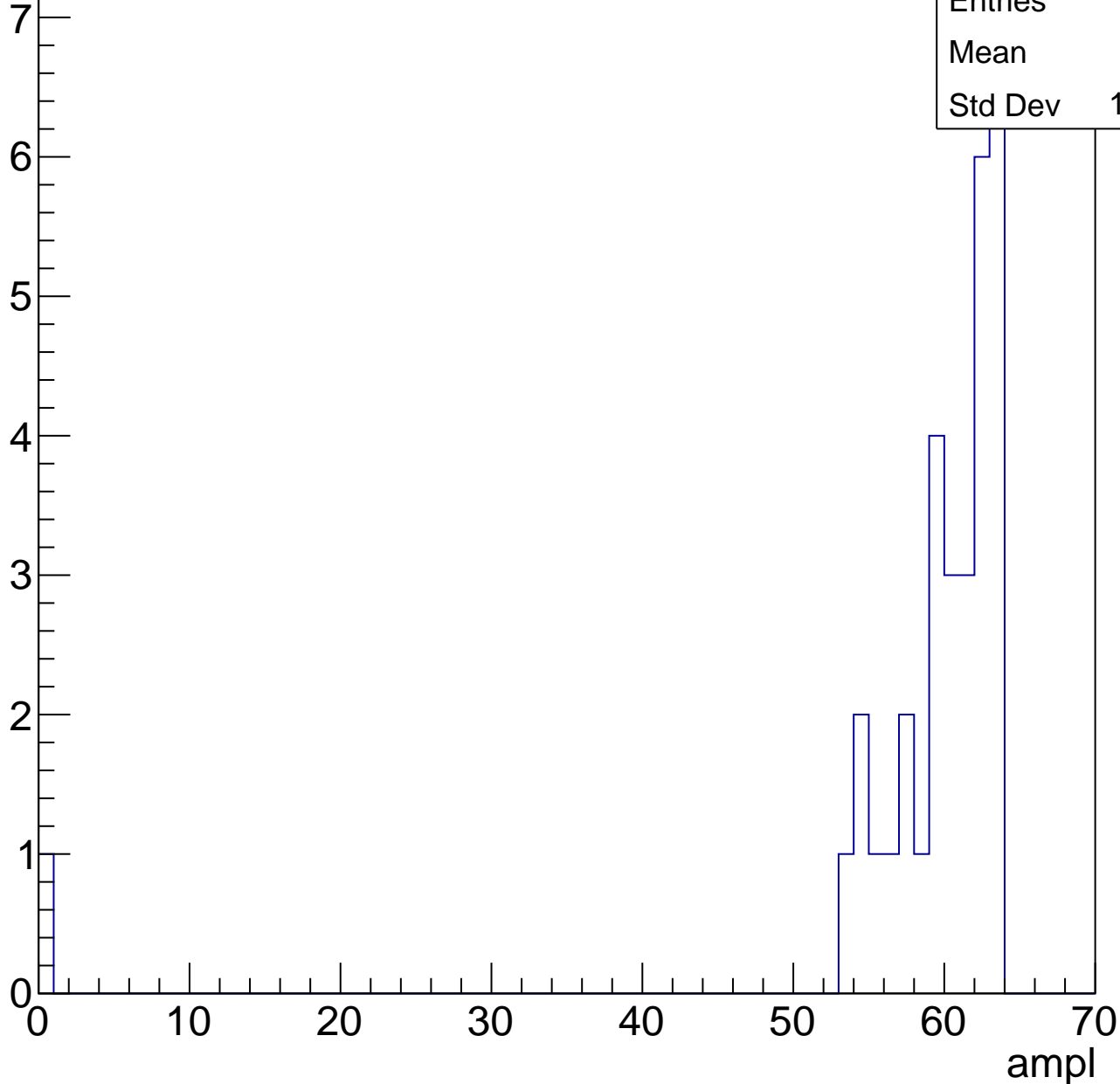


B1L101S, U17-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

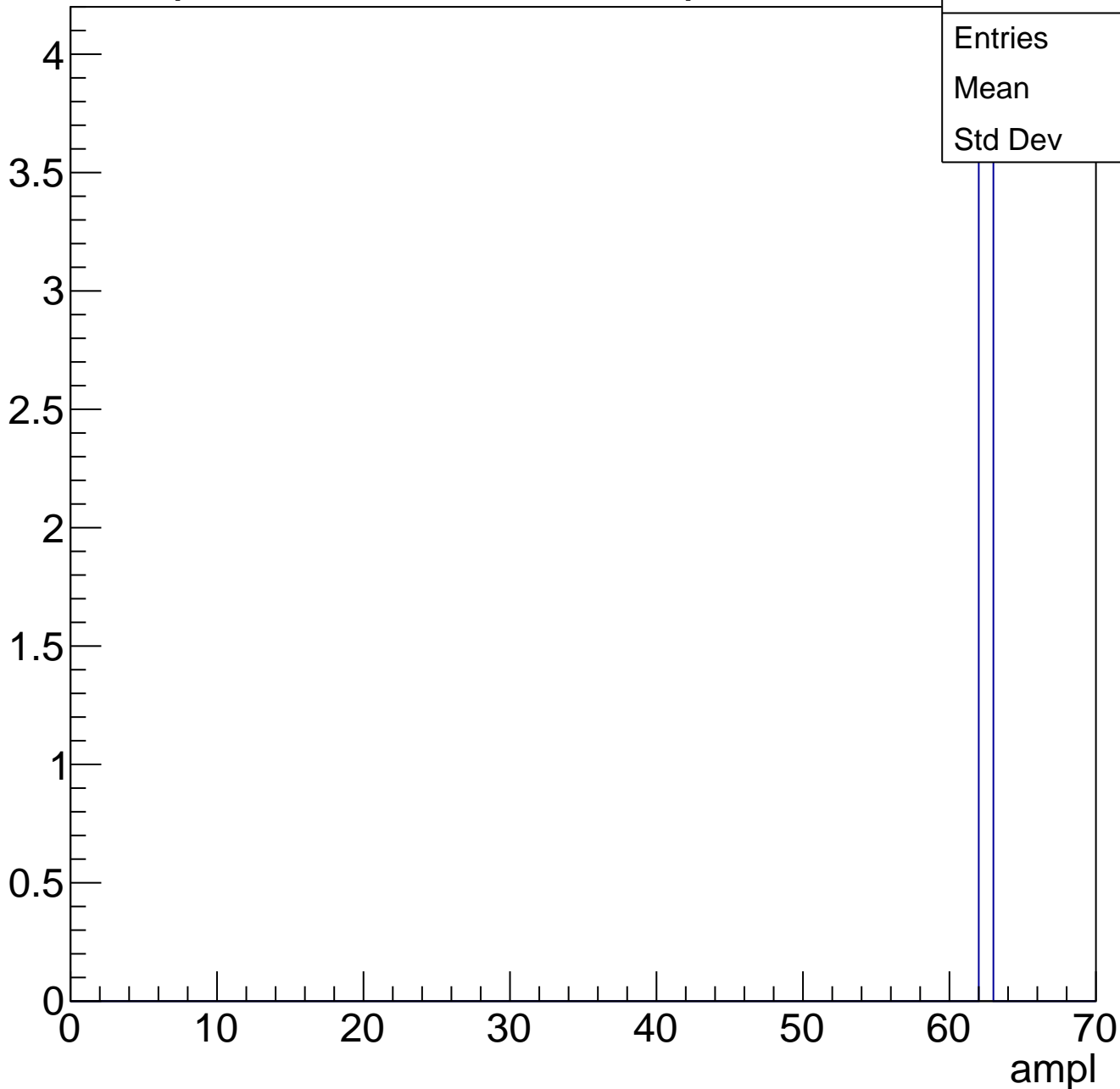
Entries	32
Mean	58
Std Dev	10.83



B1L101S, U17-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch90, adc0

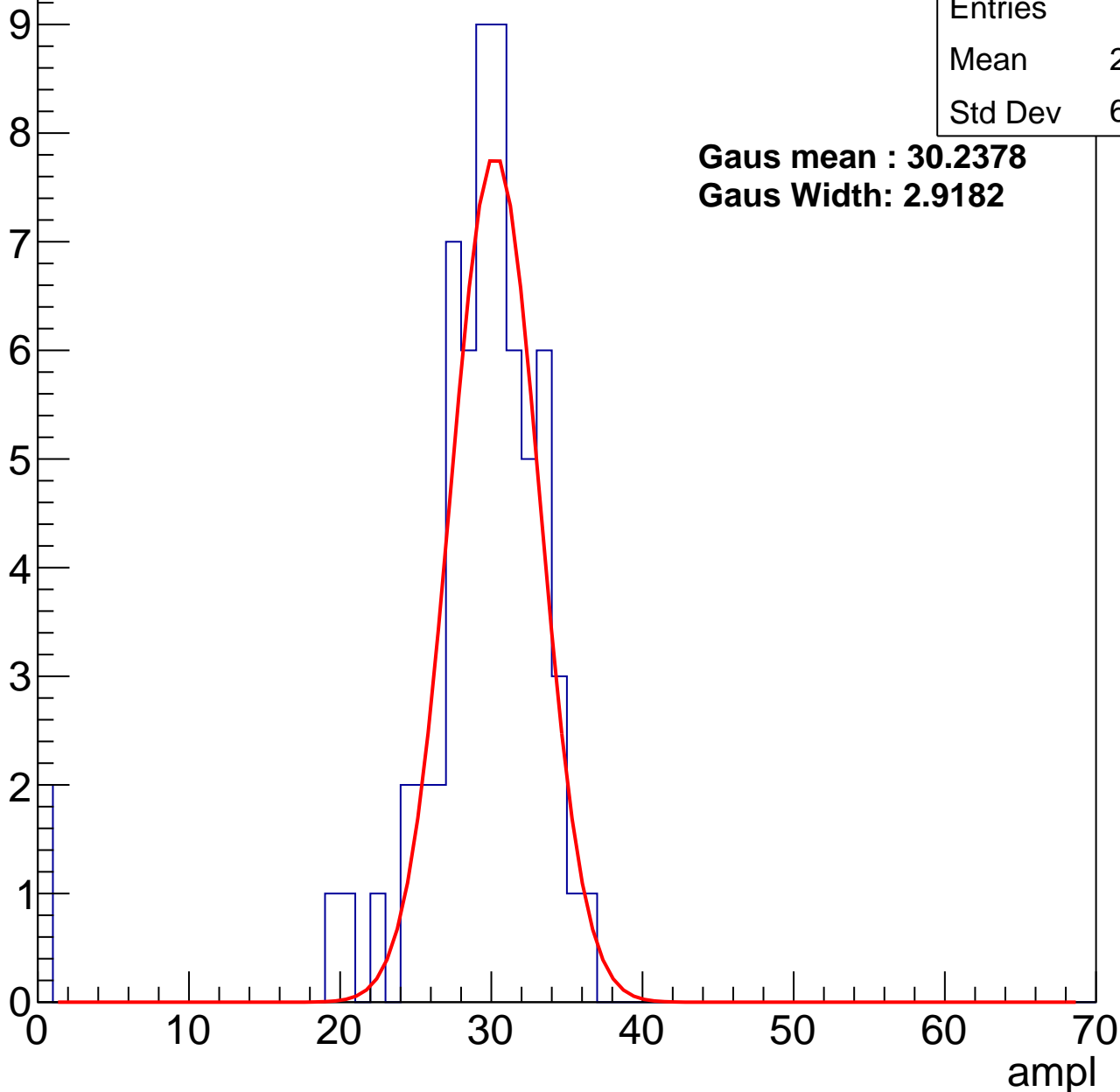
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	28.38
Std Dev	6.066

Gaus mean : 30.2378

Gaus Width: 2.9182



B1L101S, U17-ch90, adc1

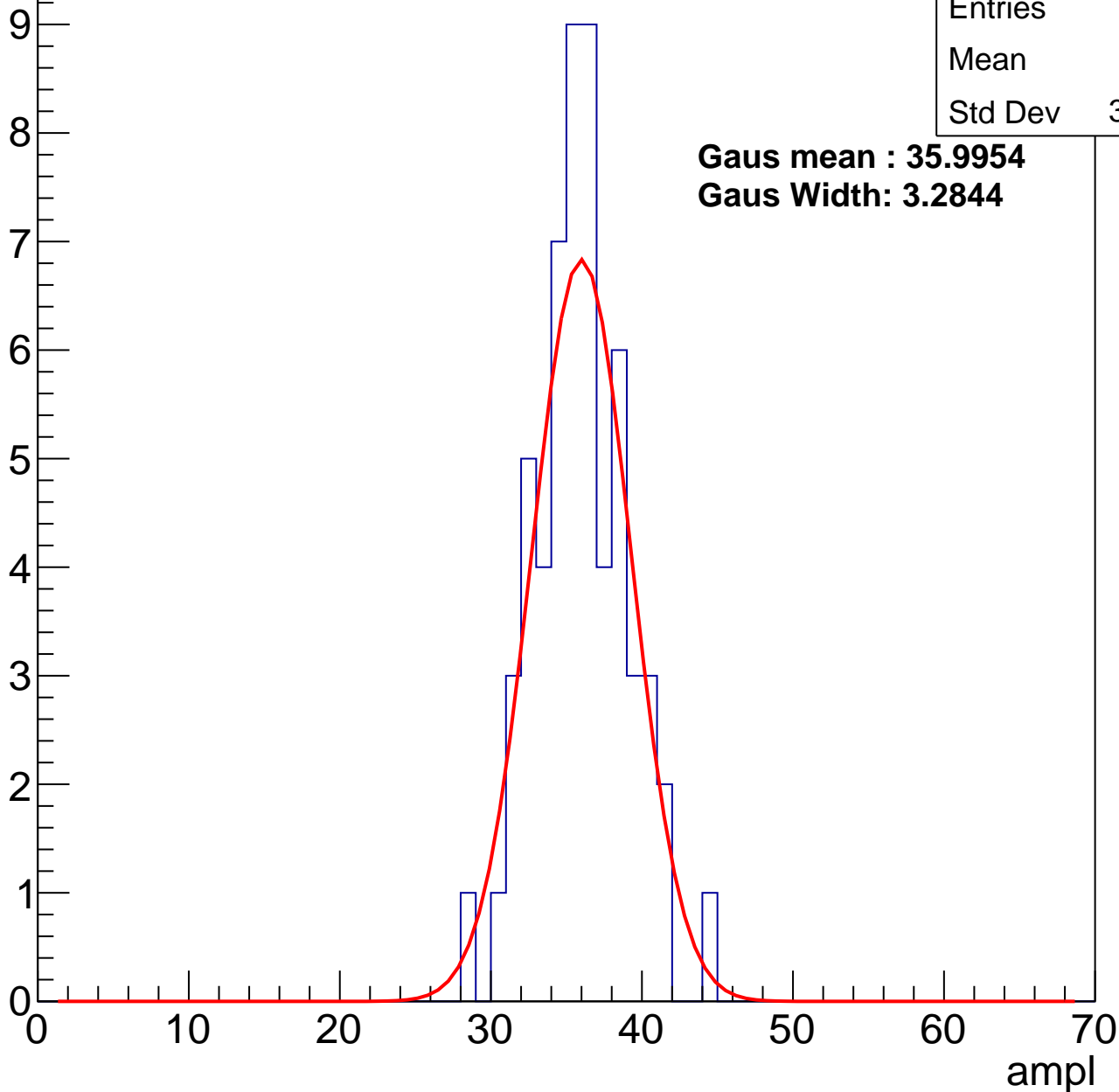
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.5
Std Dev	3.024

Gaus mean : 35.9954

Gaus Width: 3.2844



B1L101S, U17-ch90, adc2

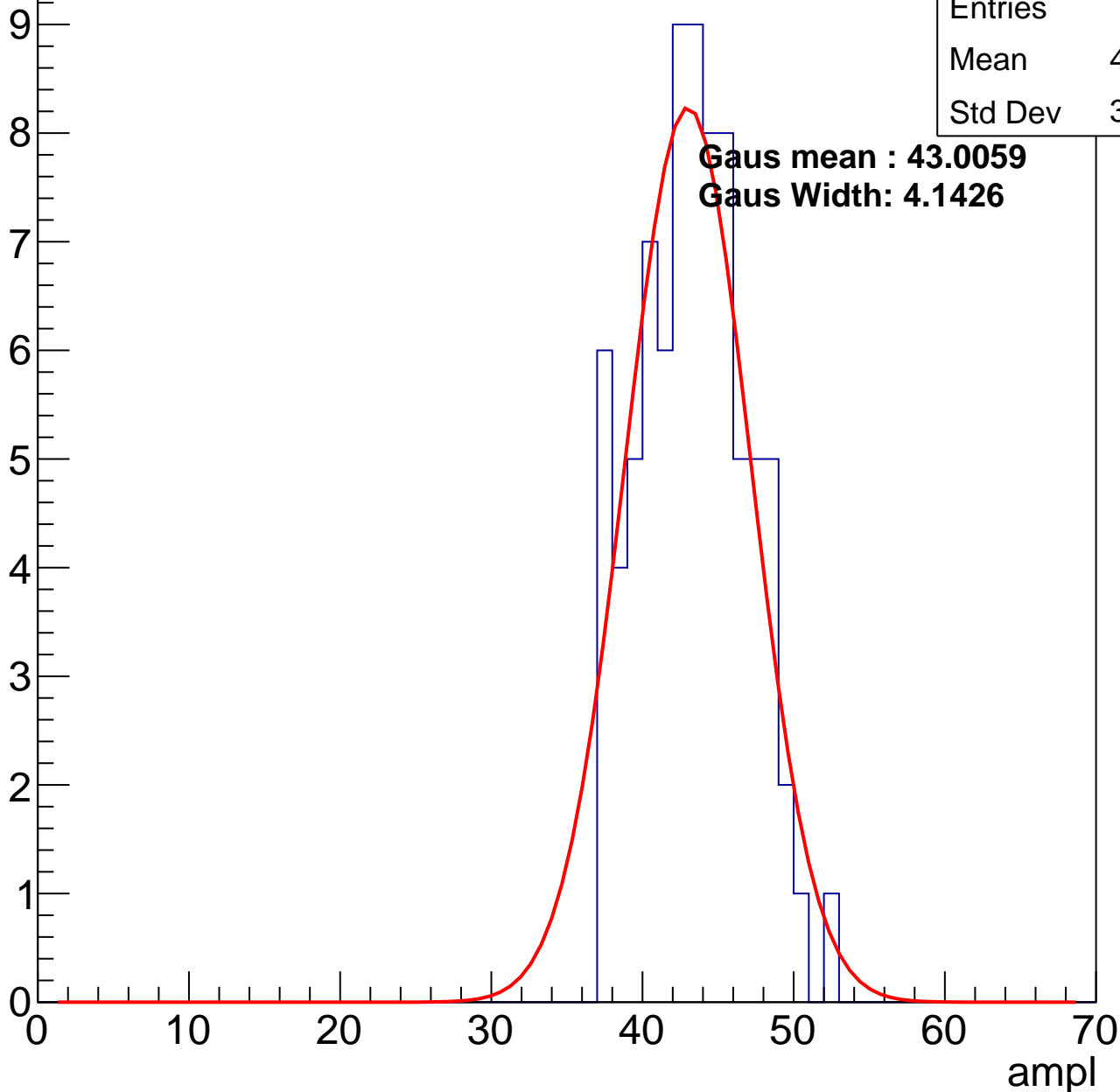
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	42.93
Std Dev	3.485

Gaus mean : 43.0059

Gaus Width: 4.1426

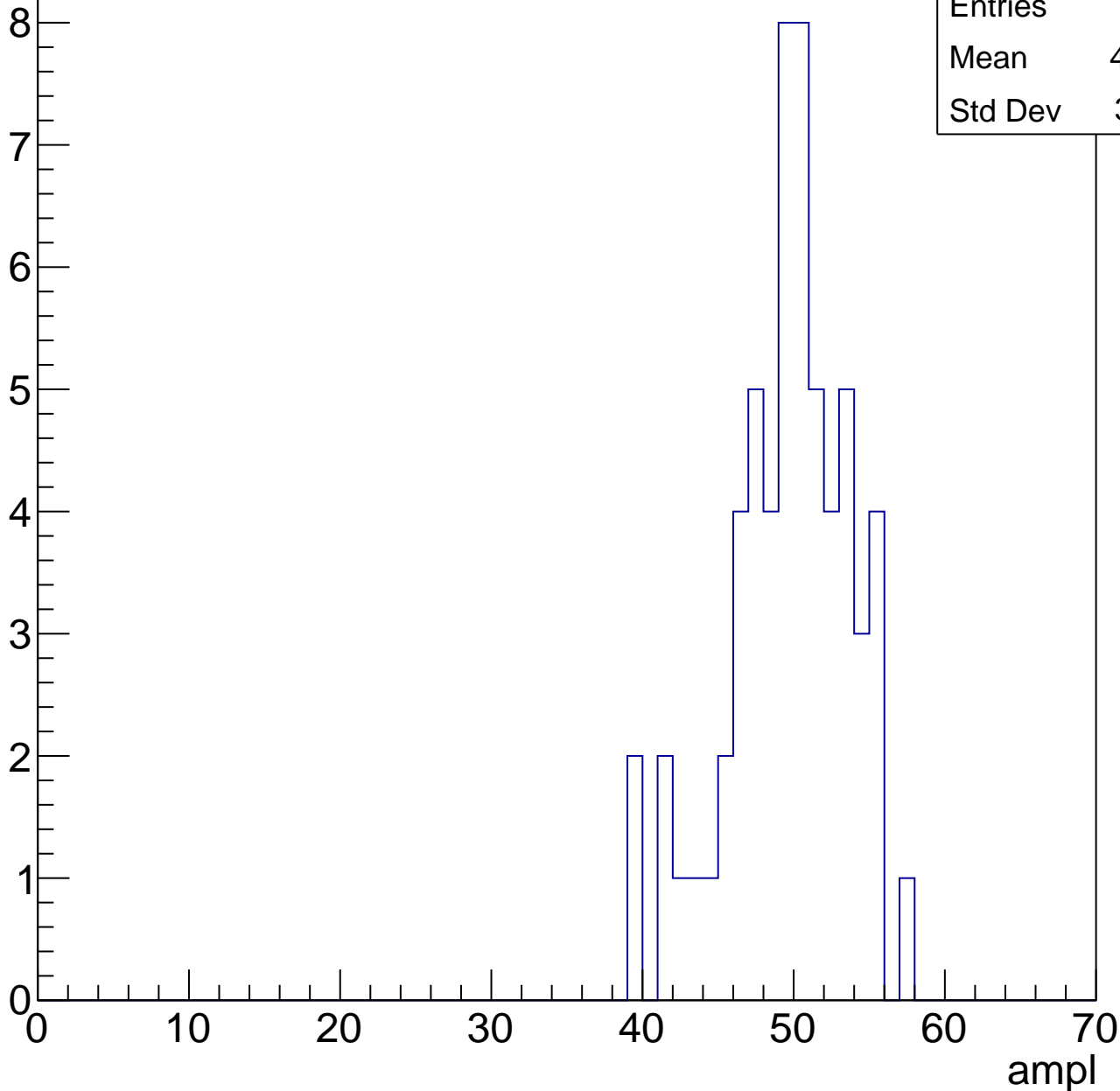


B1L101S, U17-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.15
Std Dev	3.991

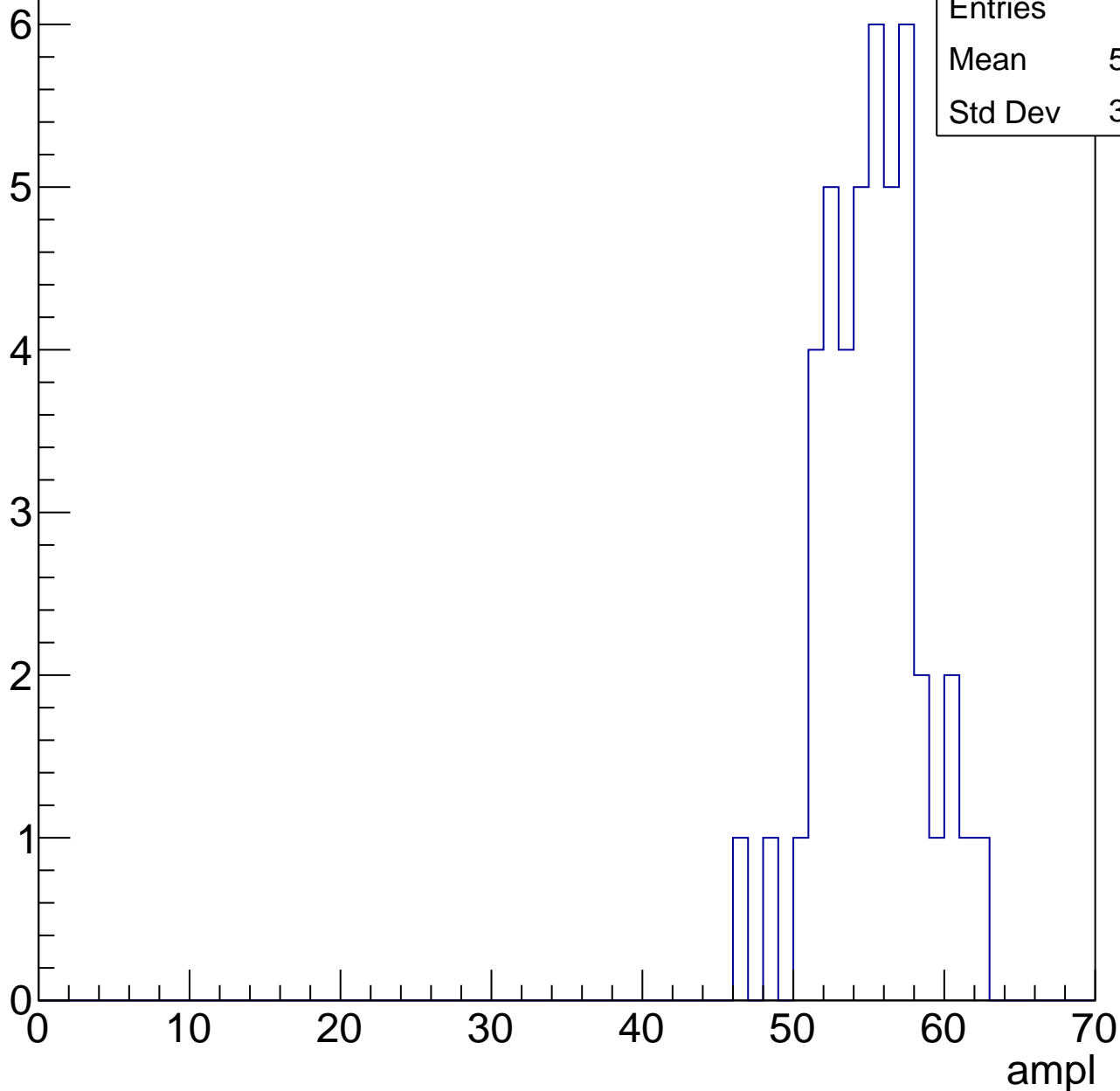


B1L101S, U17-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

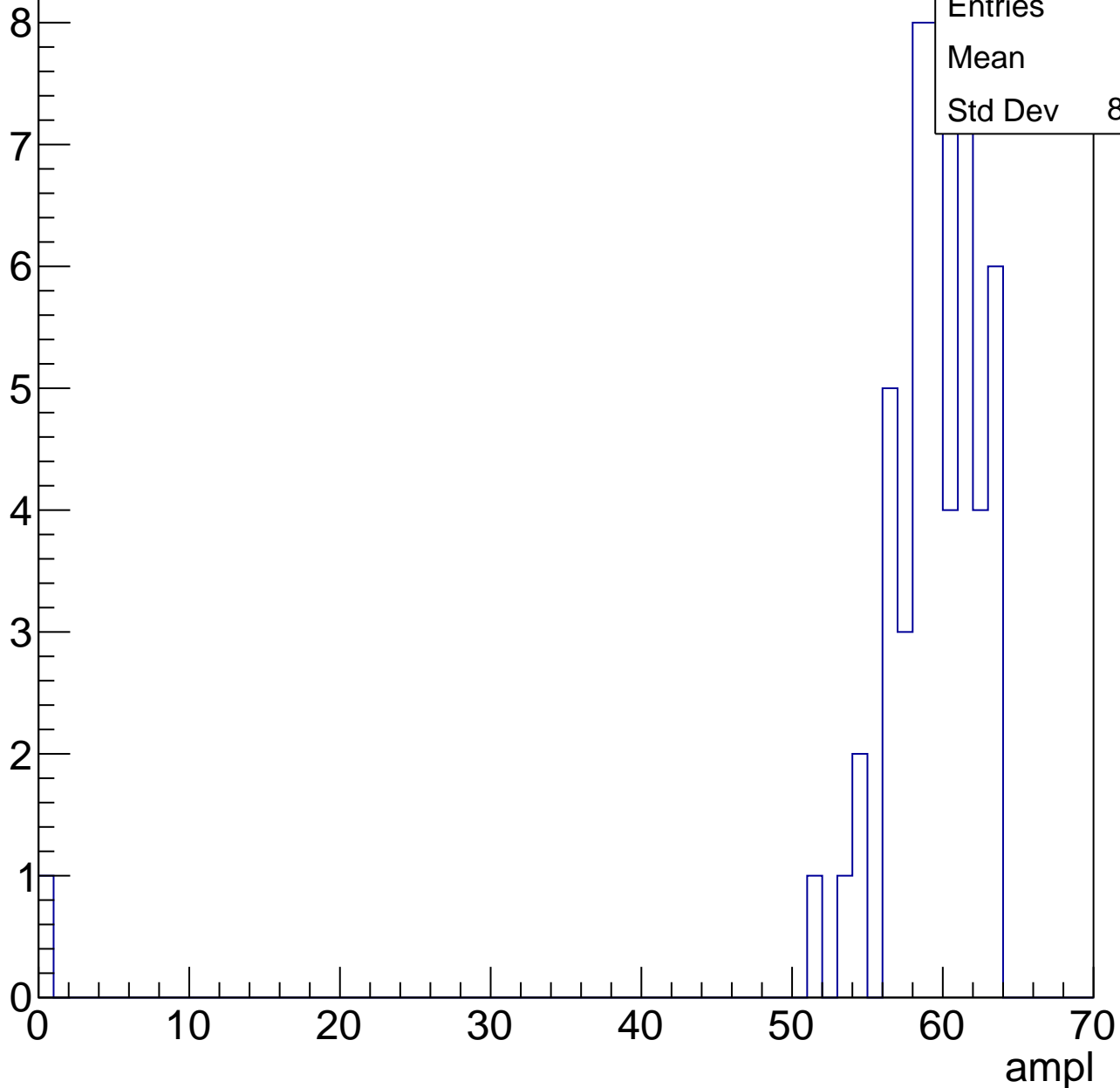
Entries	45
Mean	54.67
Std Dev	3.259



B1L101S, U17-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

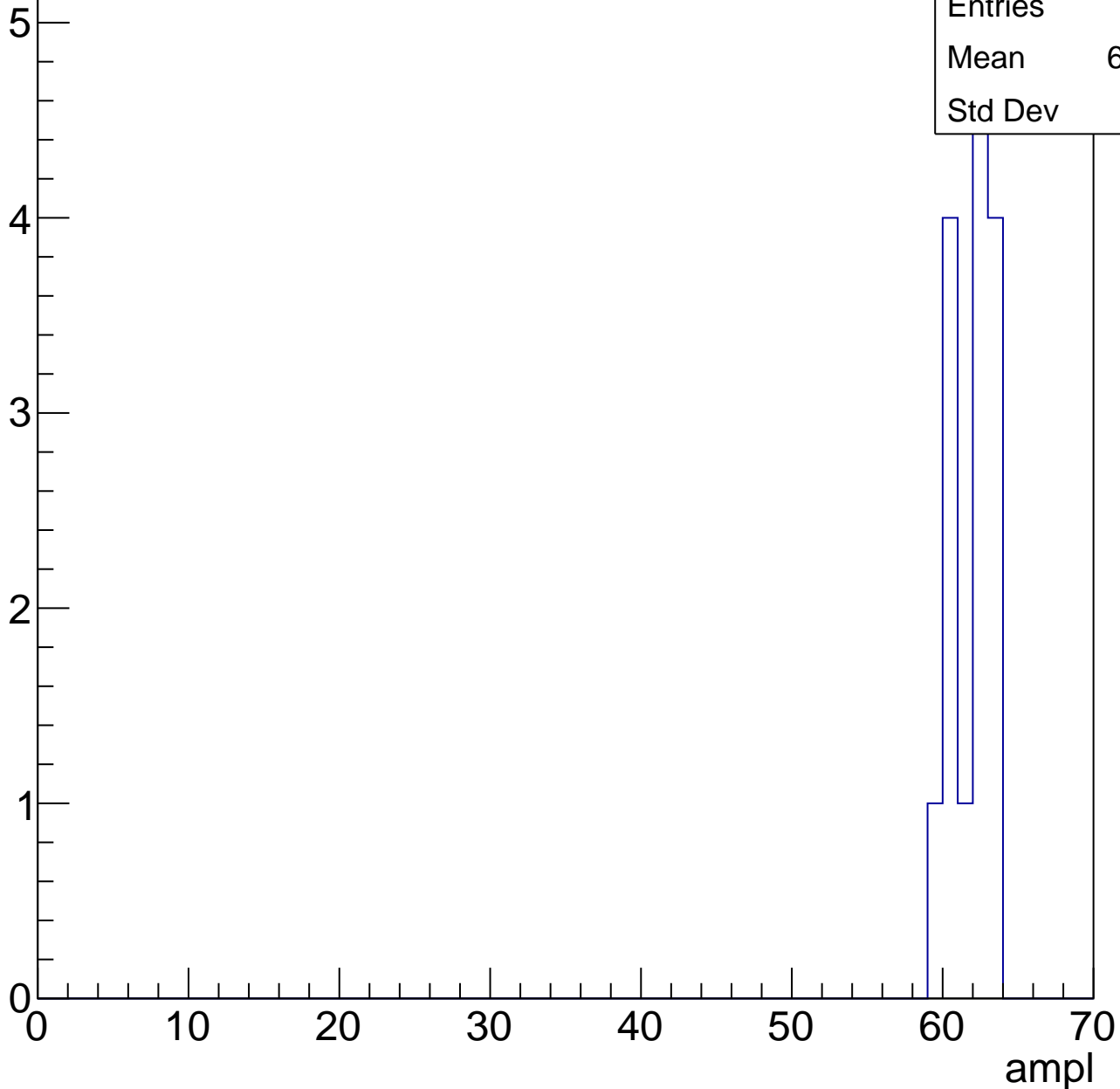


B1L101S, U17-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.47
Std Dev	1.31



B1L101S, U17-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch91, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	31.08
Std Dev	6.081

Gaus mean : 32.3410

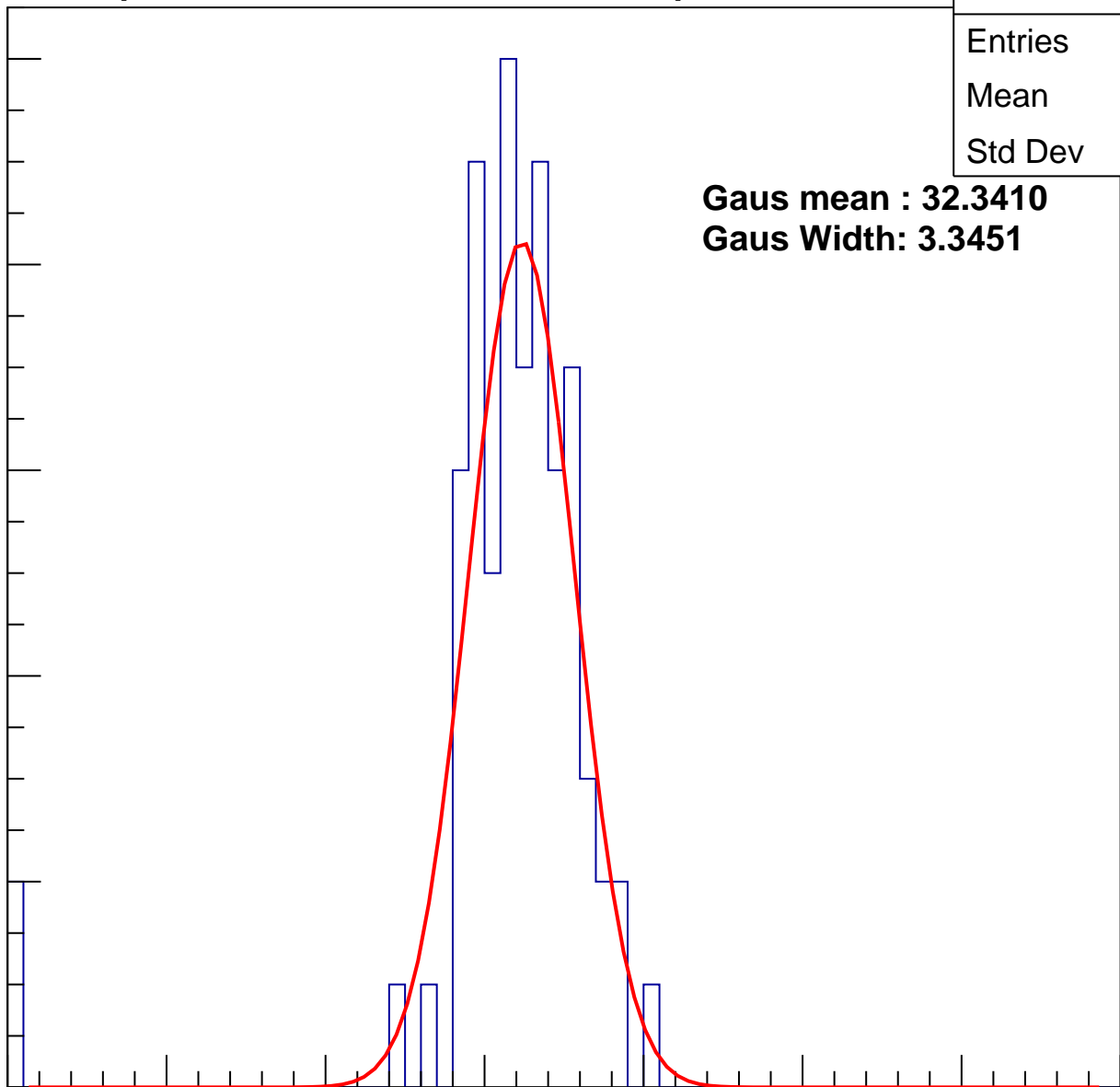
Gaus Width: 3.3451

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch91, adc1

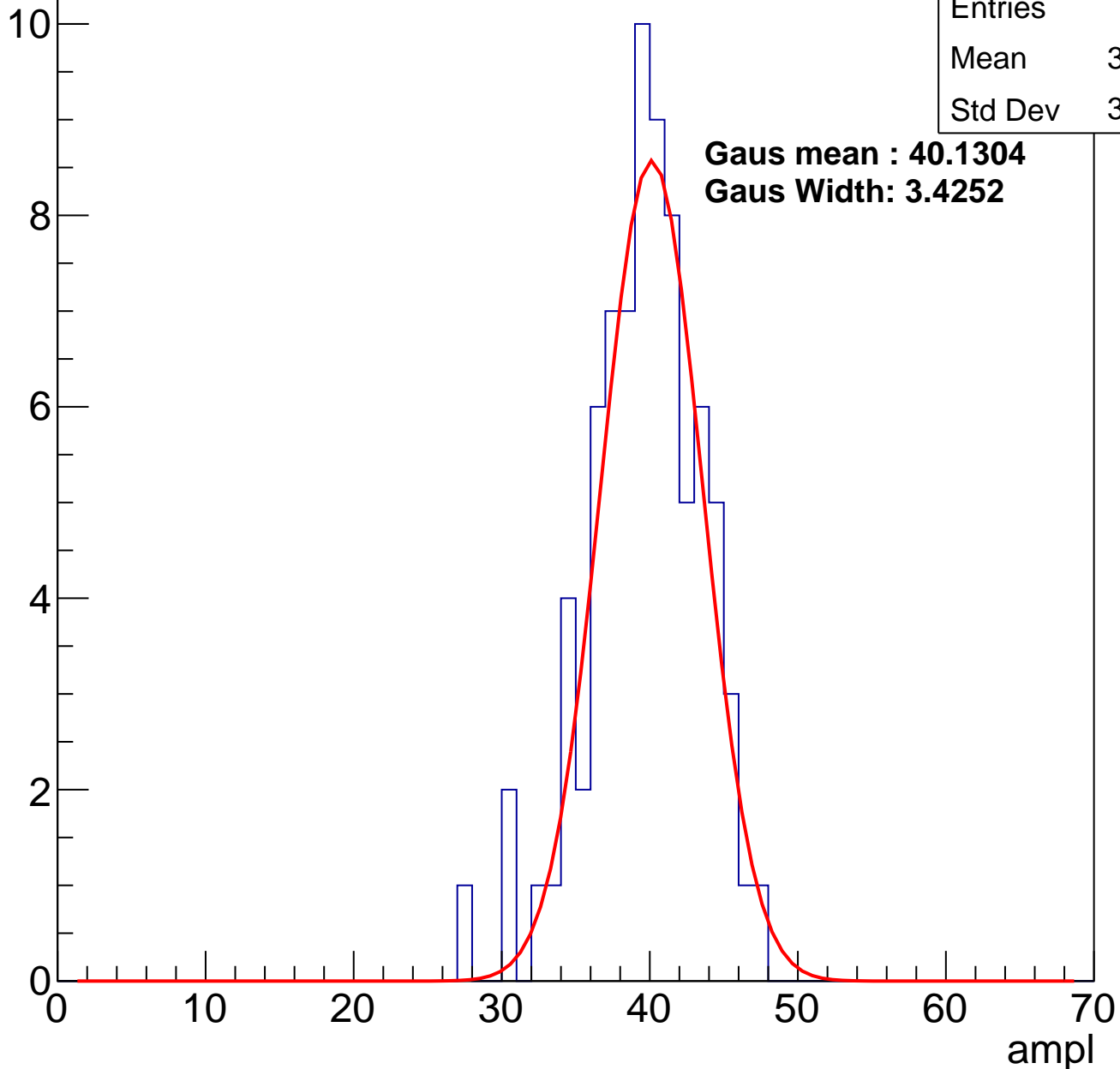
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	39.15
Std Dev	3.796

Gaus mean : 40.1304

Gaus Width: 3.4252

Entry



B1L101S, U17-ch91, adc2

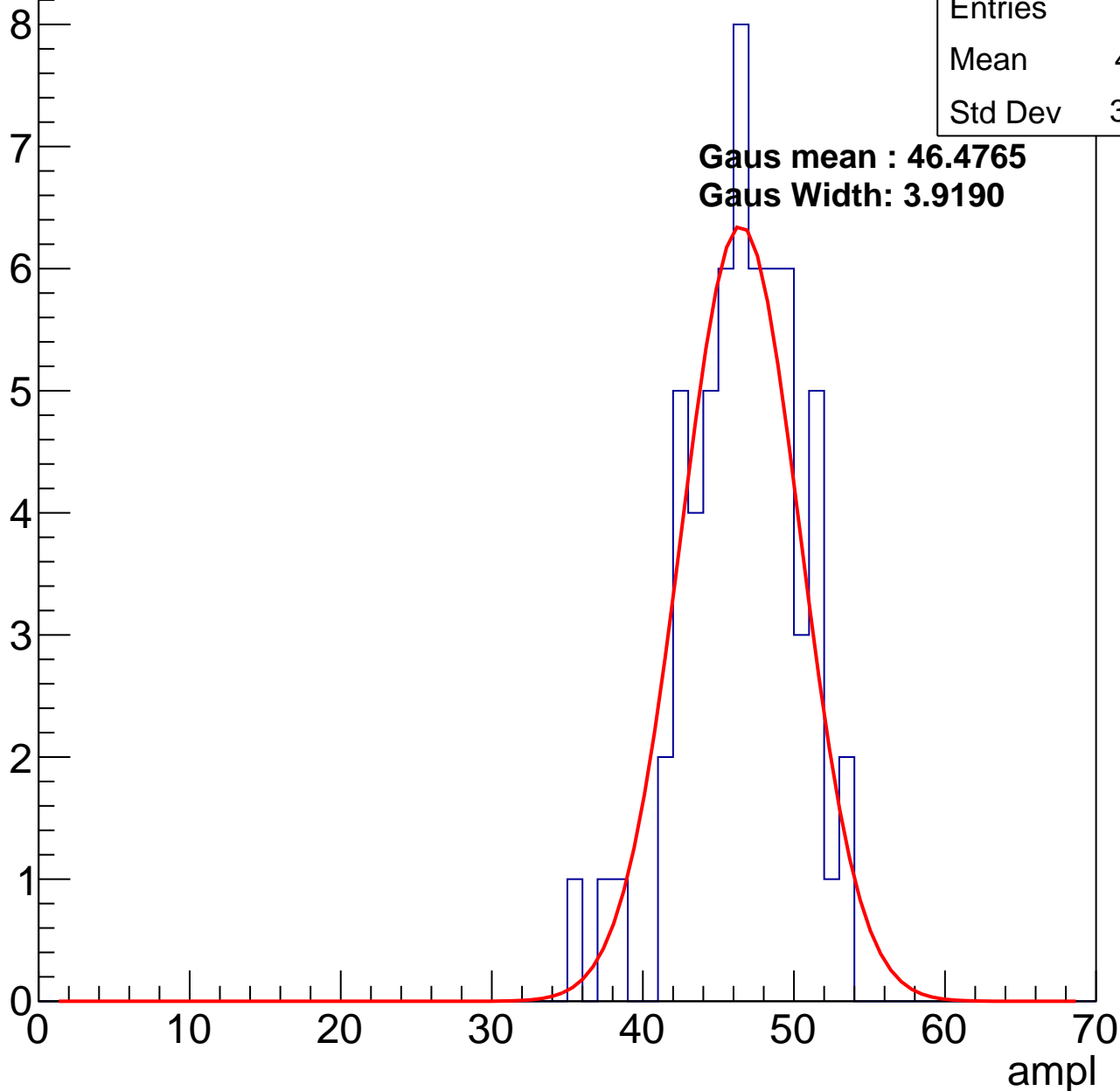
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	46.11
Std Dev	3.699

Gaus mean : 46.4765

Gaus Width: 3.9190



B1L101S, U17-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	52.56
Std Dev	3.179

Entry

10

8

6

4

2

0

0

10

20

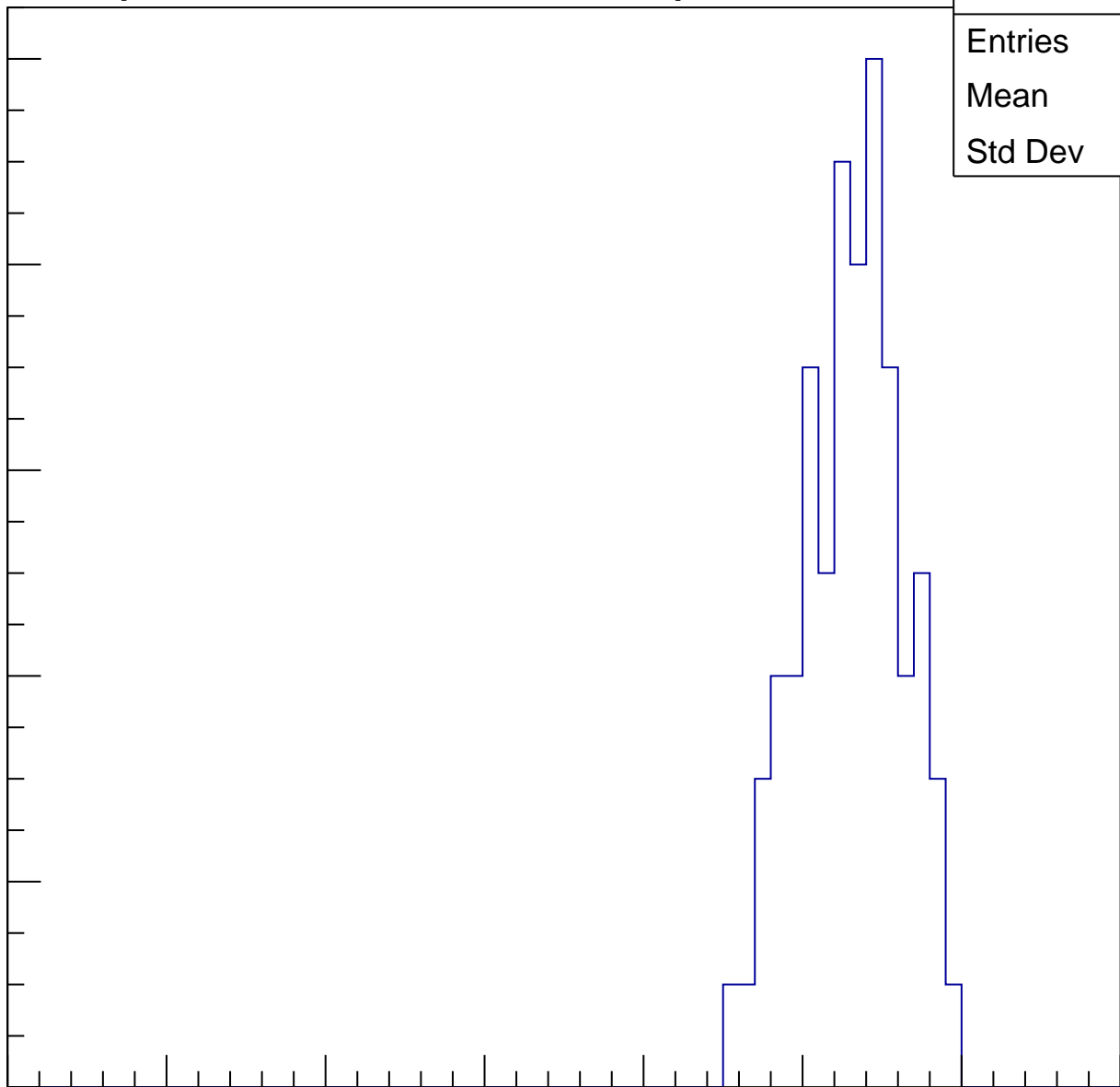
30

40

50

60

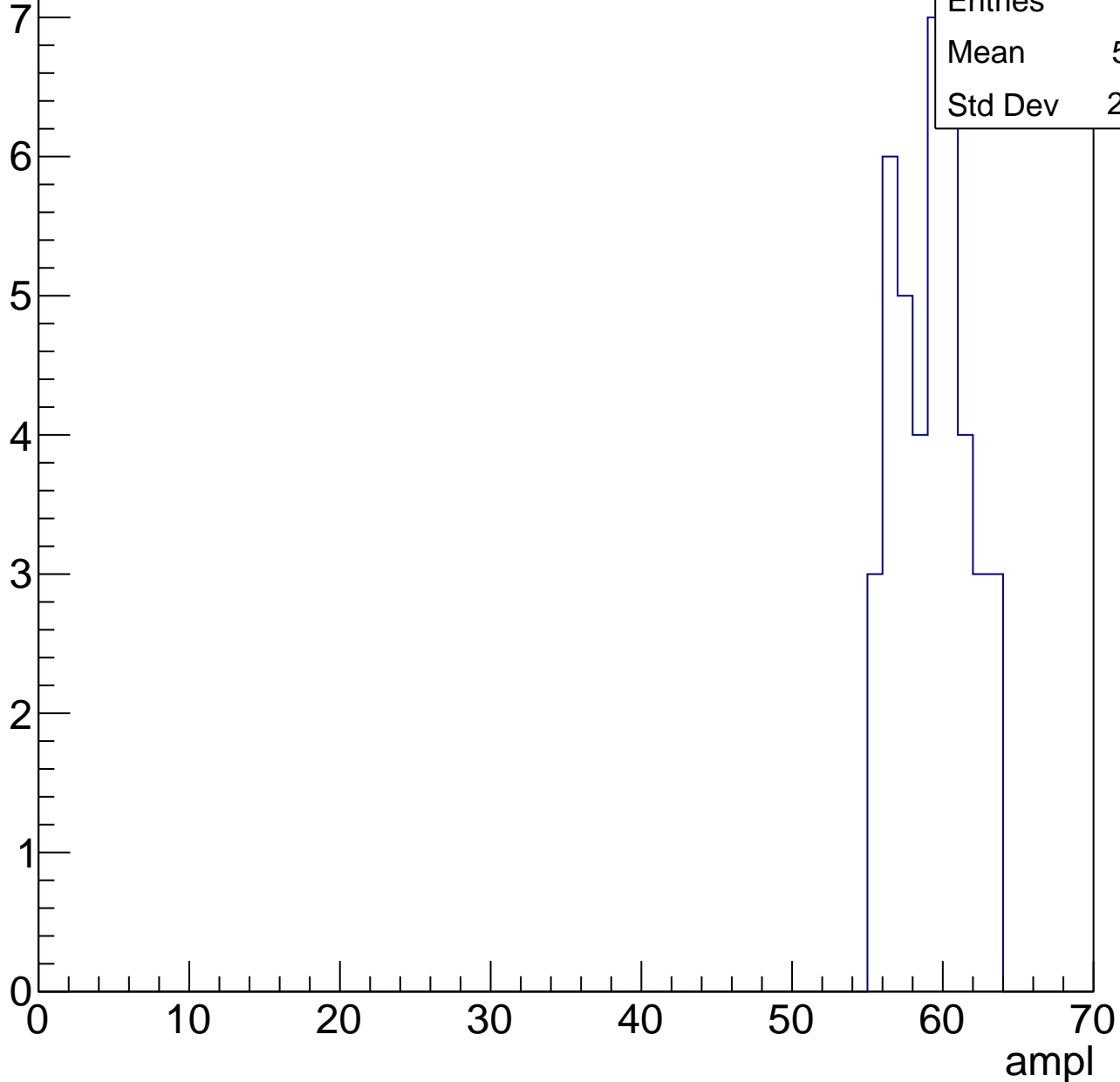
ampl



B1L101S, U17-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



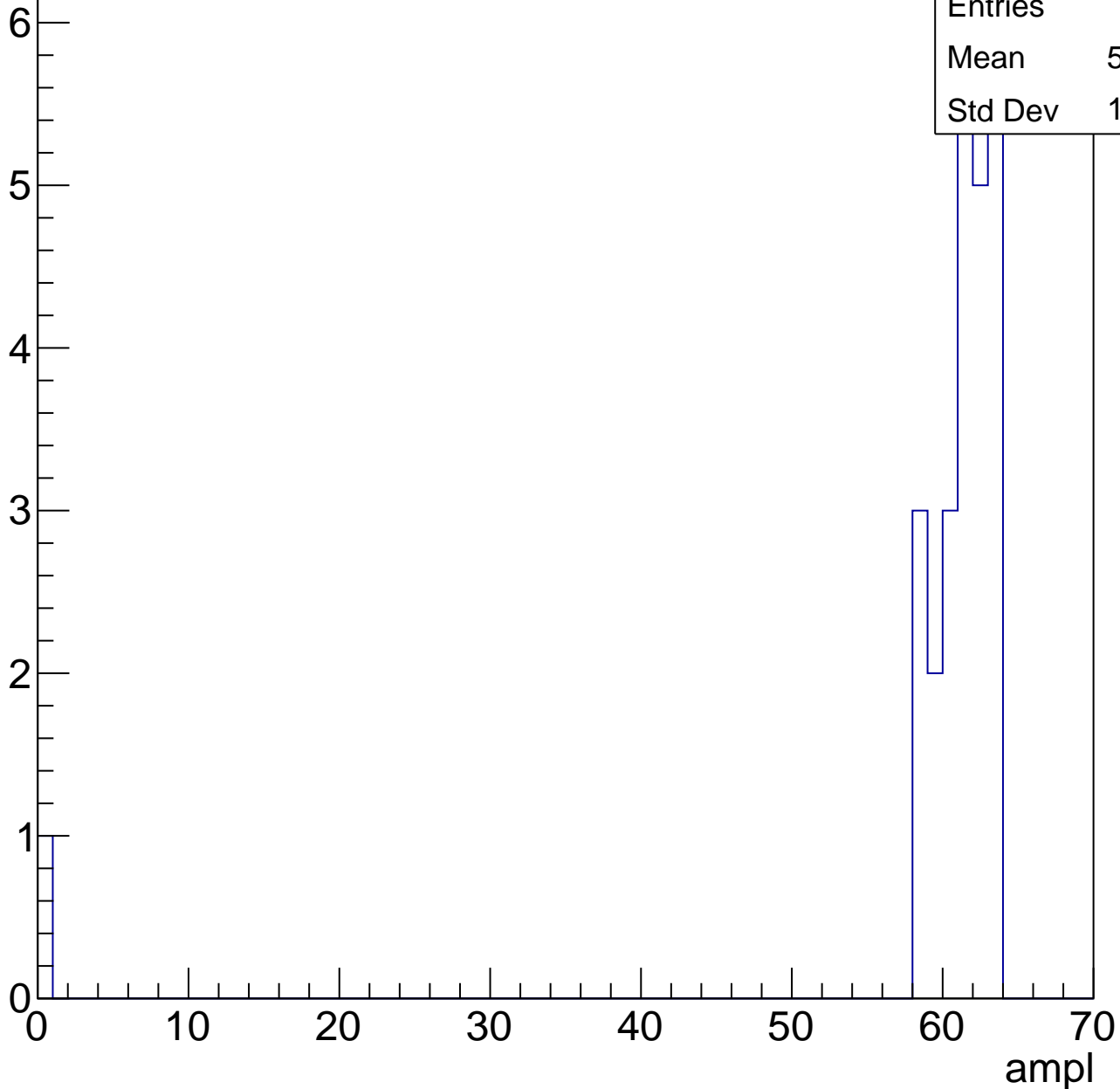
Entries	42
Mean	58.81
Std Dev	2.302

B1L101S, U17-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.69
Std Dev	11.85



B1L101S, U17-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L101S, U17-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch92, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	28.89
Std Dev	6.027

Gaus mean : 28.9224

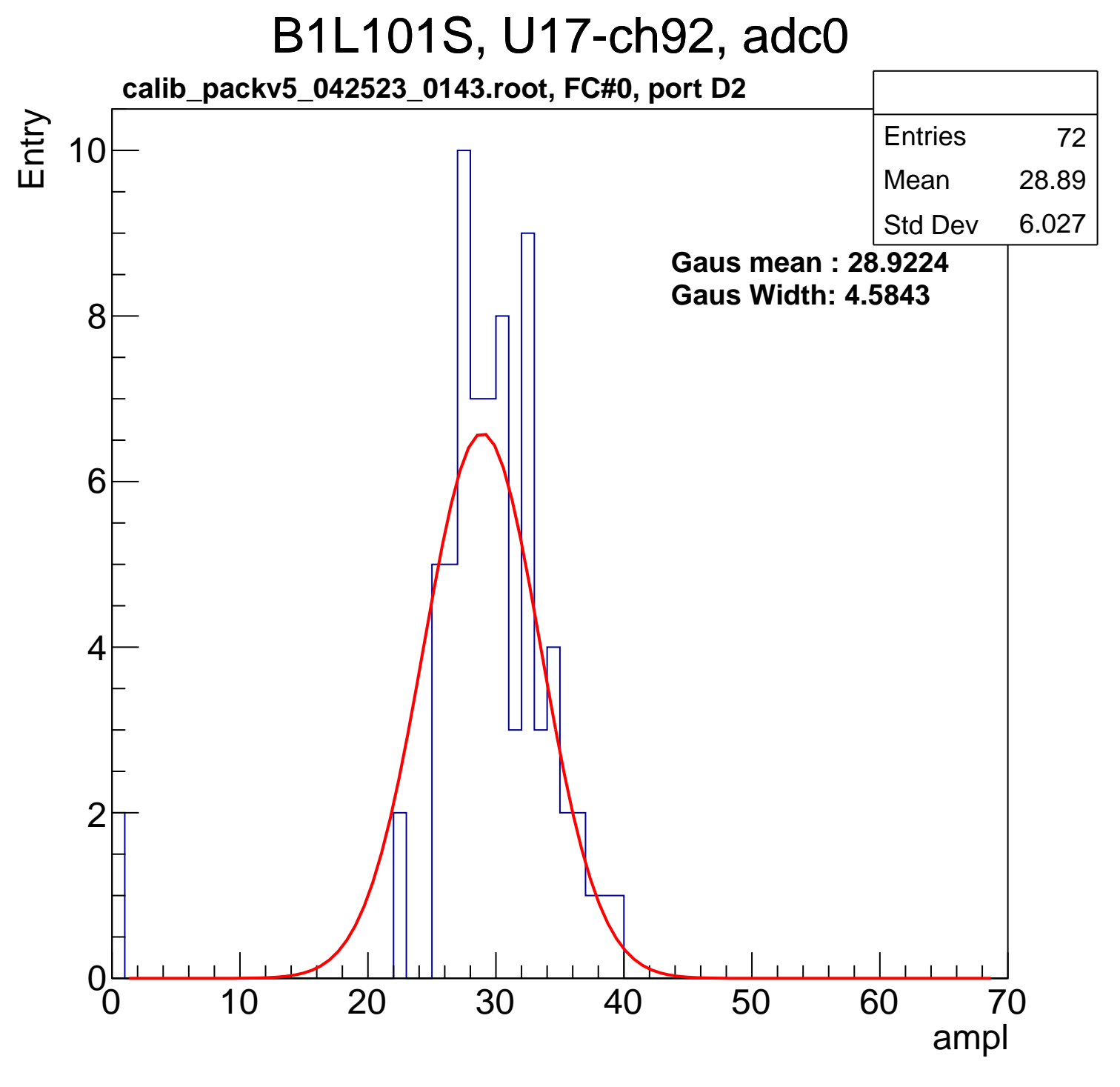
Gaus Width: 4.5843

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch92, adc1

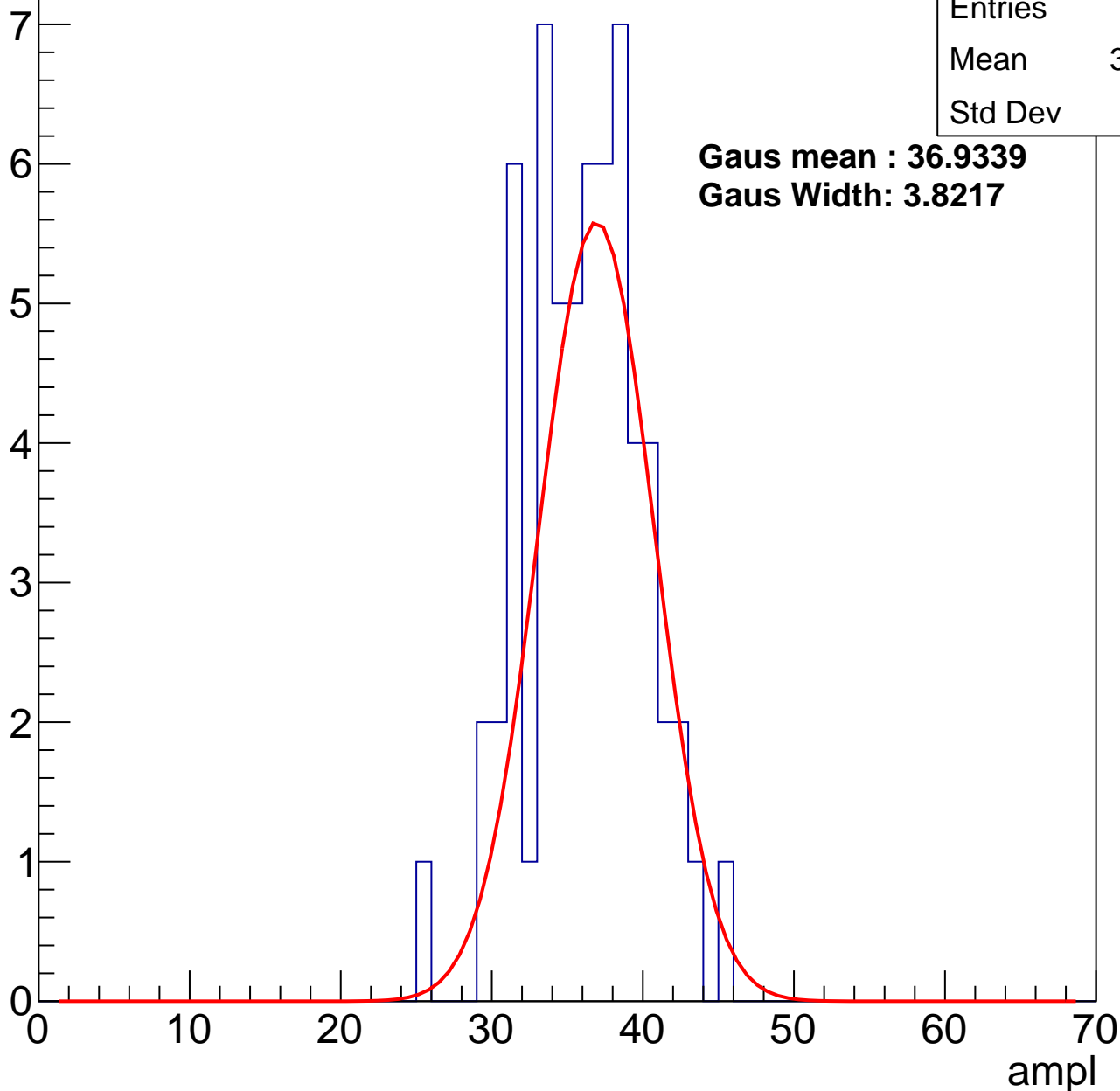
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	35.66
Std Dev	3.86

Gaus mean : 36.9339

Gaus Width: 3.8217



B1L101S, U17-ch92, adc2

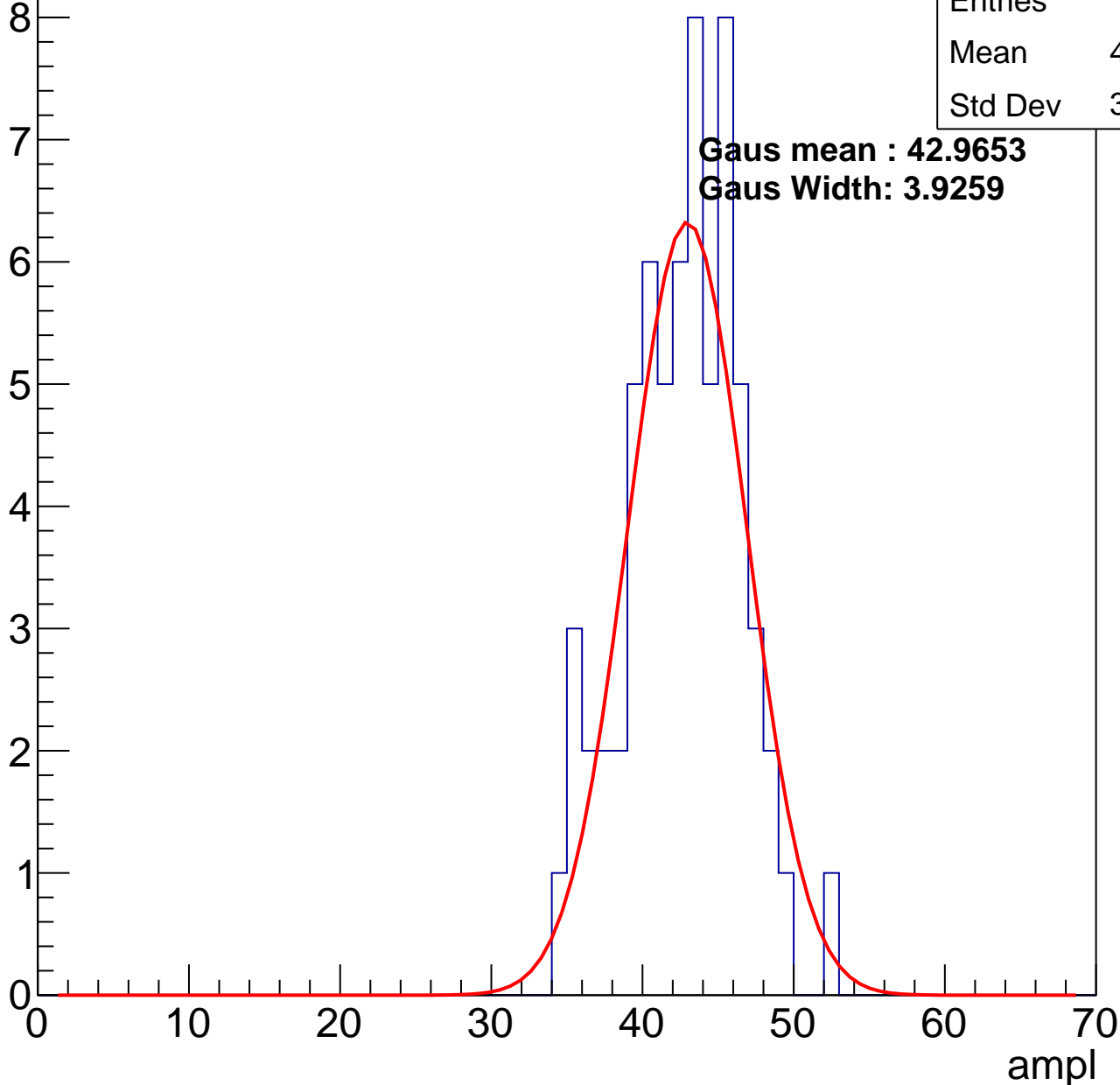
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.23
Std Dev	3.745

Gaus mean : 42.9653

Gaus Width: 3.9259

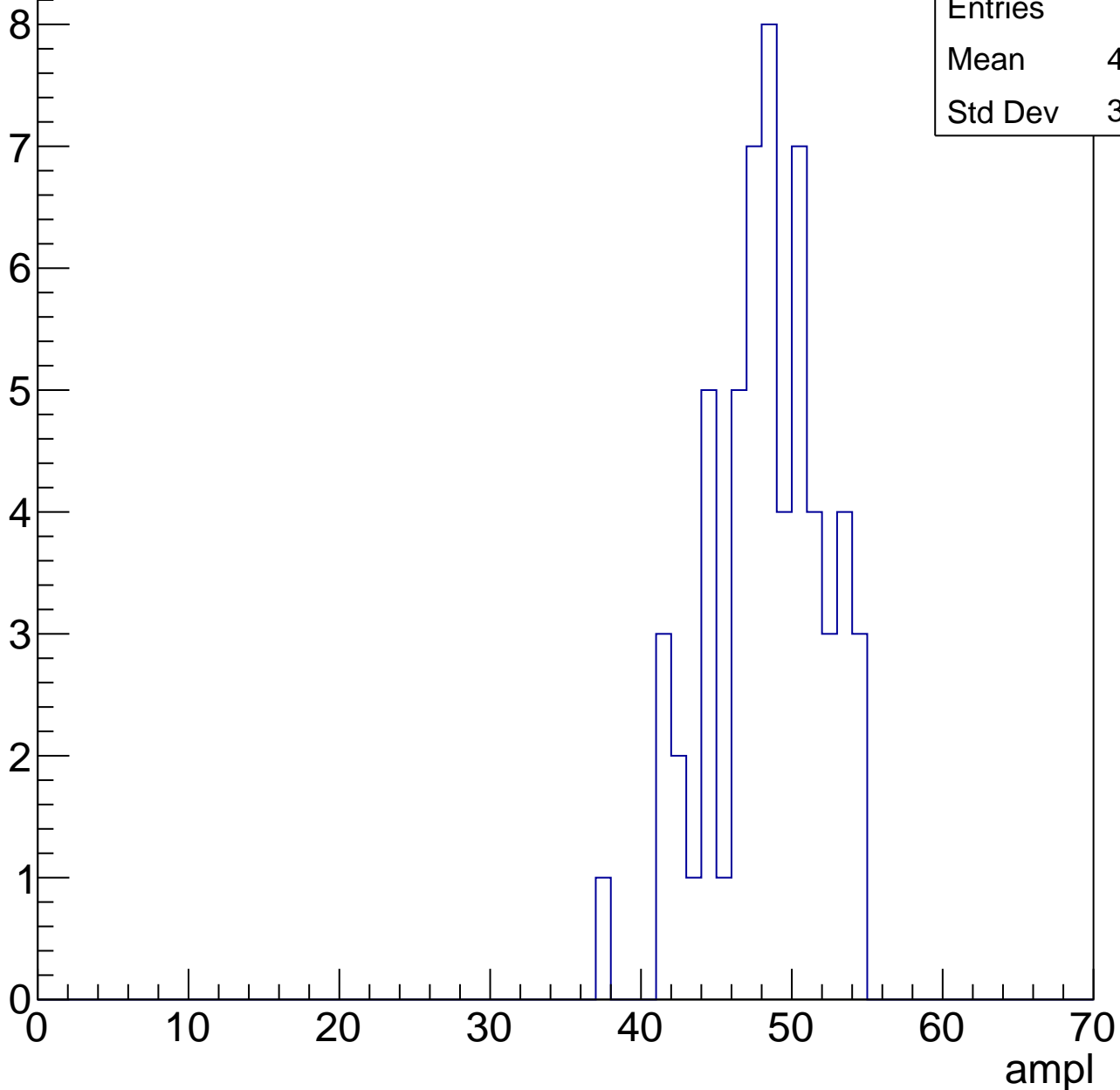


B1L101S, U17-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	47.84
Std Dev	3.722

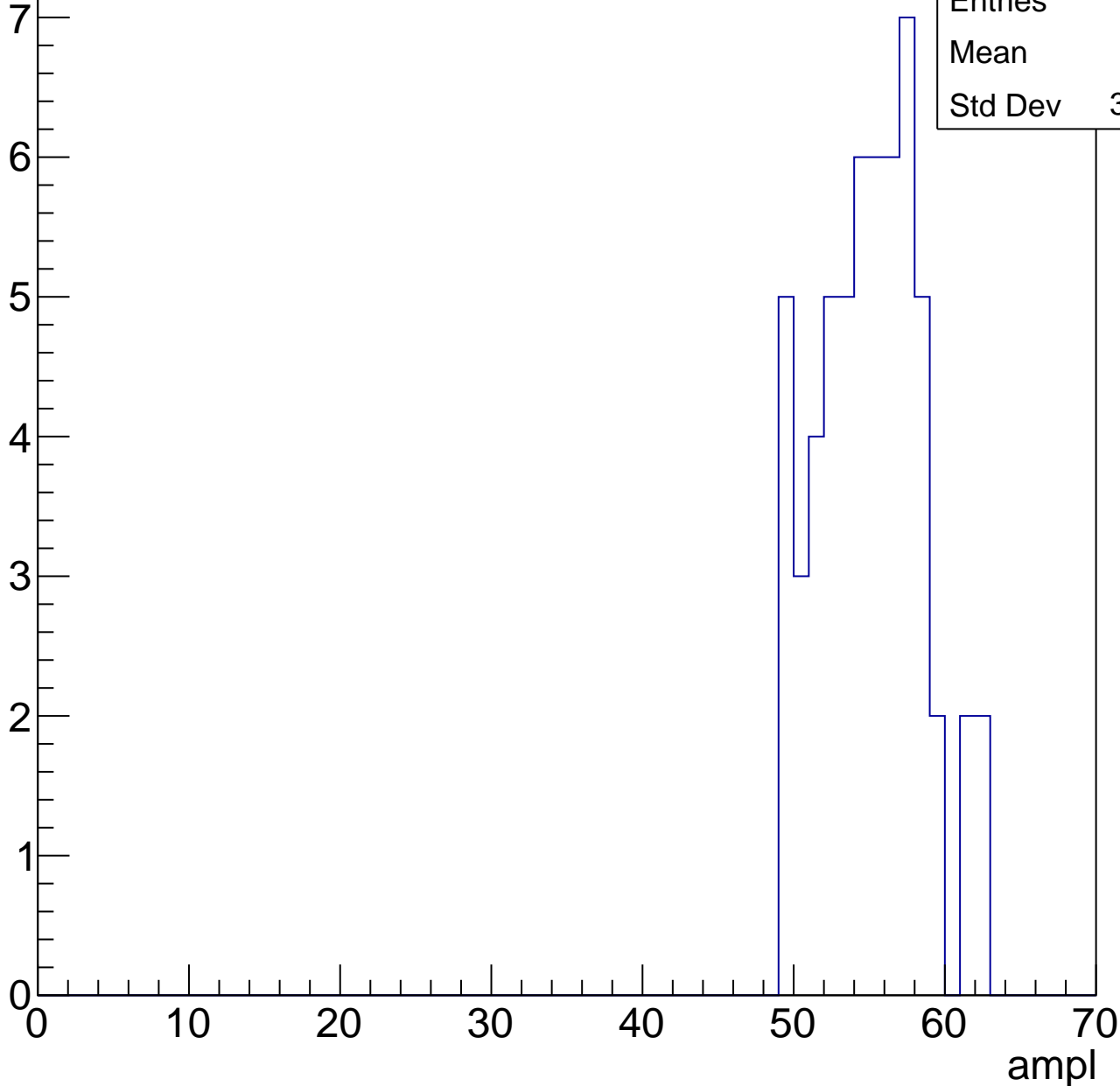


B1L101S, U17-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

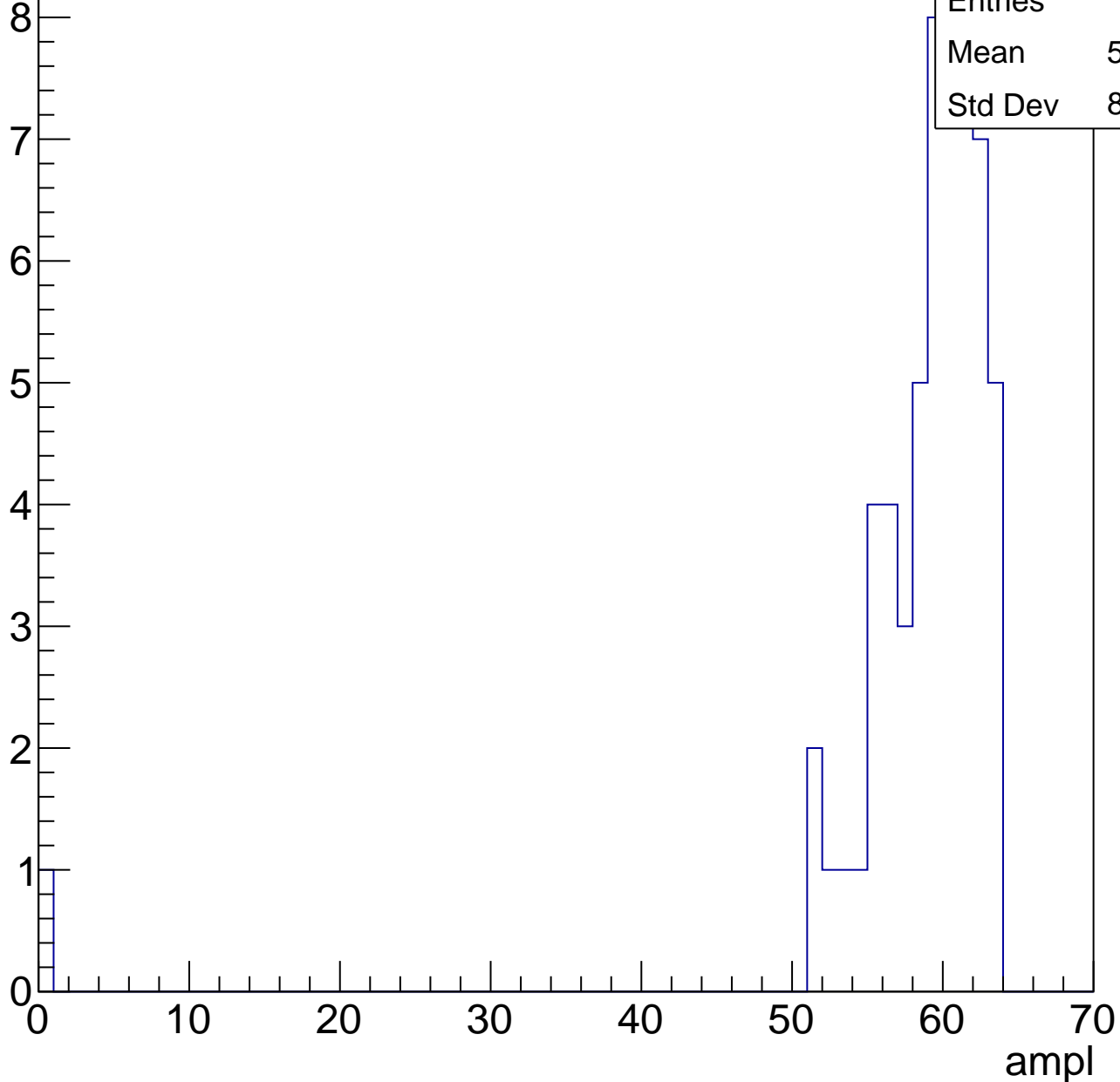
Entries	58
Mean	54.6
Std Dev	3.378



B1L101S, U17-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

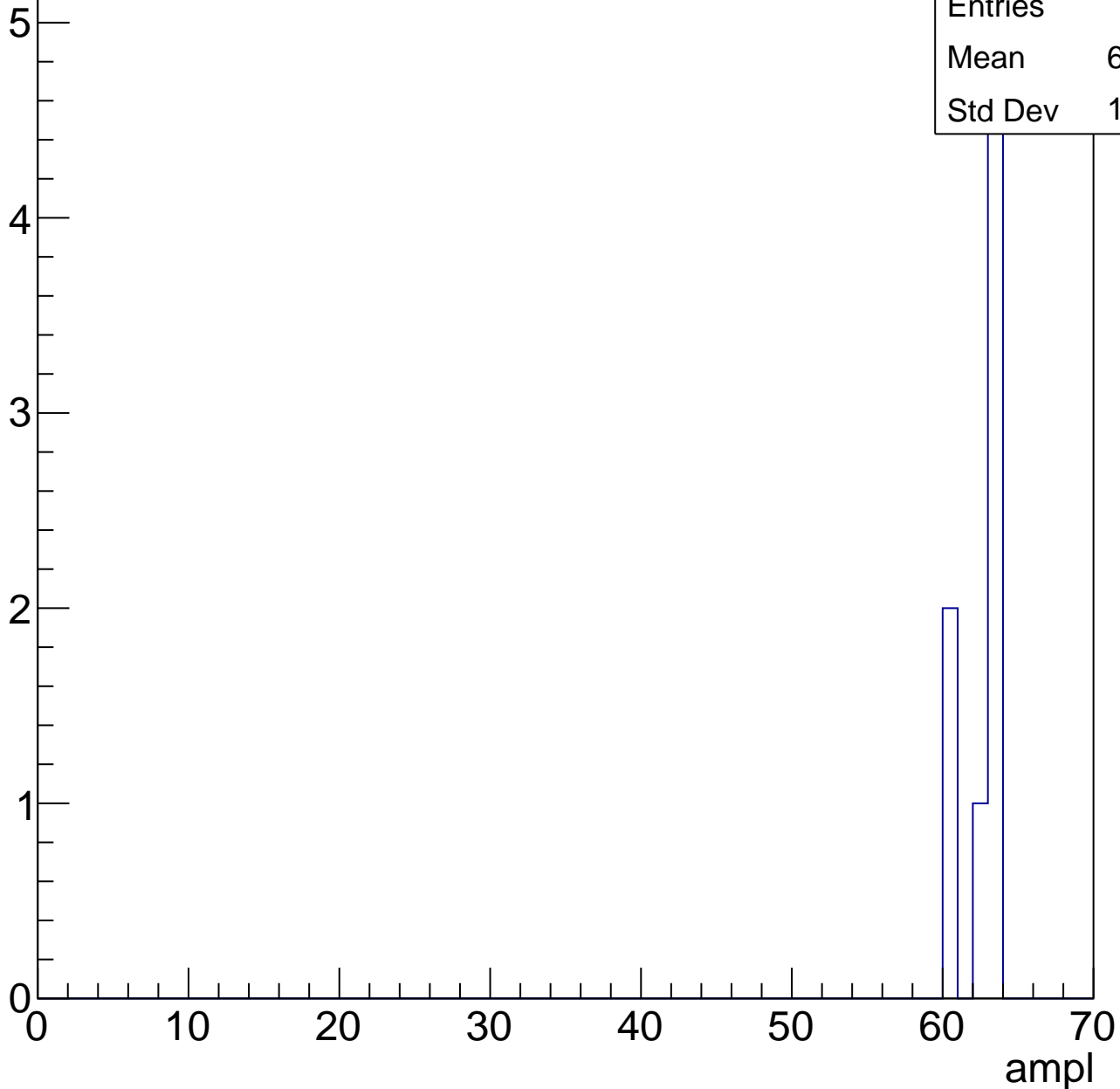


B1L101S, U17-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.12
Std Dev	1.269



B1L101S, U17-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch93, adc0

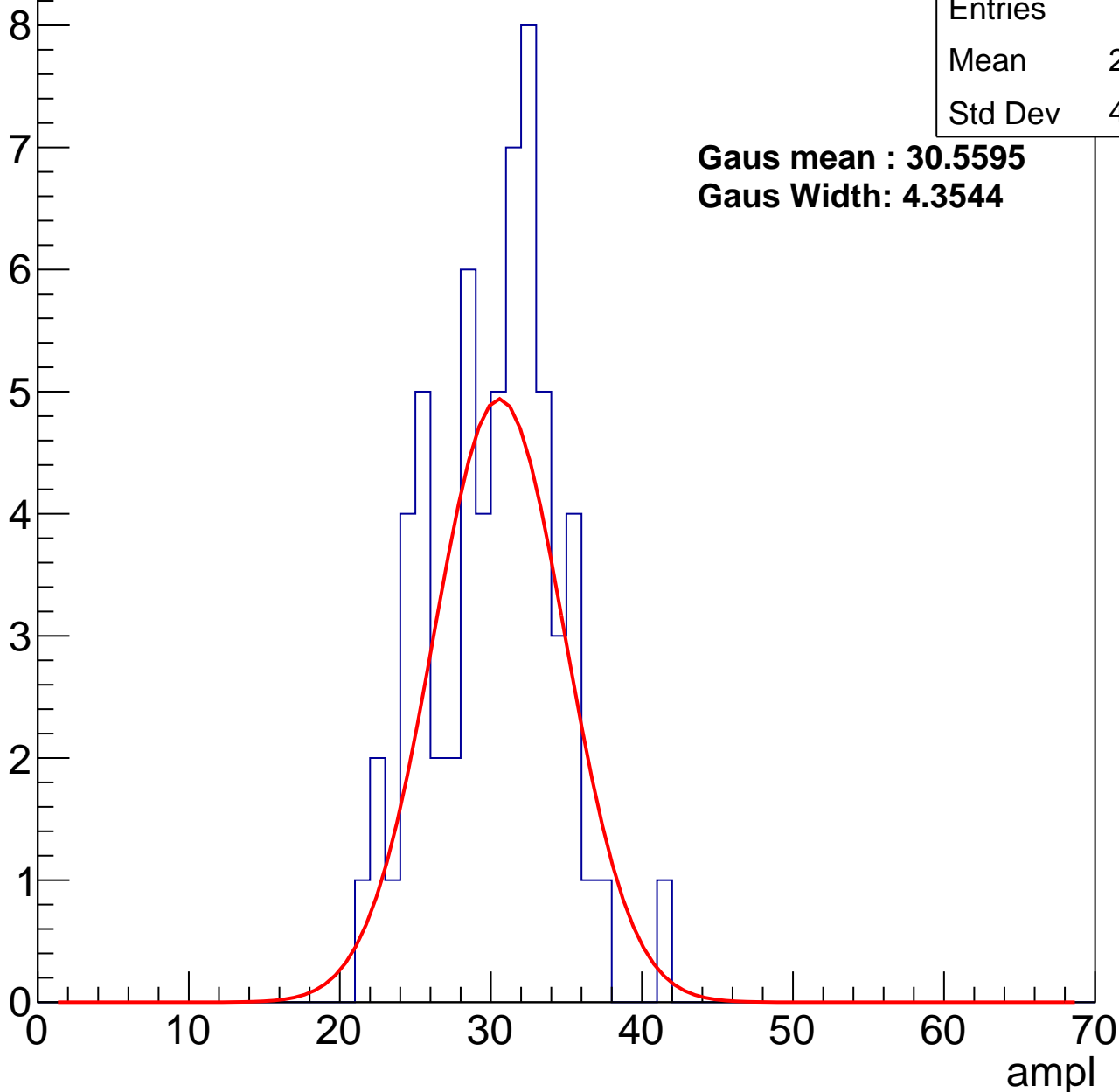
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.73
Std Dev	4.092

Gaus mean : 30.5595

Gaus Width: 4.3544



B1L101S, U17-ch93, adc1

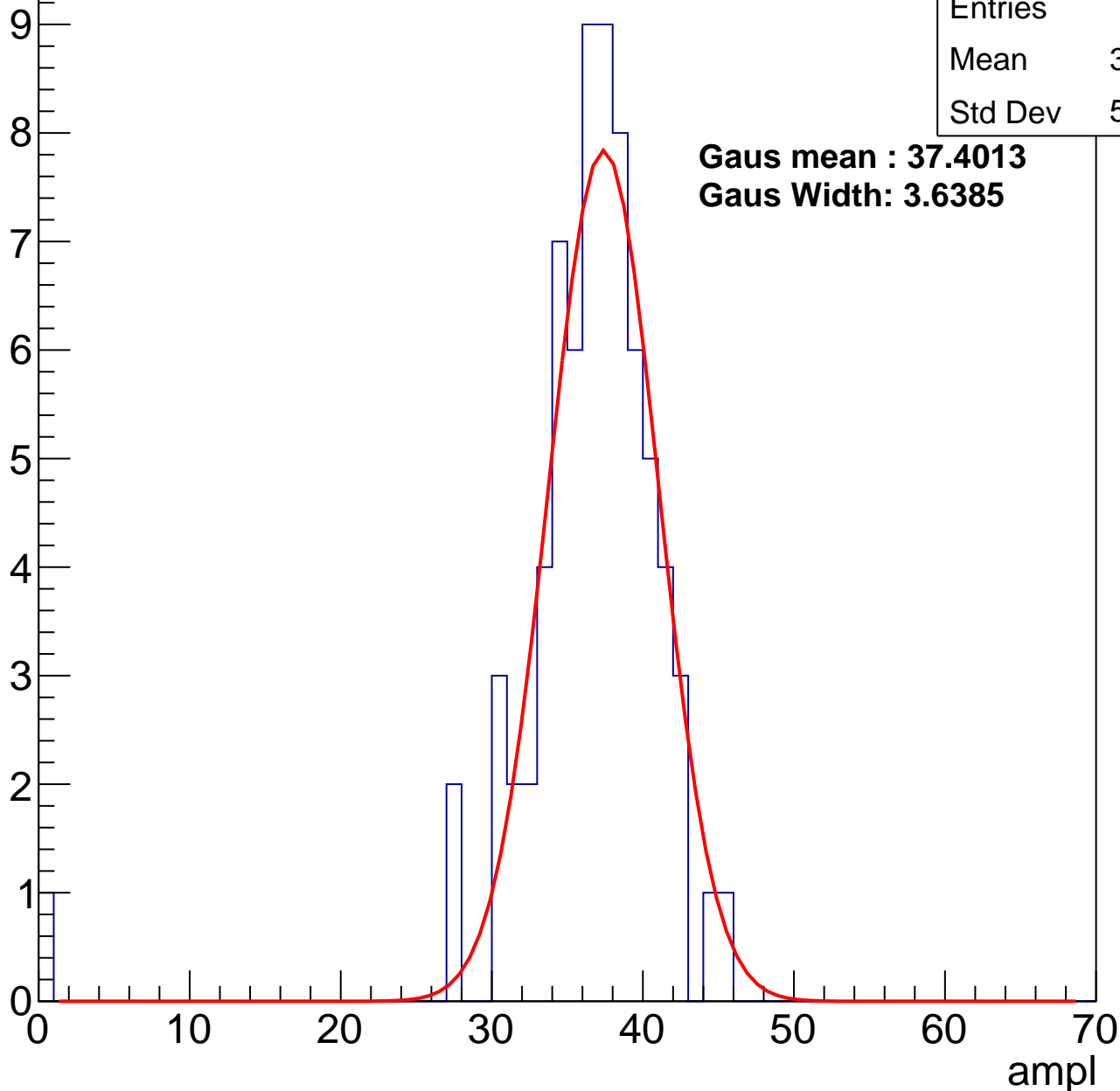
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.95
Std Dev	5.547

Gaus mean : 37.4013

Gaus Width: 3.6385



B1L101S, U17-ch93, adc2

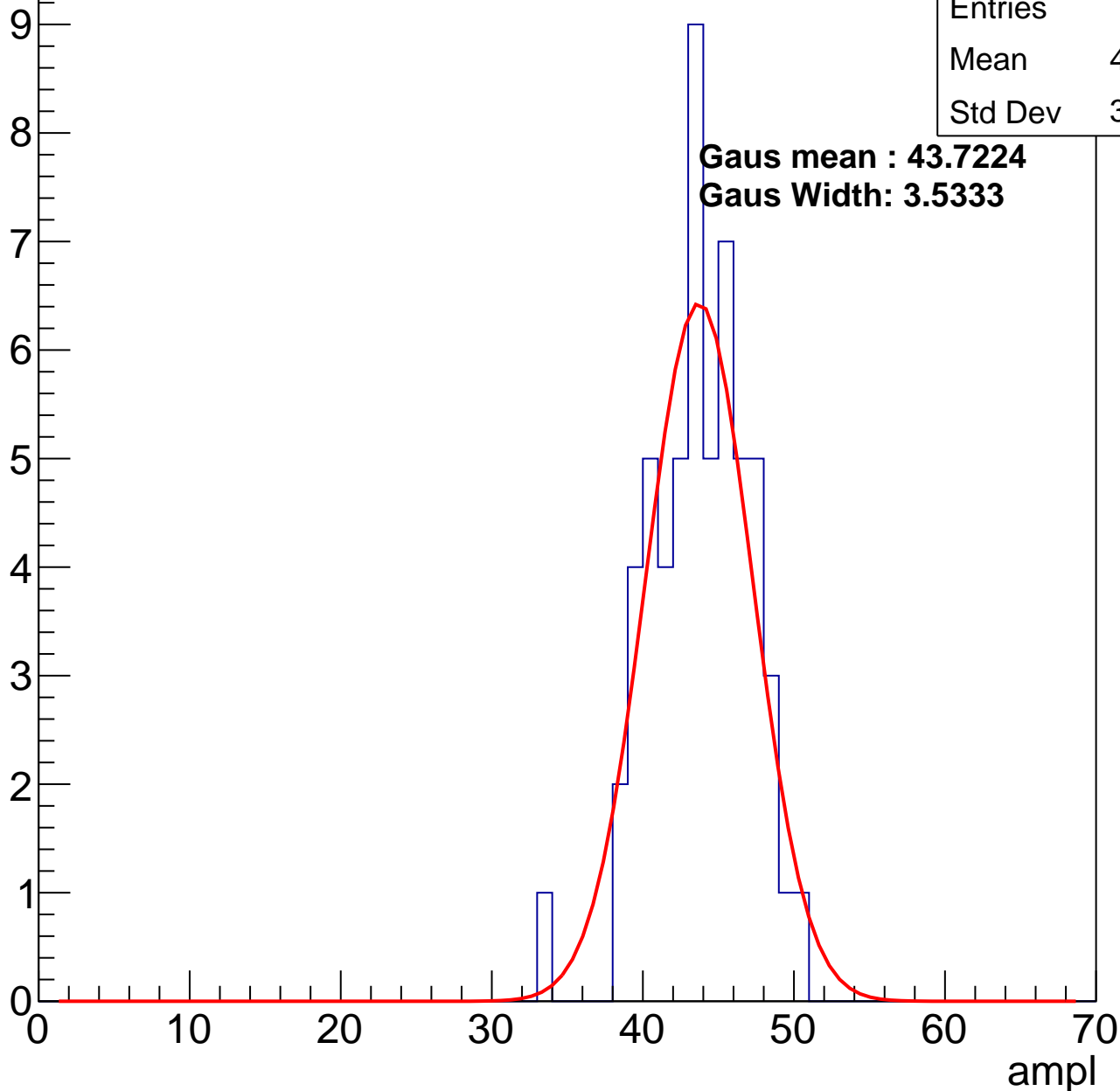
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.32
Std Dev	3.224

Gaus mean : 43.7224

Gaus Width: 3.5333

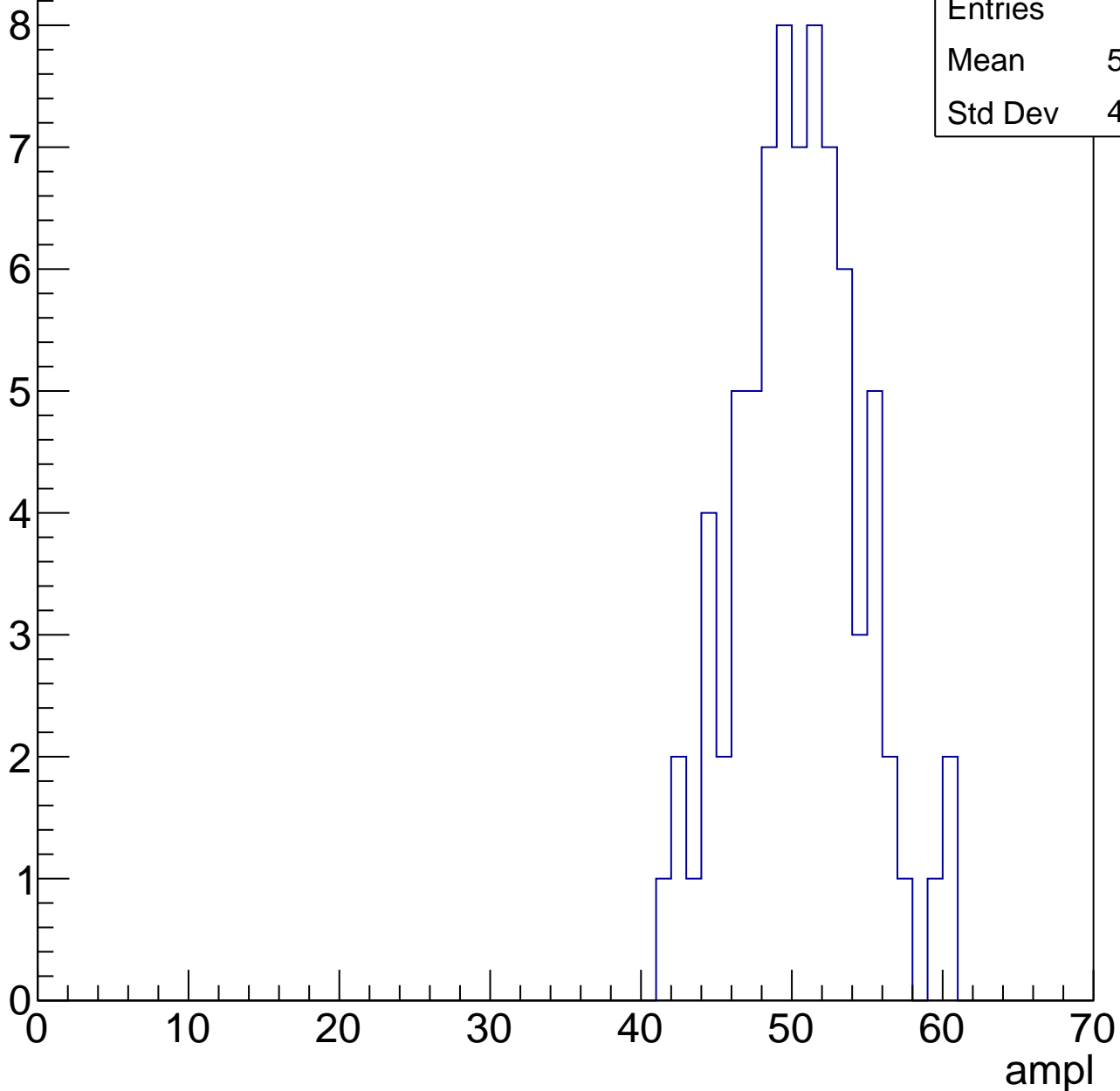


B1L101S, U17-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50.03
Std Dev	4.074

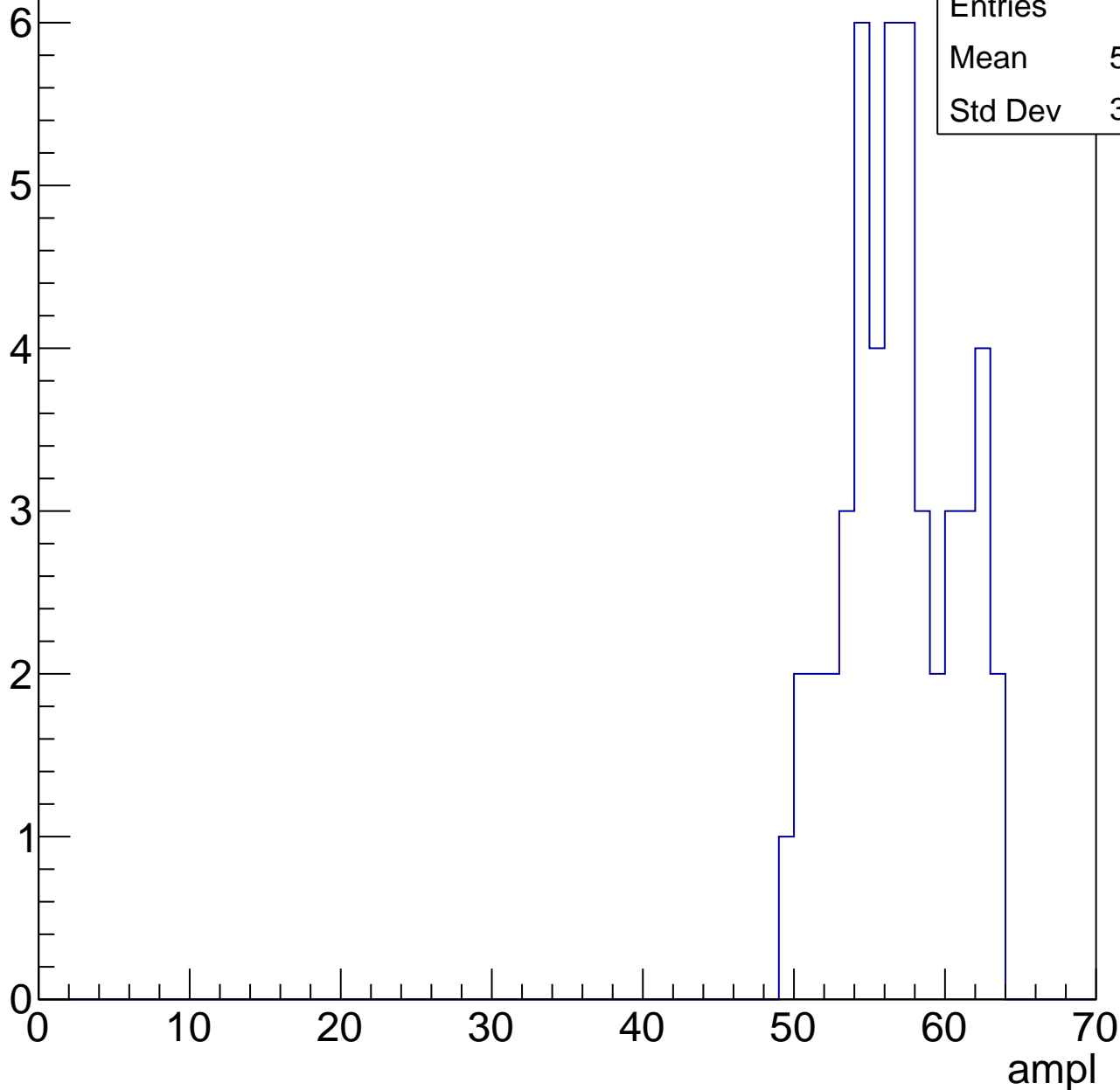


B1L101S, U17-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.43
Std Dev	3.642



B1L101S, U17-ch93, adc5

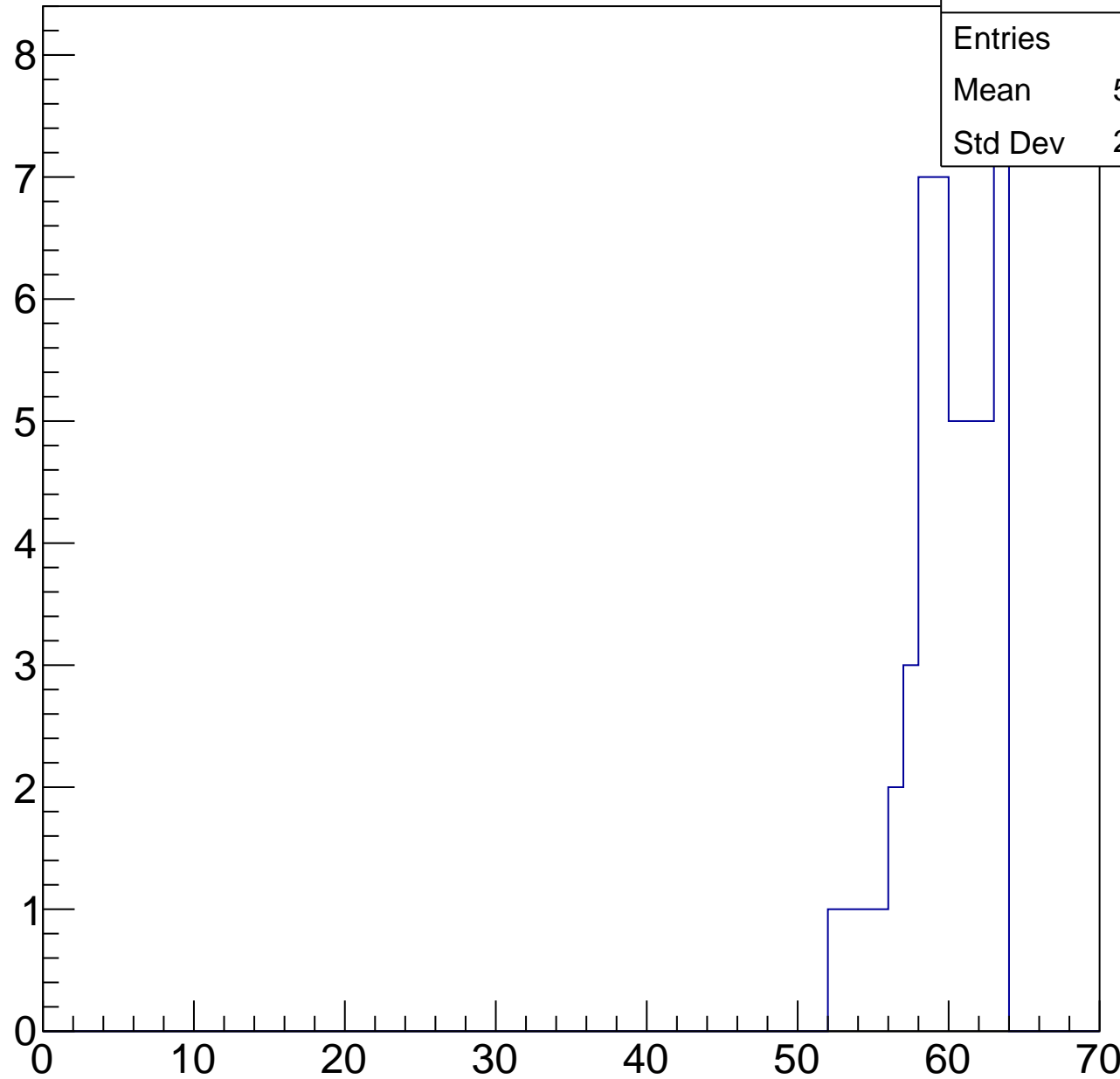
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.46
Std Dev	2.764

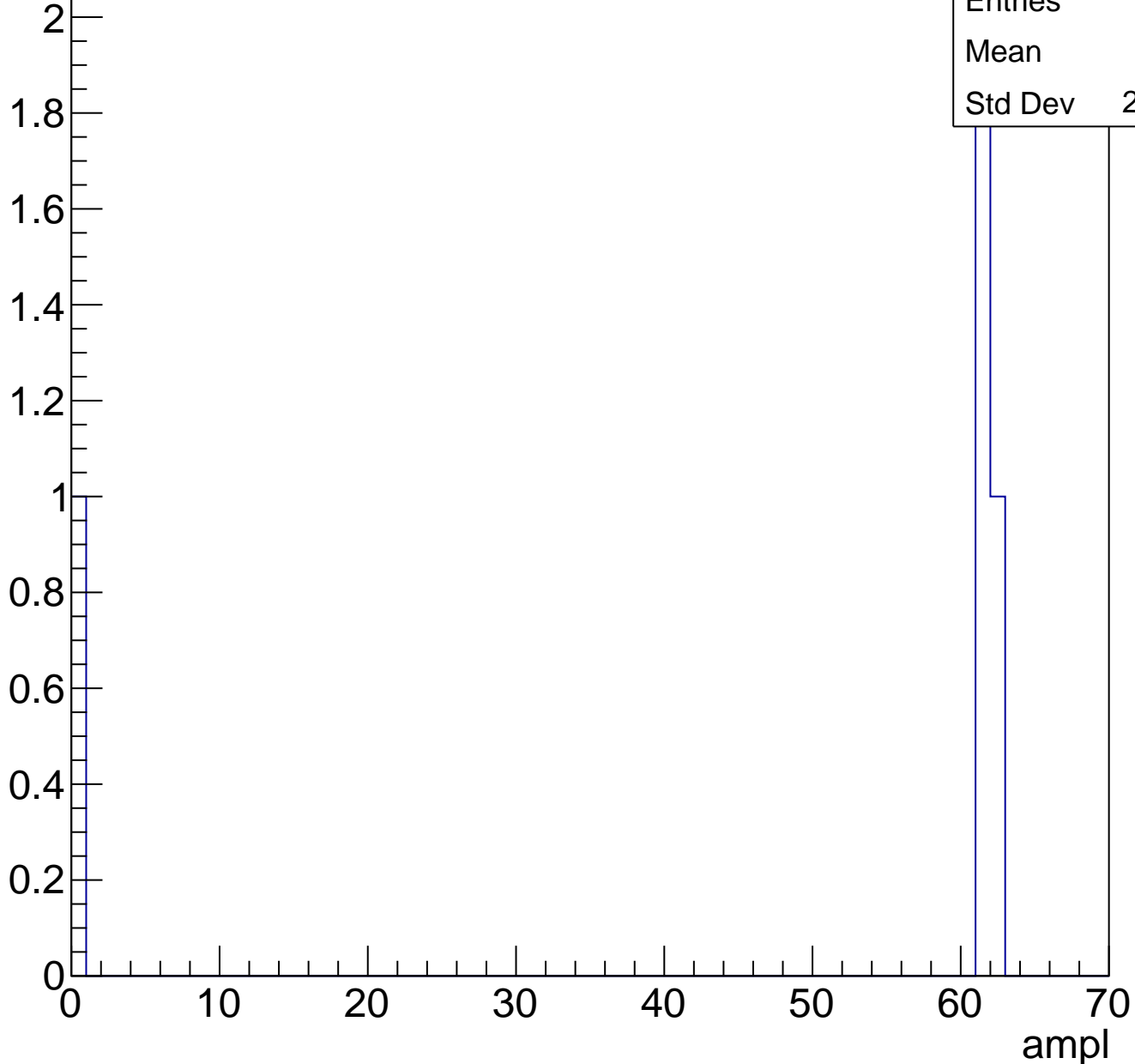
ampl



B1L101S, U17-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch94, adc0

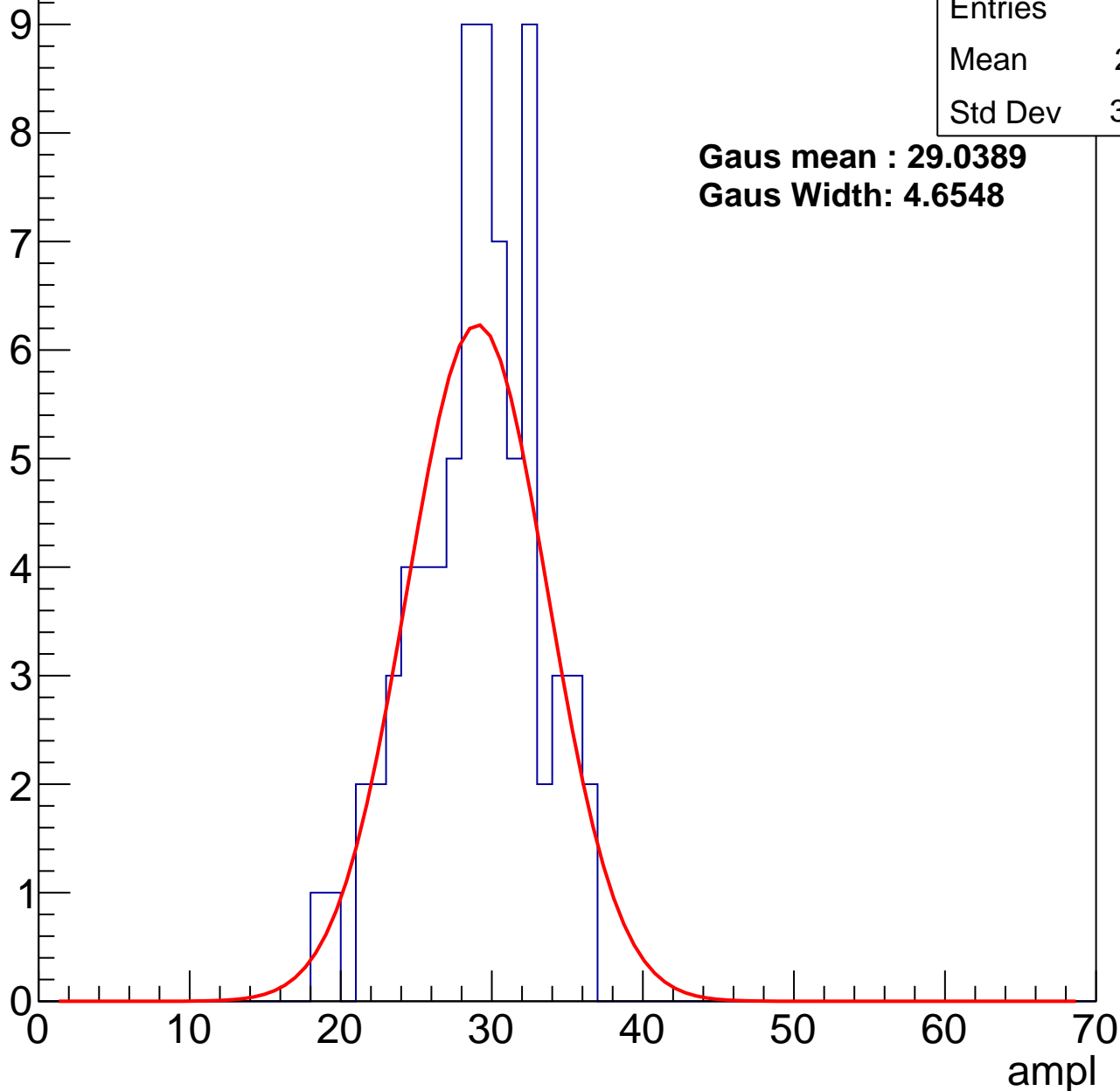
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	28.51
Std Dev	3.978

Gaus mean : 29.0389

Gaus Width: 4.6548



B1L101S, U17-ch94, adc1

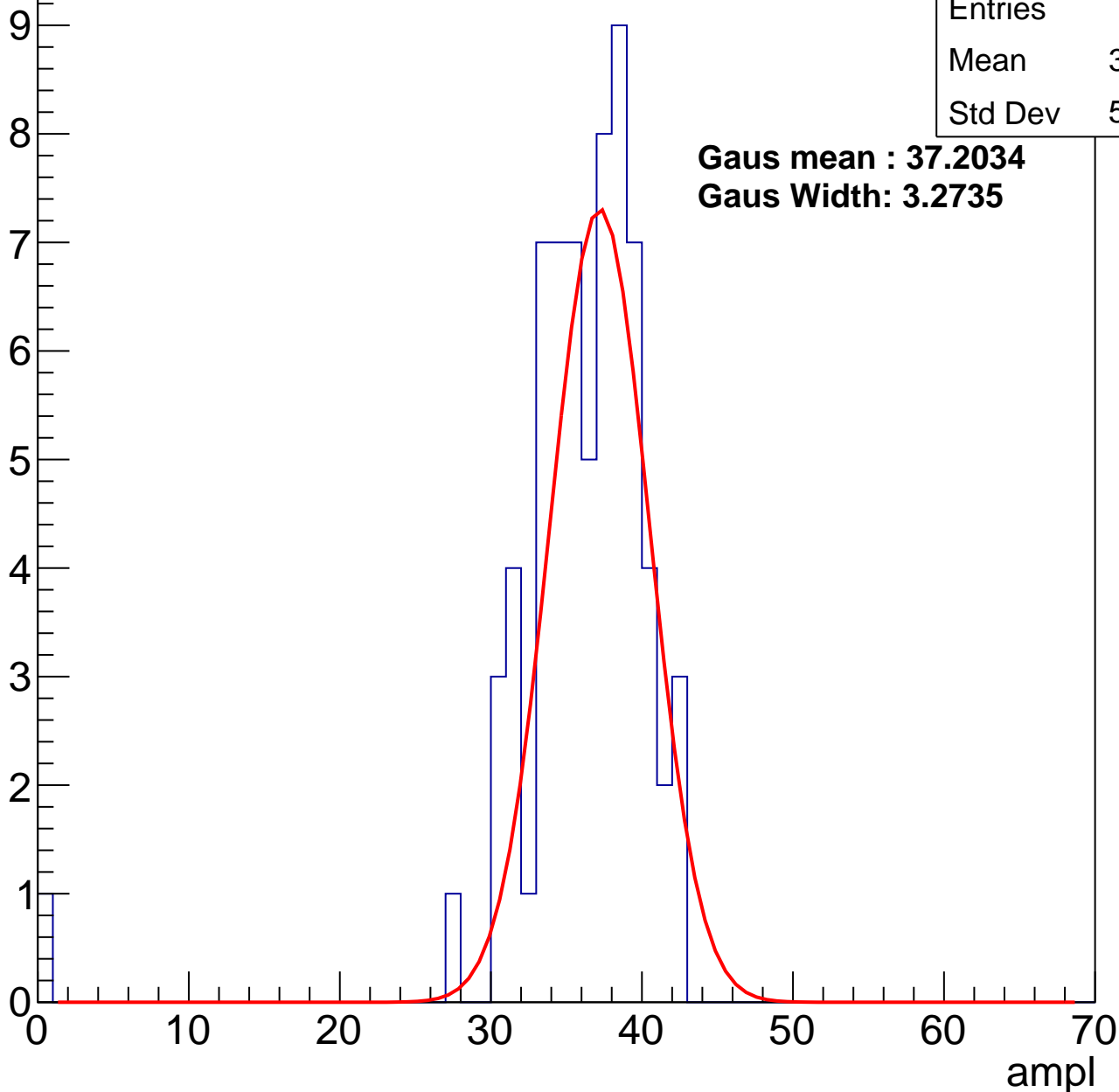
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.45
Std Dev	5.393

Gaus mean : 37.2034

Gaus Width: 3.2735



B1L101S, U17-ch94, adc2

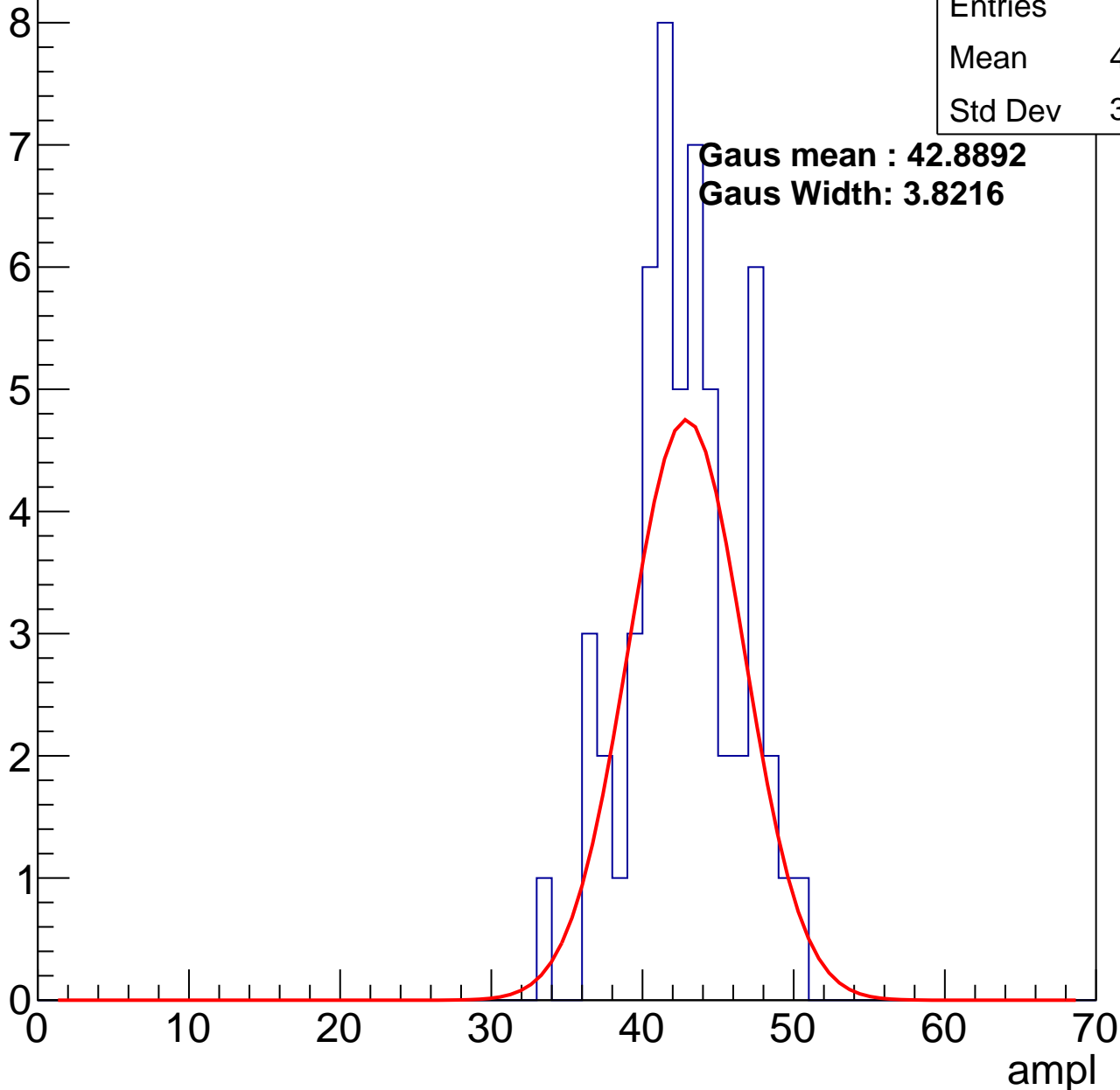
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.33
Std Dev	3.618

Gaus mean : 42.8892

Gaus Width: 3.8216

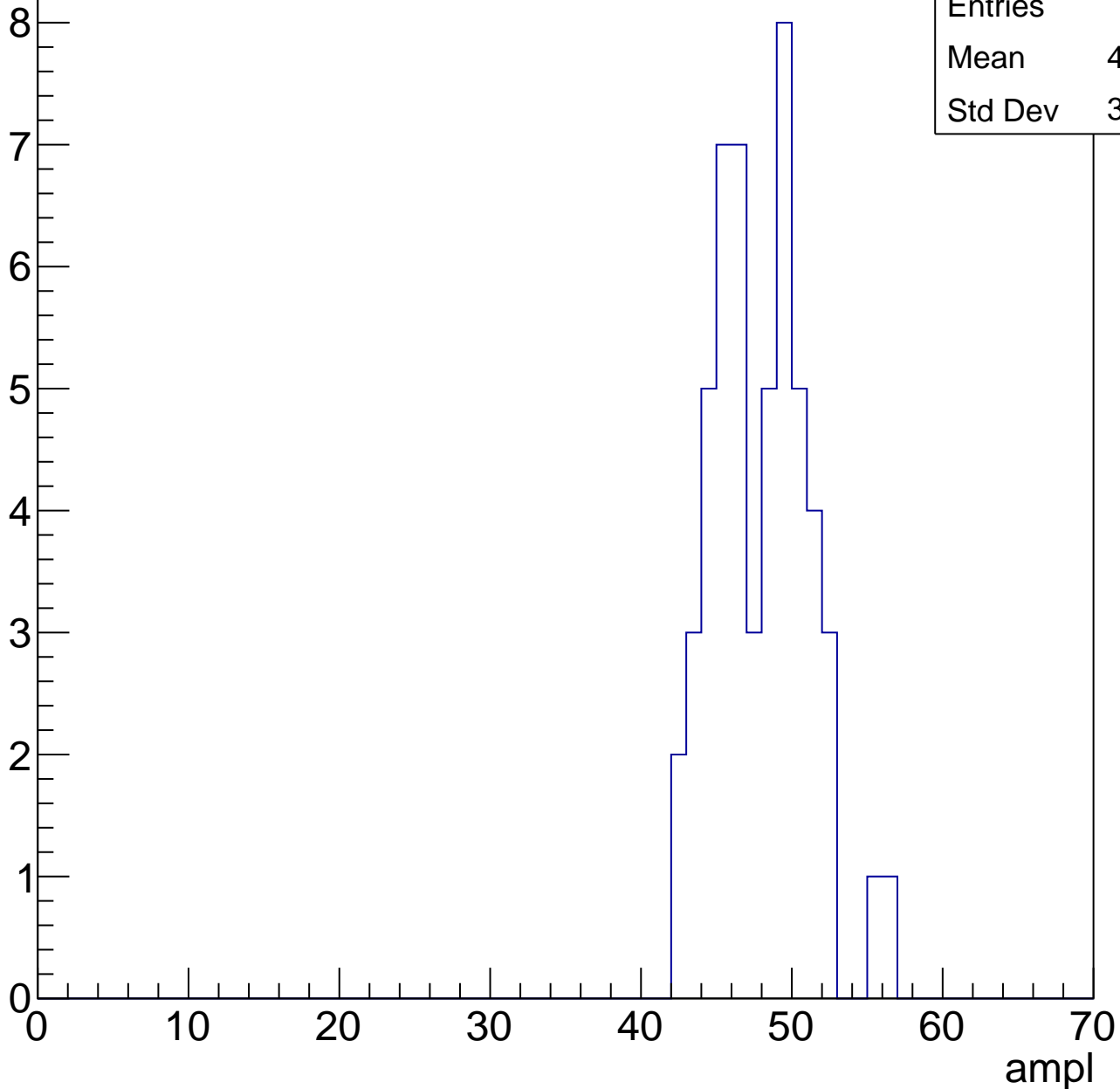


B1L101S, U17-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	47.48
Std Dev	3.137

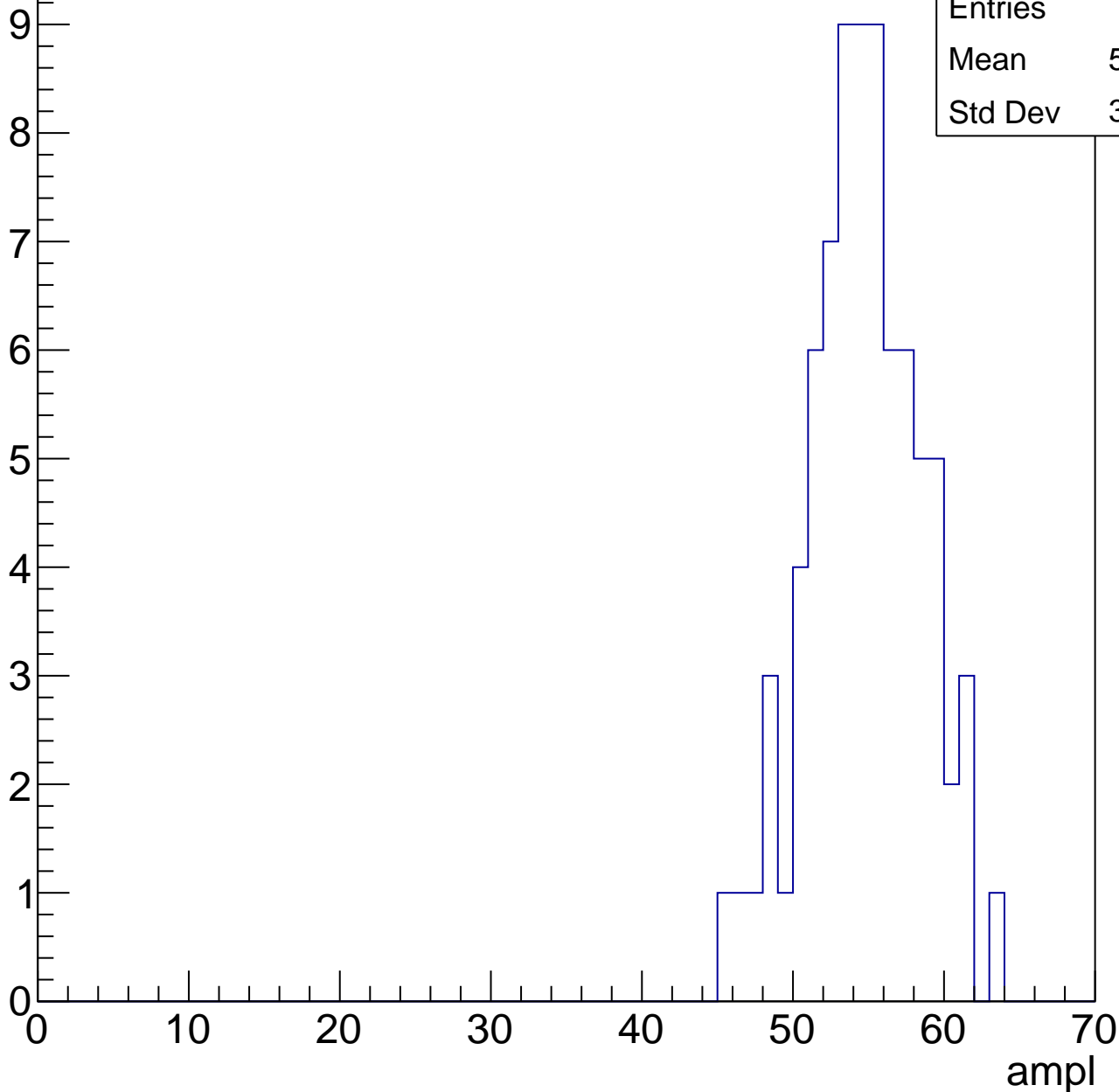


B1L101S, U17-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	54.28
Std Dev	3.677

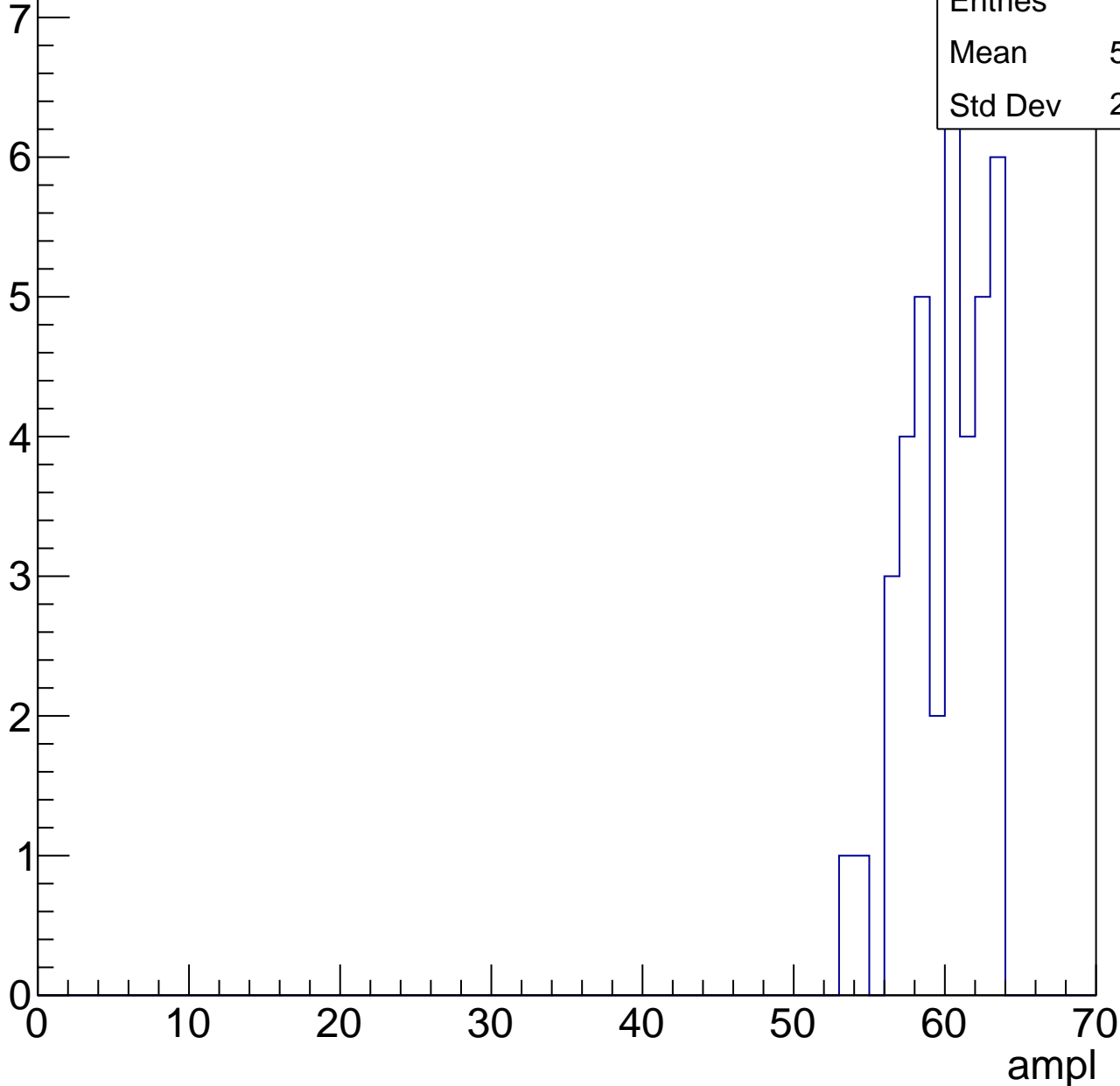


B1L101S, U17-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59.55
Std Dev	2.623

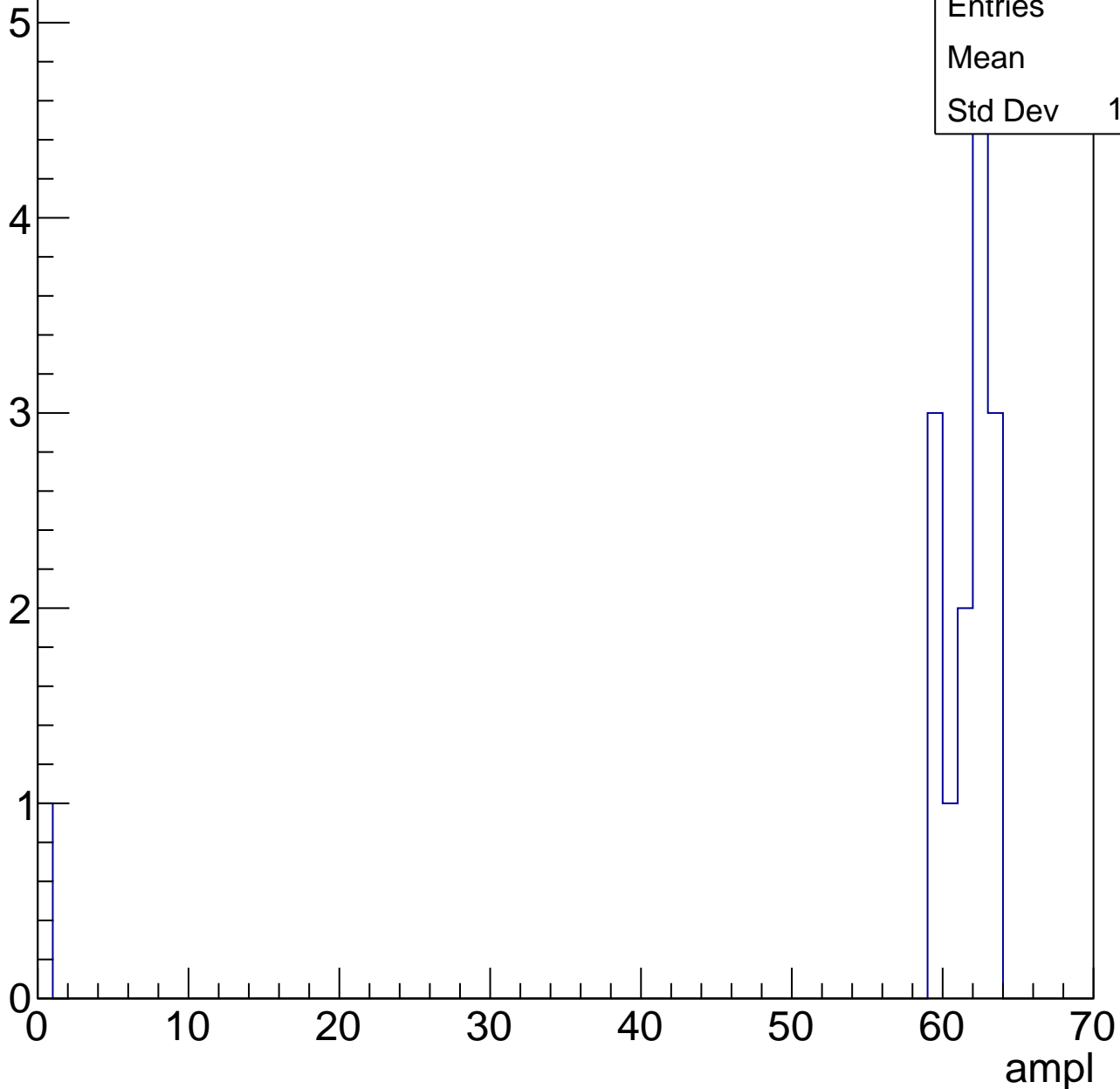


B1L101S, U17-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.2
Std Dev	15.35



B1L101S, U17-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch95, adc0

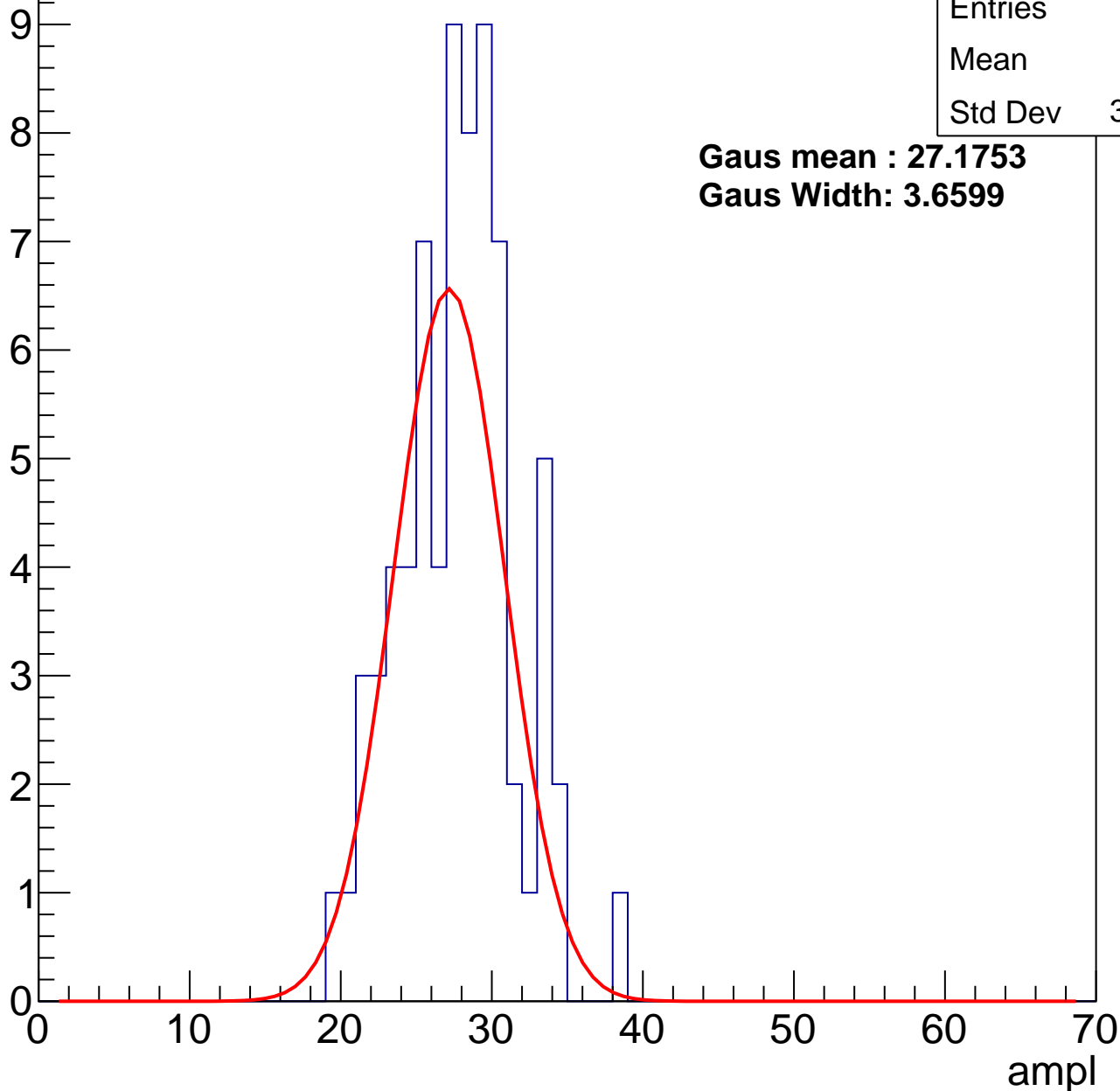
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	27.3
Std Dev	3.724

Gaus mean : 27.1753

Gaus Width: 3.6599



B1L101S, U17-ch95, adc1

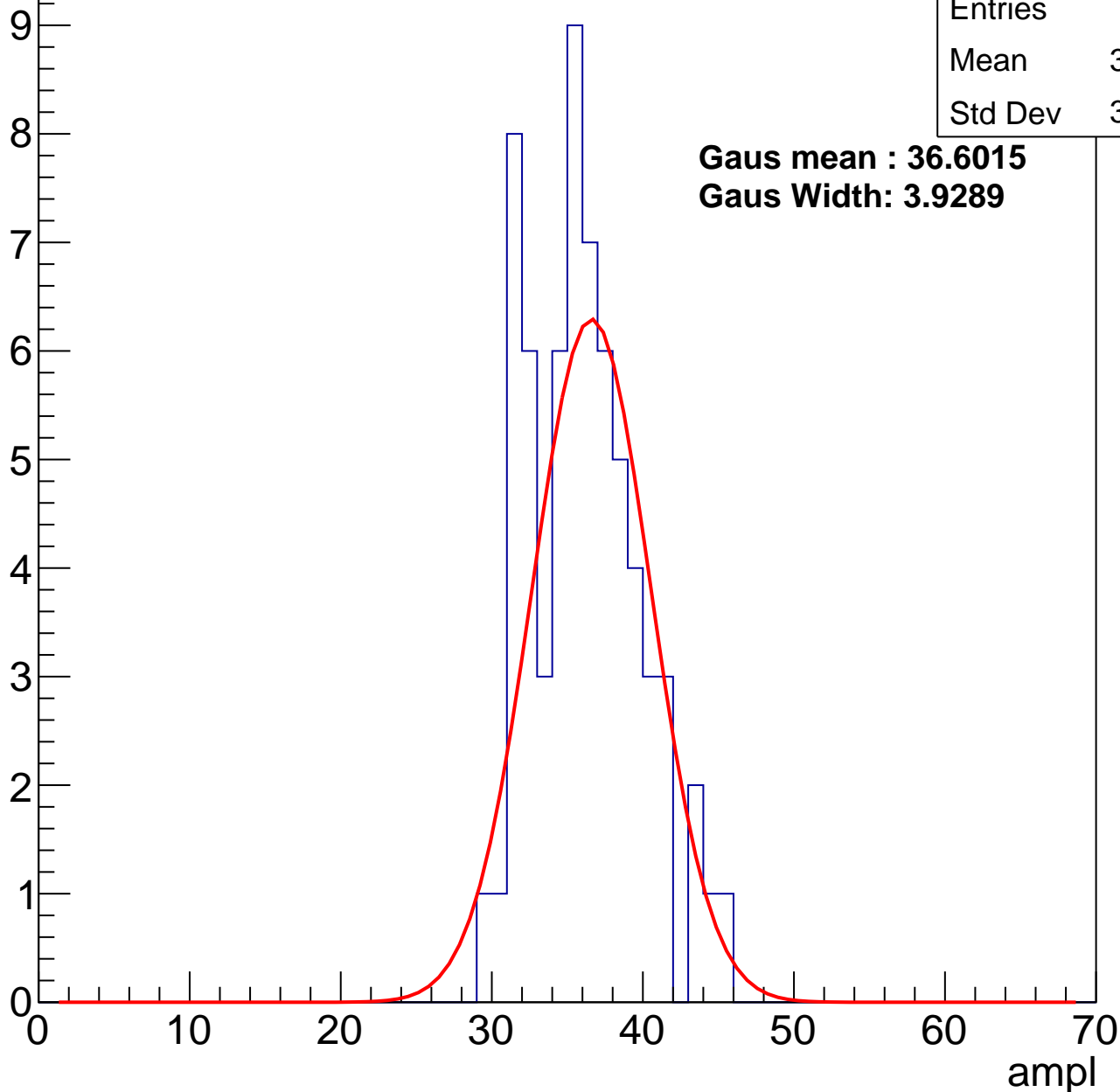
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.68
Std Dev	3.615

Gaus mean : 36.6015

Gaus Width: 3.9289



B1L101S, U17-ch95, adc2

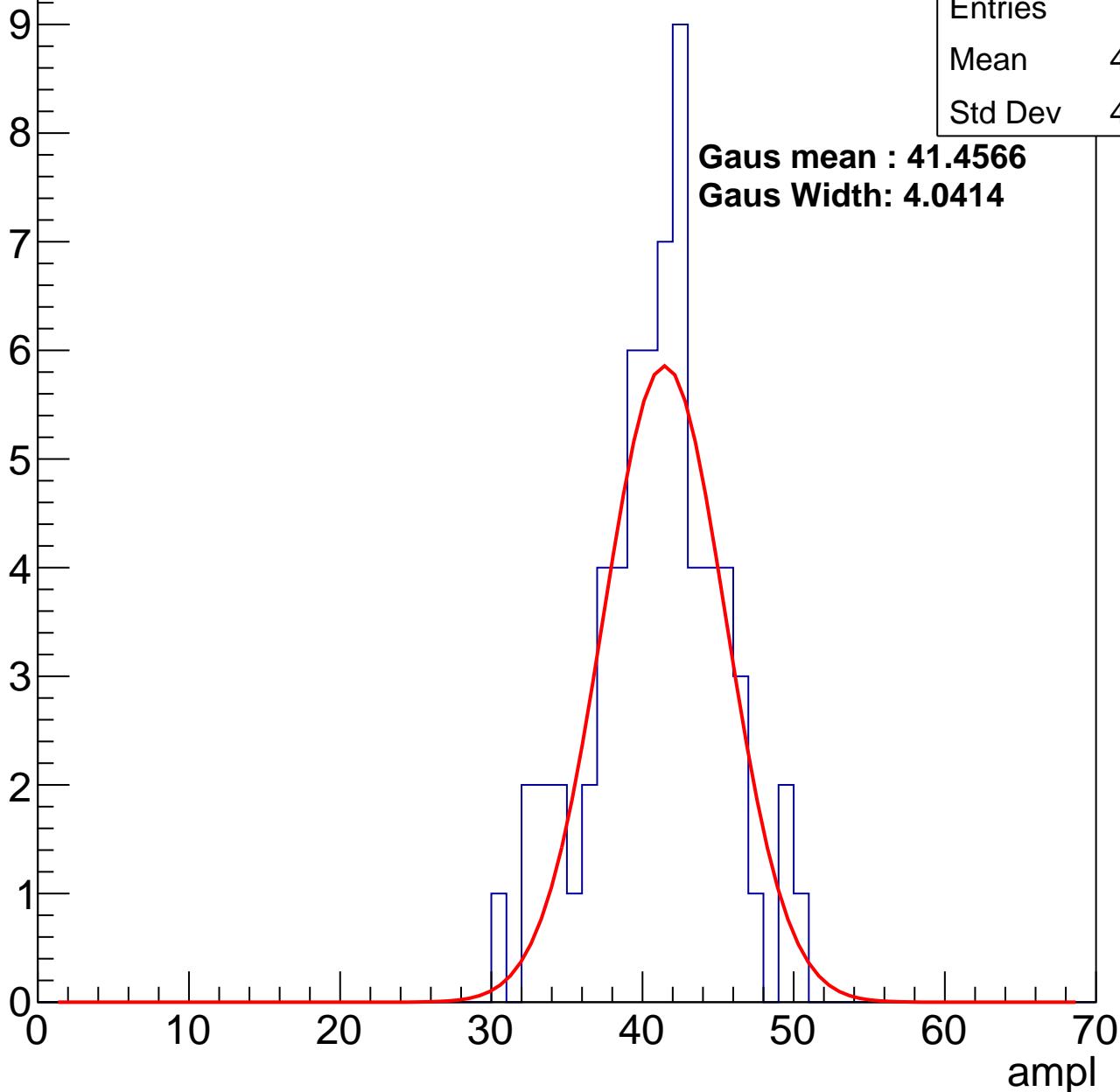
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	40.54
Std Dev	4.217

Gaus mean : 41.4566

Gaus Width: 4.0414

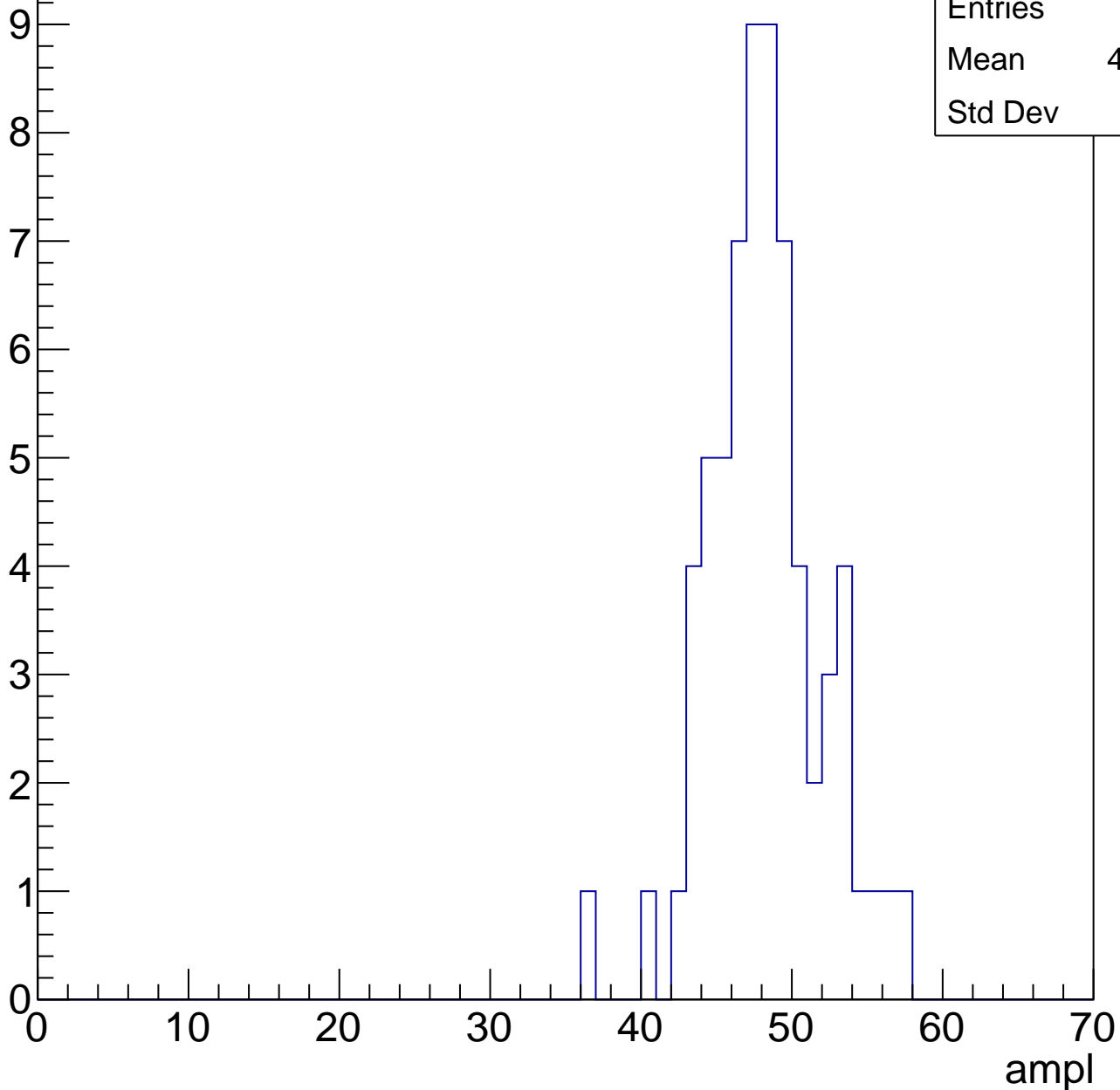


B1L101S, U17-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	47.68
Std Dev	3.73

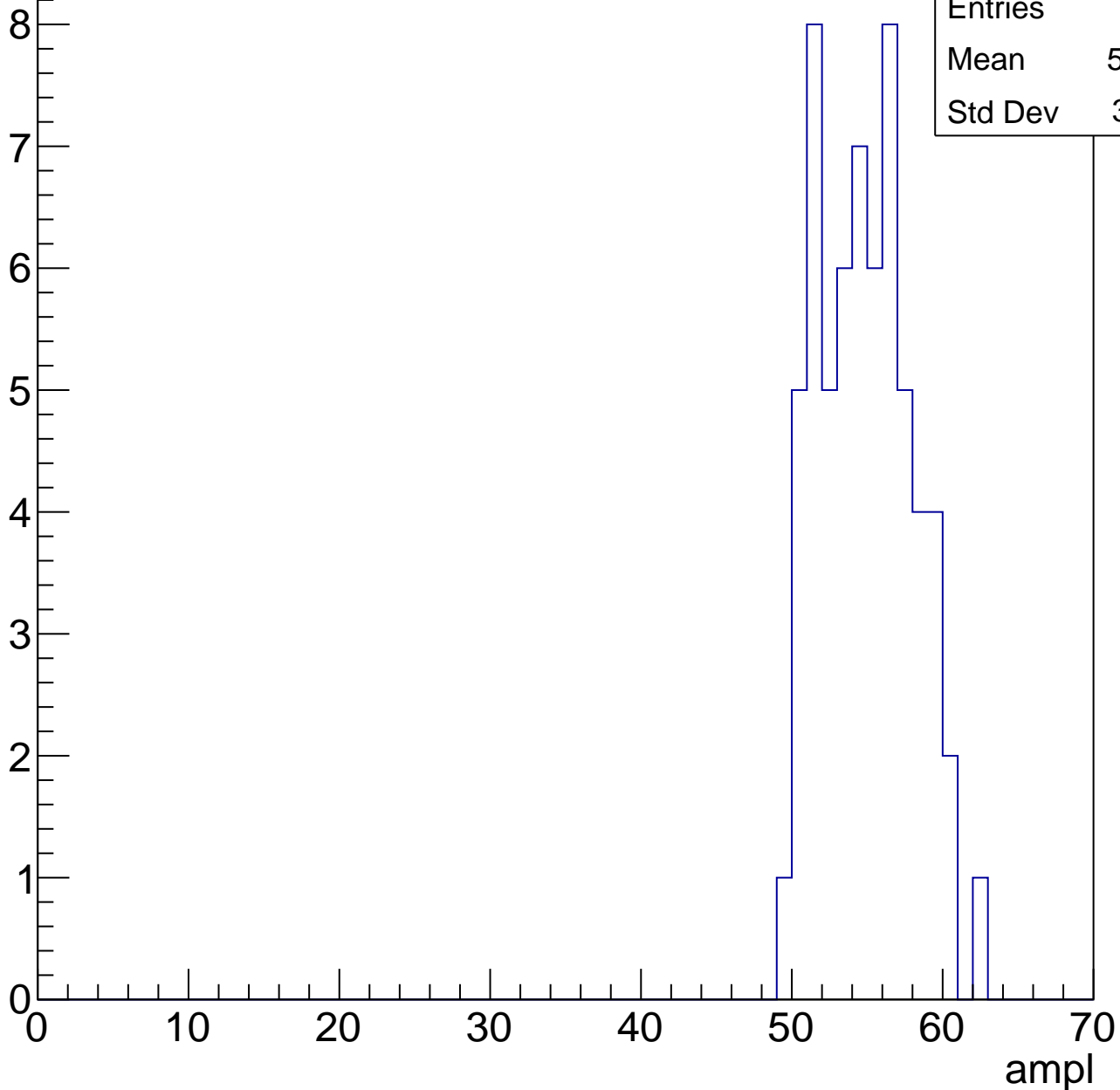


B1L101S, U17-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.45
Std Dev	3.041

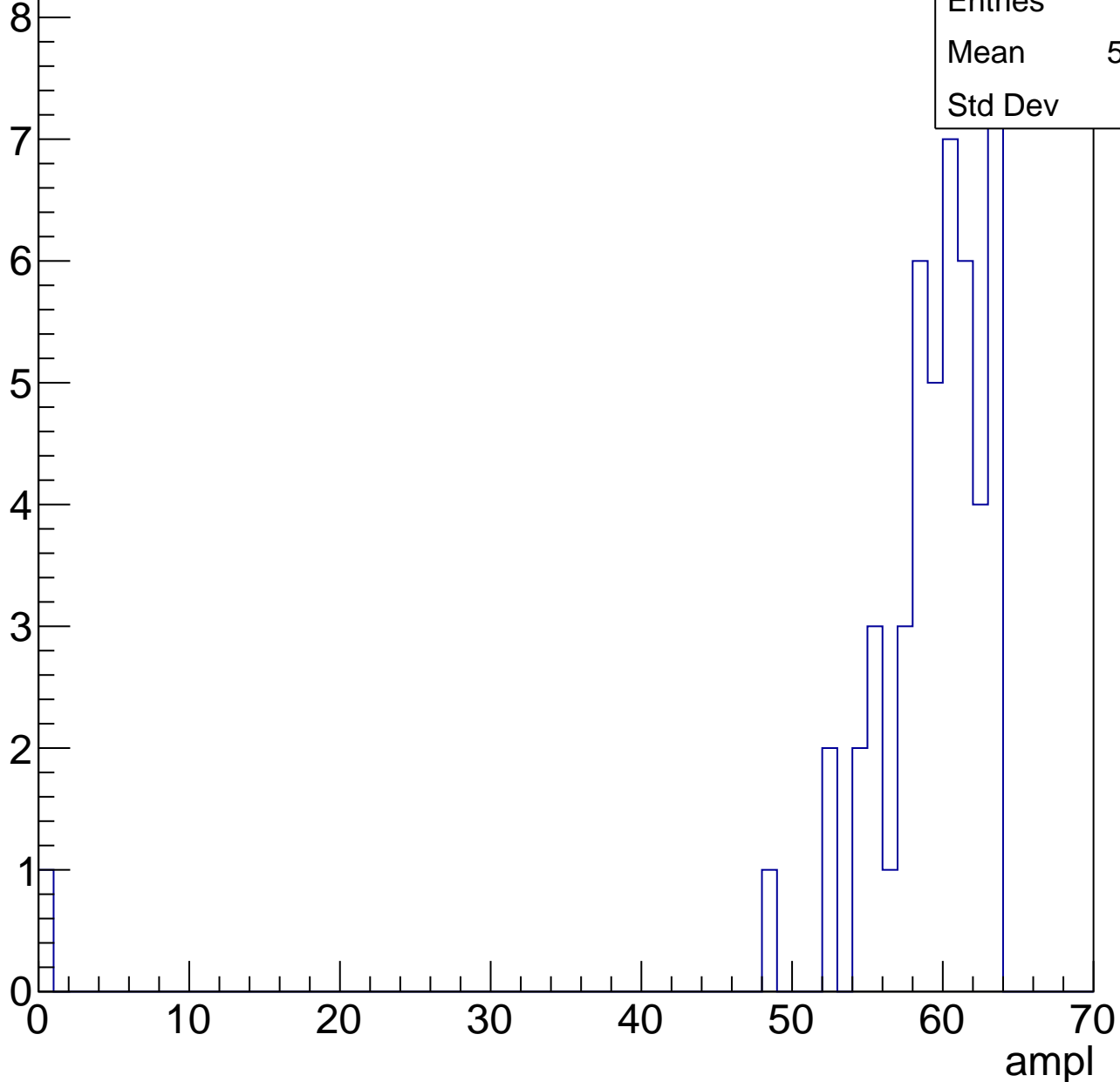


B1L101S, U17-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.82
Std Dev	8.98

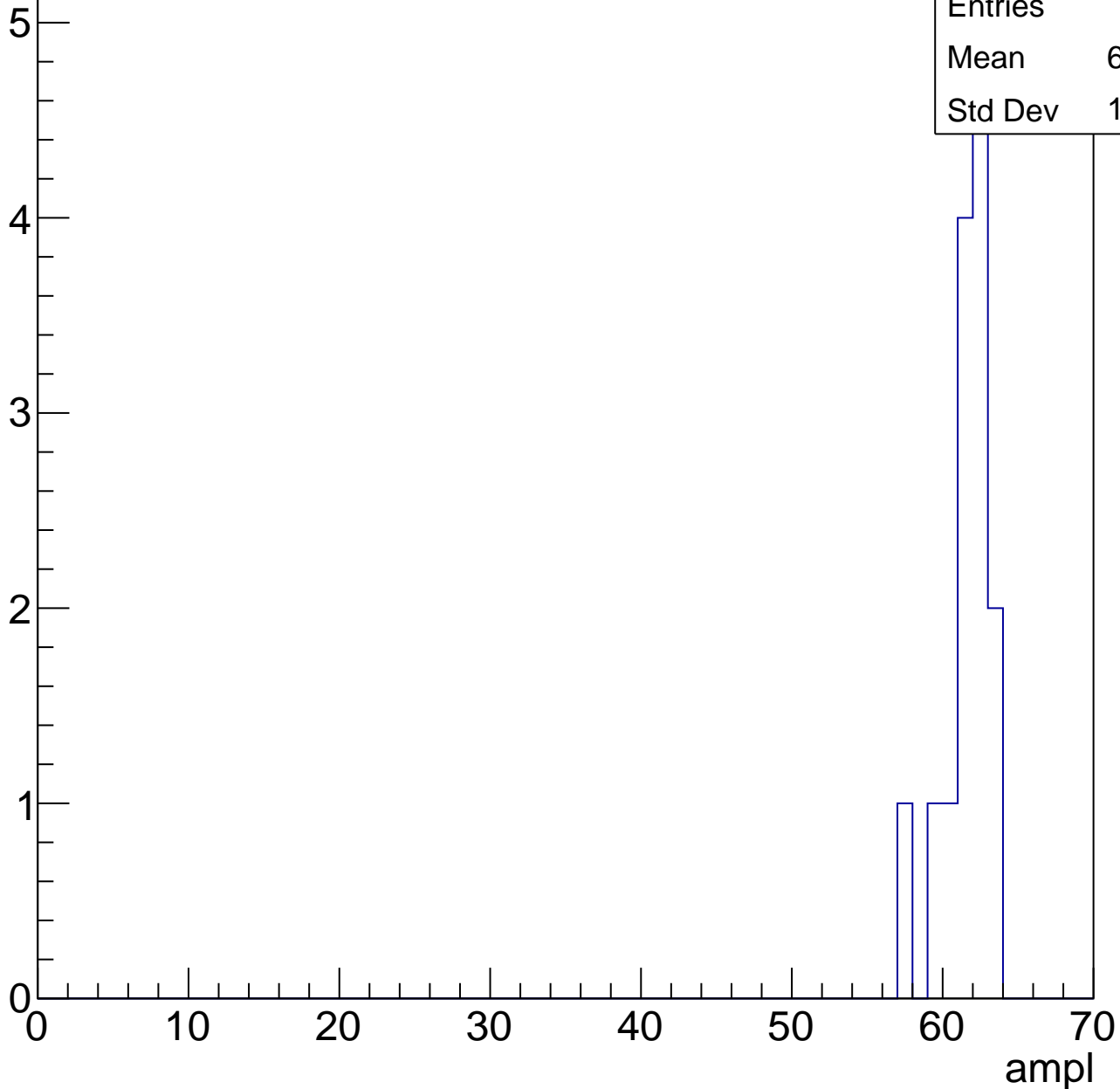


B1L101S, U17-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.14
Std Dev	1.552



B1L101S, U17-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch96, adc0

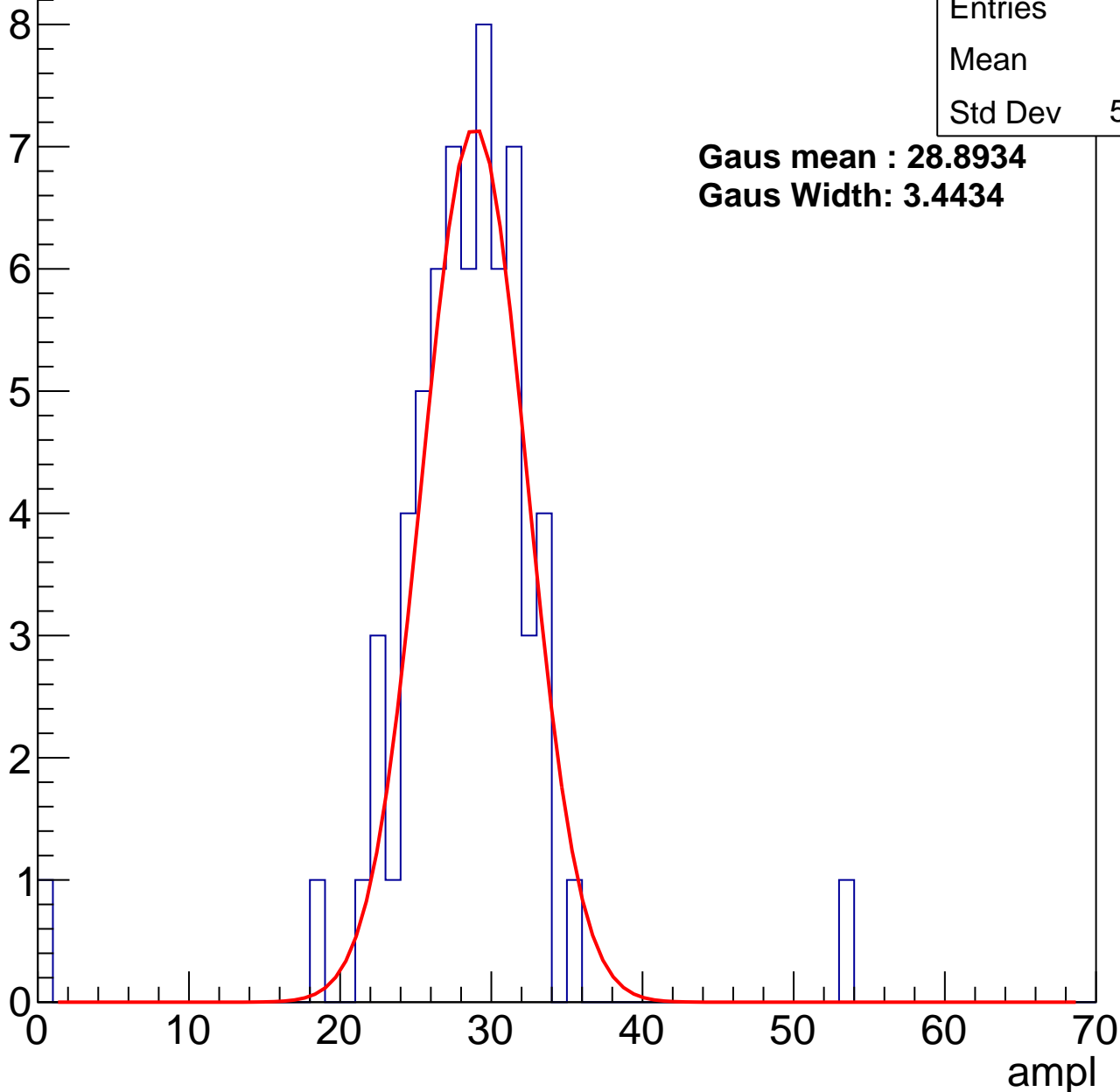
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.8
Std Dev	5.712

Gaus mean : 28.8934

Gaus Width: 3.4434



B1L101S, U17-ch96, adc1

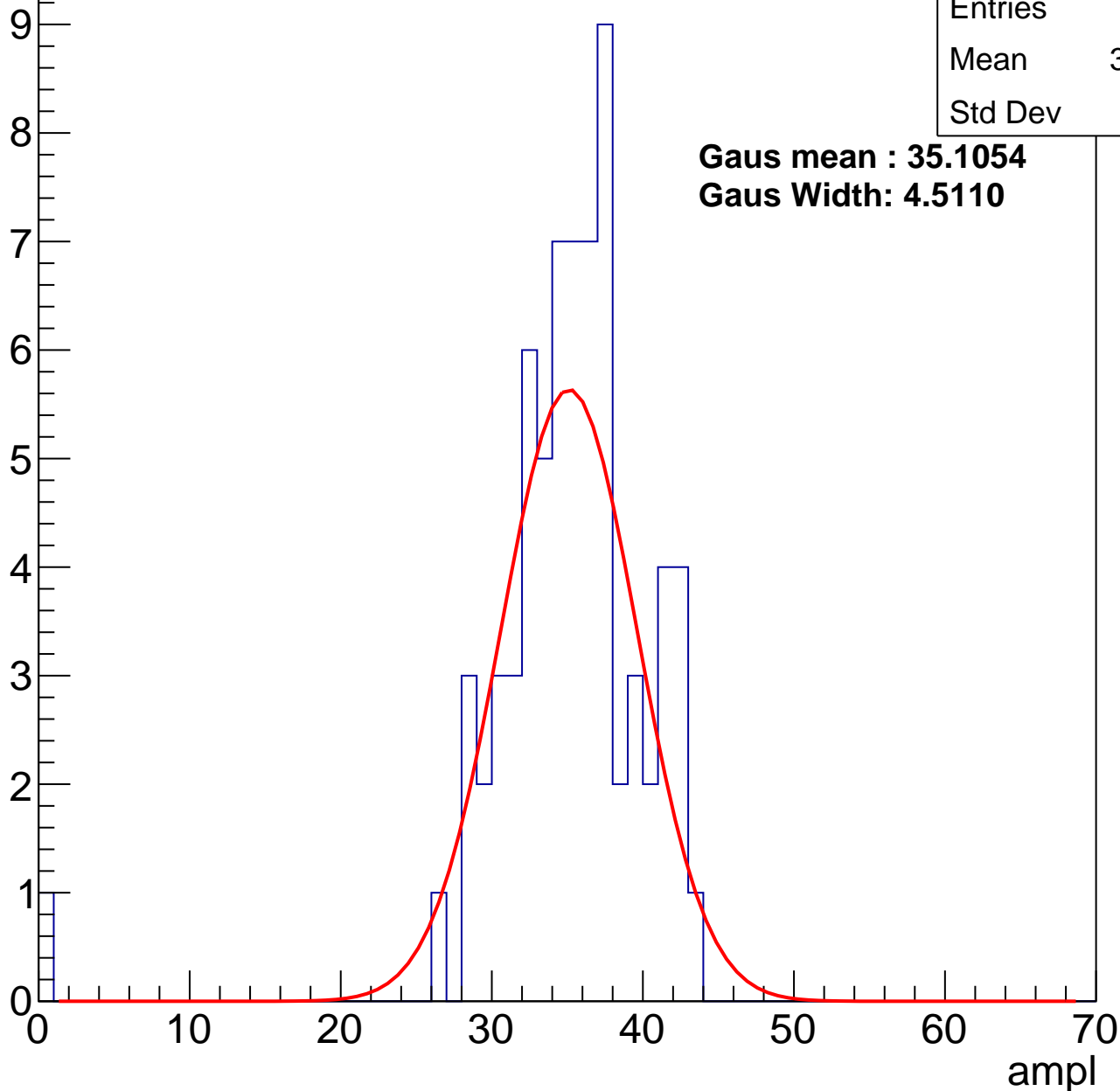
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	34.63
Std Dev	5.71

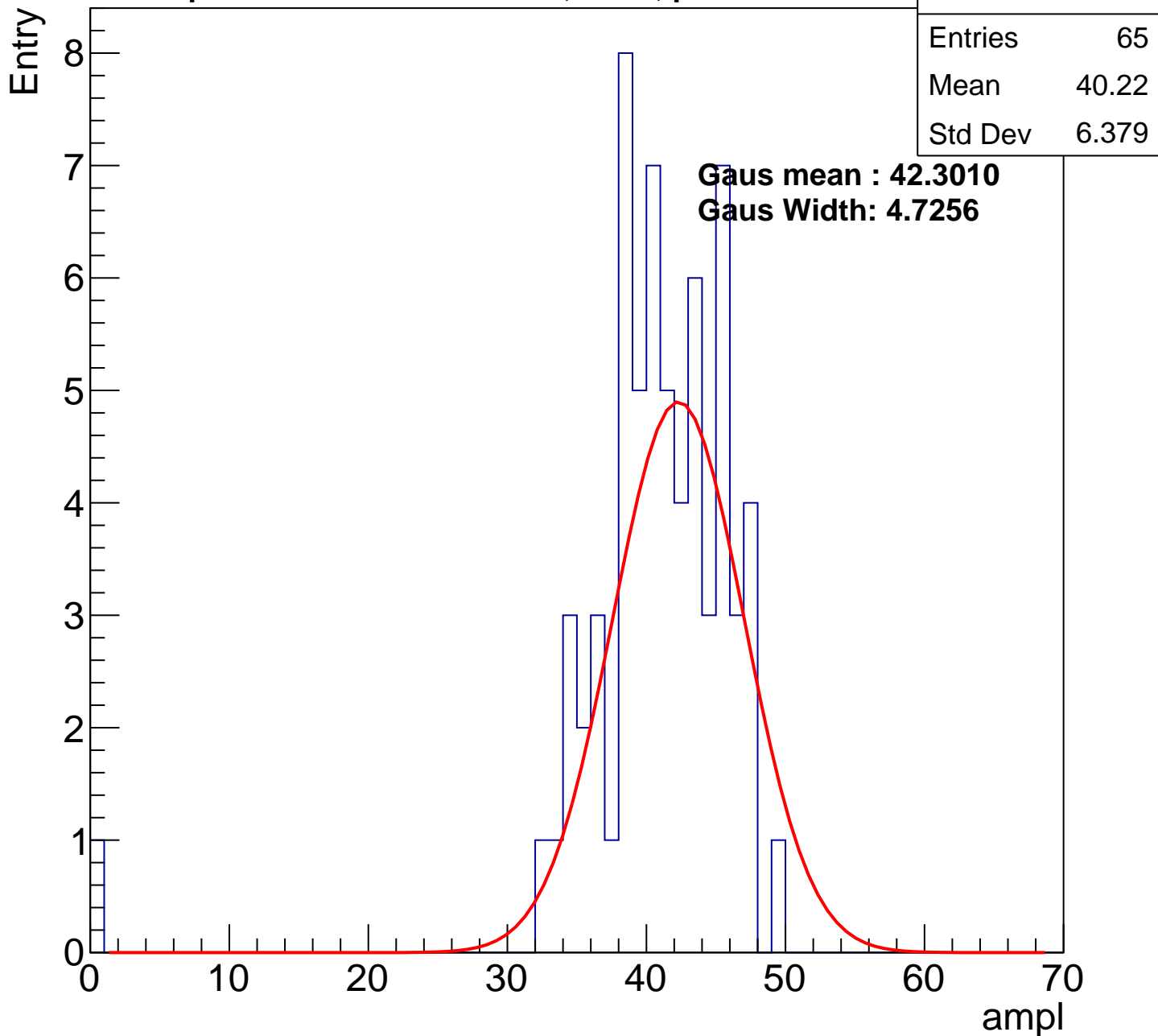
Gaus mean : 35.1054

Gaus Width: 4.5110



B1L101S, U17-ch96, adc2

calib_packv5_042523_0143.root, FC#0, port D2

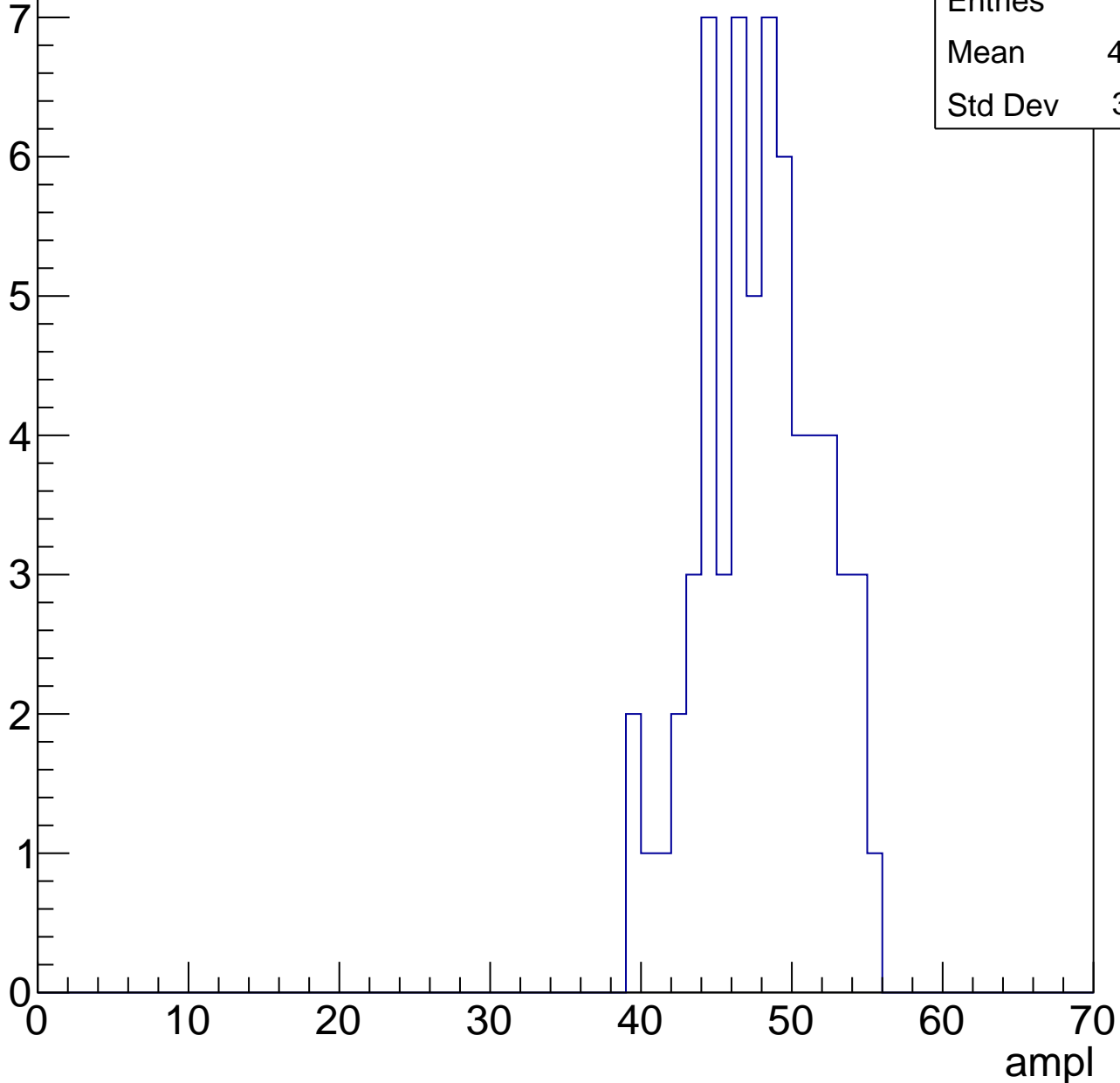


B1L101S, U17-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	47.46
Std Dev	3.841



B1L101S, U17-ch96, adc4

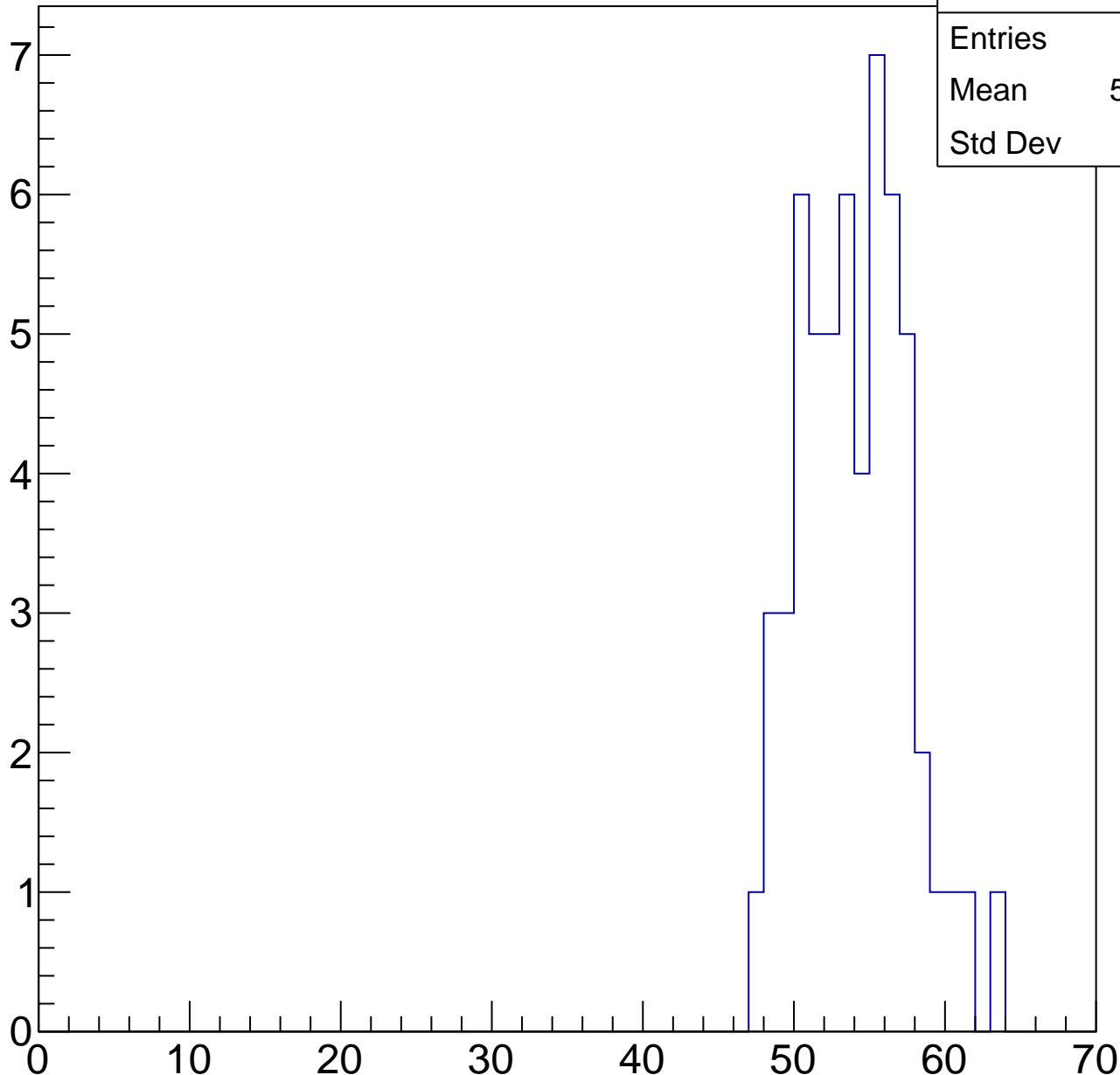
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	57
Mean	53.54
Std Dev	3.47

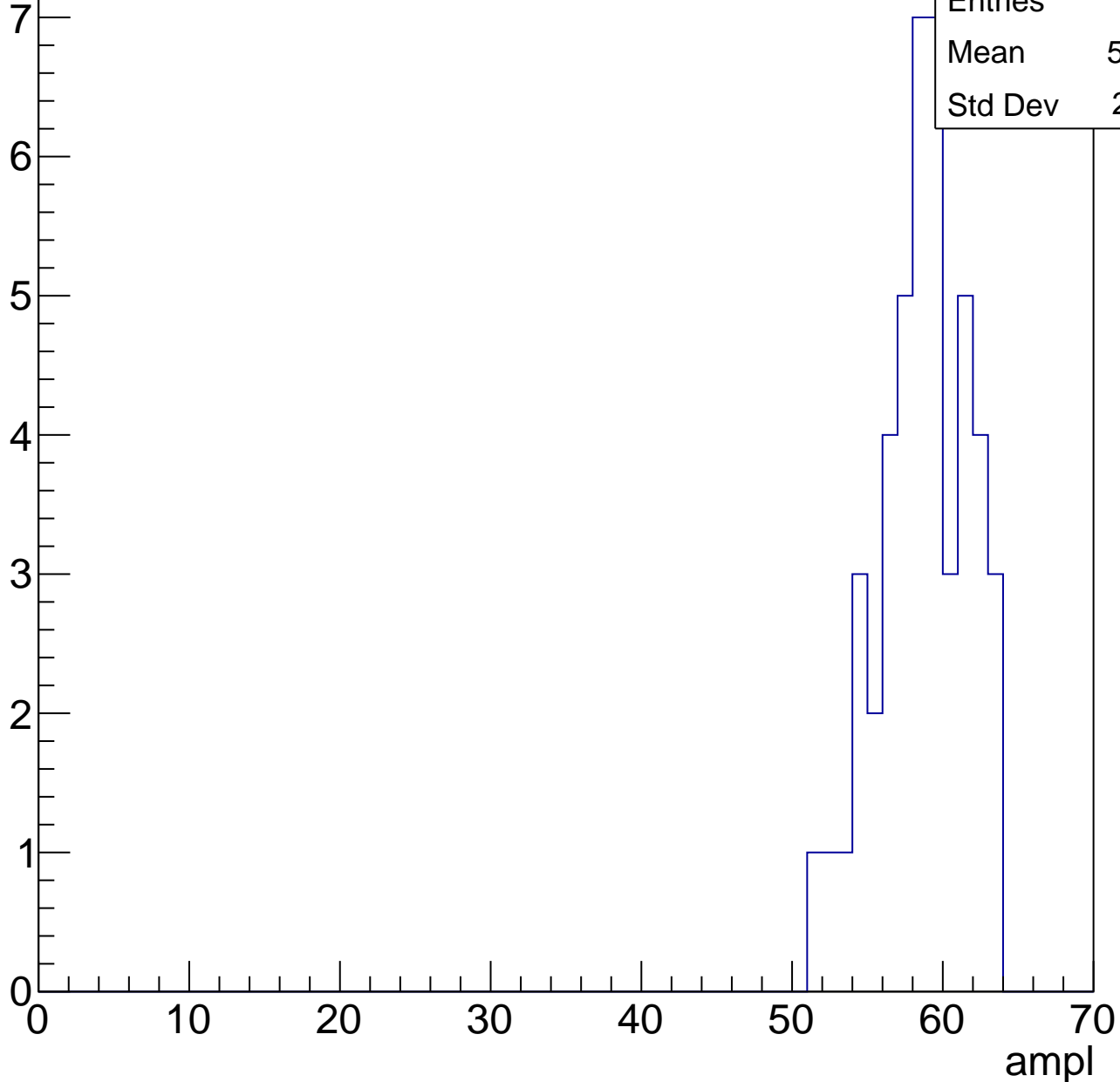
ampl



B1L101S, U17-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

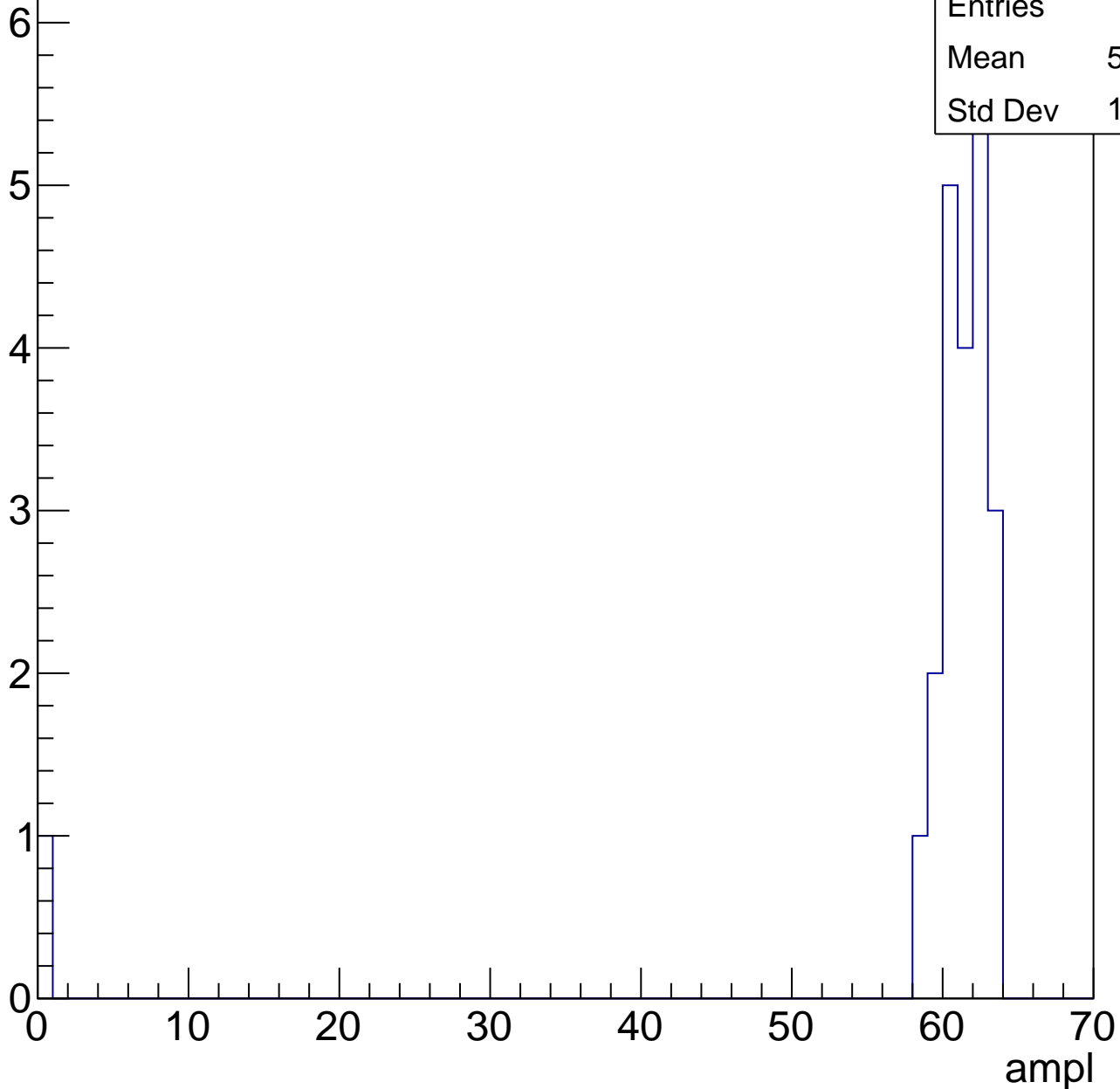


B1L101S, U17-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

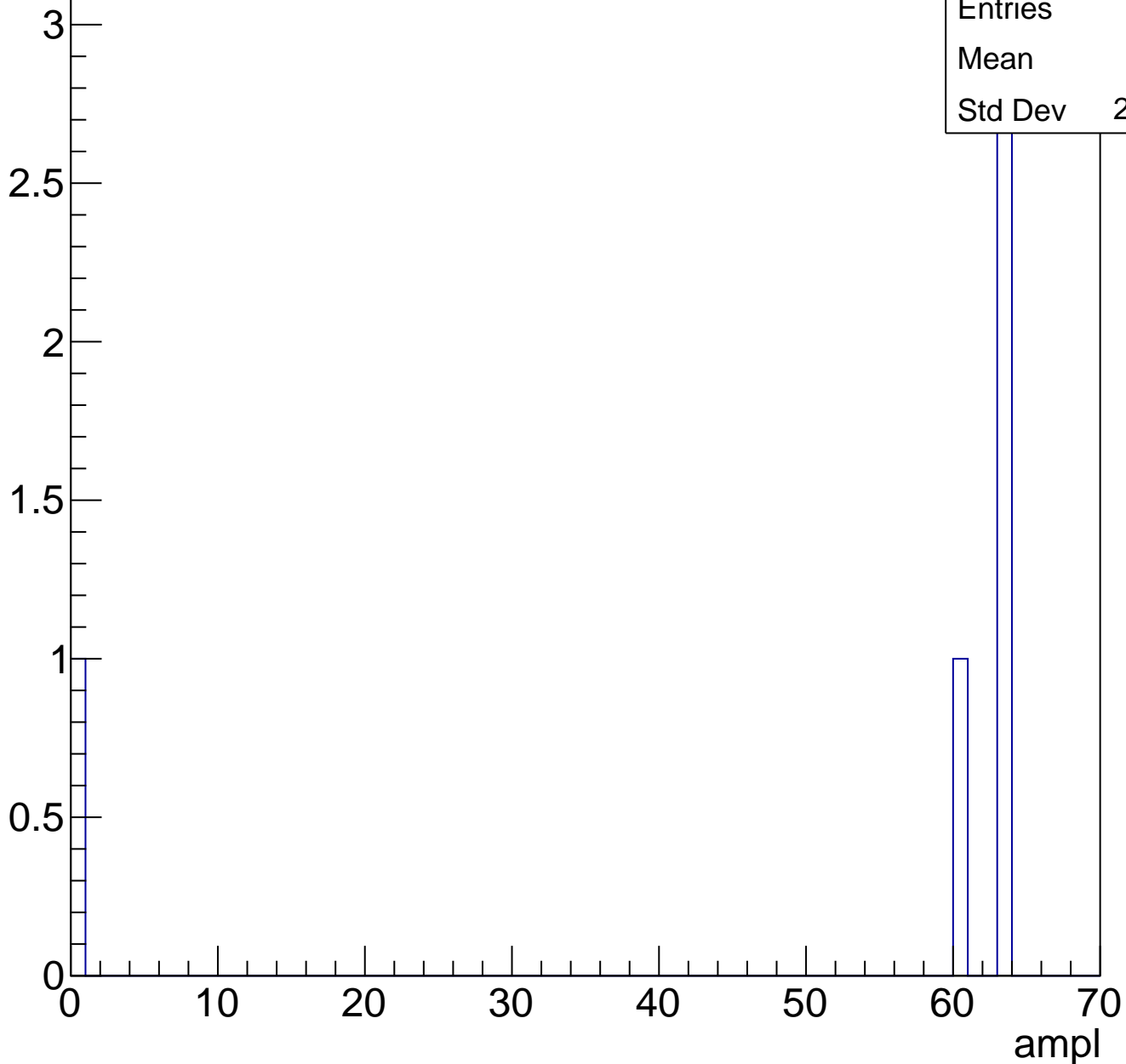
Entries	22
Mean	58.23
Std Dev	12.78



B1L101S, U17-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch97, adc0

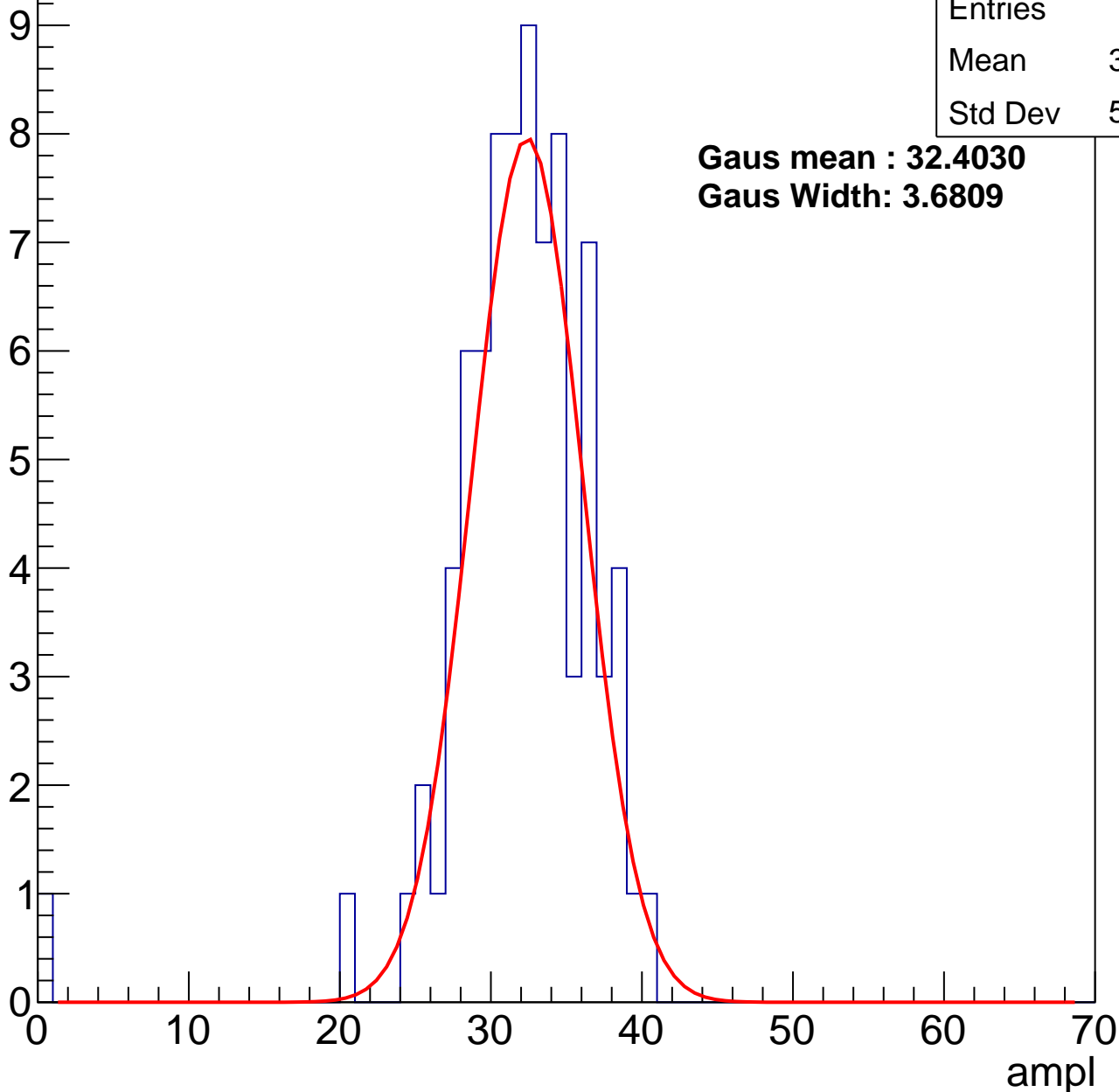
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	31.46
Std Dev	5.143

Gaus mean : 32.4030

Gaus Width: 3.6809



B1L101S, U17-ch97, adc1

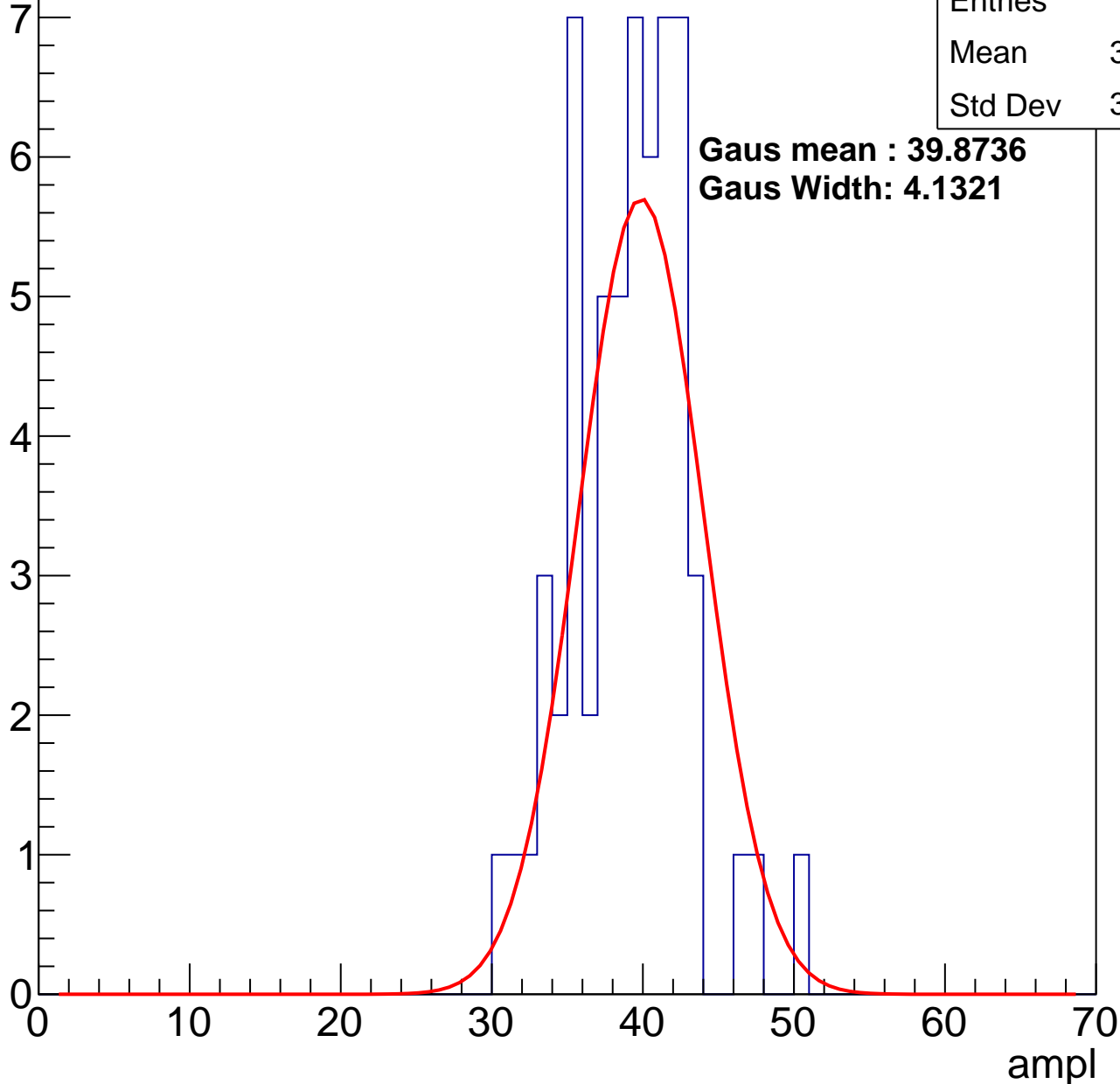
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.63
Std Dev	3.825

Gaus mean : 39.8736

Gaus Width: 4.1321



B1L101S, U17-ch97, adc2

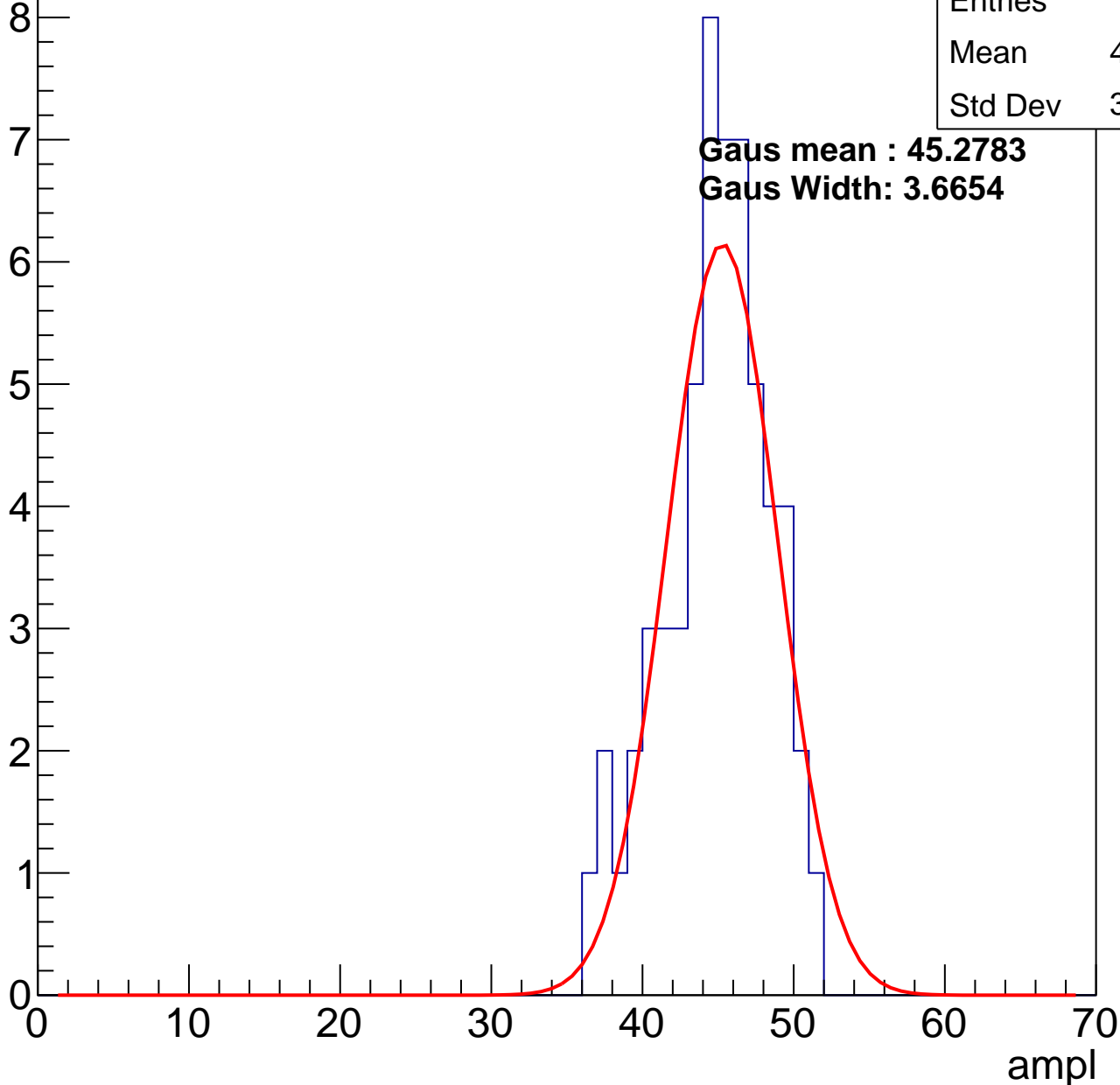
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.36
Std Dev	3.468

Gaus mean : 45.2783

Gaus Width: 3.6654

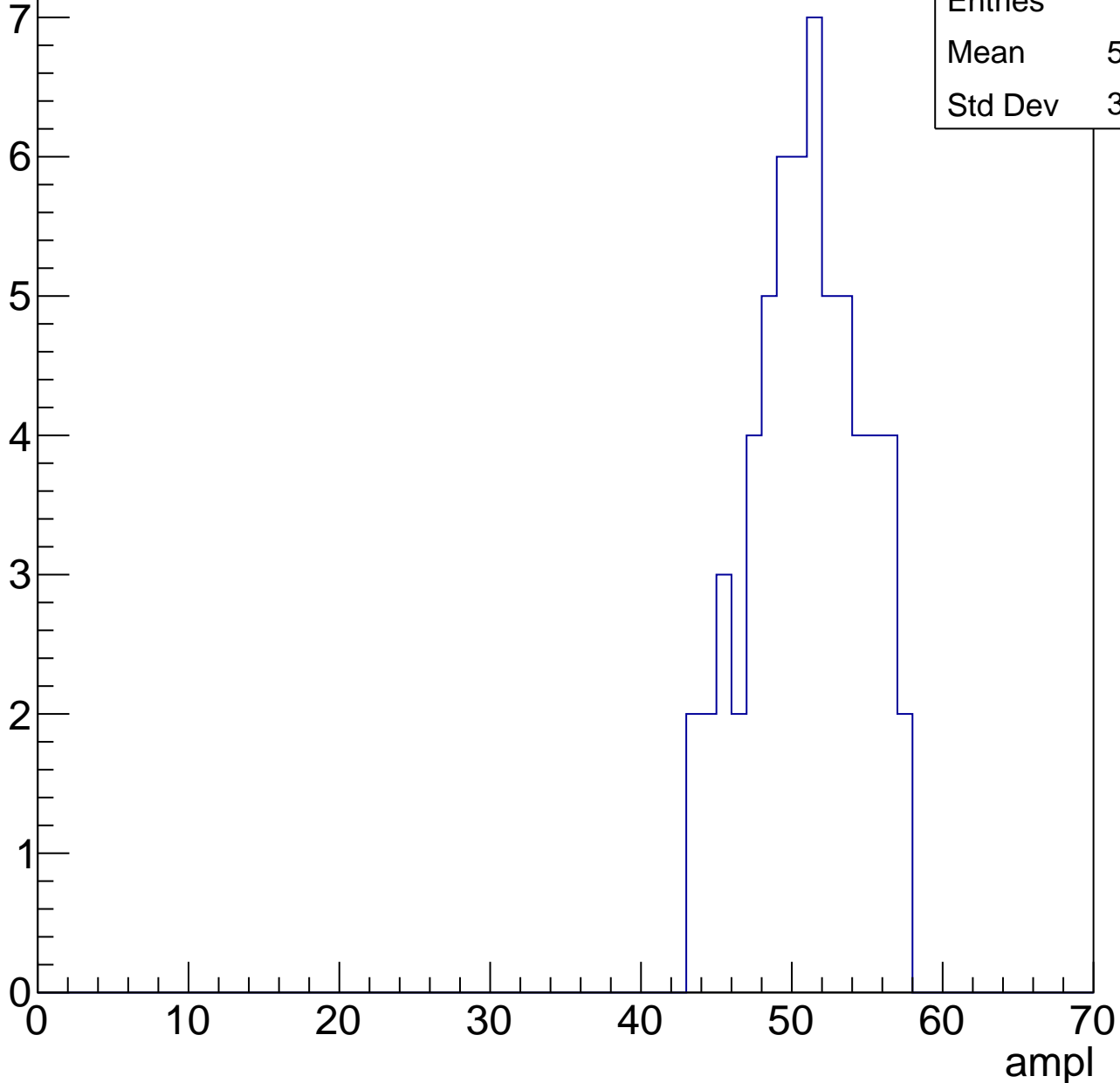


B1L101S, U17-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	50.48
Std Dev	3.629



B1L101S, U17-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	57.23
Std Dev	3.589

Entry

10

8

6

4

2

0

0

10

20

30

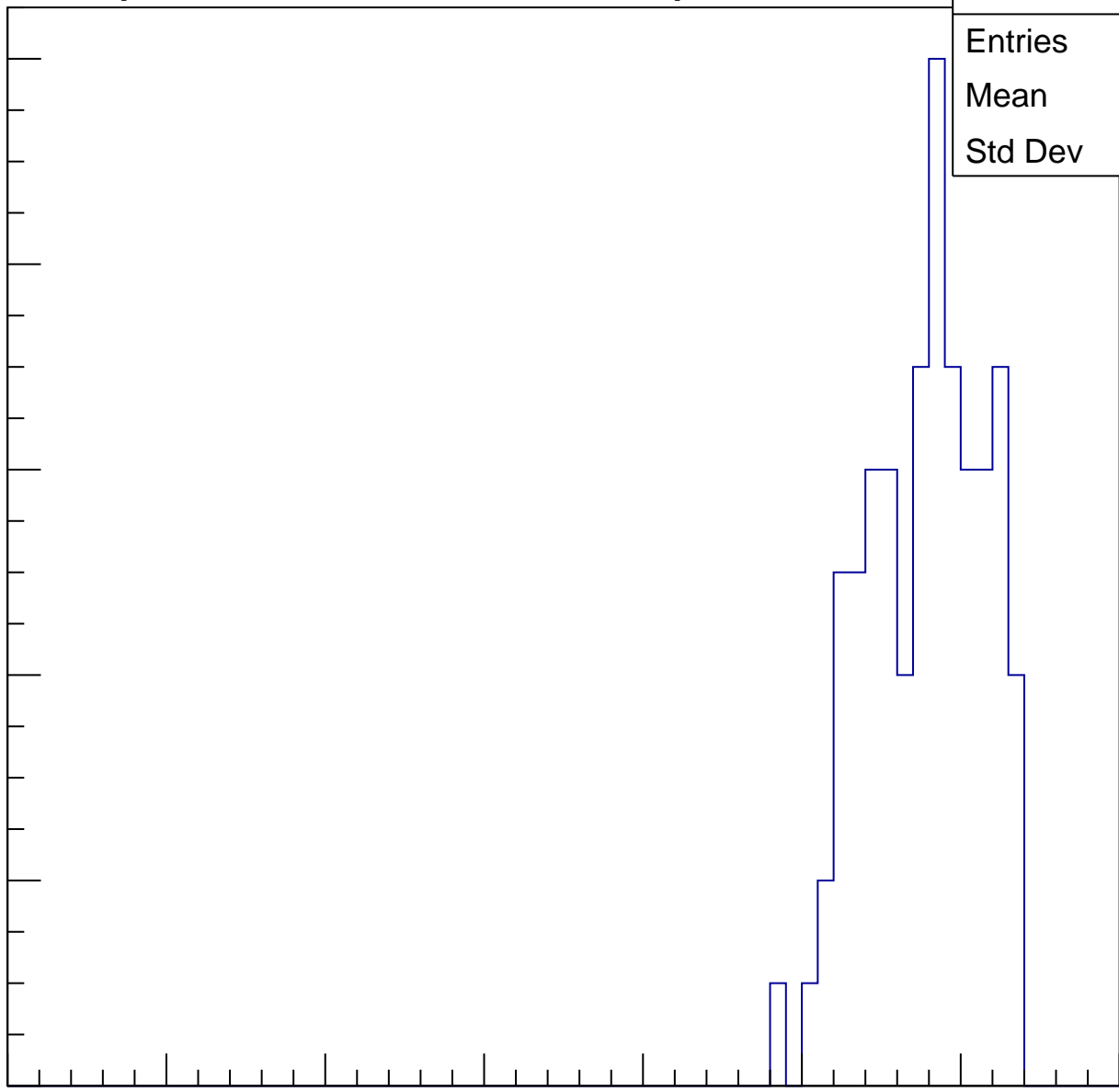
40

50

60

70

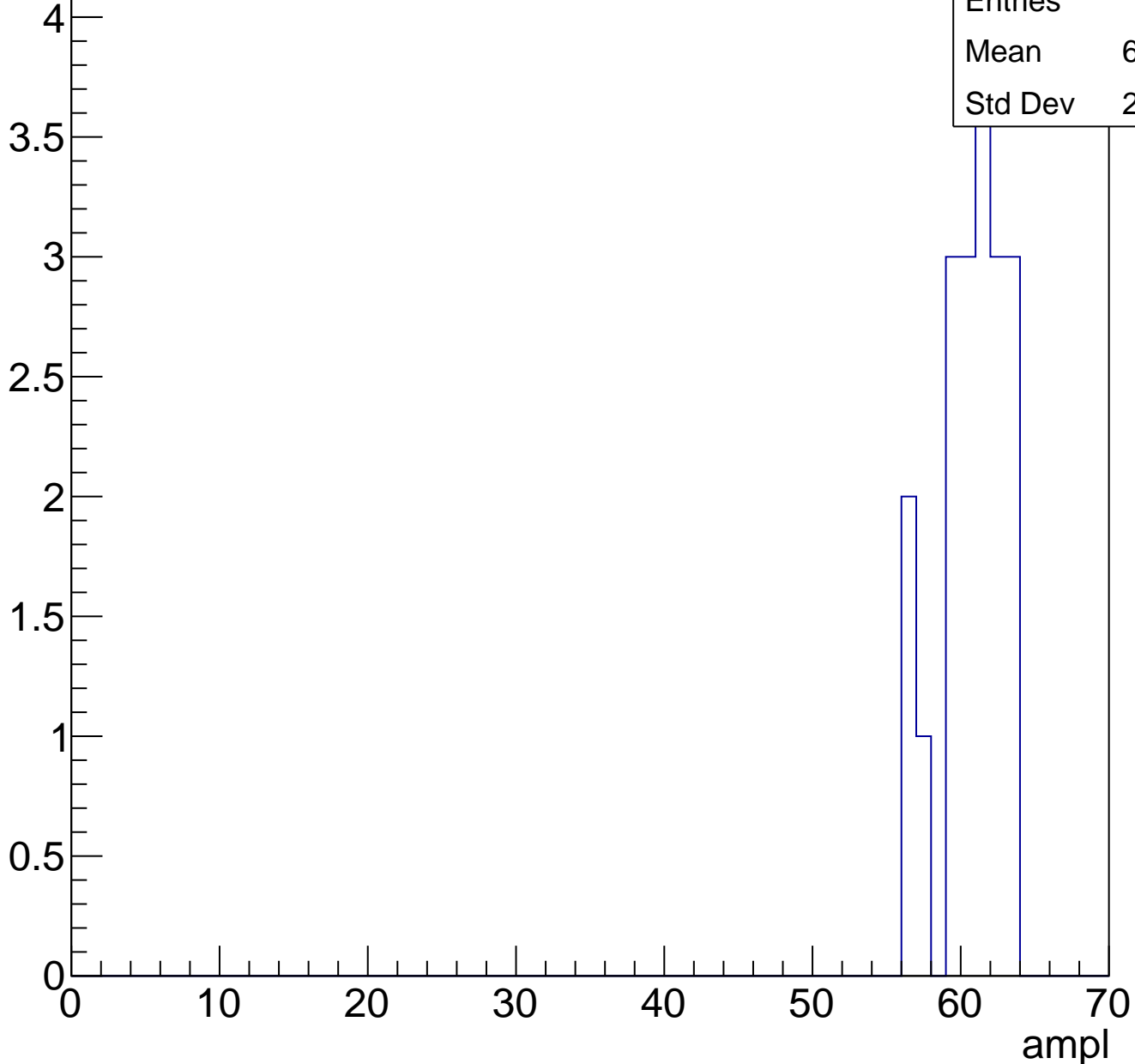
ampl



B1L101S, U17-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

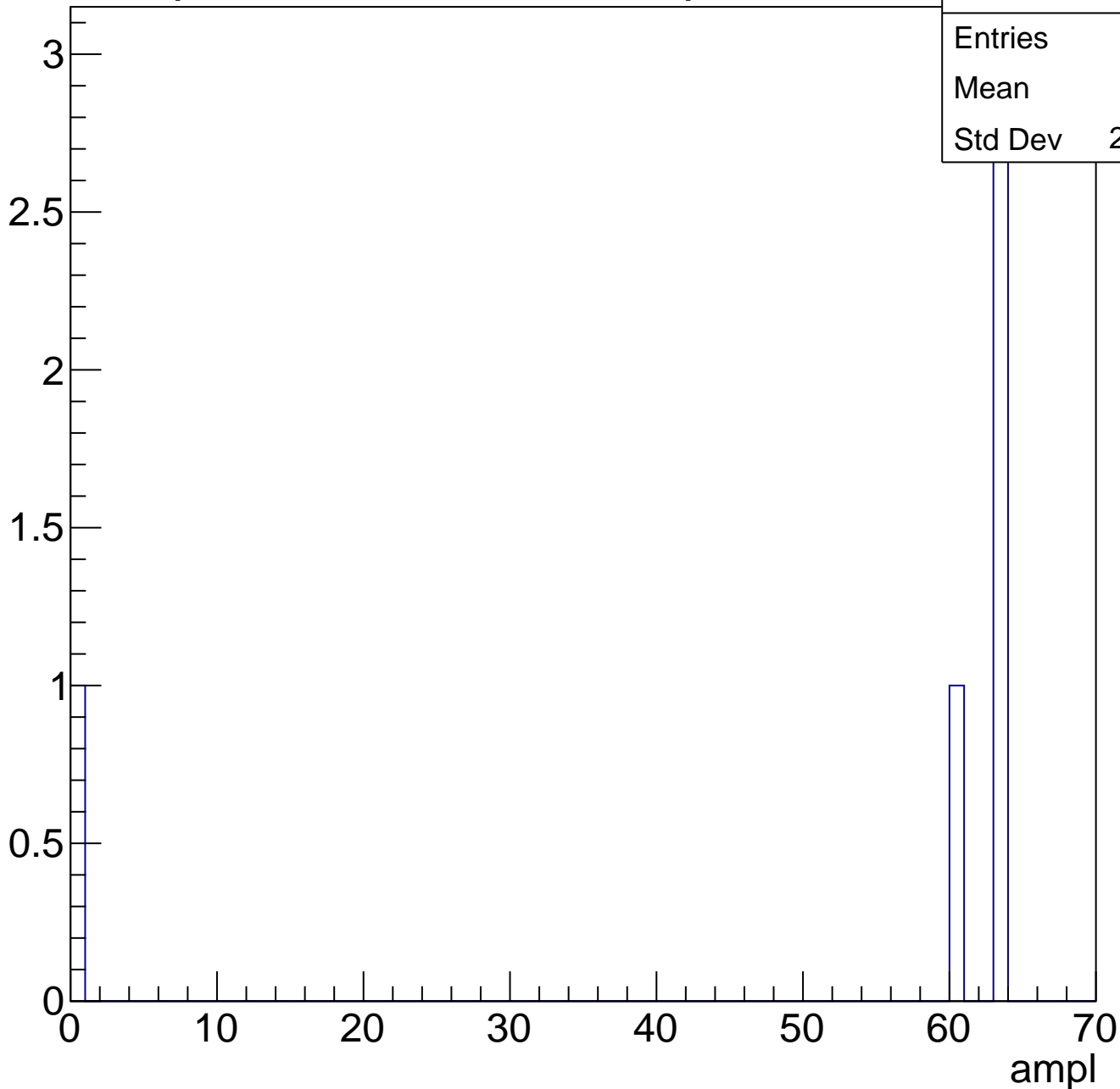


Entries	19
Mean	60.26
Std Dev	2.124

B1L101S, U17-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch98, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	28.42
Std Dev	5.178

Gaus mean : 29.1042

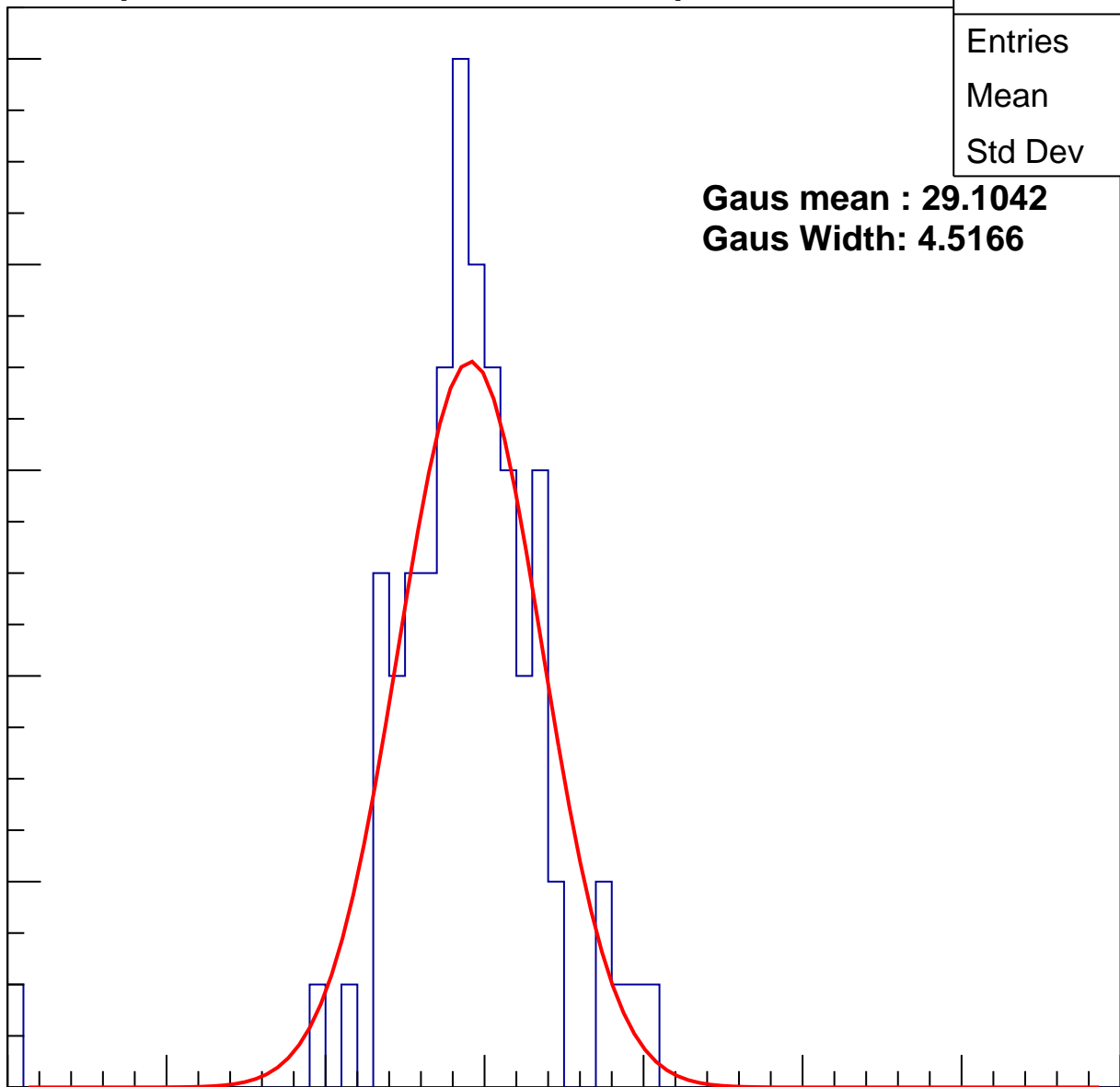
Gaus Width: 4.5166

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch98, adc1

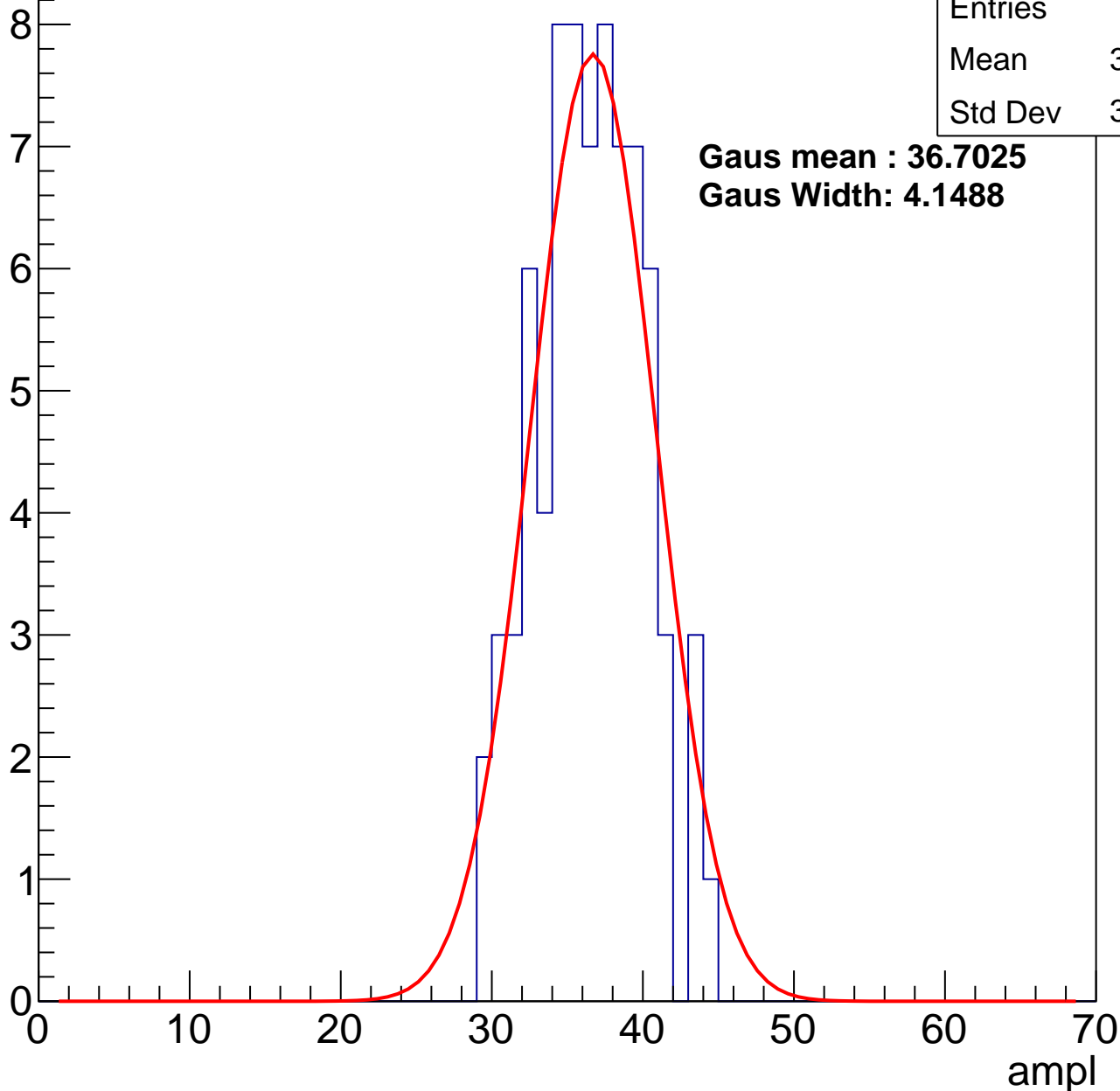
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.05
Std Dev	3.505

Gaus mean : 36.7025

Gaus Width: 4.1488



B1L101S, U17-ch98, adc2

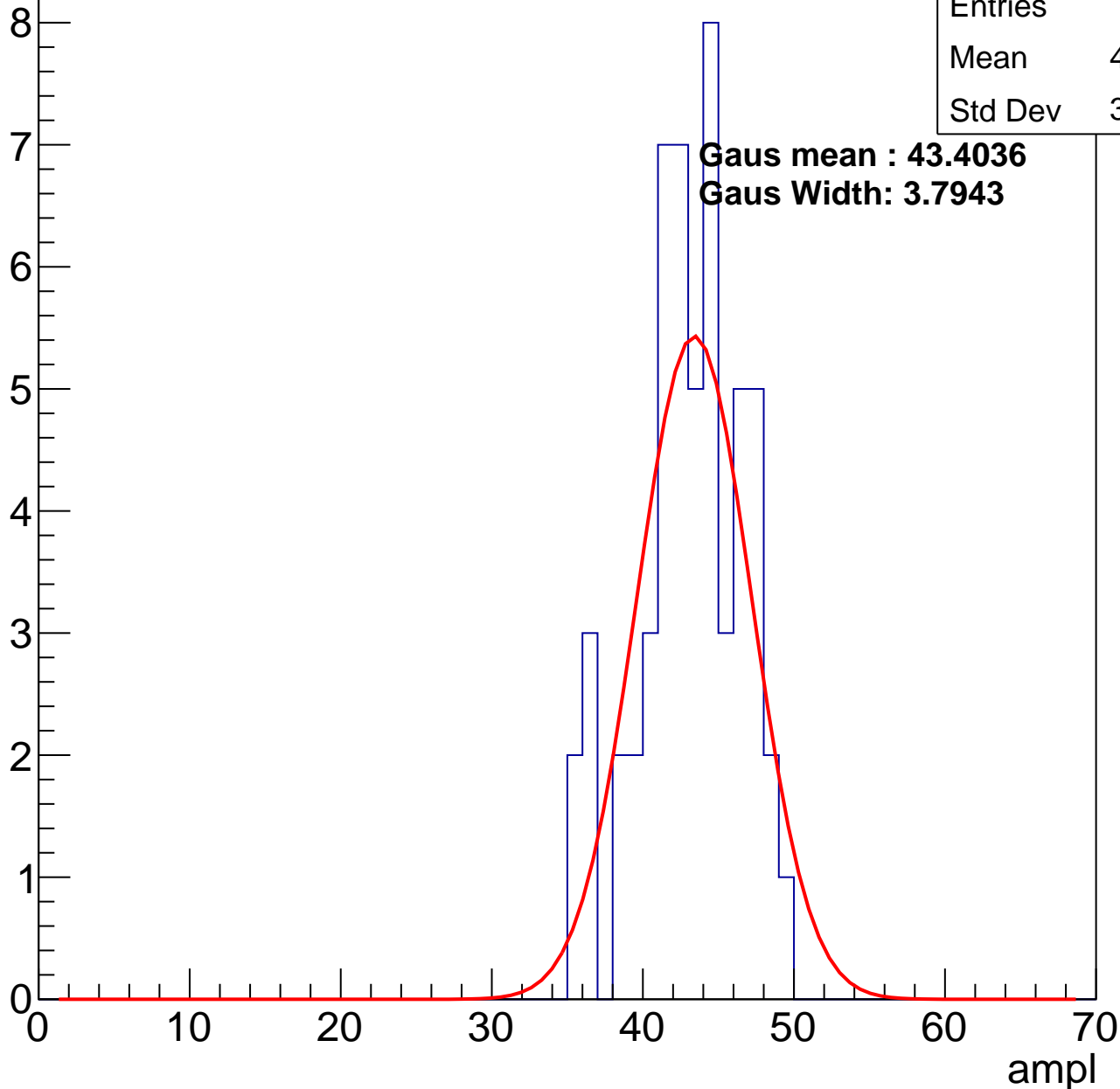
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.64
Std Dev	3.418

Gaus mean : 43.4036

Gaus Width: 3.7943

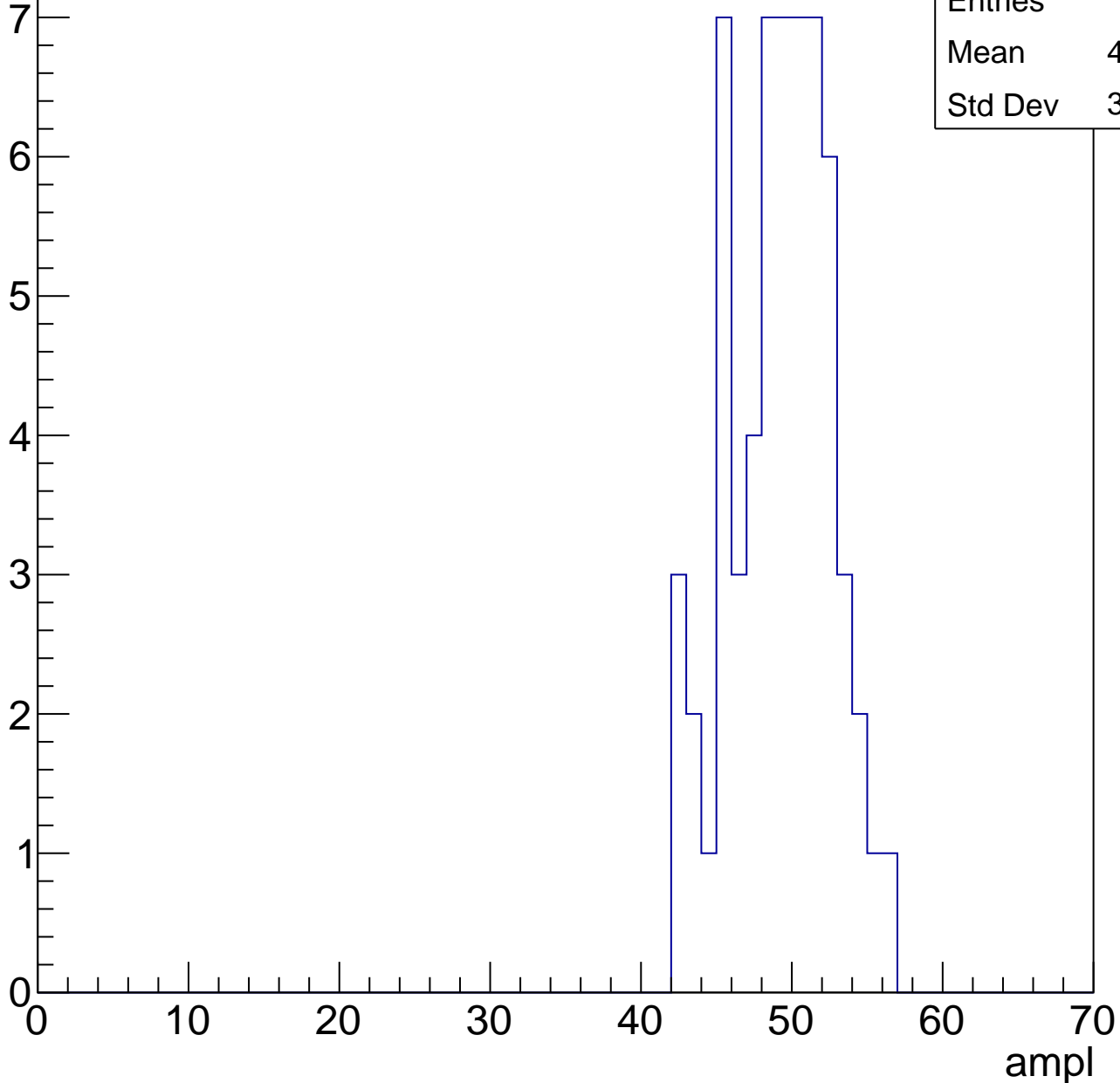


B1L101S, U17-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.74
Std Dev	3.324

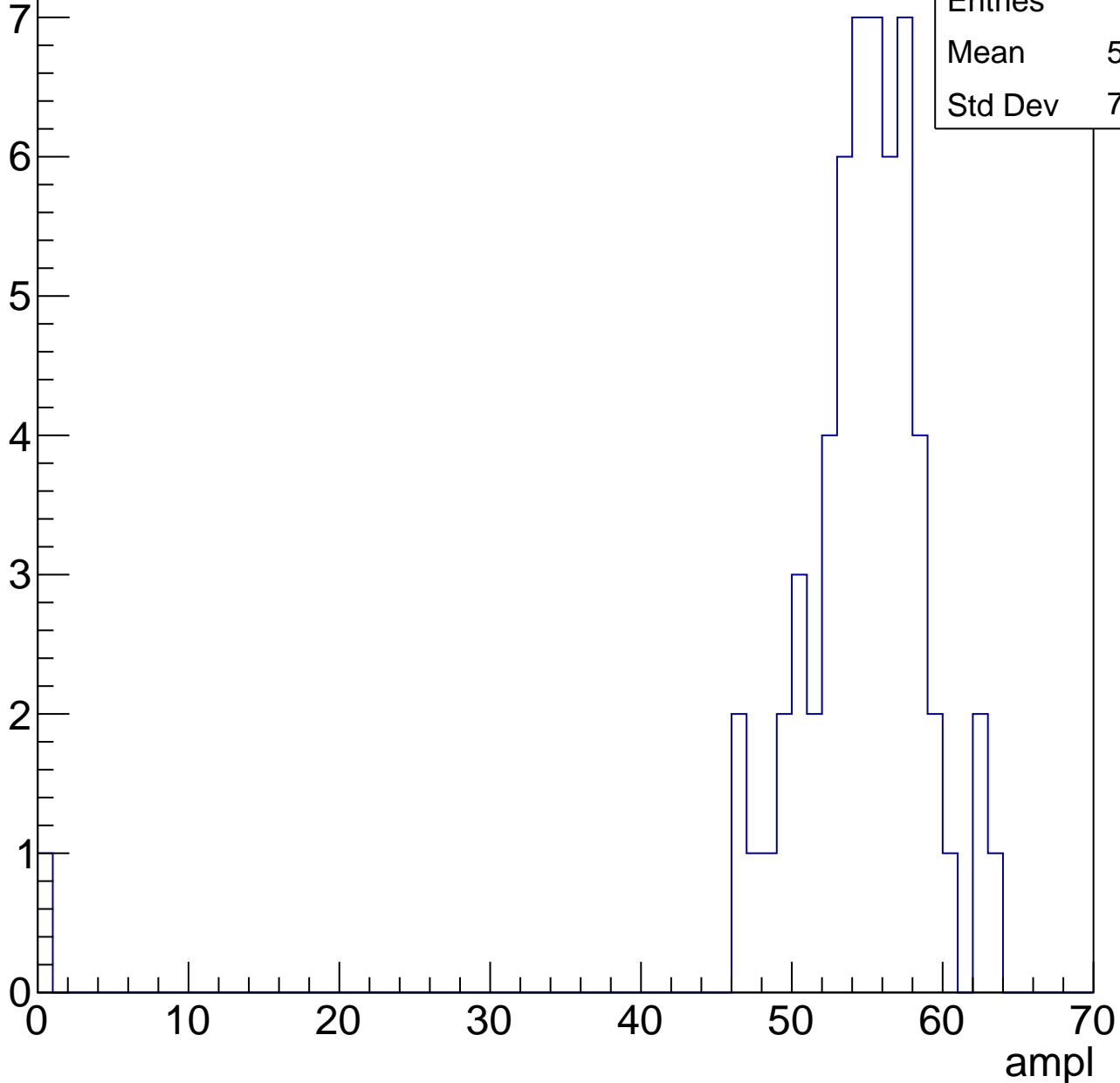


B1L101S, U17-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	53.53
Std Dev	7.926

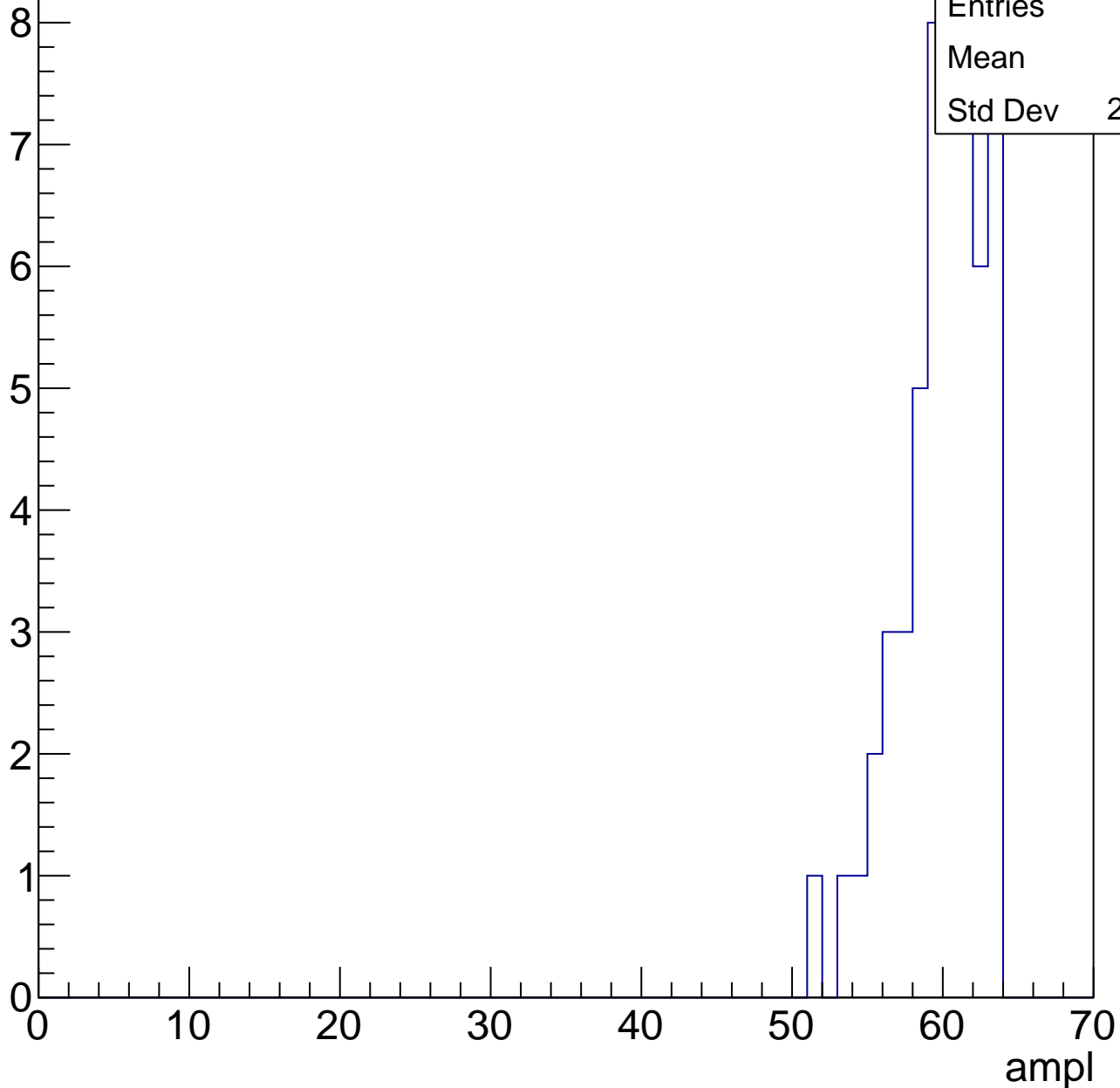


B1L101S, U17-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

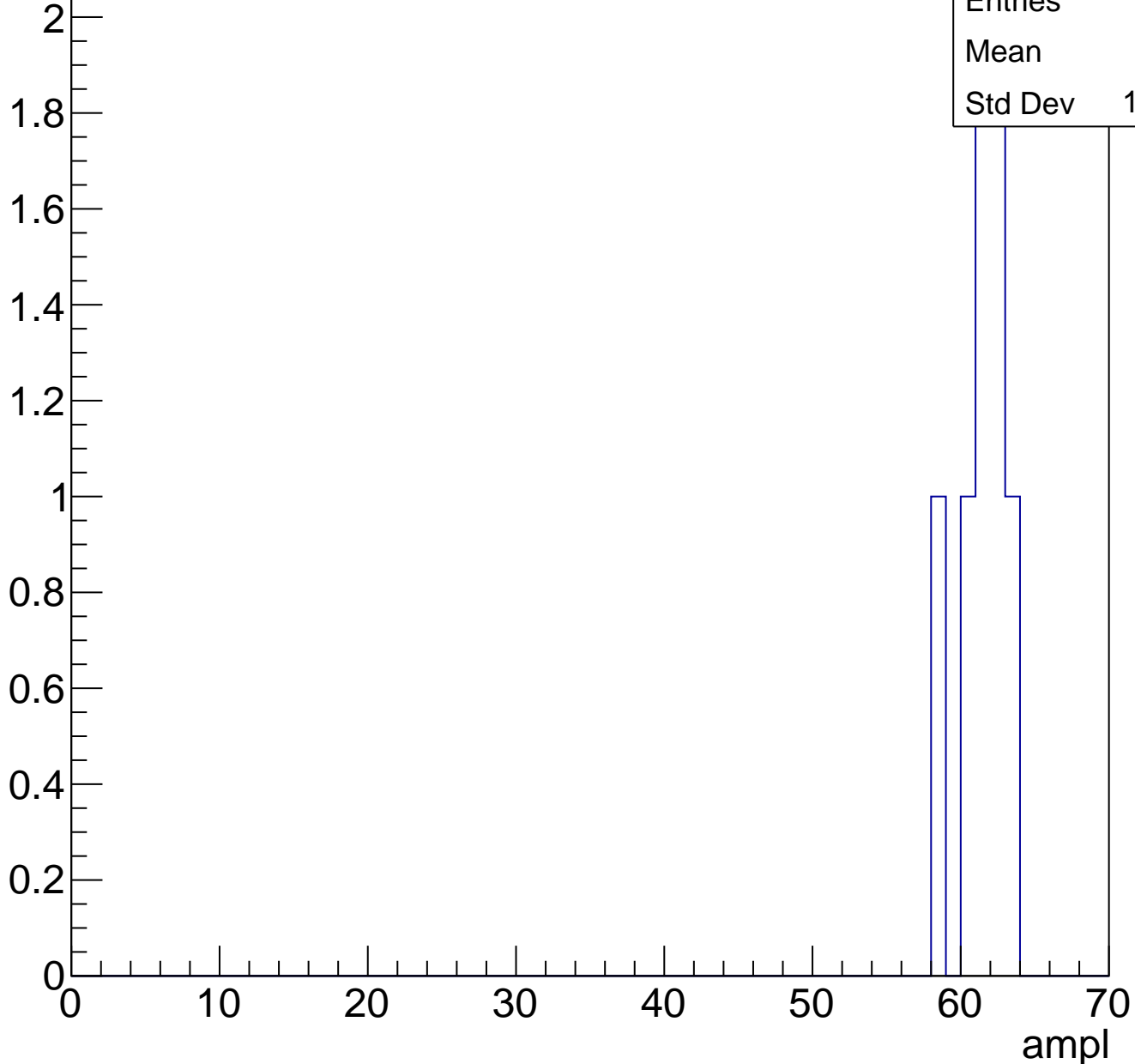
Entries	54
Mean	59.5
Std Dev	2.754



B1L101S, U17-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch99, adc0

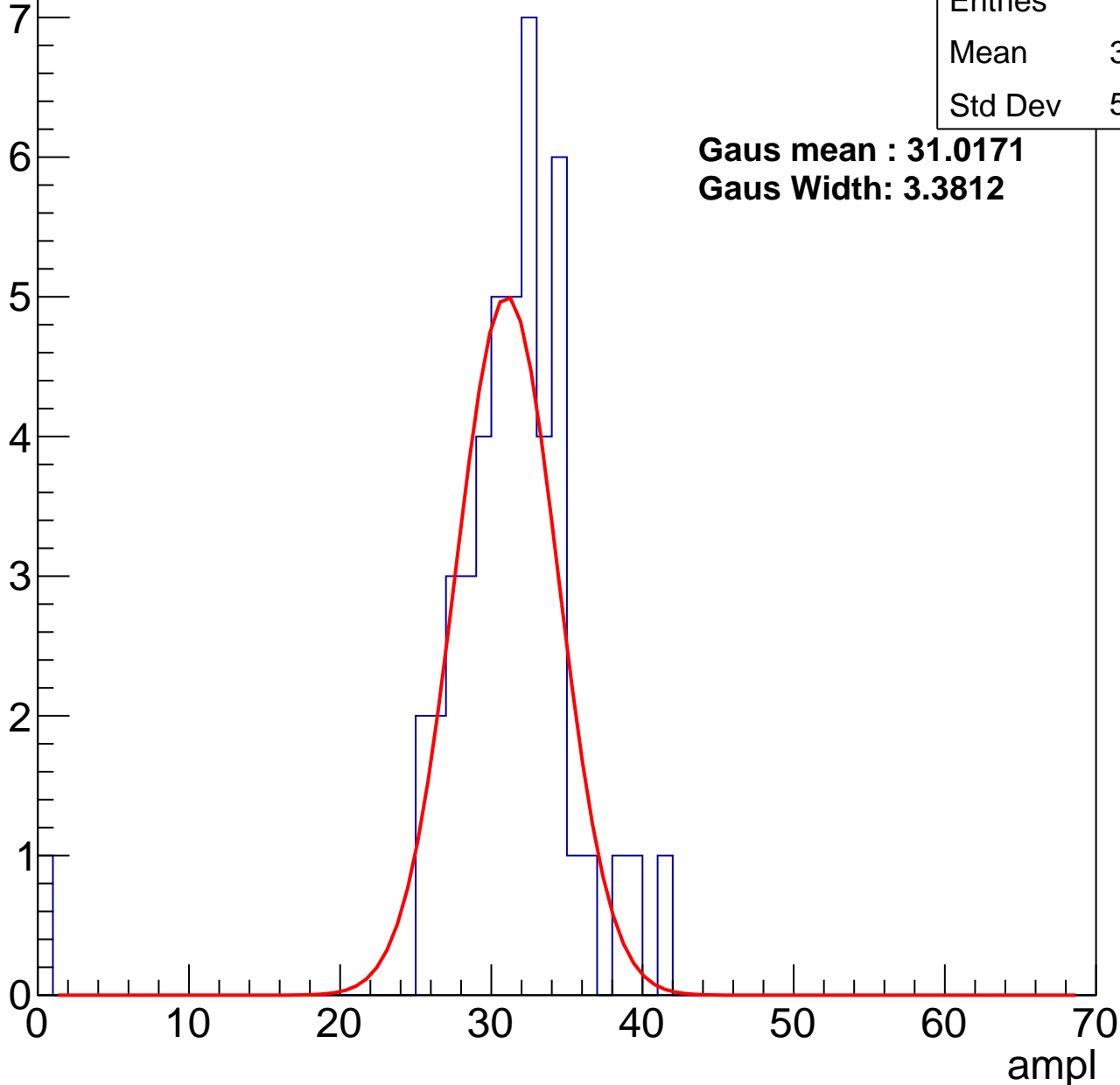
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	30.57
Std Dev	5.652

Gaus mean : 31.0171

Gaus Width: 3.3812



B1L101S, U17-ch99, adc1

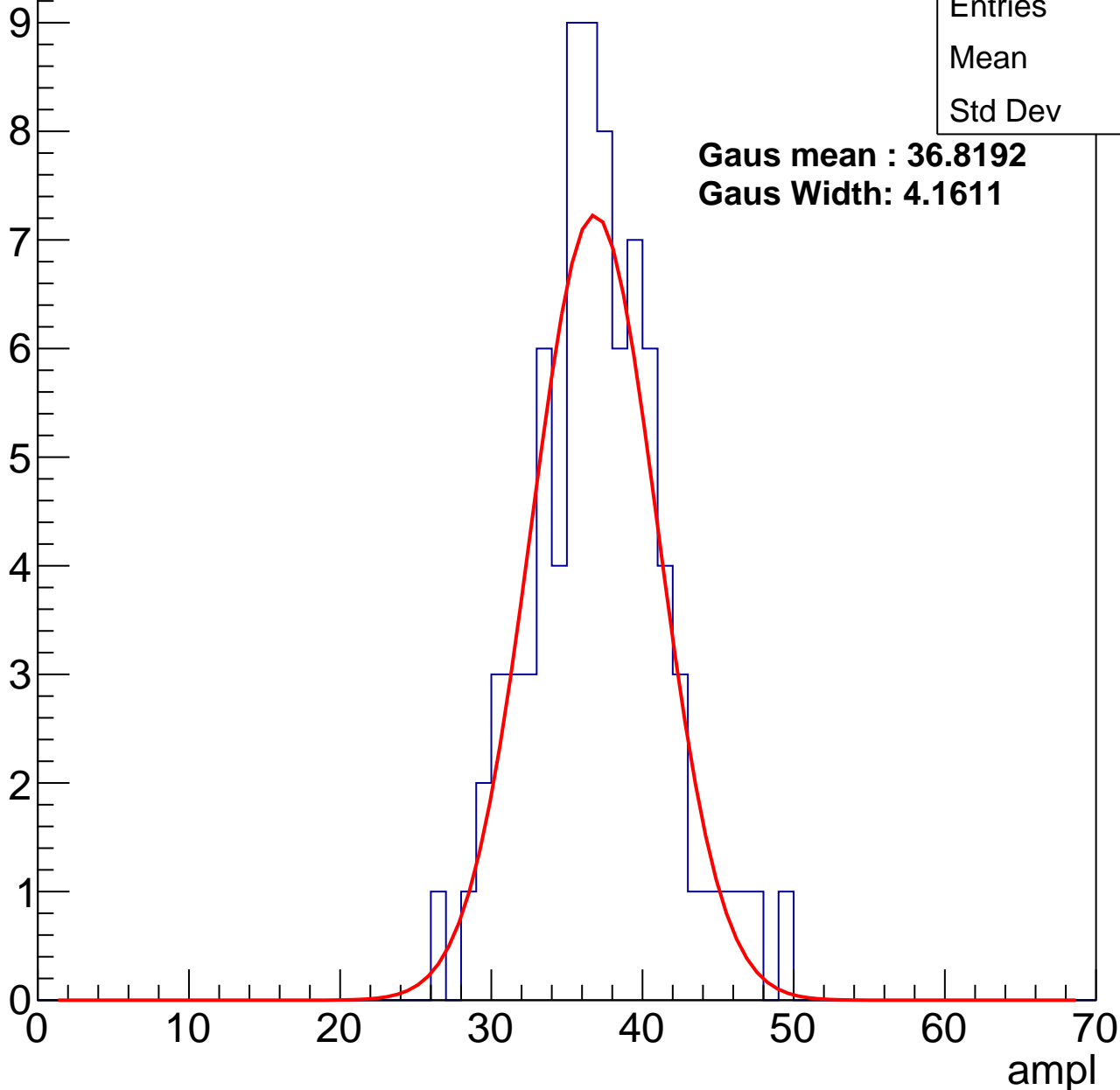
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	36.6
Std Dev	4.34

Gaus mean : 36.8192

Gaus Width: 4.1611



B1L101S, U17-ch99, adc2

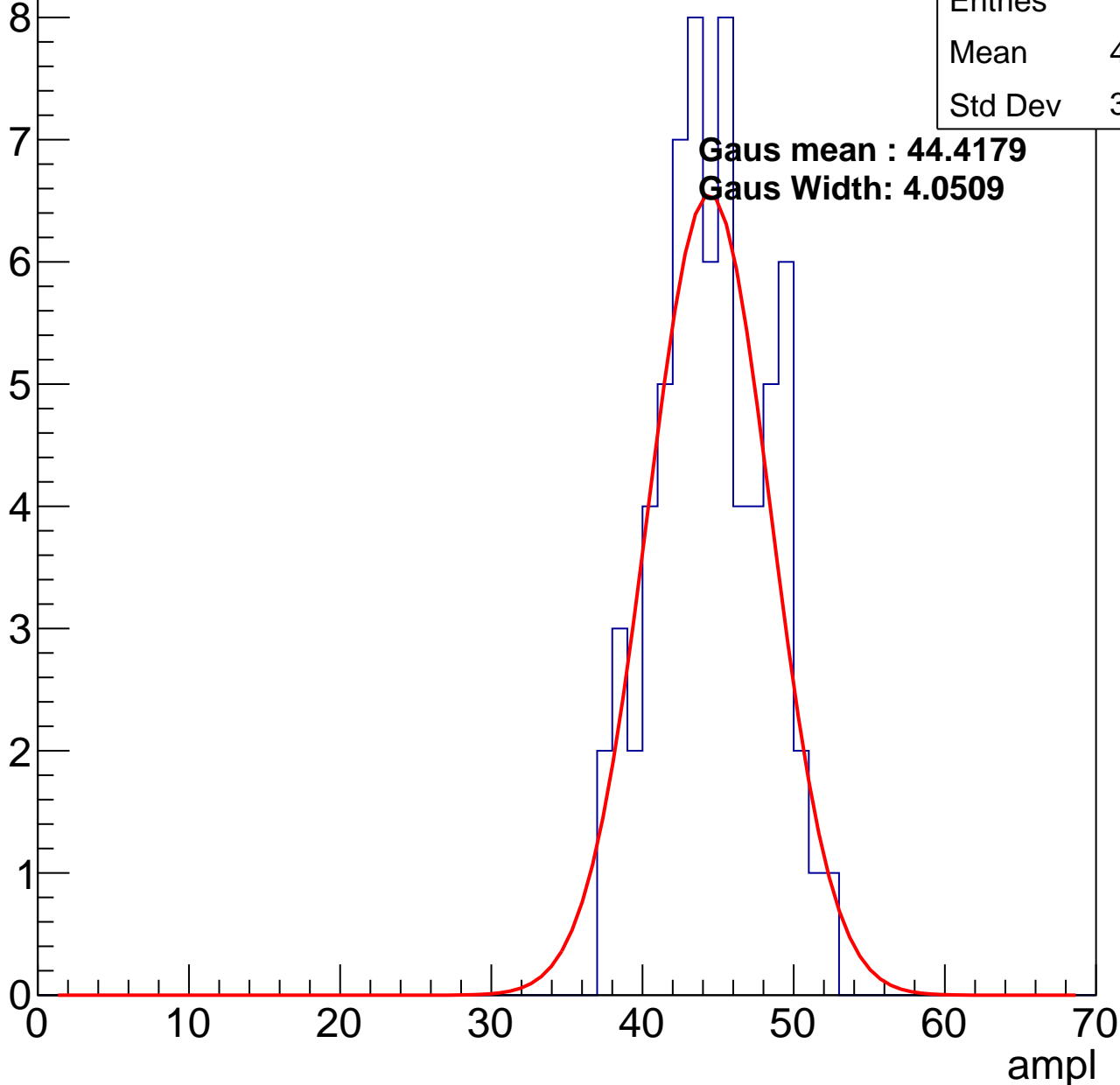
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.15
Std Dev	3.586

Gaus mean : 44.4179

Gaus Width: 4.0509

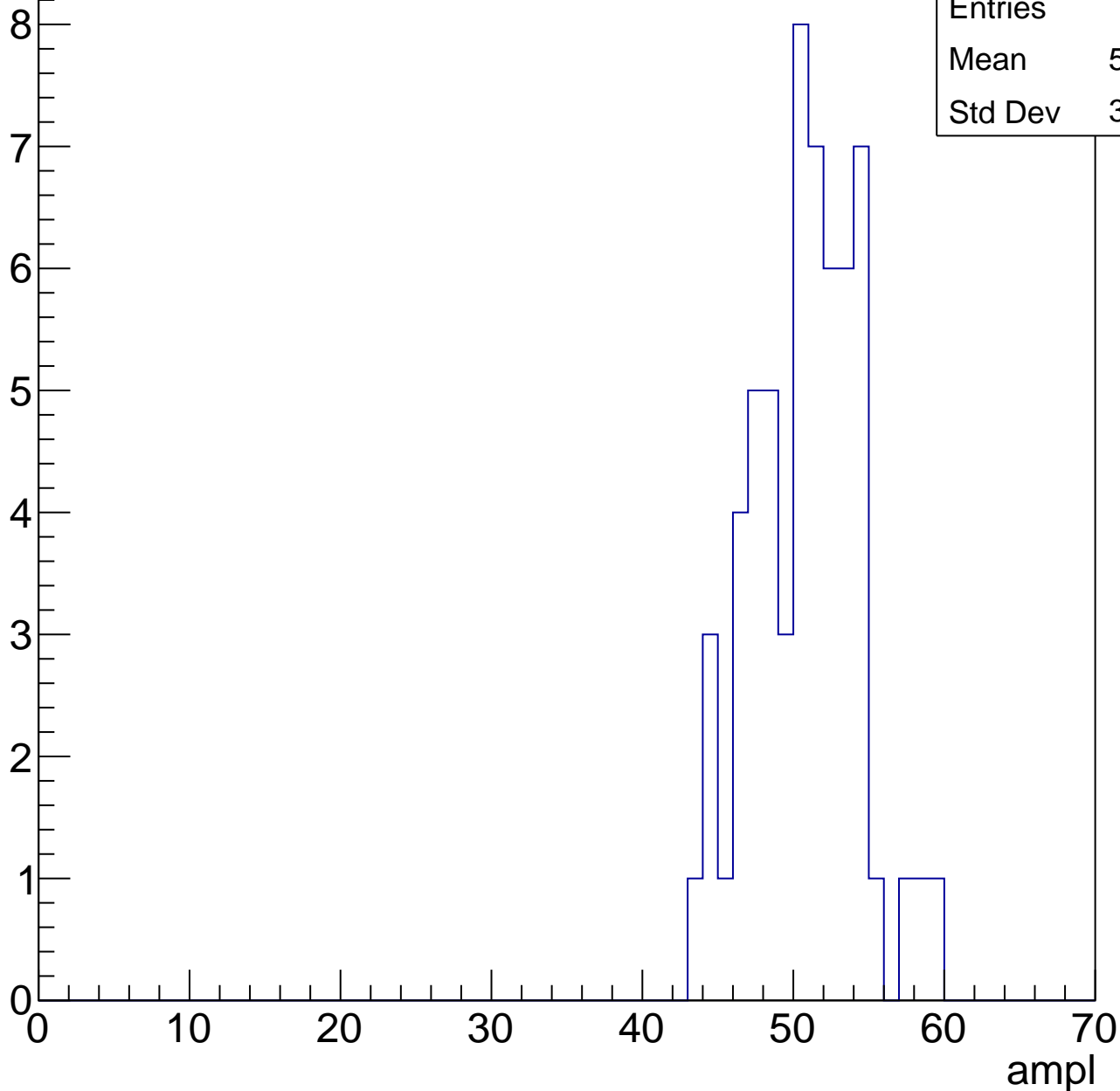


B1L101S, U17-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.33
Std Dev	3.472

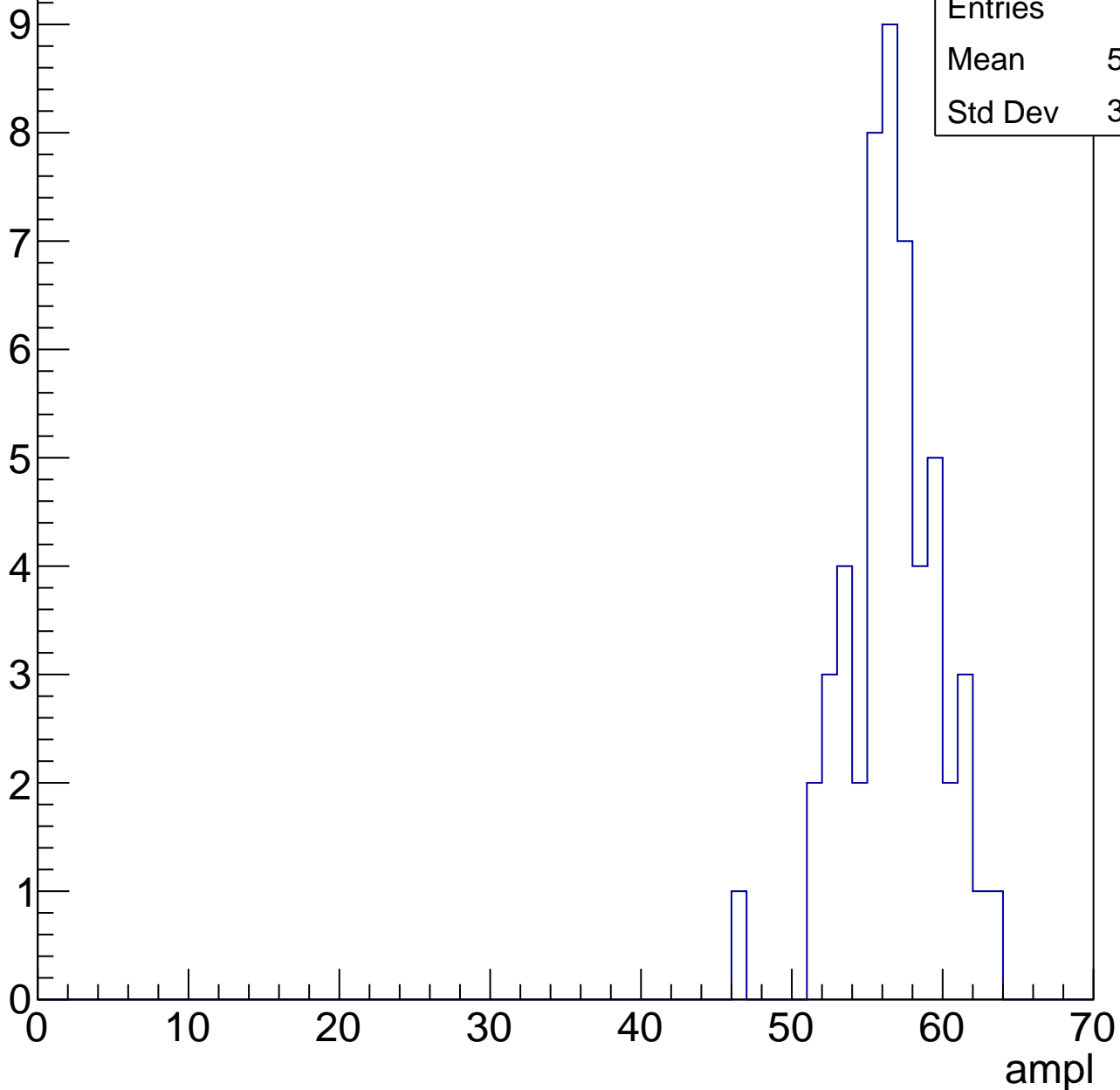


B1L101S, U17-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

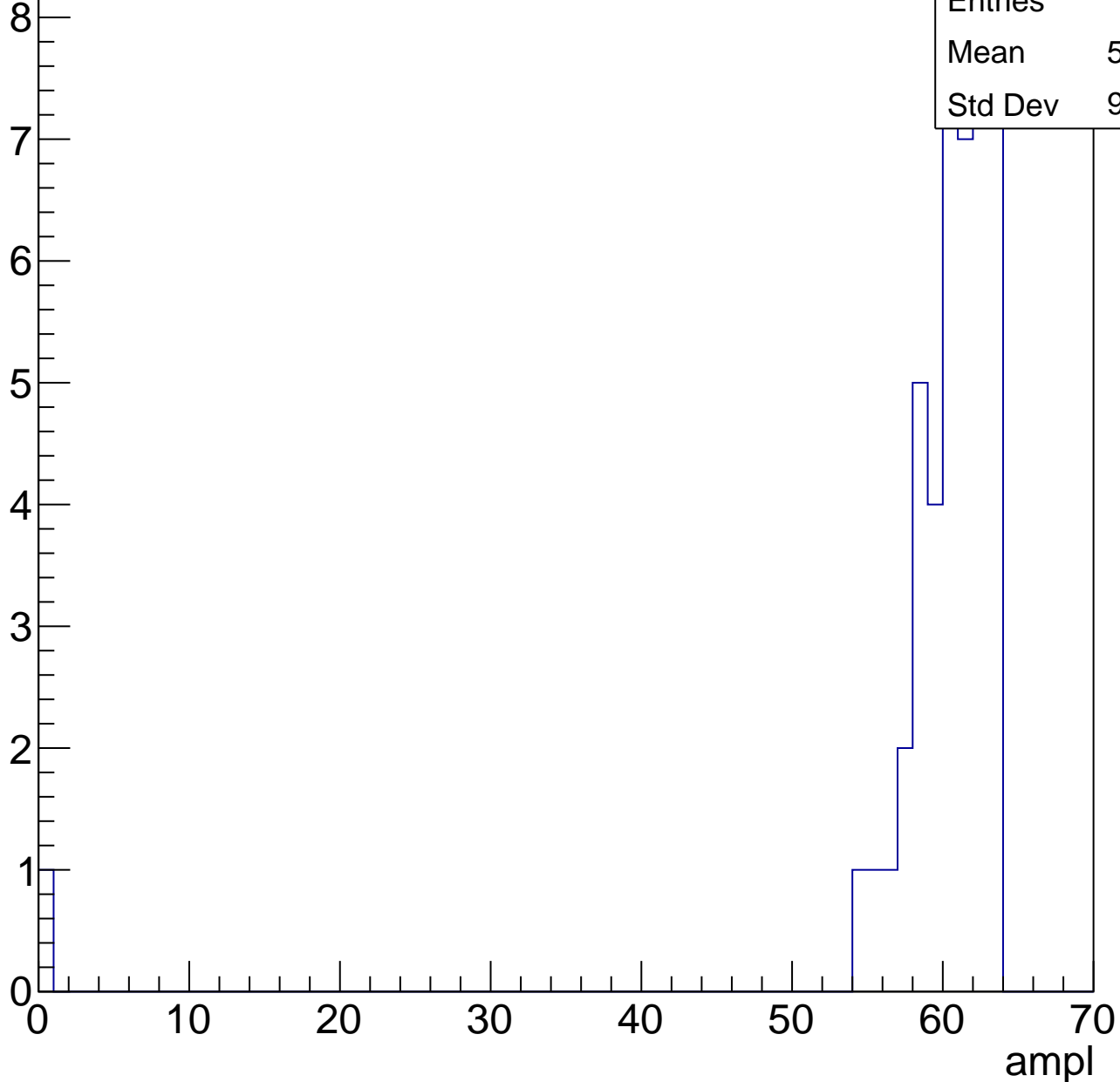
Entries	52
Mean	56.19
Std Dev	3.126



B1L101S, U17-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U17-ch100, adc0

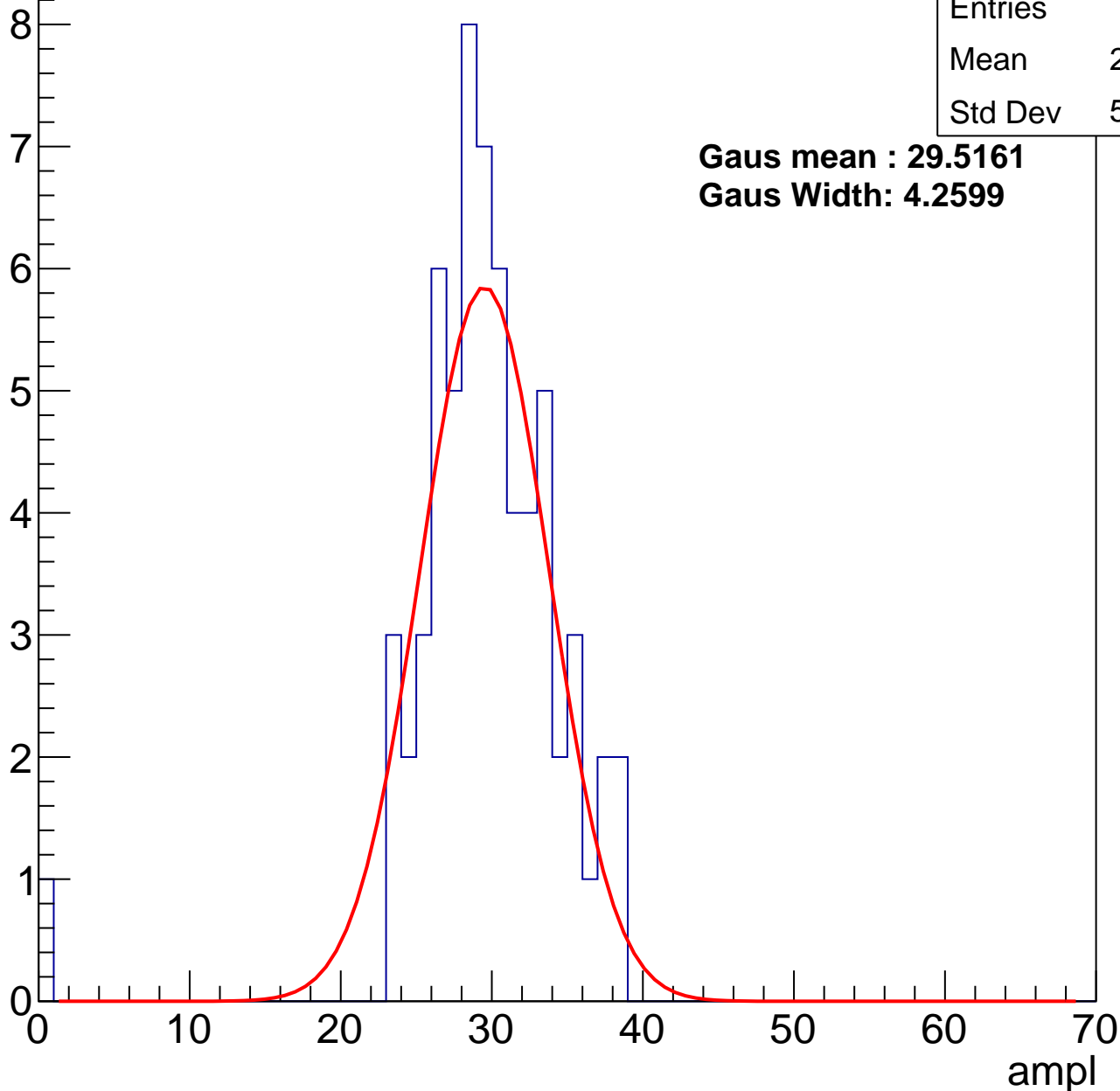
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.16
Std Dev	5.248

Gaus mean : 29.5161

Gaus Width: 4.2599



B1L101S, U17-ch100, adc1

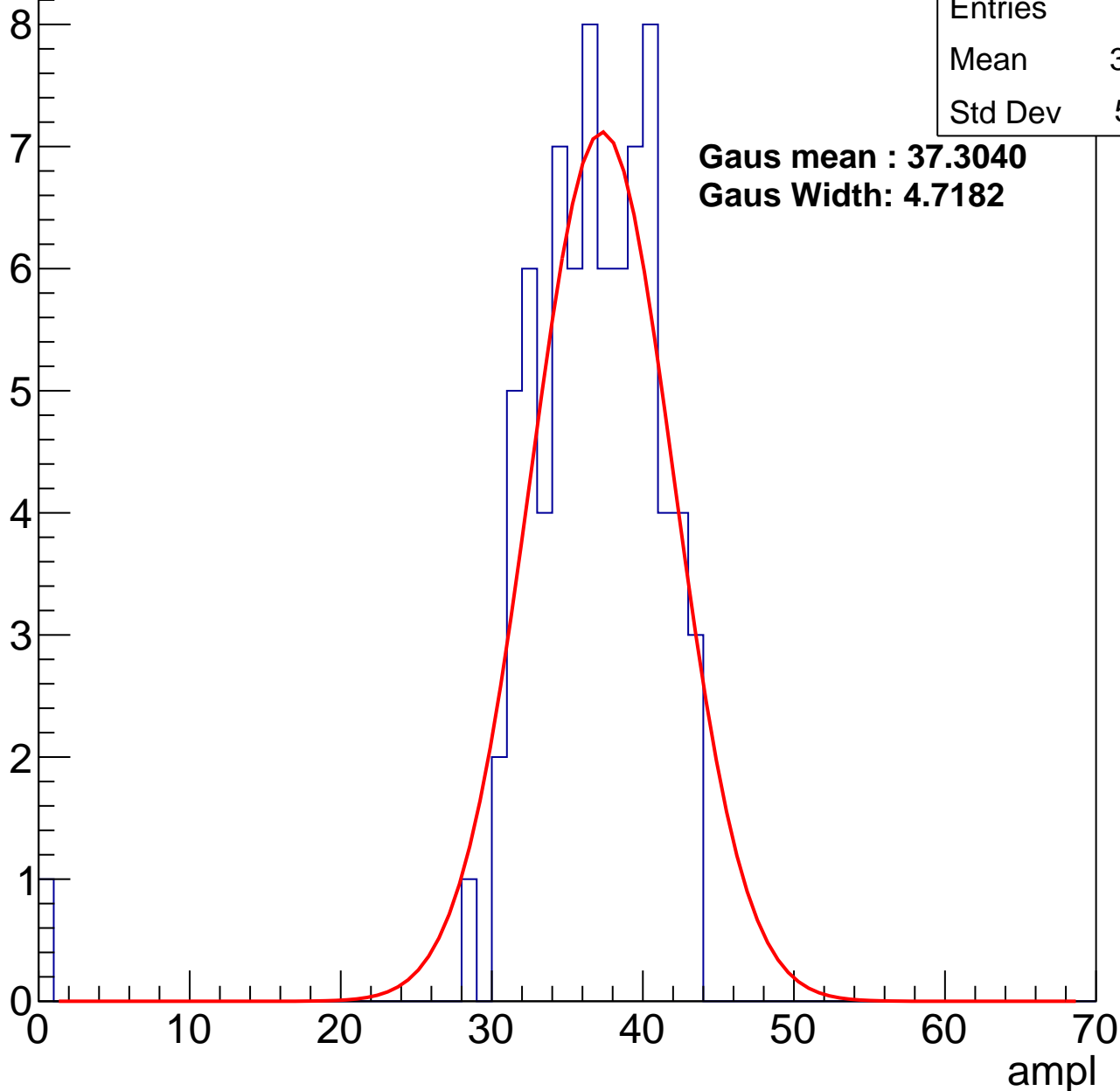
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.99
Std Dev	5.471

Gaus mean : 37.3040

Gaus Width: 4.7182



B1L101S, U17-ch100, adc2

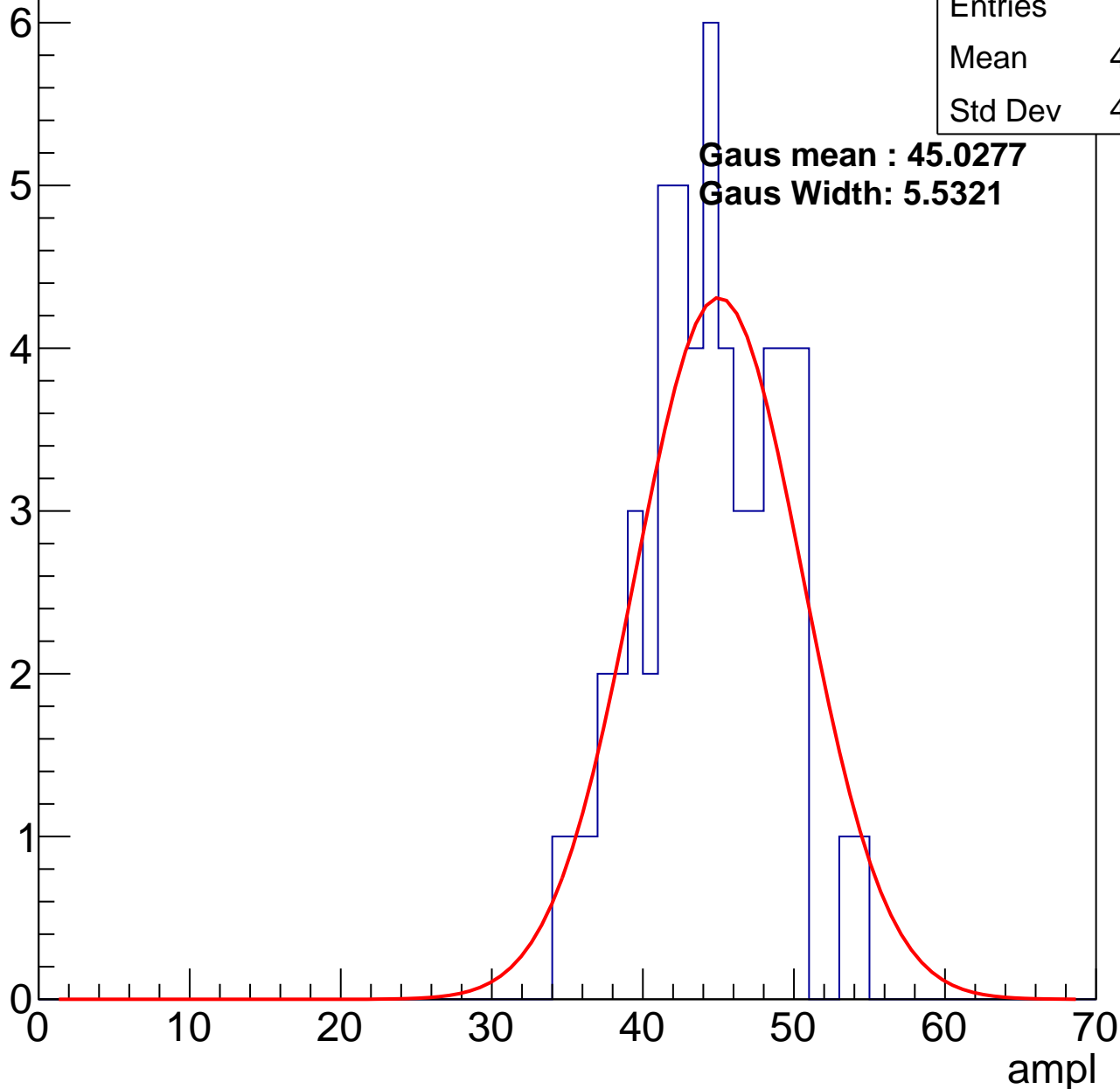
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.88
Std Dev	4.476

Gaus mean : 45.0277

Gaus Width: 5.5321

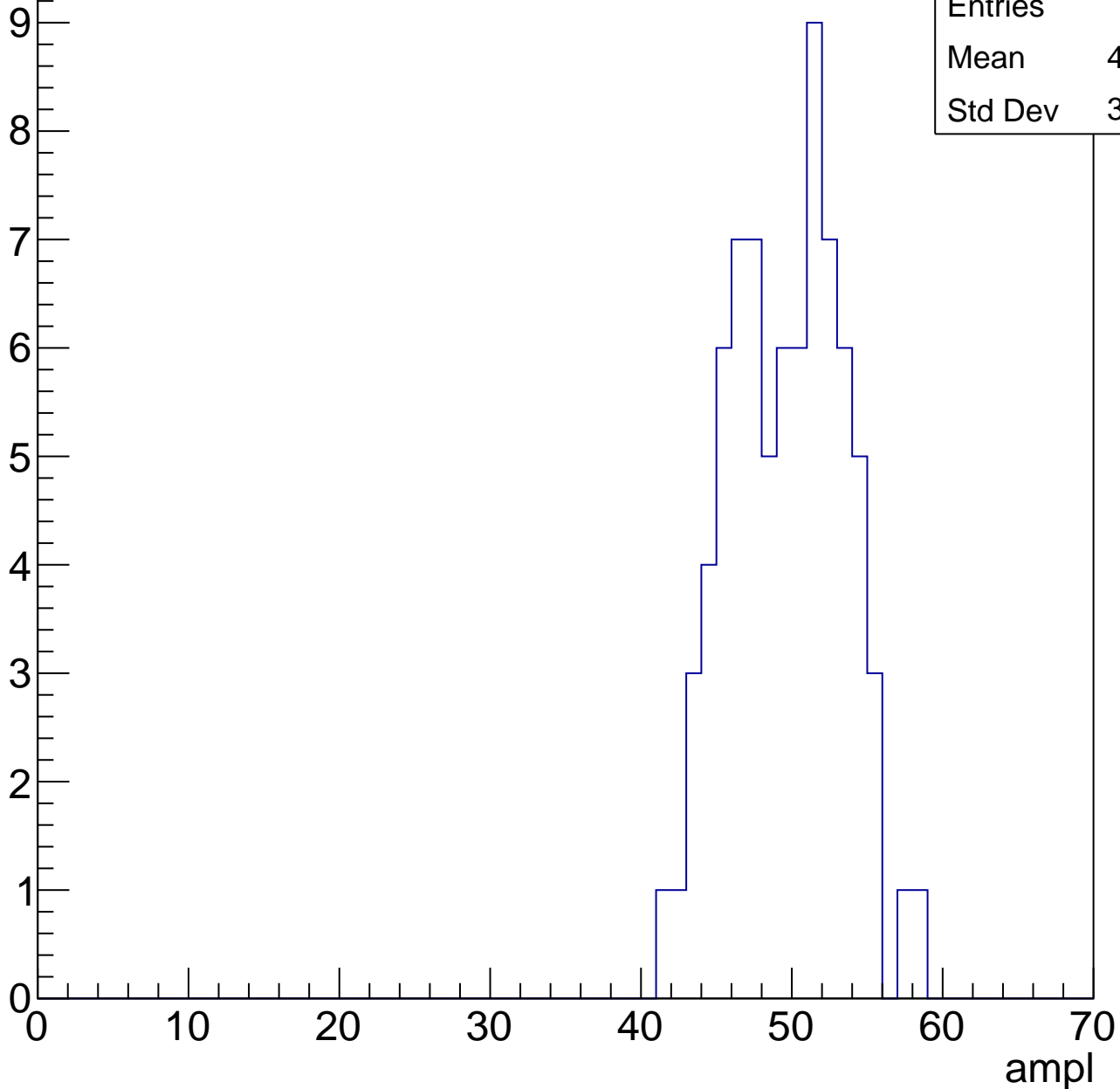


B1L101S, U17-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	49.15
Std Dev	3.738

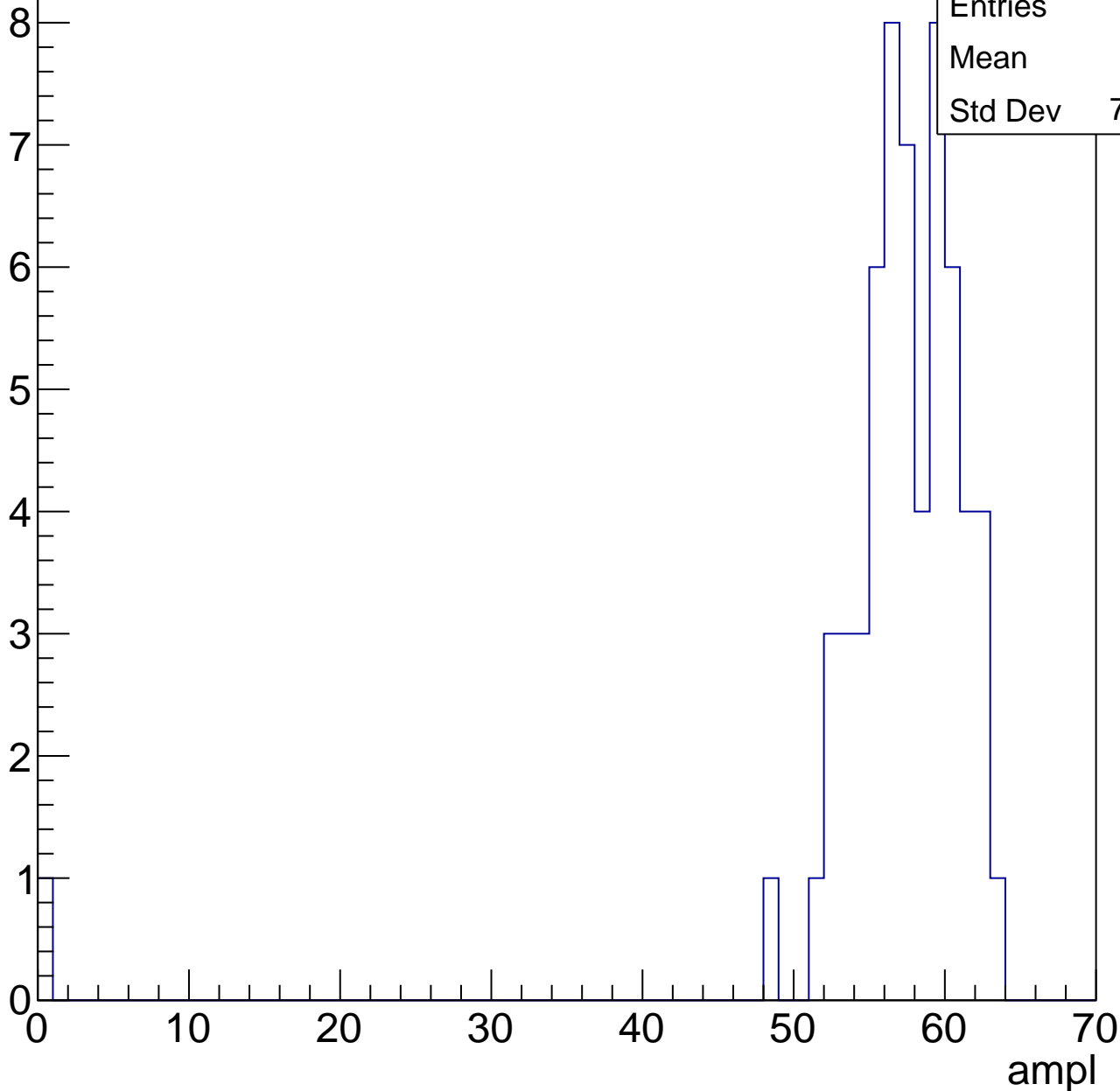


B1L101S, U17-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

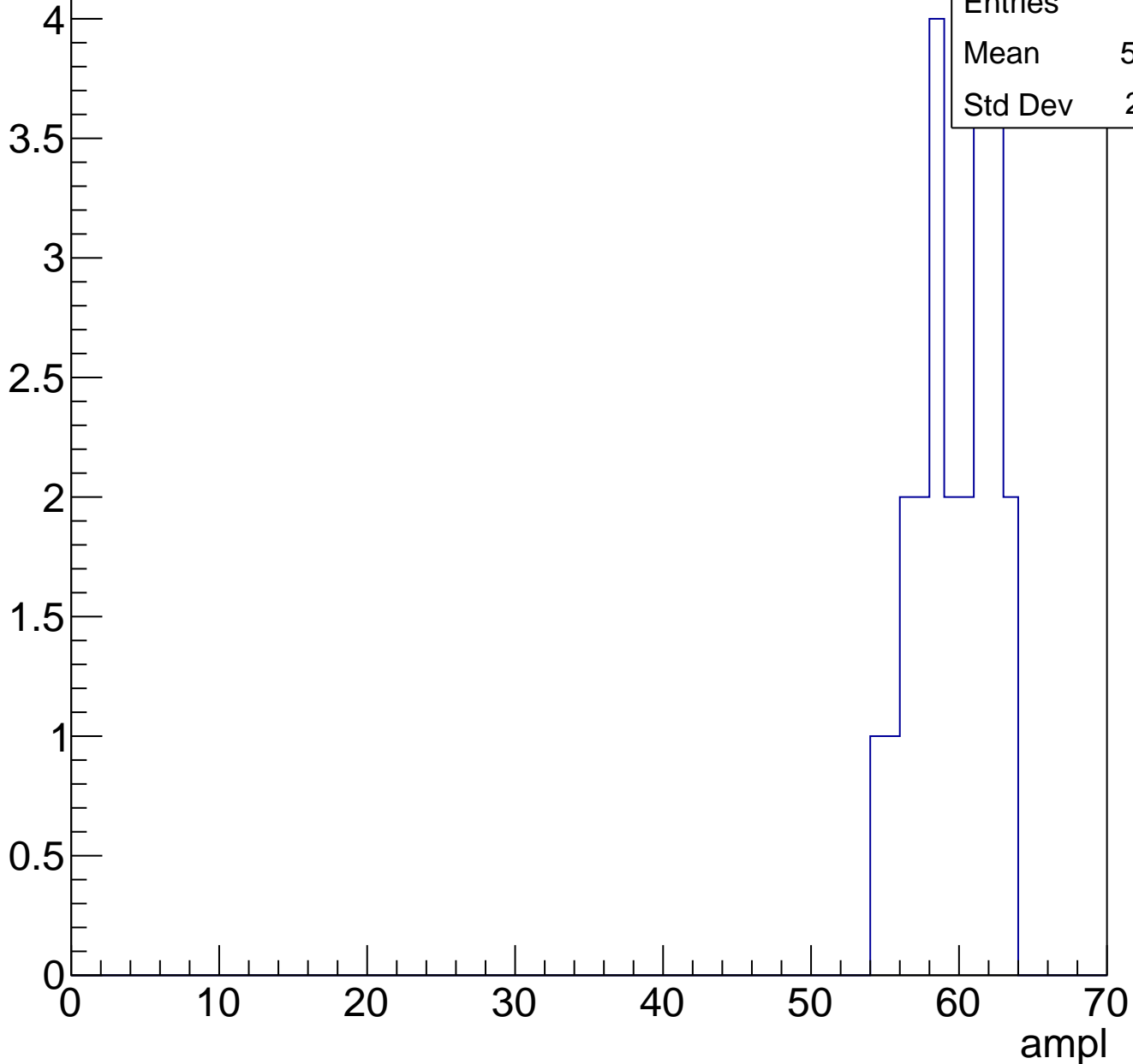
Entries	60
Mean	56.2
Std Dev	7.958



B1L101S, U17-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



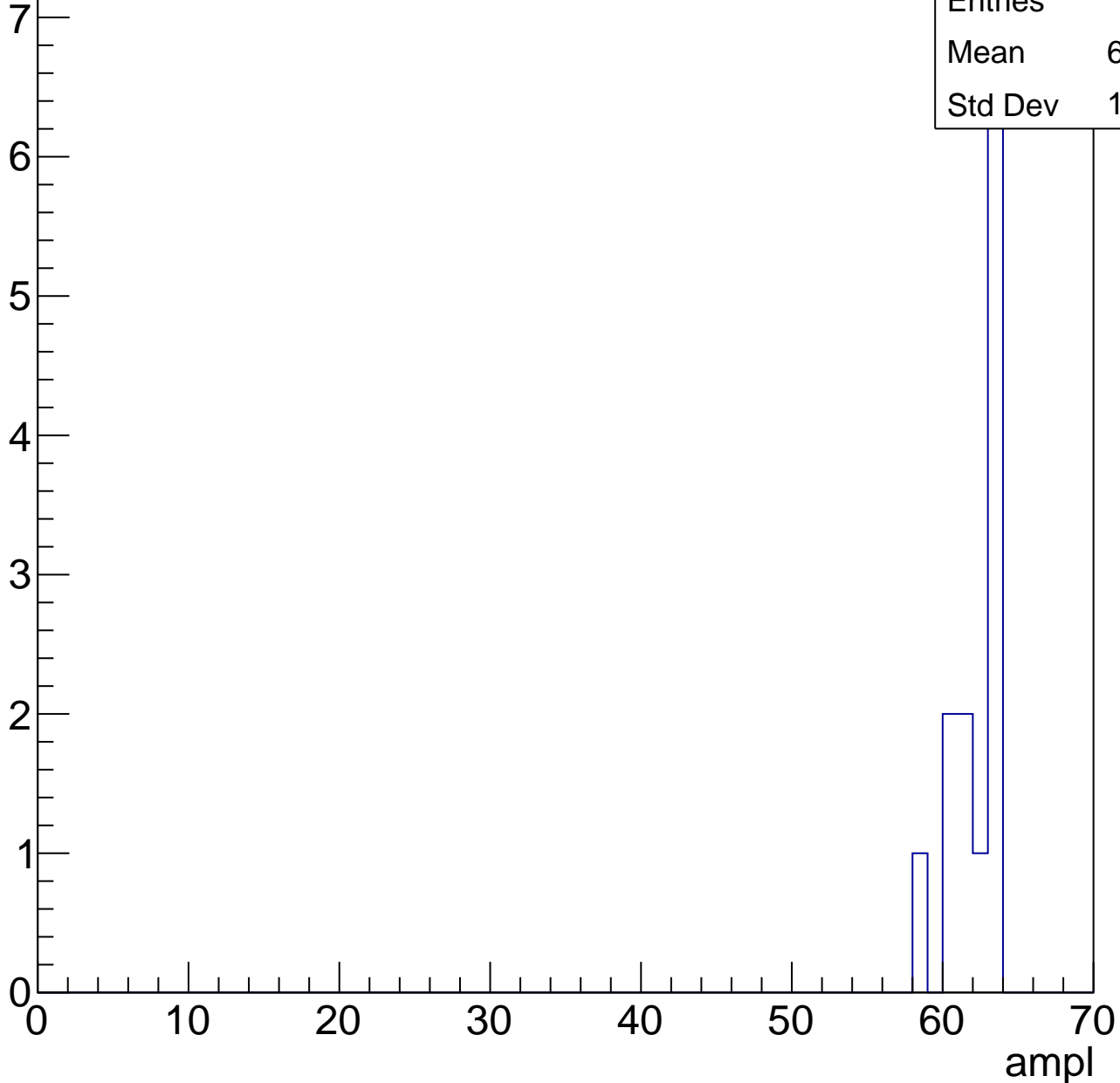
Entries	24
Mean	59.29
Std Dev	2.541

B1L101S, U17-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.77
Std Dev	1.576



B1L101S, U17-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch101, adc0

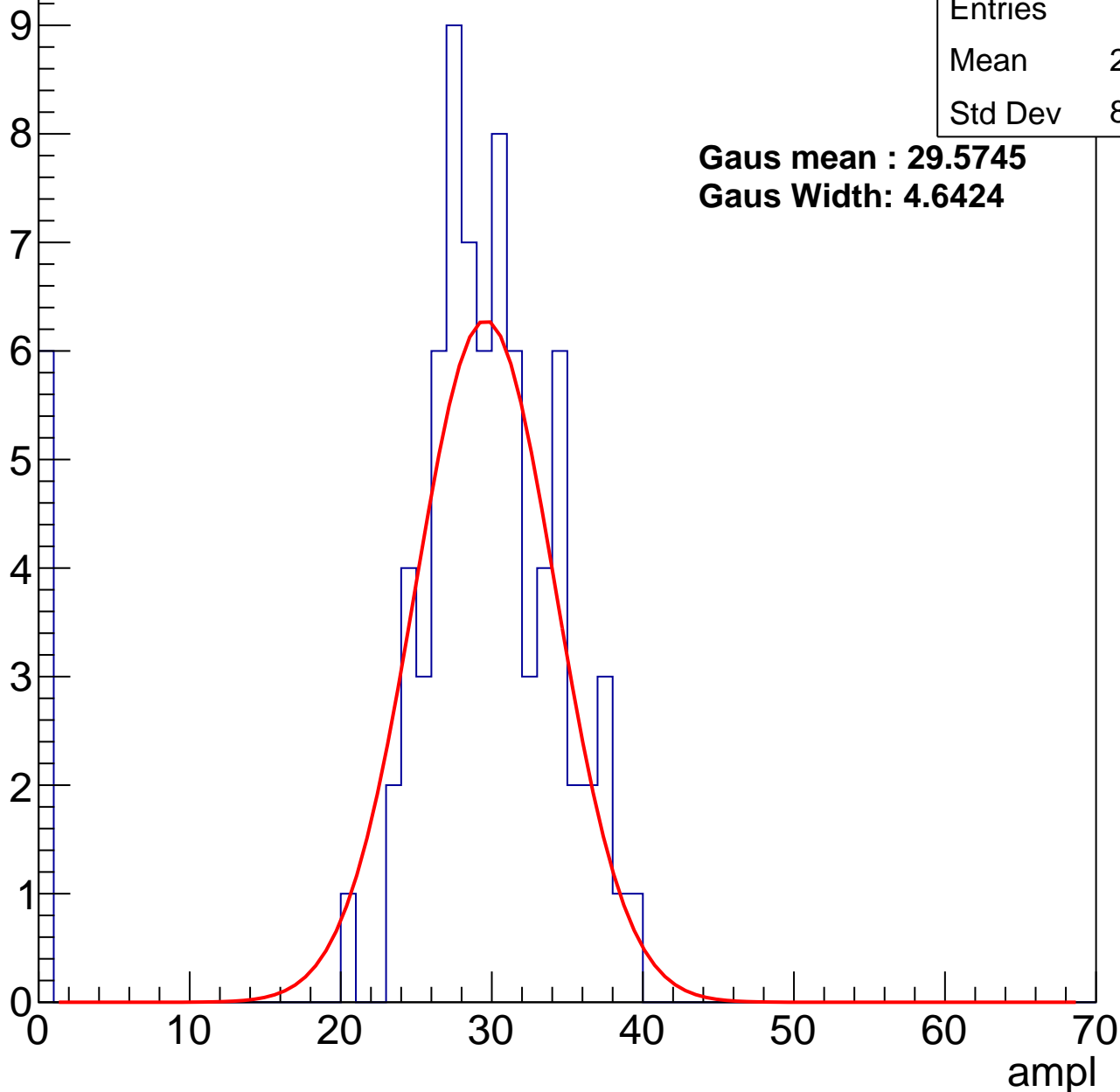
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	27.43
Std Dev	8.705

Gaus mean : 29.5745

Gaus Width: 4.6424



B1L101S, U17-ch101, adc1

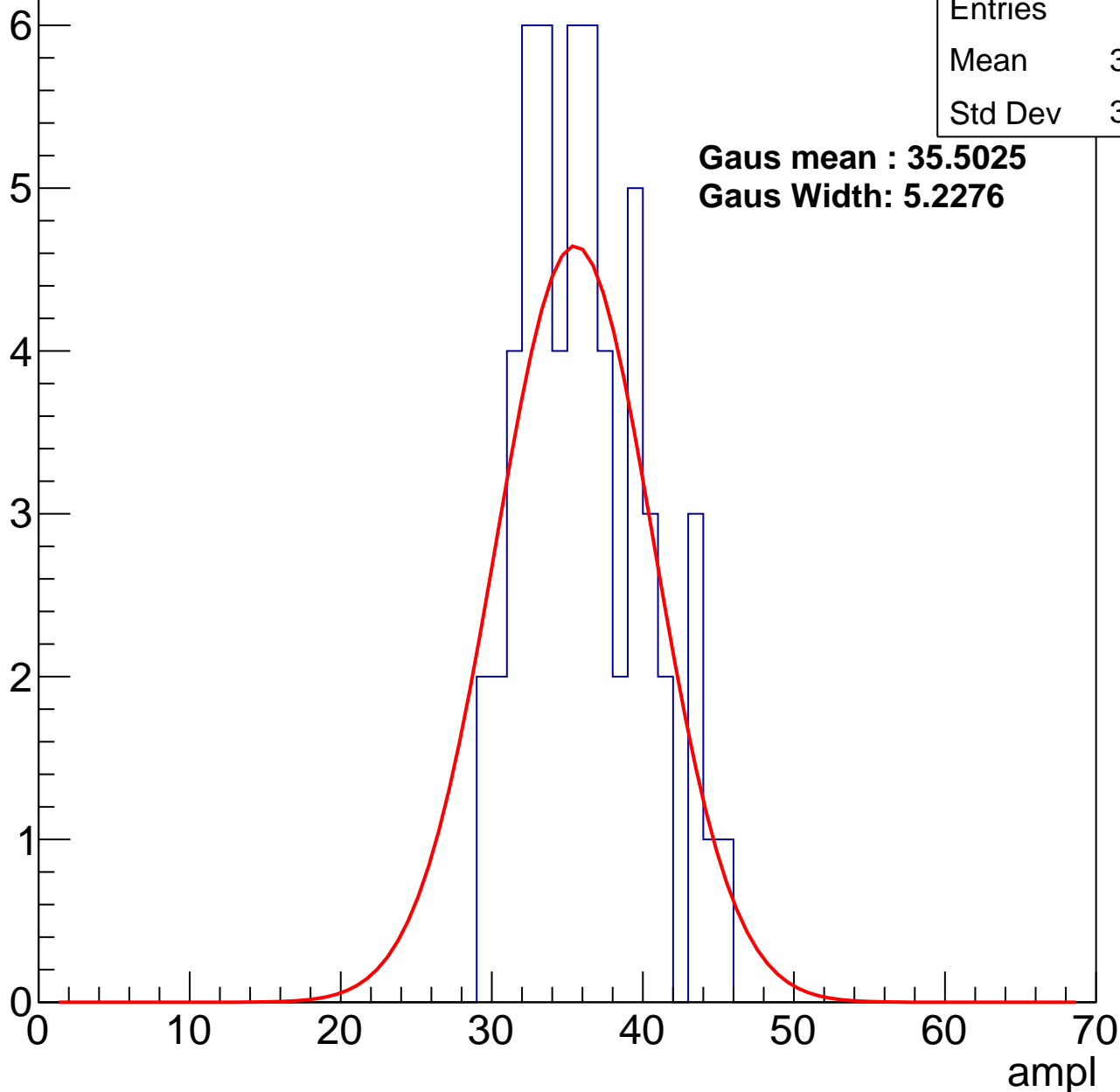
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.67
Std Dev	3.935

Gaus mean : 35.5025

Gaus Width: 5.2276



B1L101S, U17-ch101, adc2

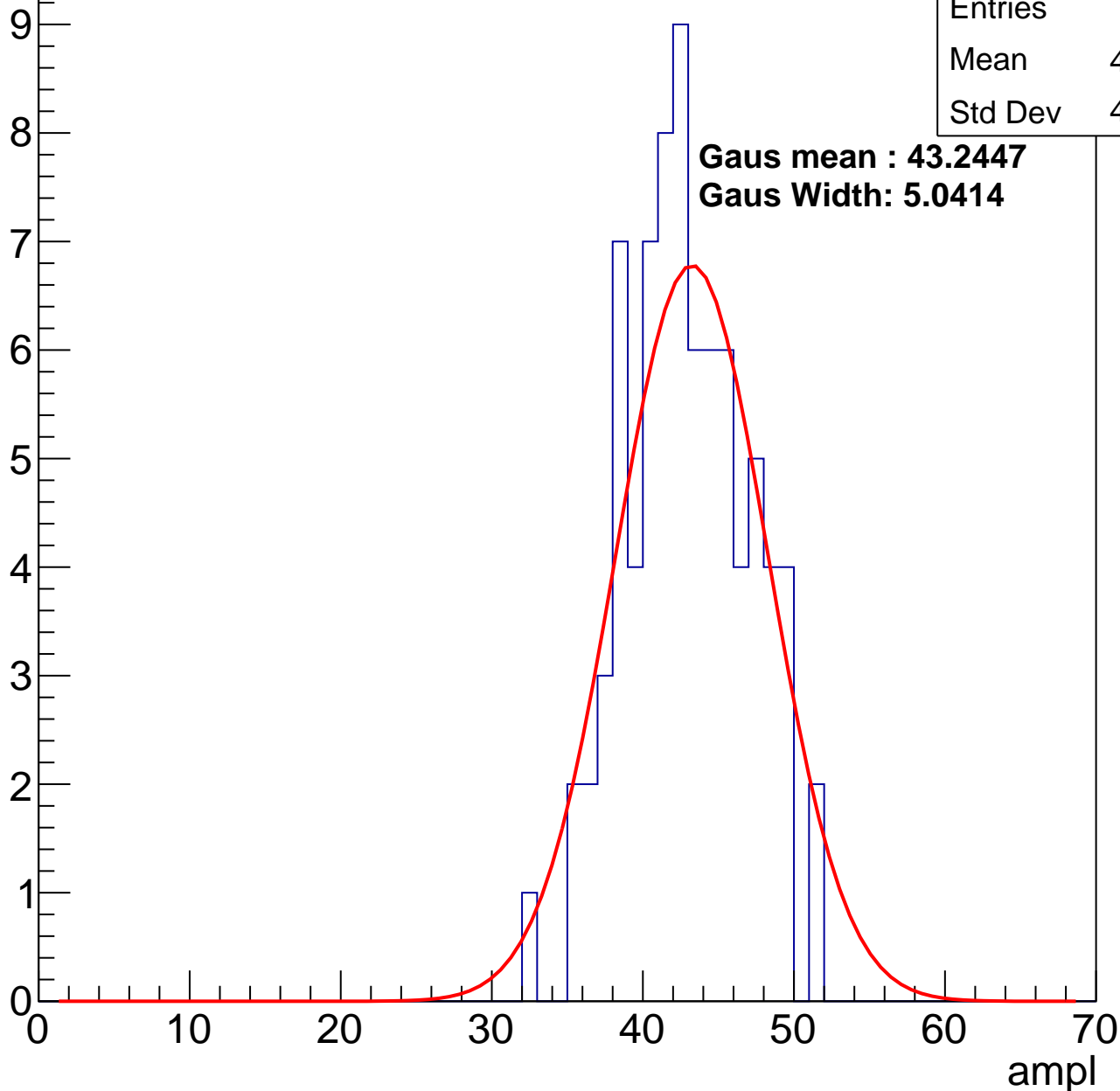
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	42.42
Std Dev	4.015

Gaus mean : 43.2447

Gaus Width: 5.0414

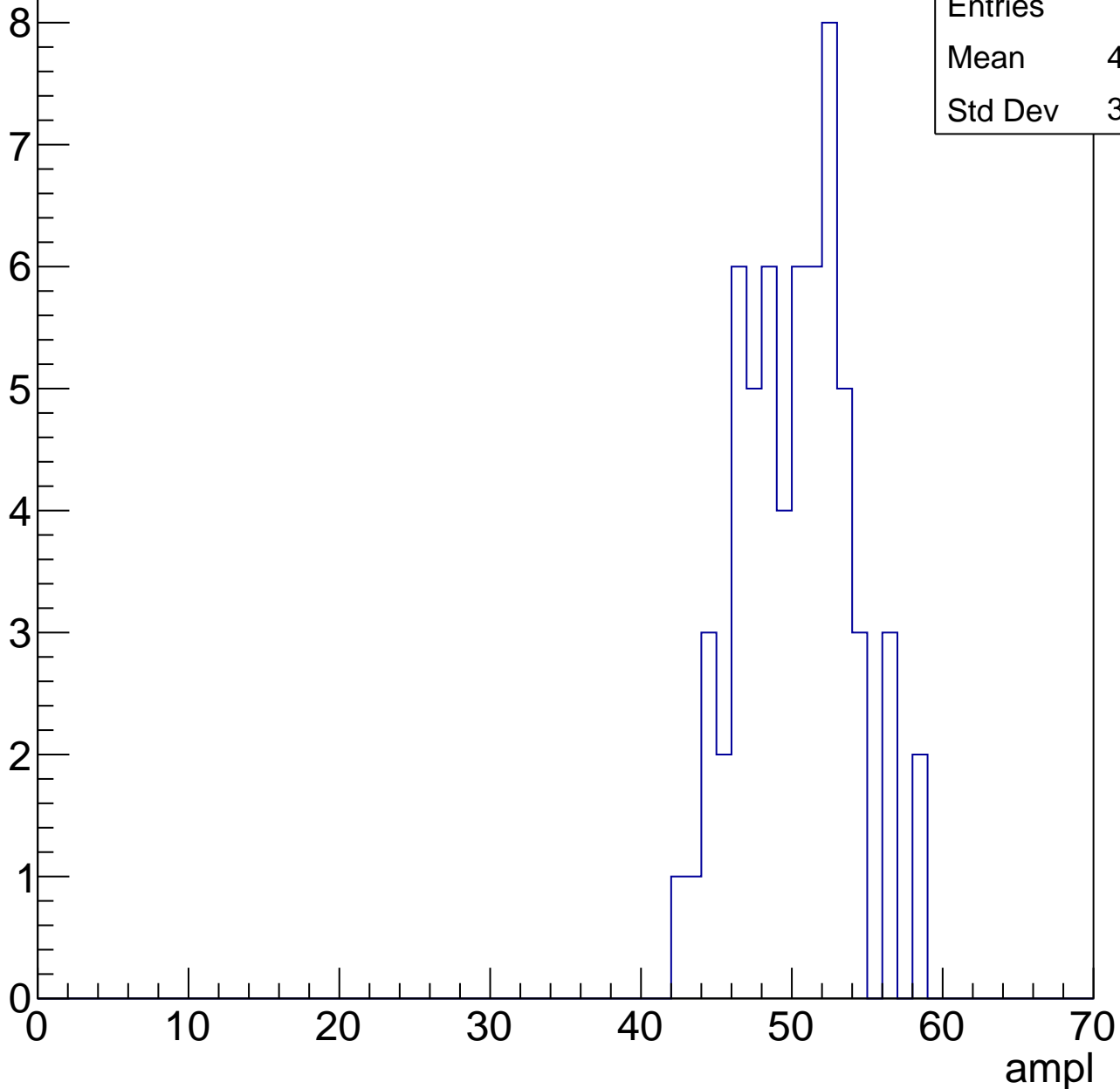


B1L101S, U17-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.75
Std Dev	3.629

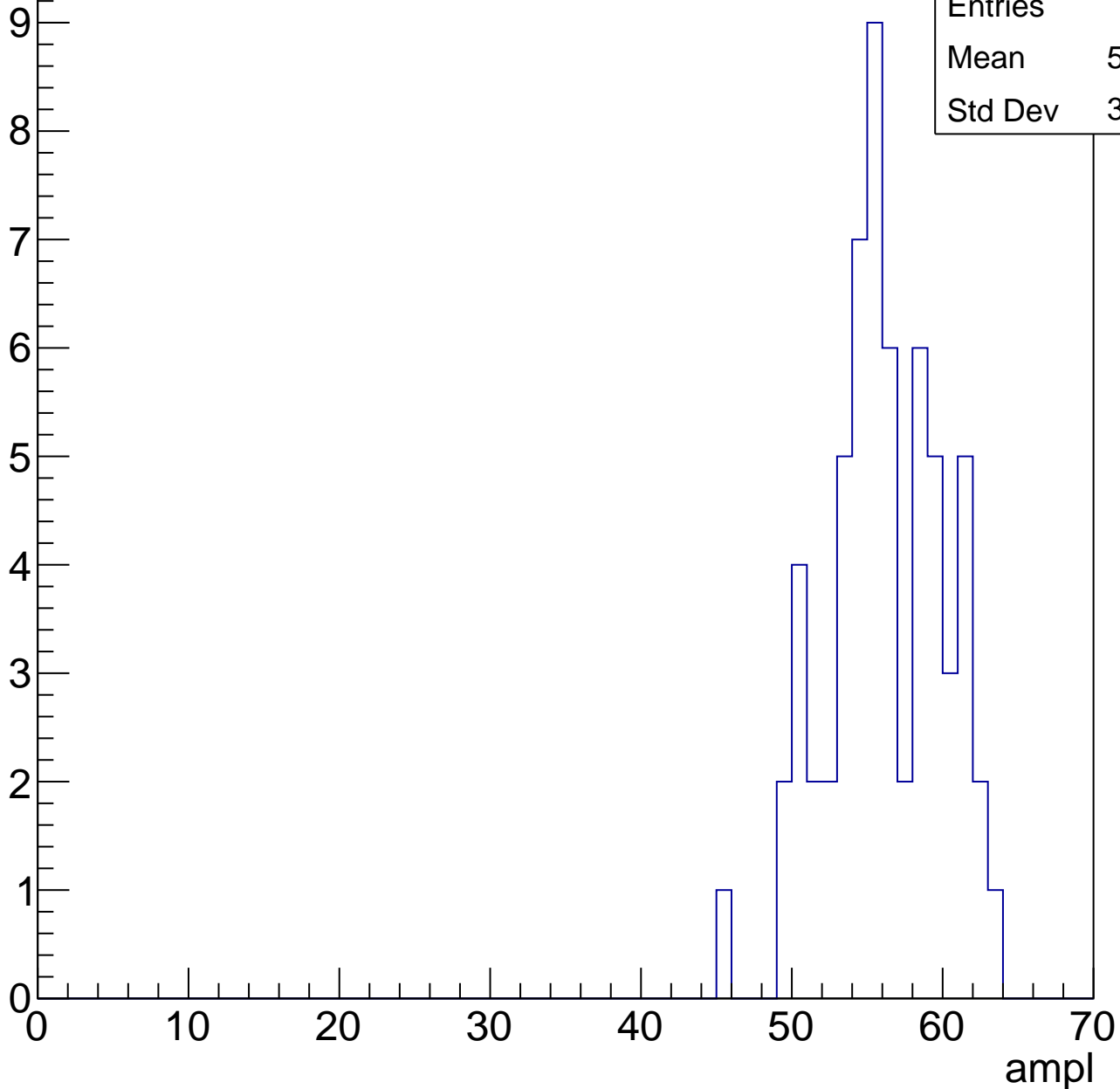


B1L101S, U17-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

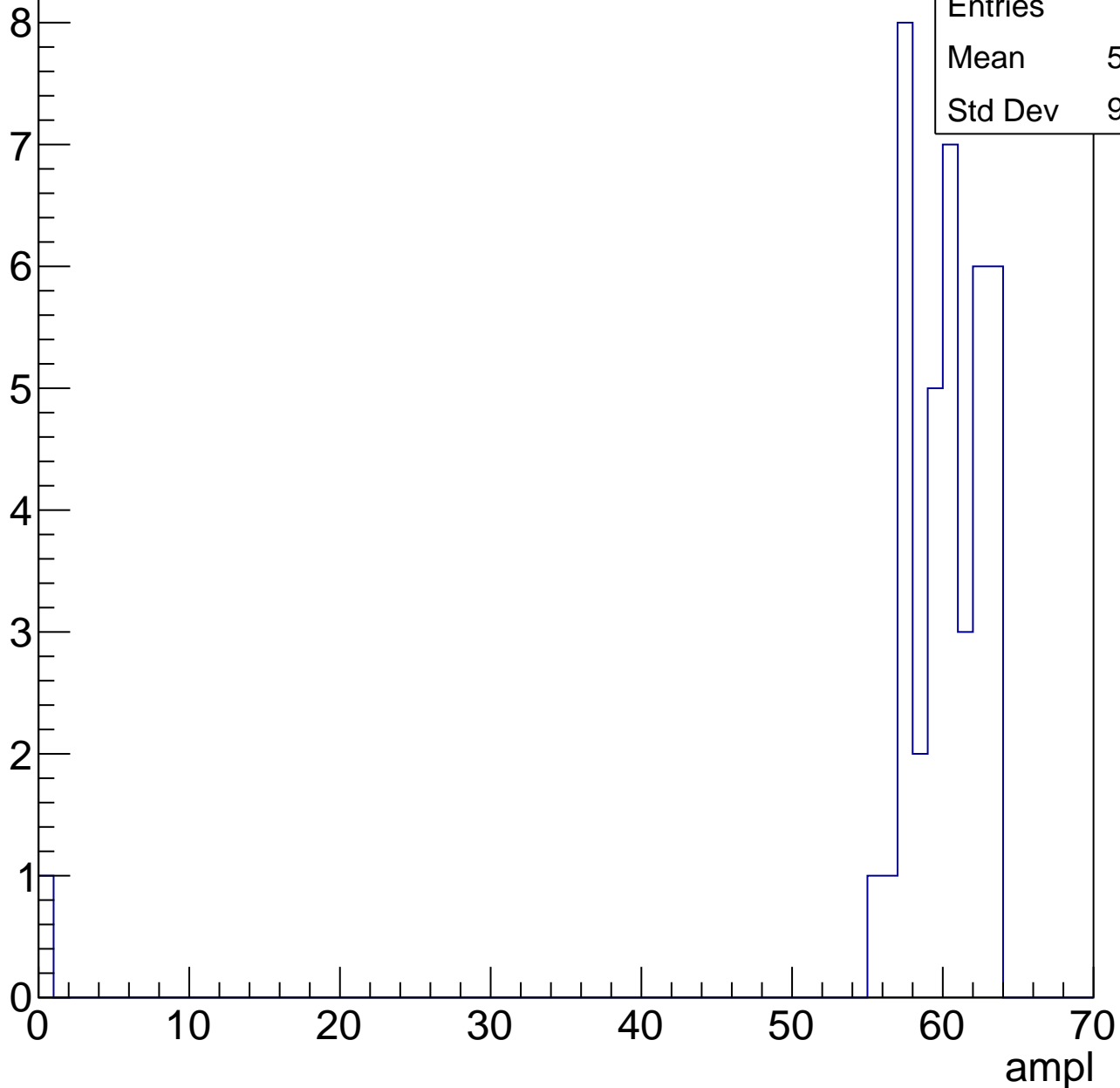
Entries	62
Mean	55.68
Std Dev	3.779



B1L101S, U17-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch102, adc0

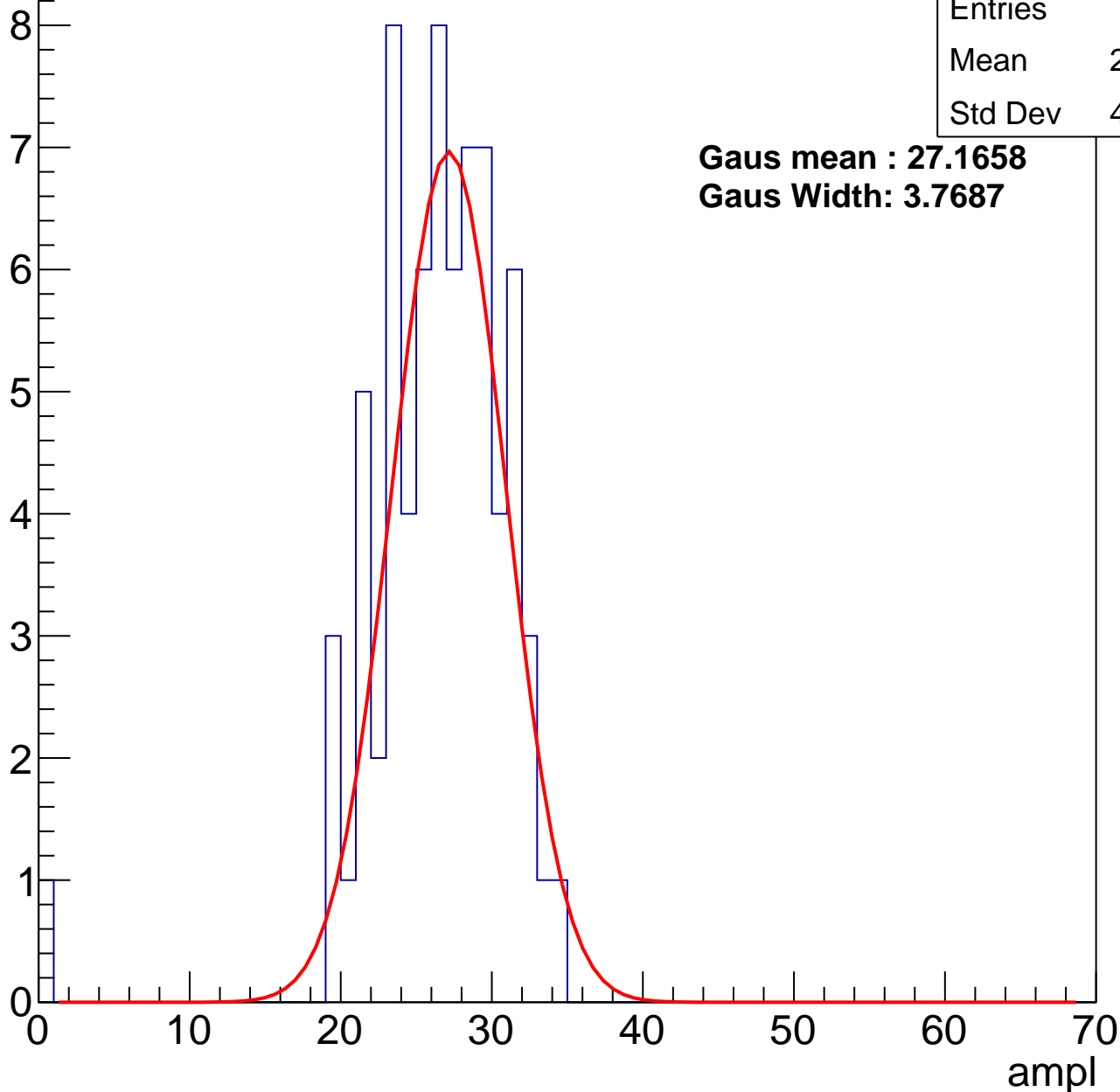
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	25.95
Std Dev	4.742

Gaus mean : 27.1658

Gaus Width: 3.7687



B1L101S, U17-ch102, adc1

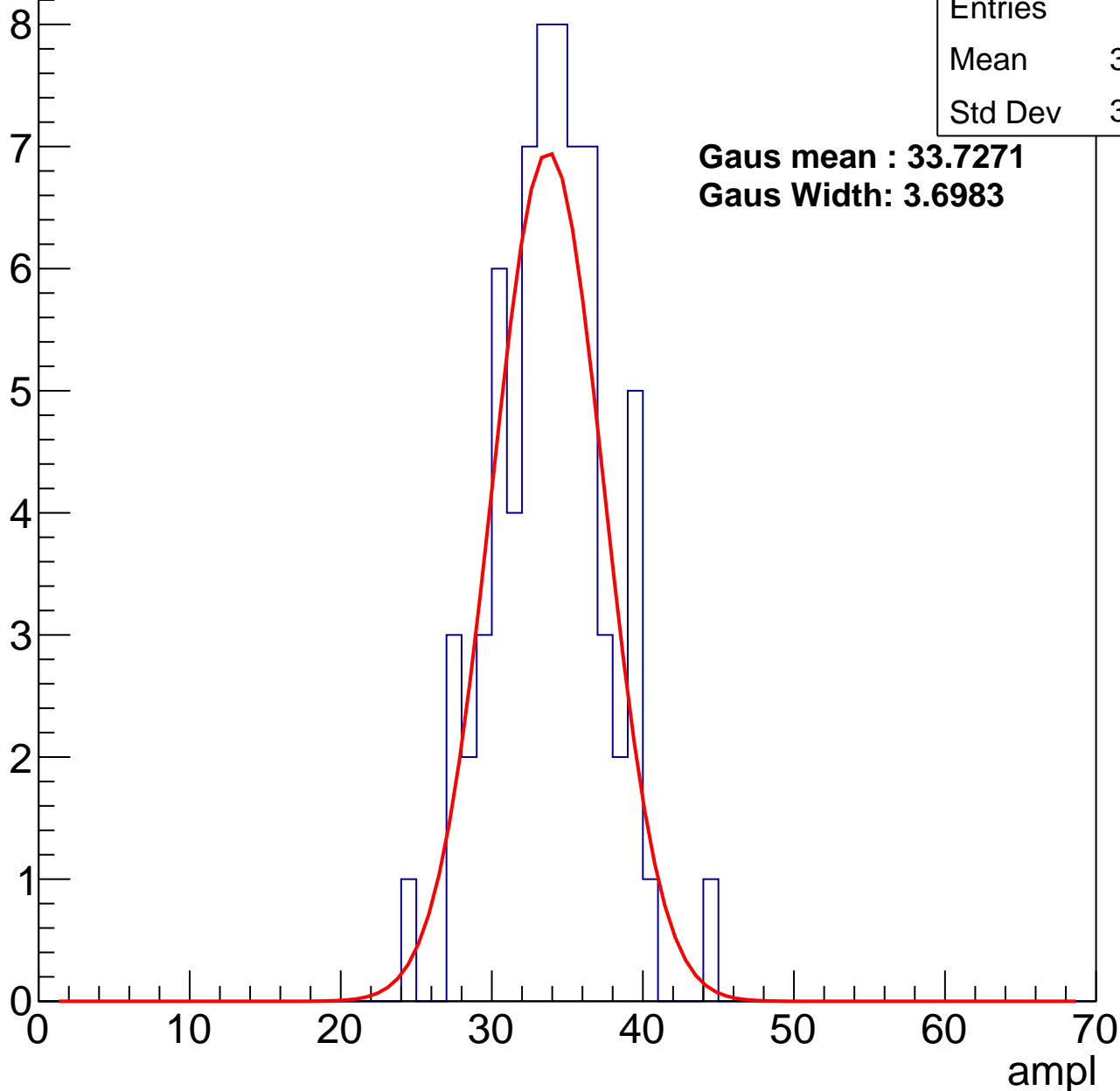
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	33.46
Std Dev	3.632

Gaus mean : 33.7271

Gaus Width: 3.6983



B1L101S, U17-ch102, adc2

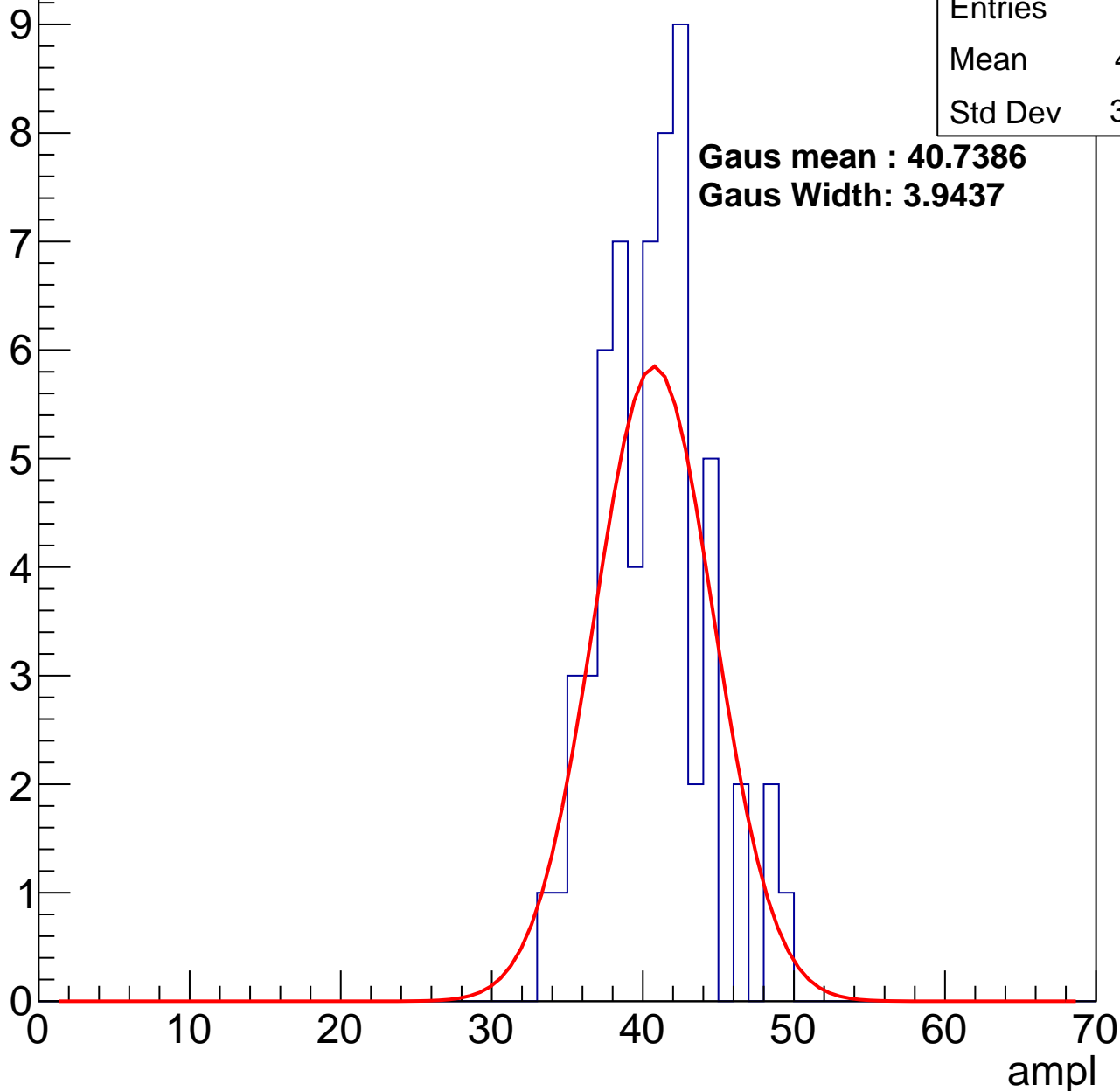
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	40.21
Std Dev	3.422

Gaus mean : 40.7386

Gaus Width: 3.9437

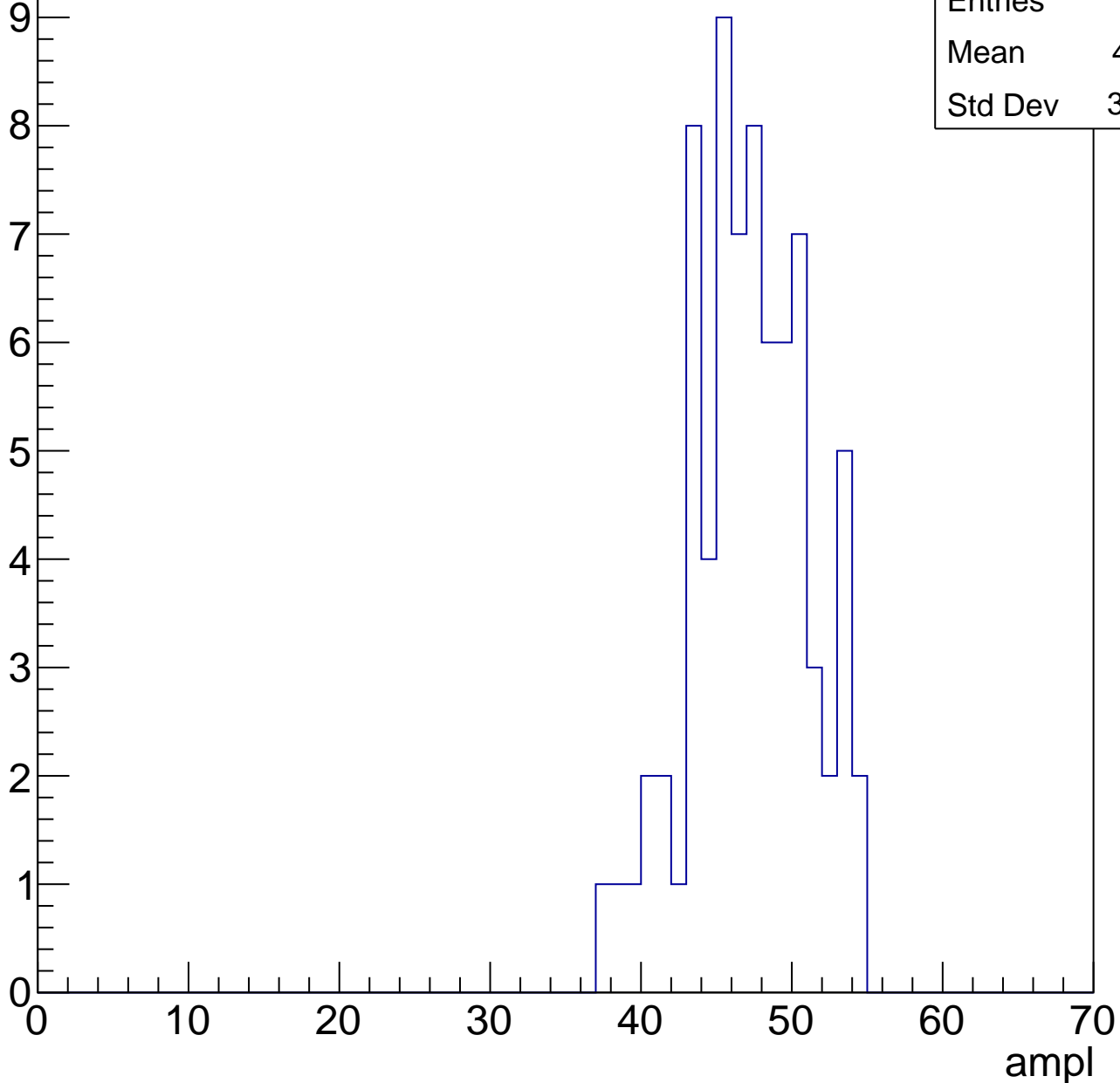


B1L101S, U17-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	46.71
Std Dev	3.846

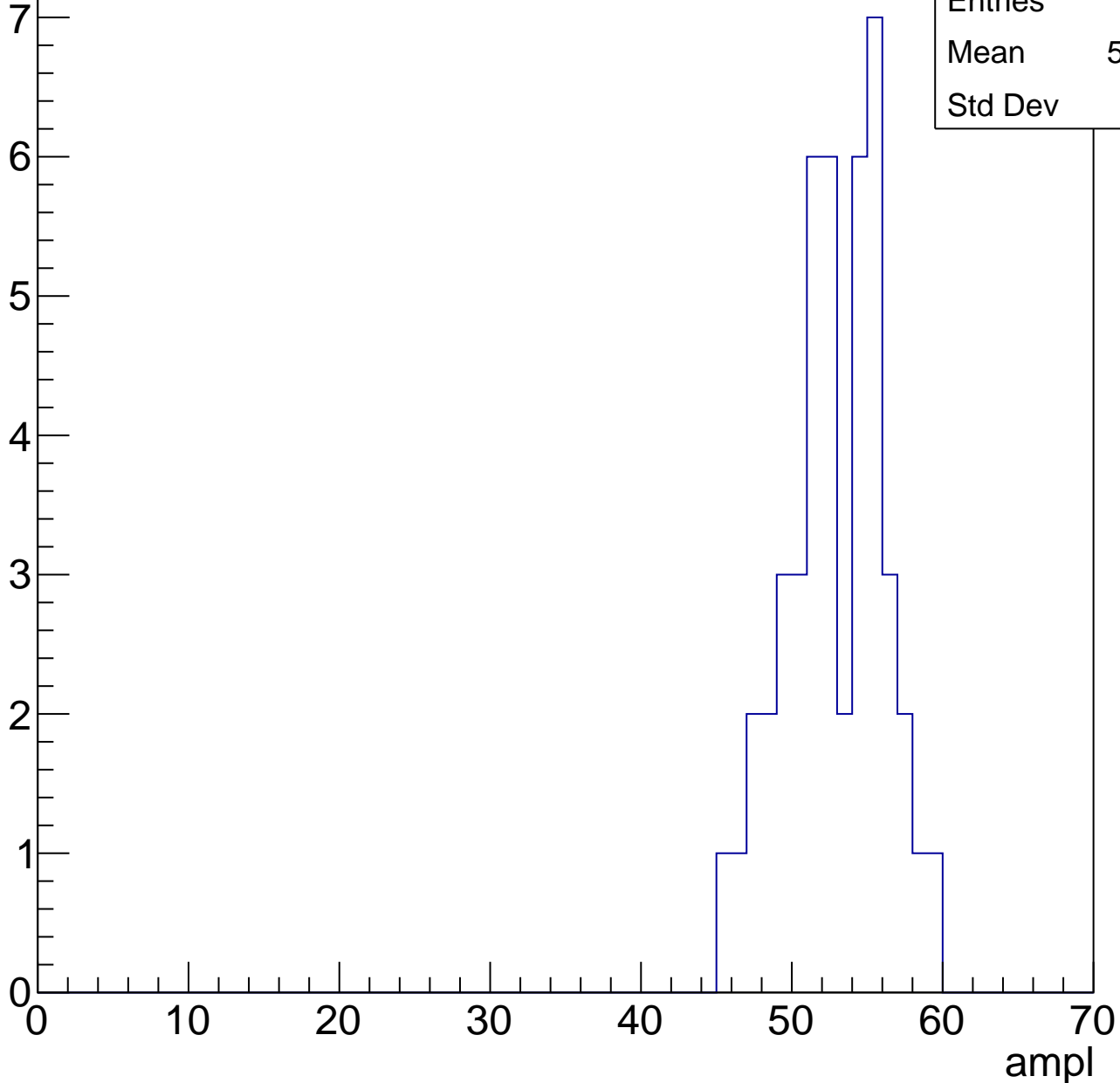


B1L101S, U17-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	52.39
Std Dev	3.22

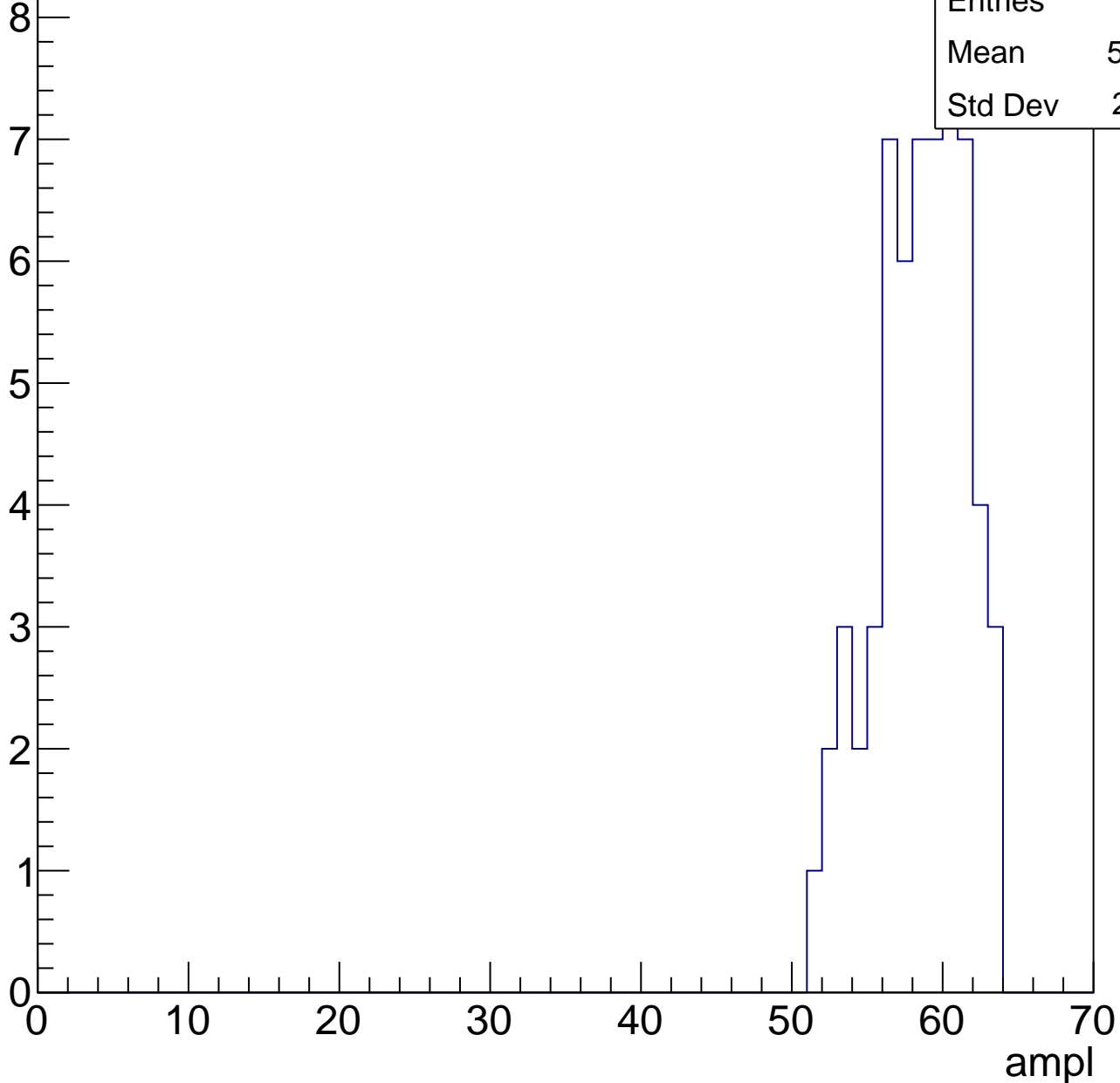


B1L101S, U17-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	58.07
Std Dev	2.971

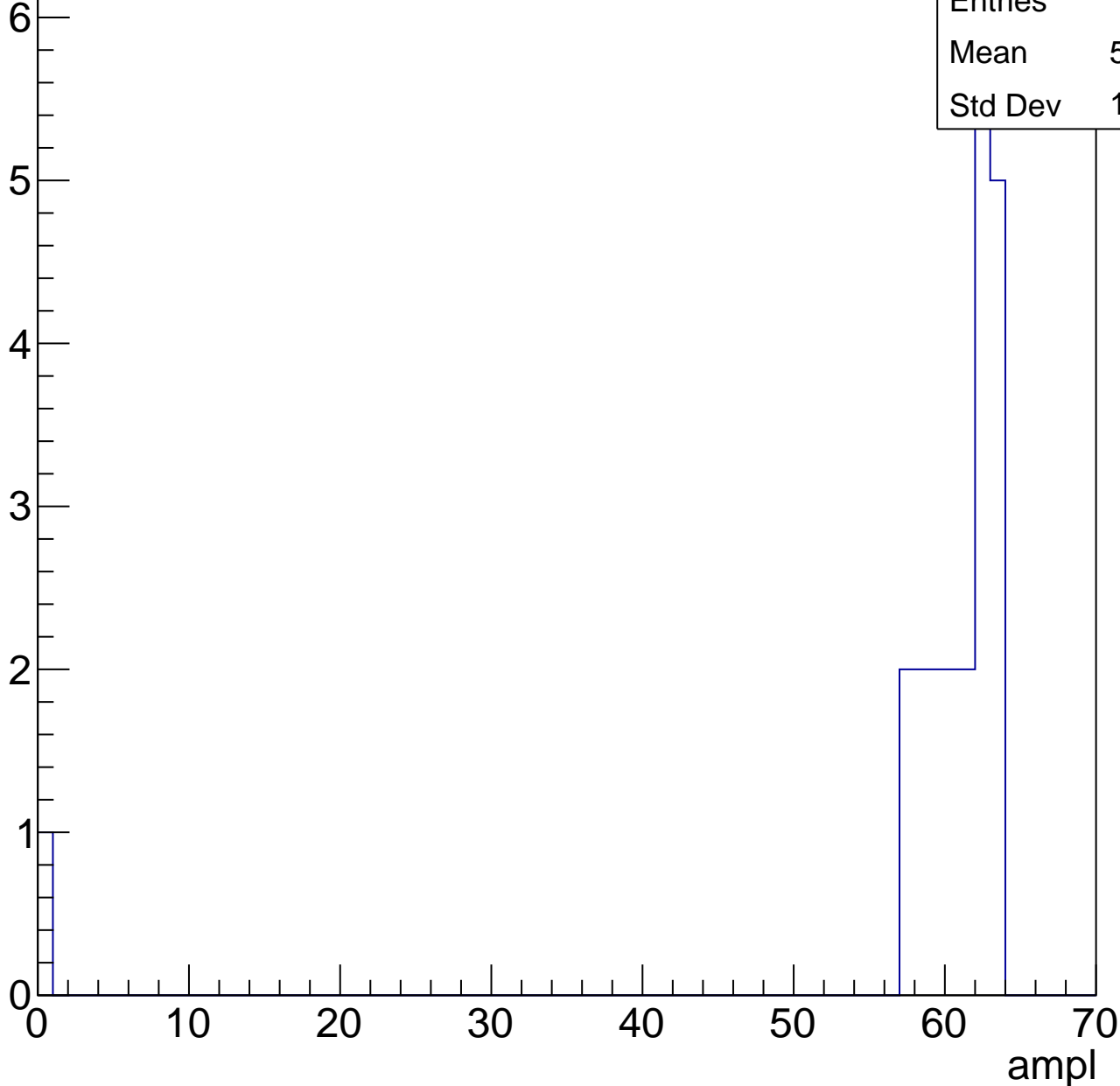


B1L101S, U17-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

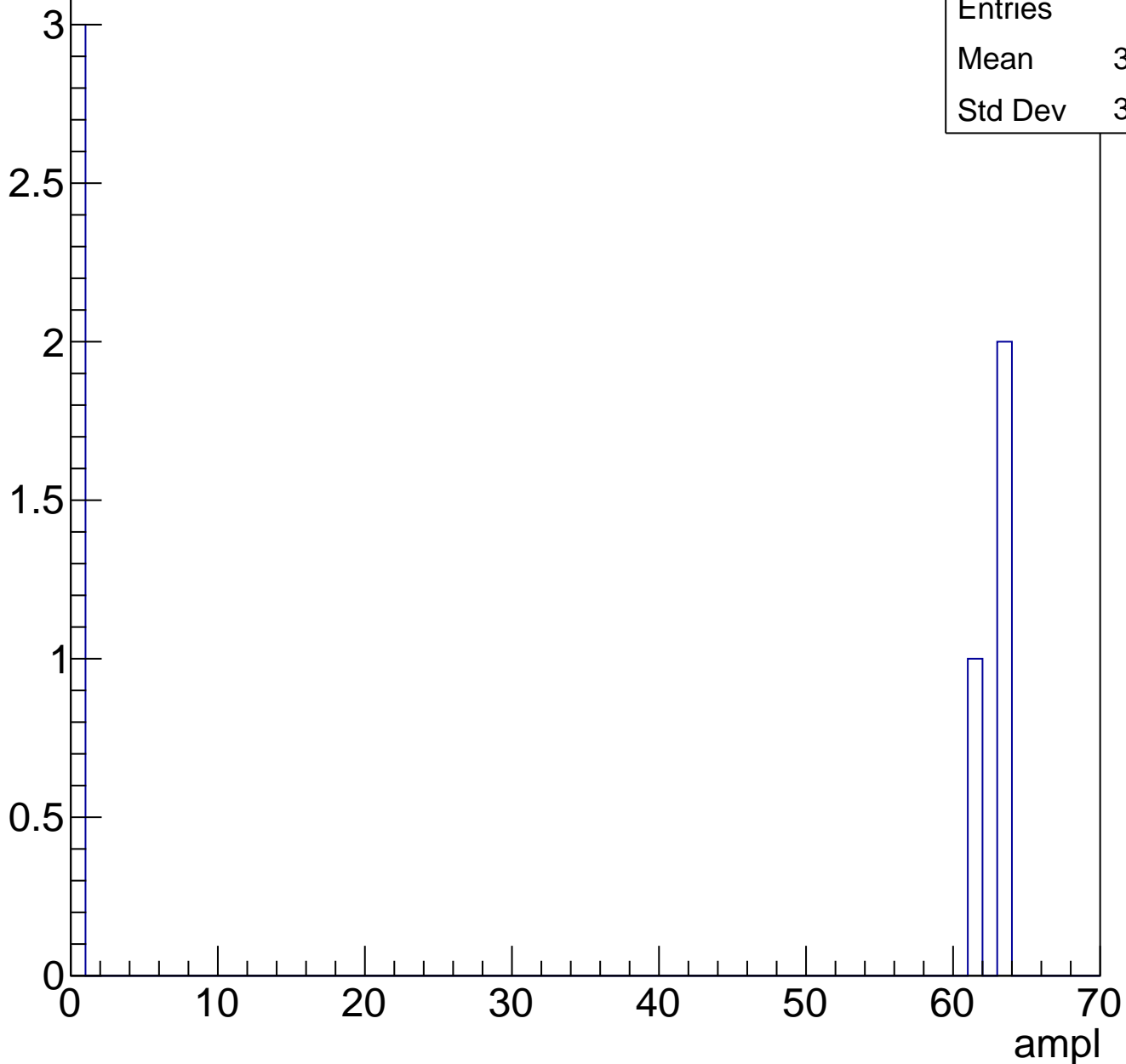
Entries	22
Mean	58.05
Std Dev	12.82



B1L101S, U17-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch103, adc0

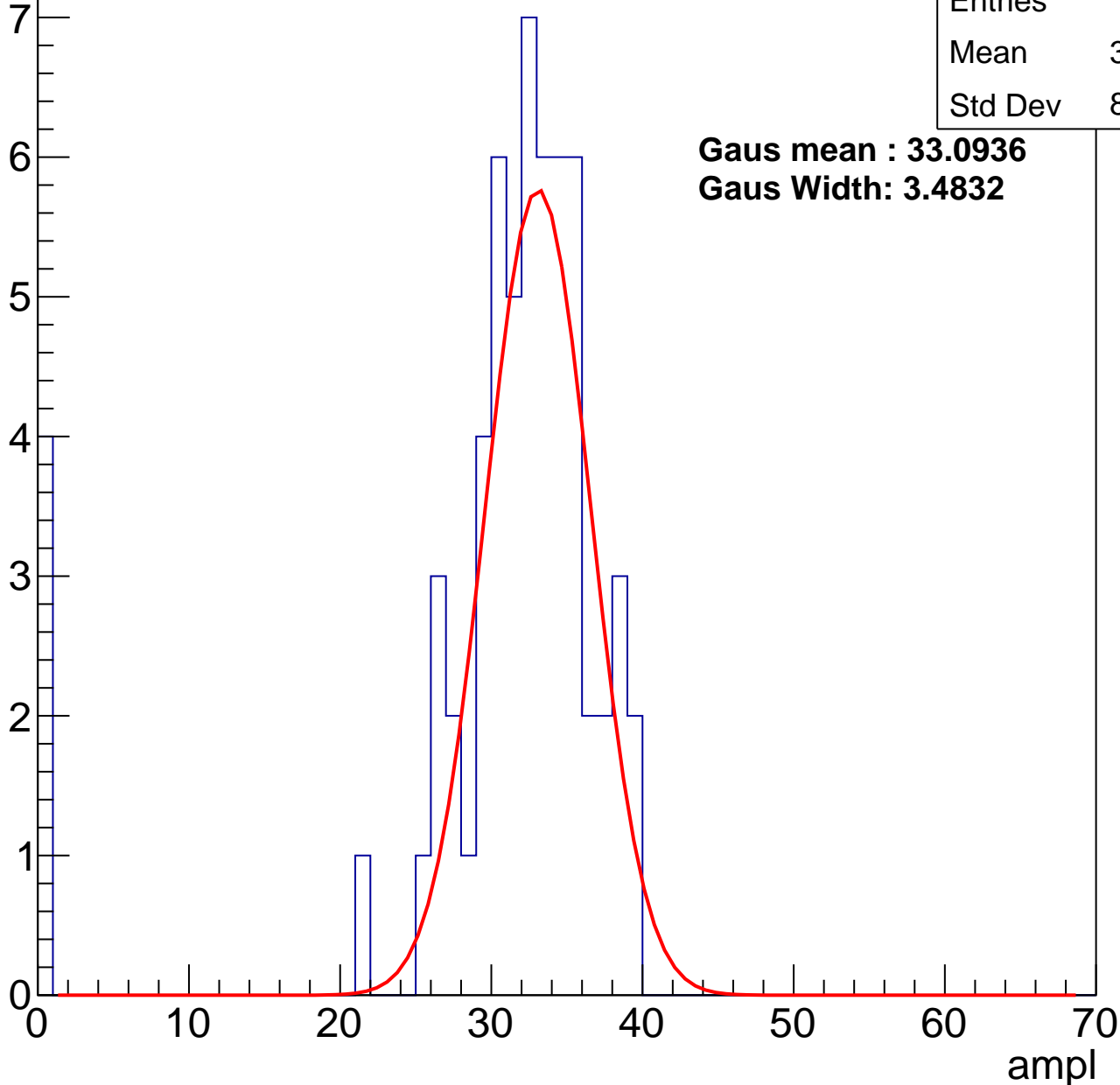
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	30.02
Std Dev	8.719

Gaus mean : 33.0936

Gaus Width: 3.4832



B1L101S, U17-ch103, adc1

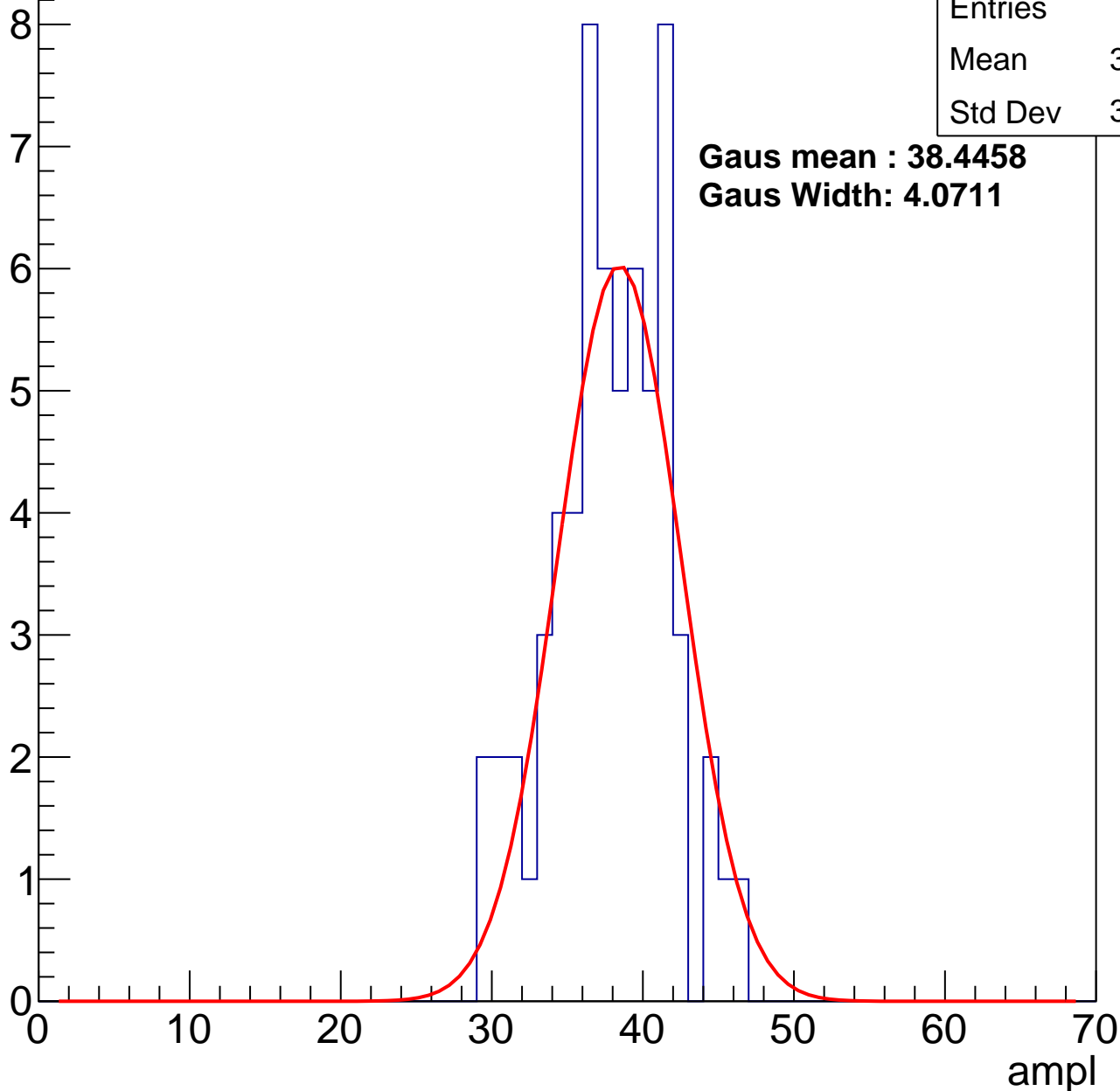
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.37
Std Dev	3.868

Gaus mean : 38.4458

Gaus Width: 4.0711



B1L101S, U17-ch103, adc2

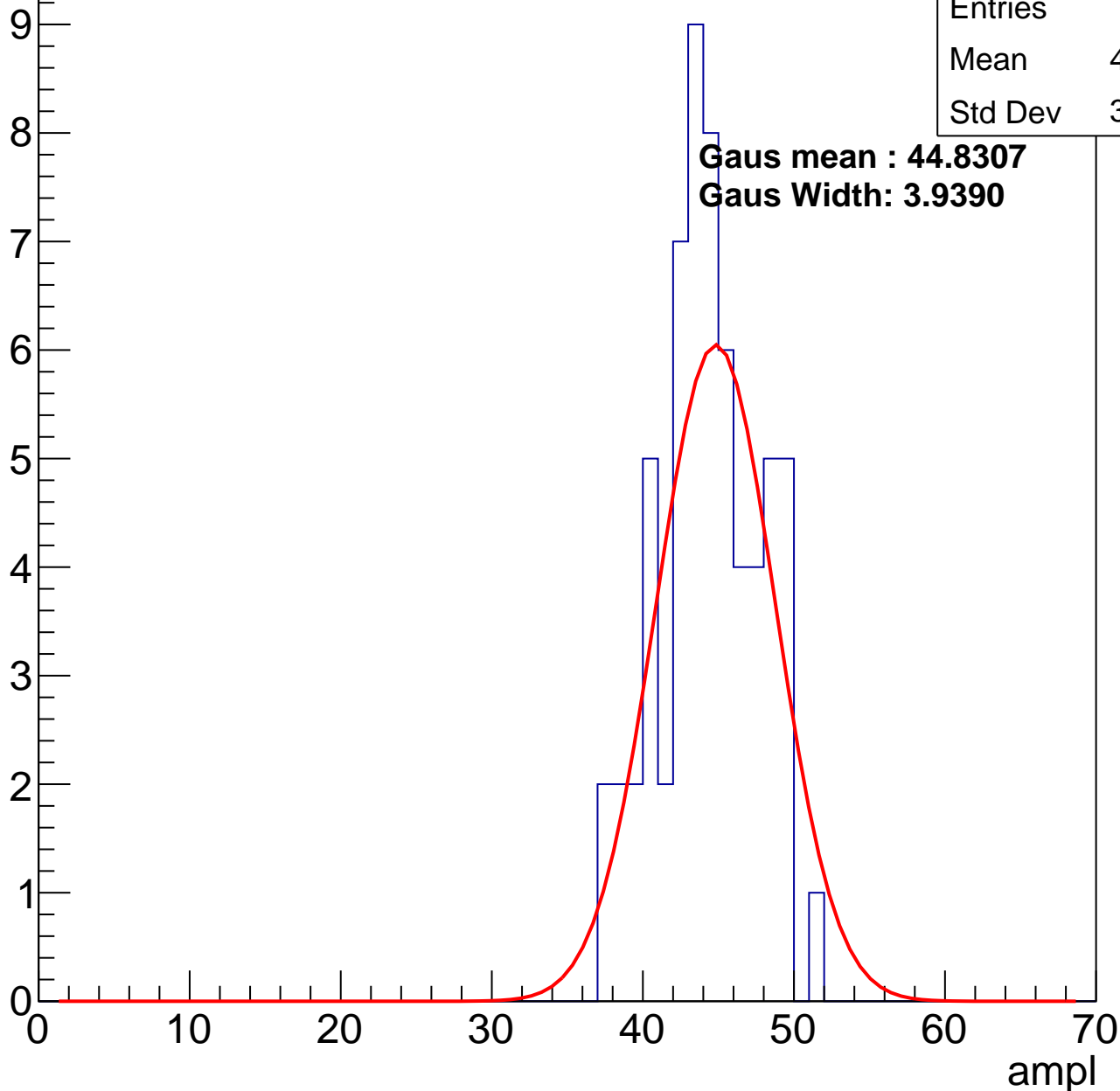
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.89
Std Dev	3.278

Gaus mean : 44.8307

Gaus Width: 3.9390

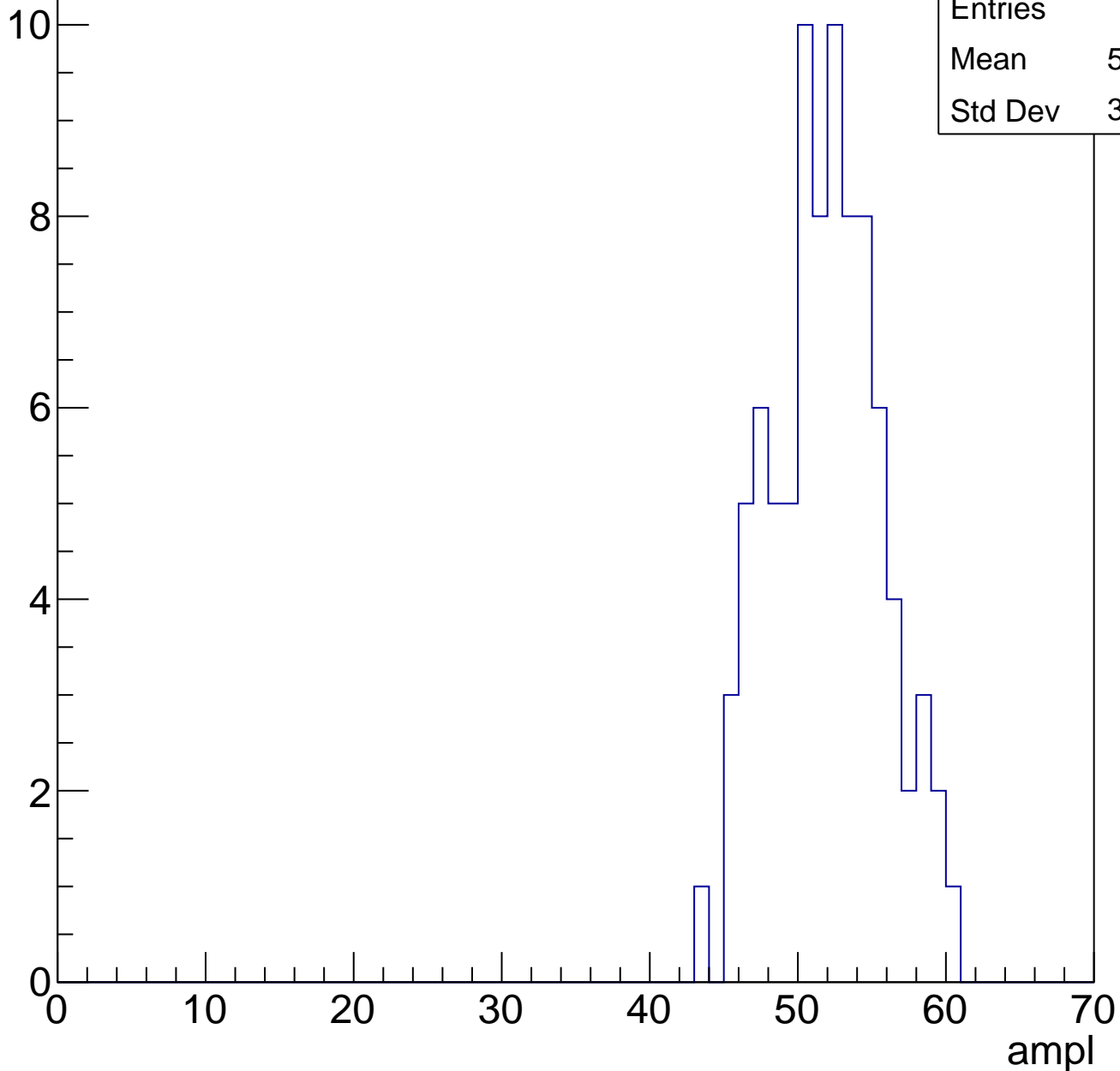


B1L101S, U17-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	51.48
Std Dev	3.692

Entry

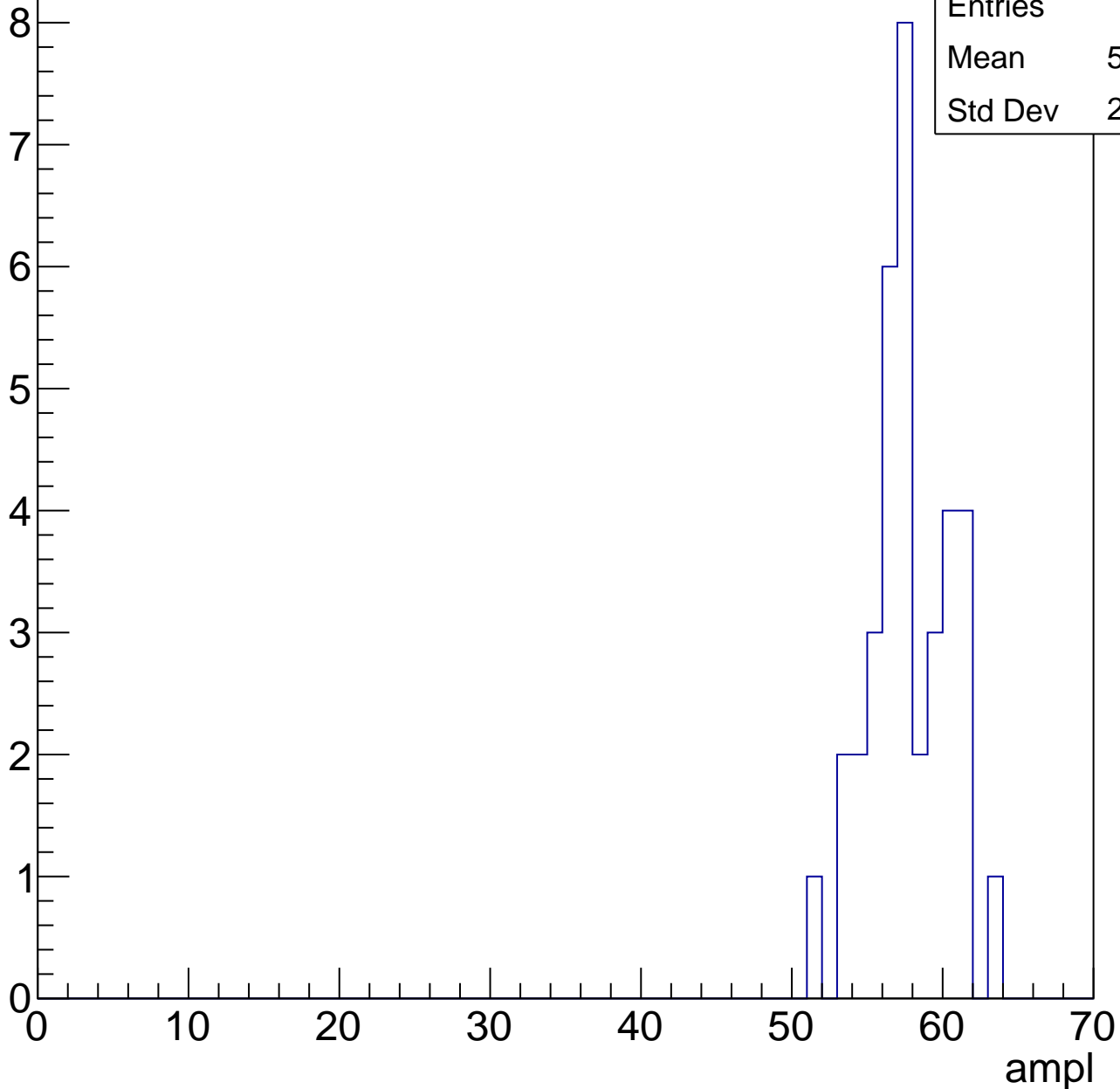


B1L101S, U17-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	57.28
Std Dev	2.642



B1L101S, U17-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	37
Mean	58.97
Std Dev	10

Entry

10

8

6

4

2

0

0

10

20

30

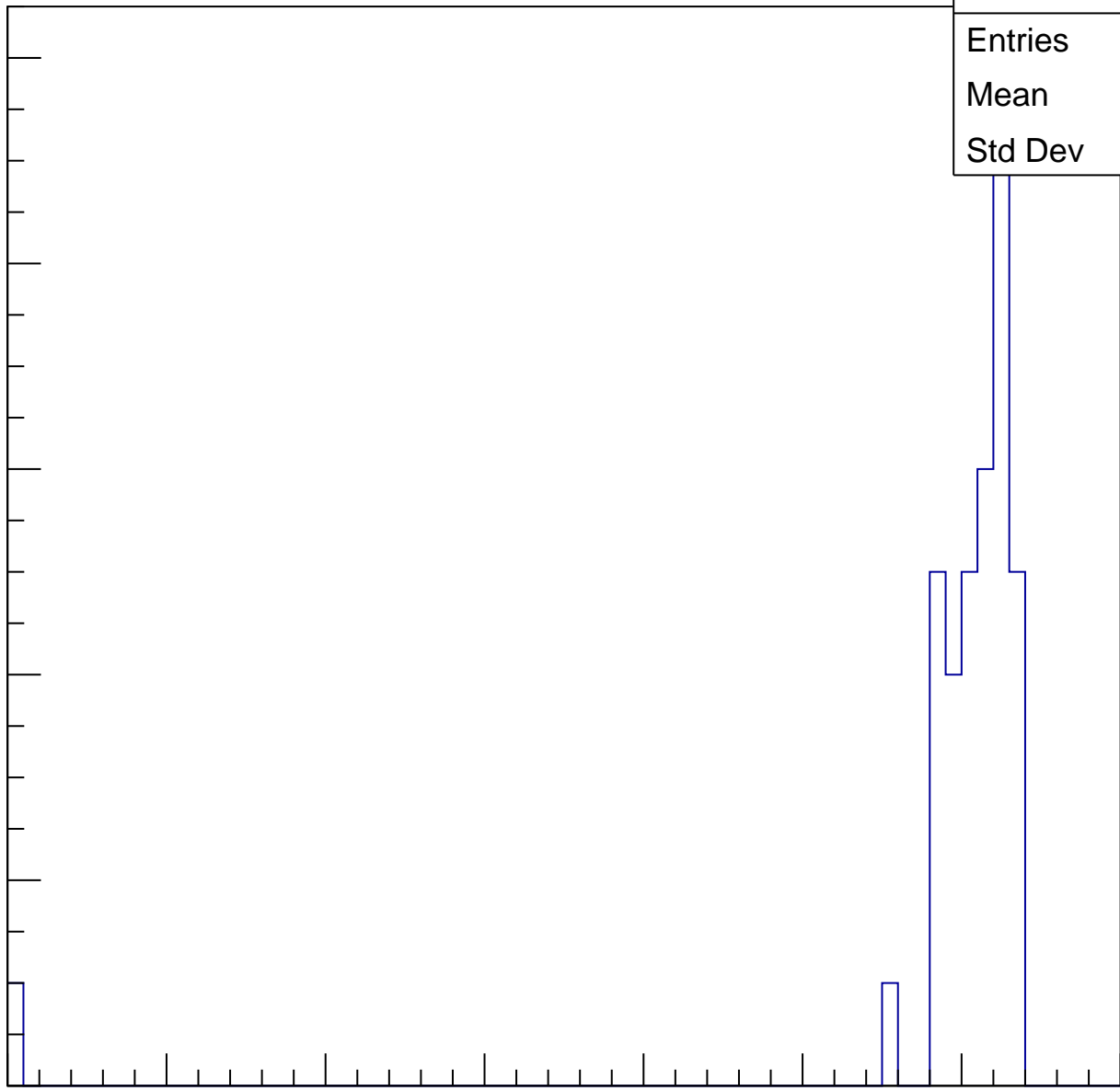
40

50

60

70

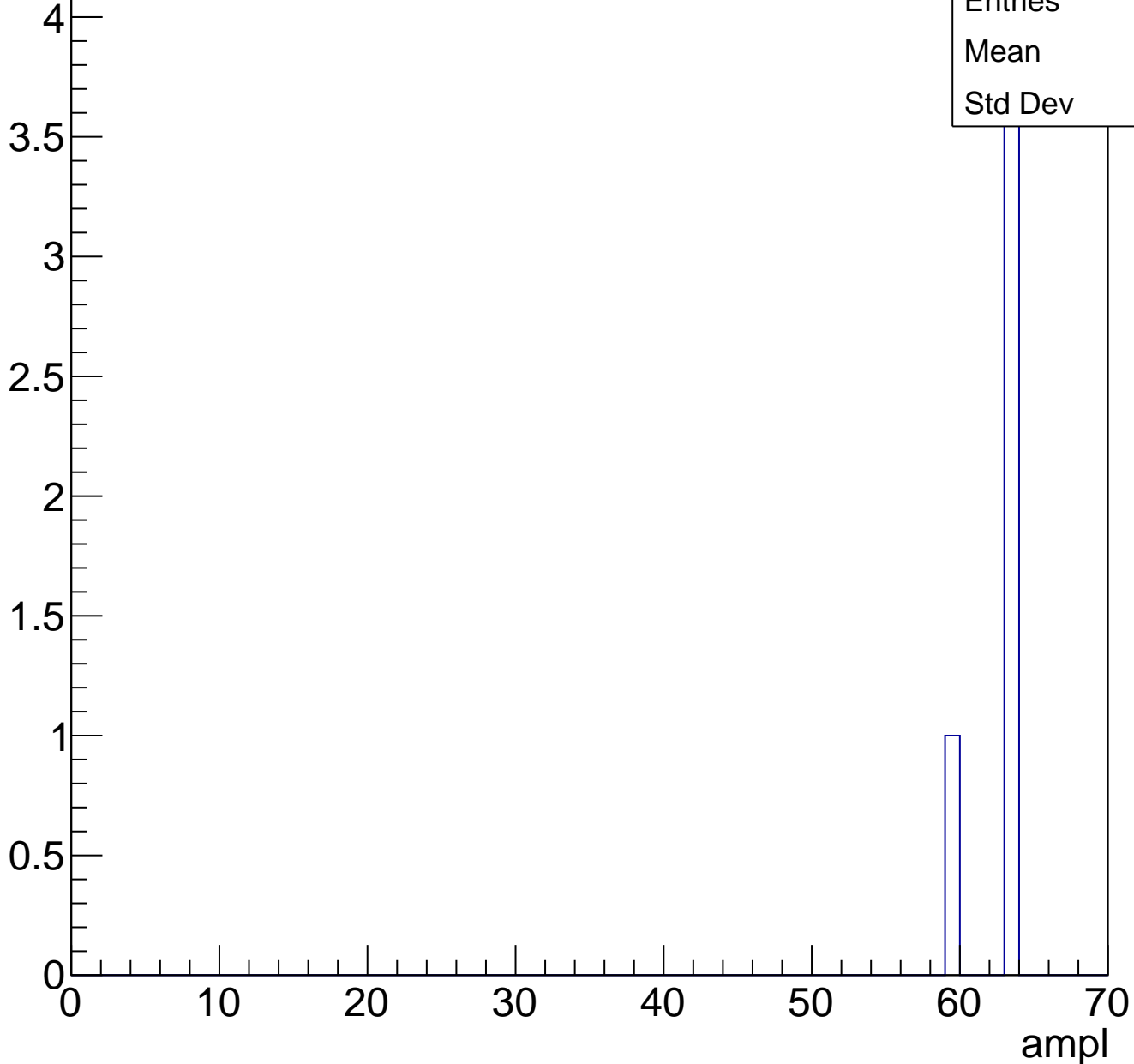
ampl



B1L101S, U17-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch104, adc0

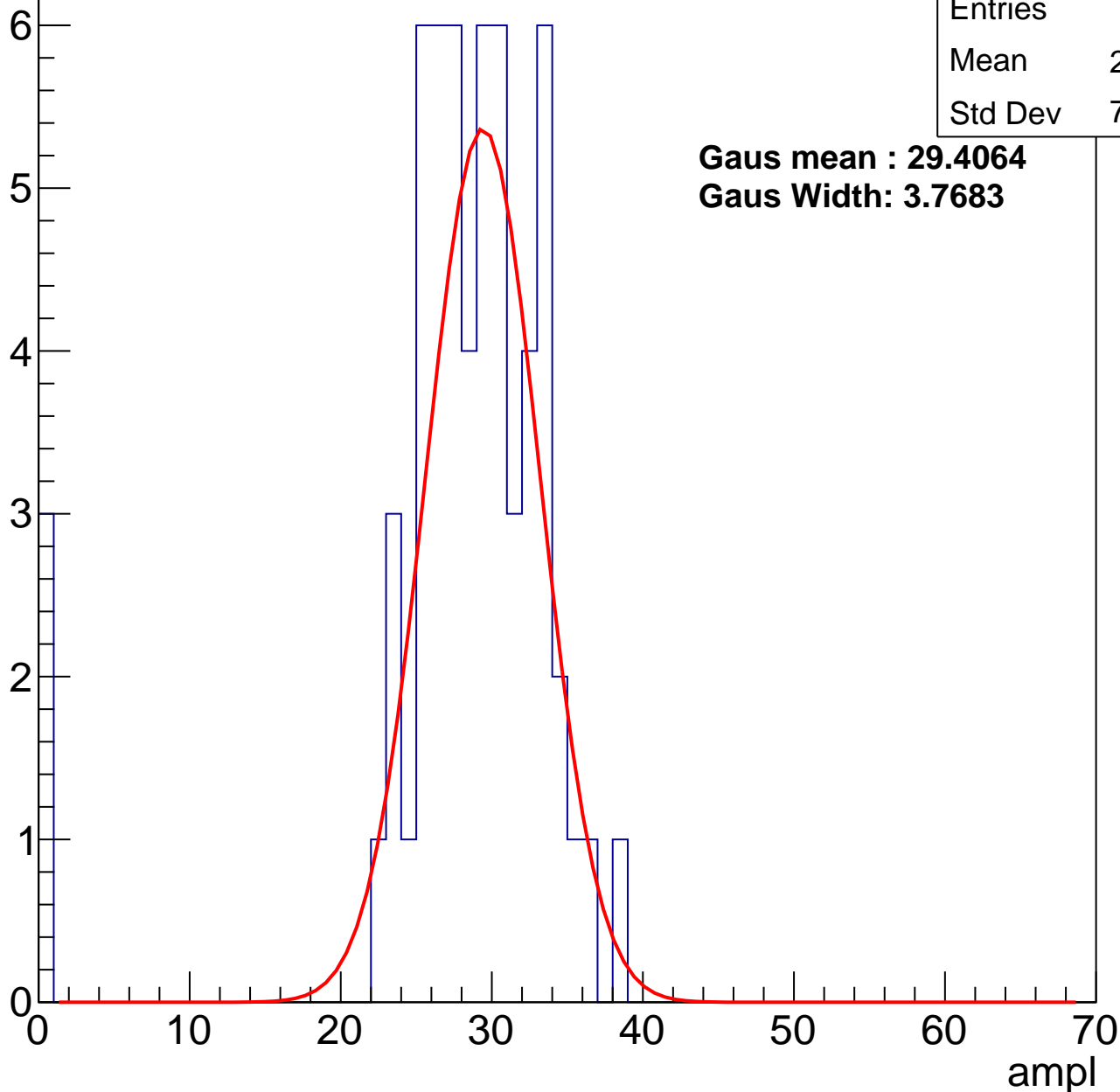
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	27.42
Std Dev	7.186

Gaus mean : 29.4064

Gaus Width: 3.7683



B1L101S, U17-ch104, adc1

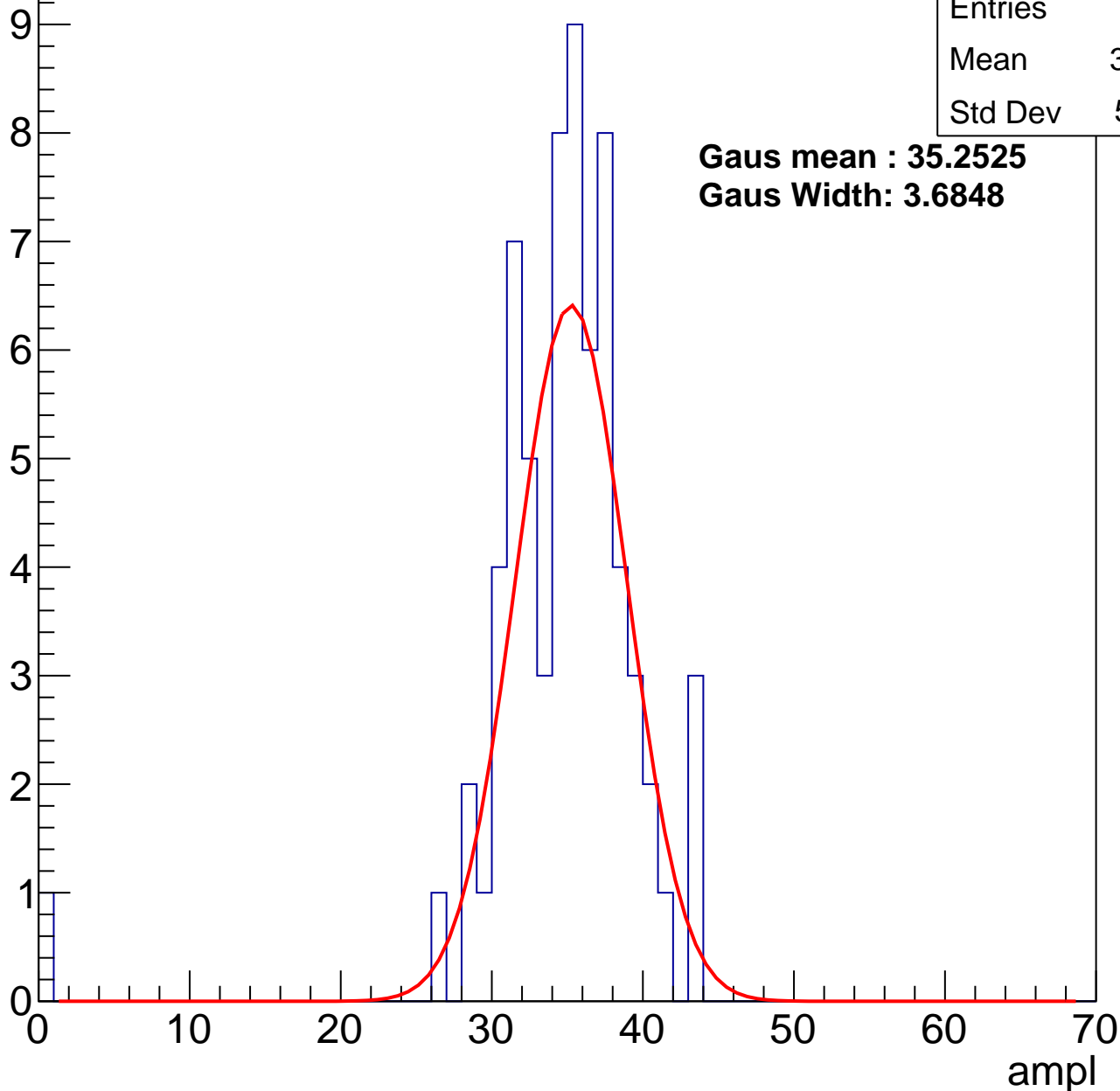
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	34.19
Std Dev	5.521

Gaus mean : 35.2525

Gaus Width: 3.6848



B1L101S, U17-ch104, adc2

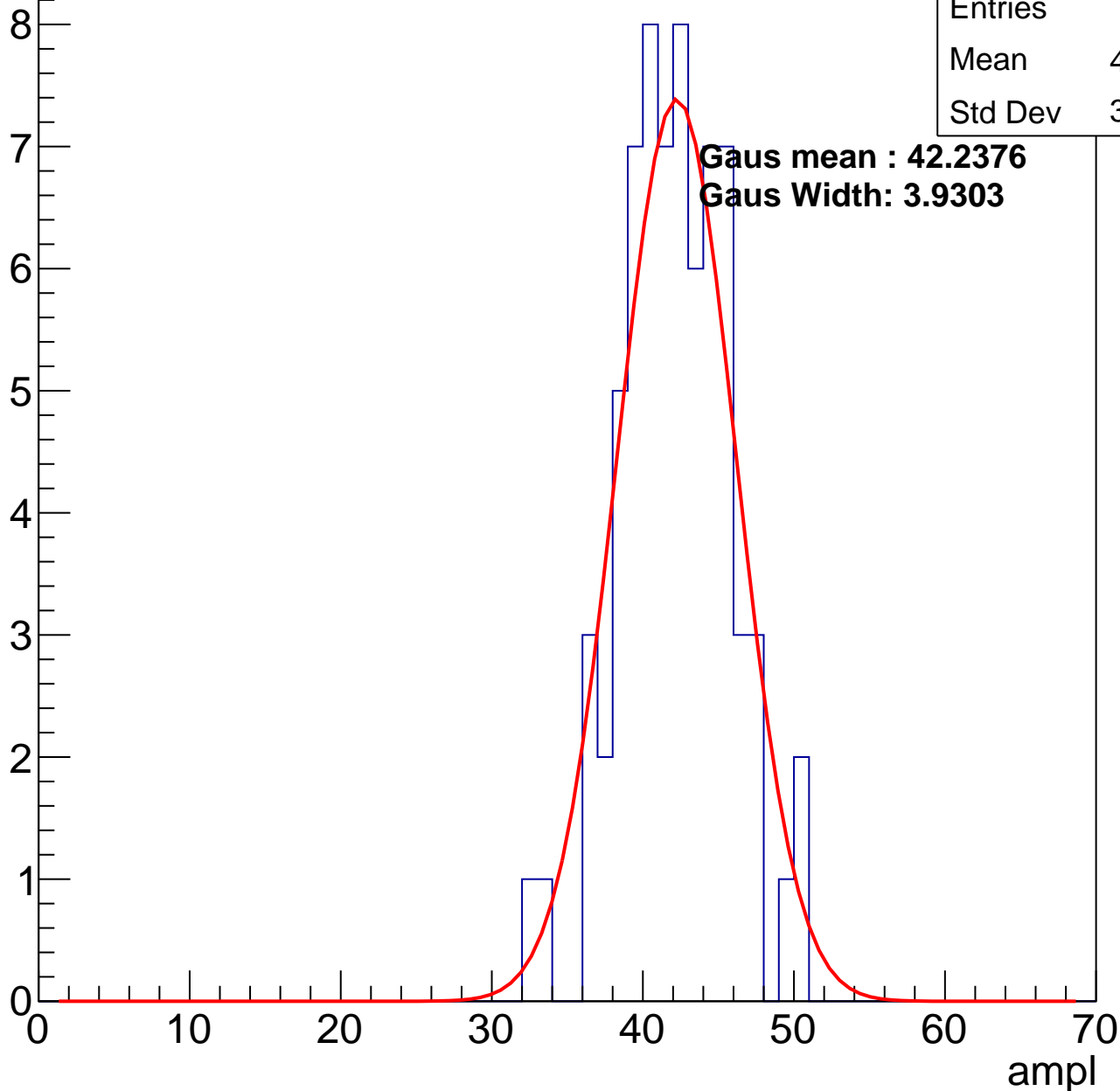
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	41.72
Std Dev	3.585

Gaus mean : 42.2376

Gaus Width: 3.9303

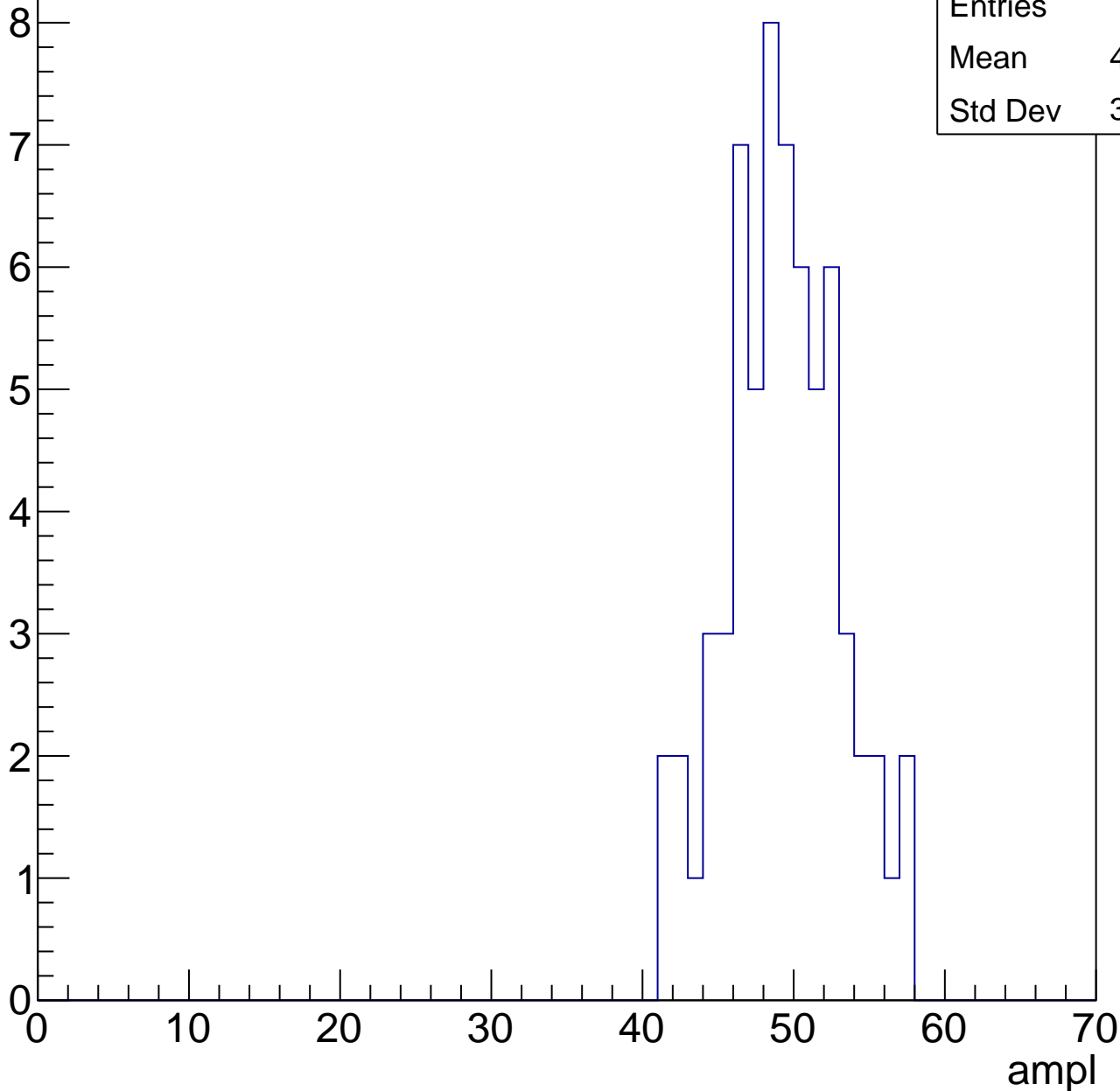


B1L101S, U17-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.83
Std Dev	3.723

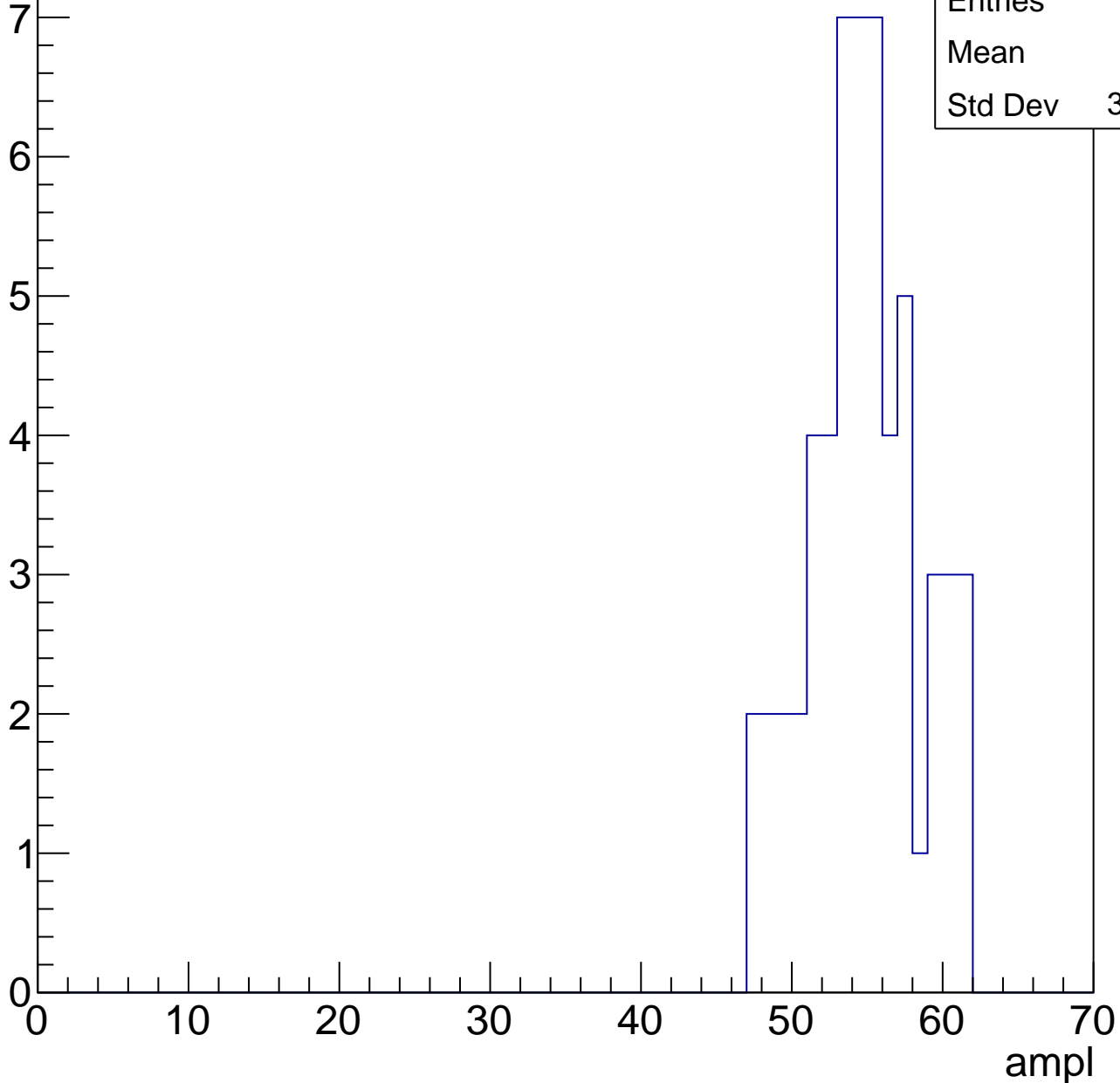


B1L101S, U17-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	54.3
Std Dev	3.585

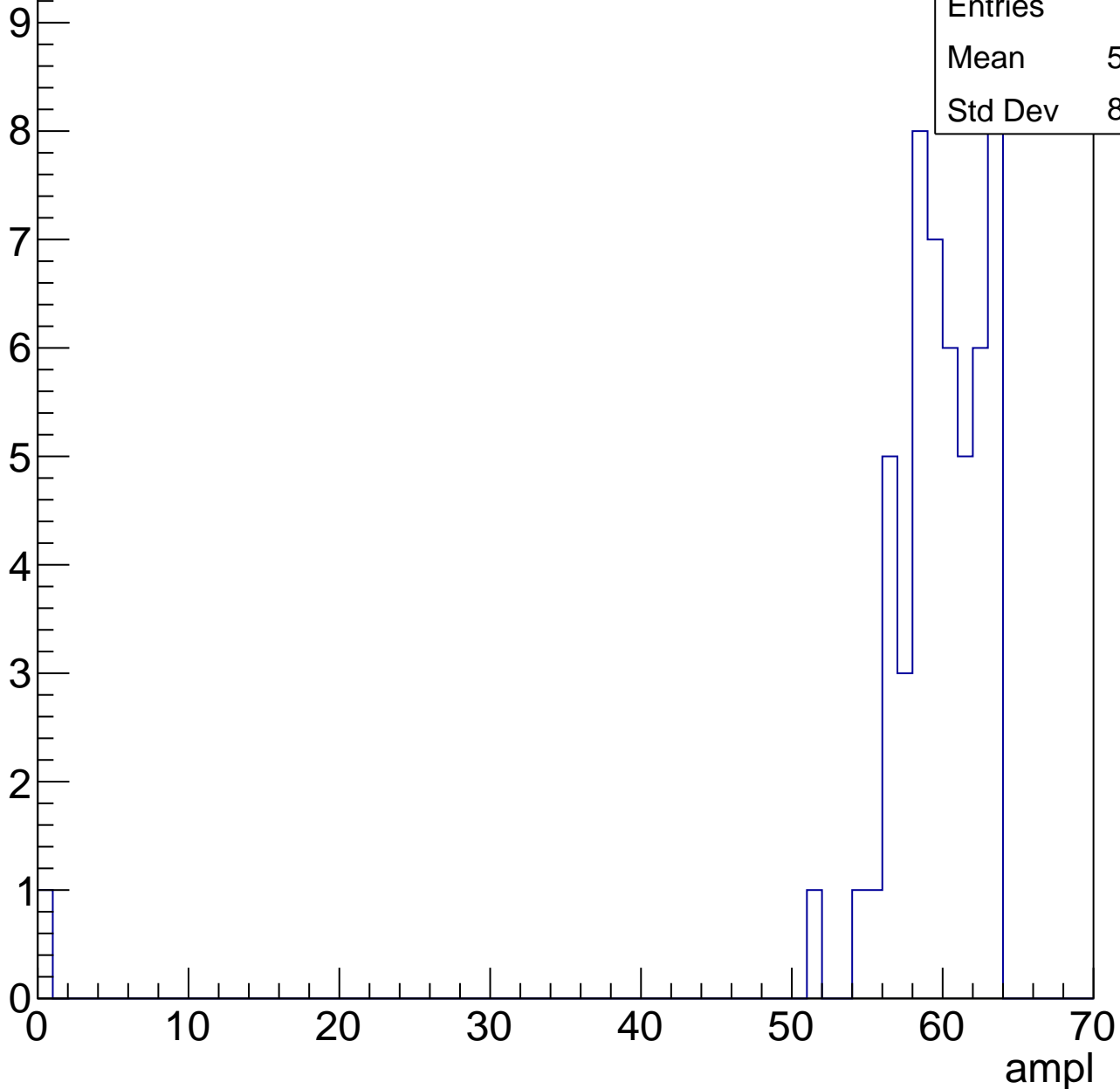


B1L101S, U17-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.34
Std Dev	8.525



B1L101S, U17-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

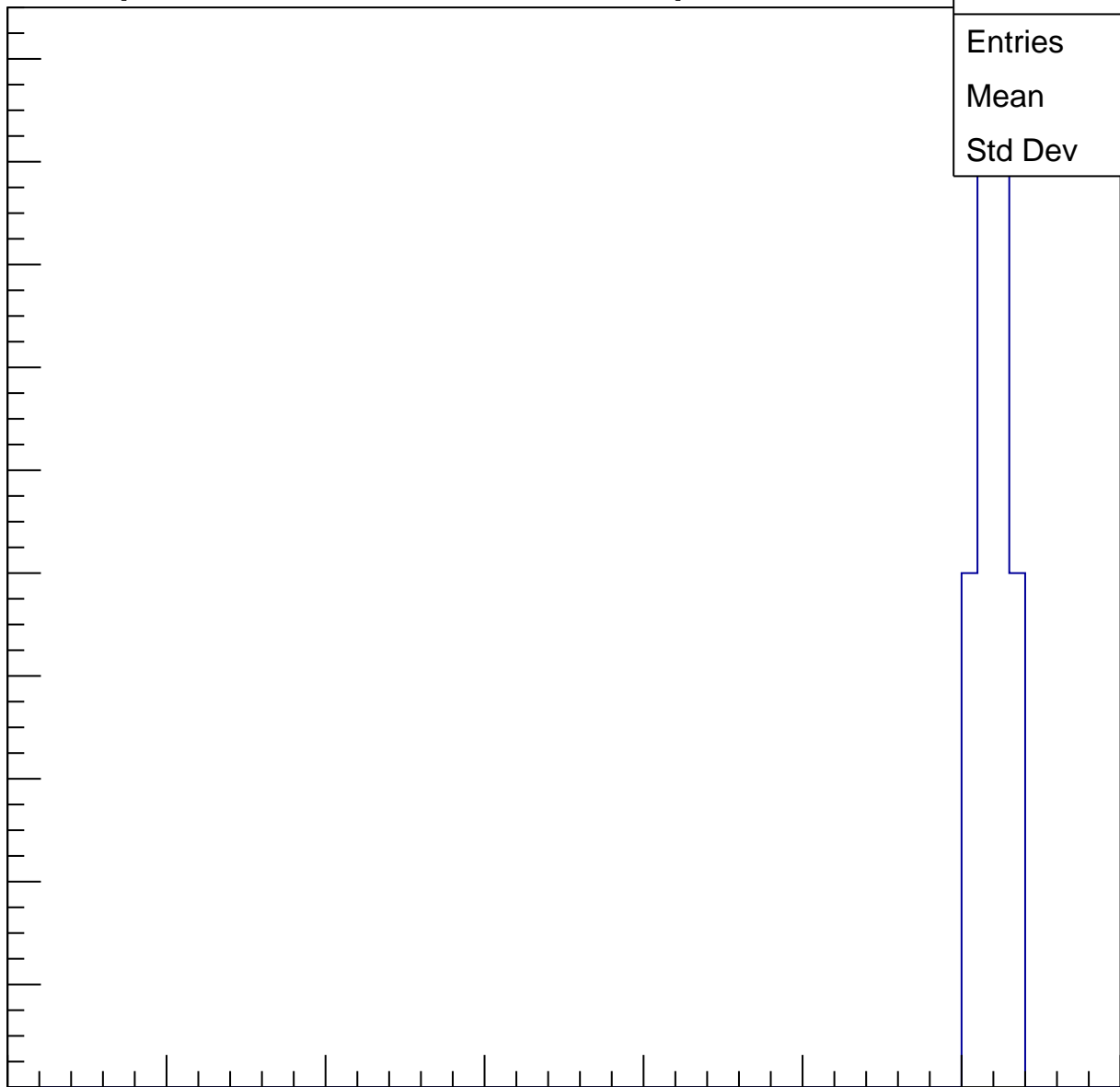
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	0.9574

0 10 20 30 40 50 60 70

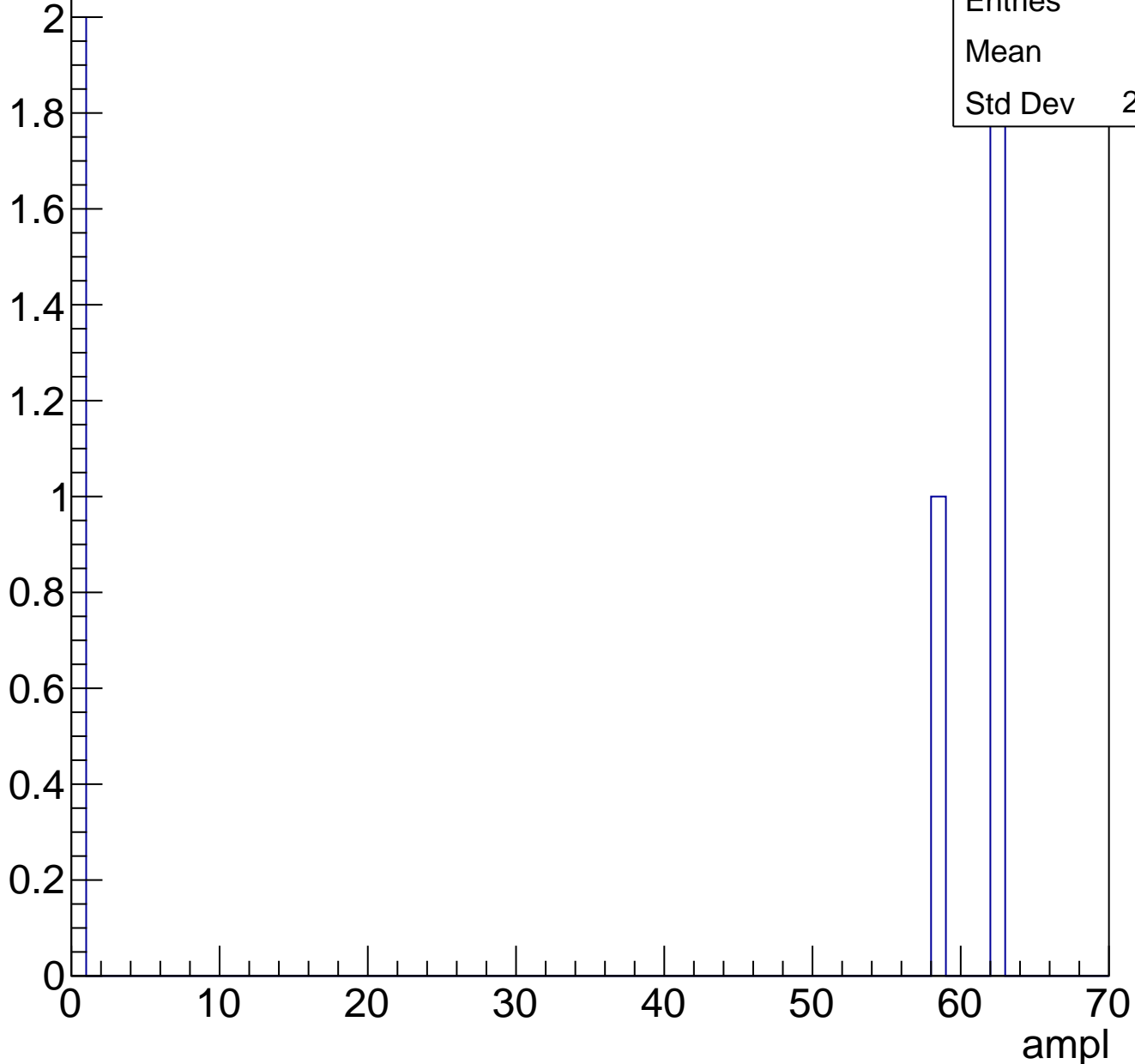
ampl



B1L101S, U17-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch105, adc0

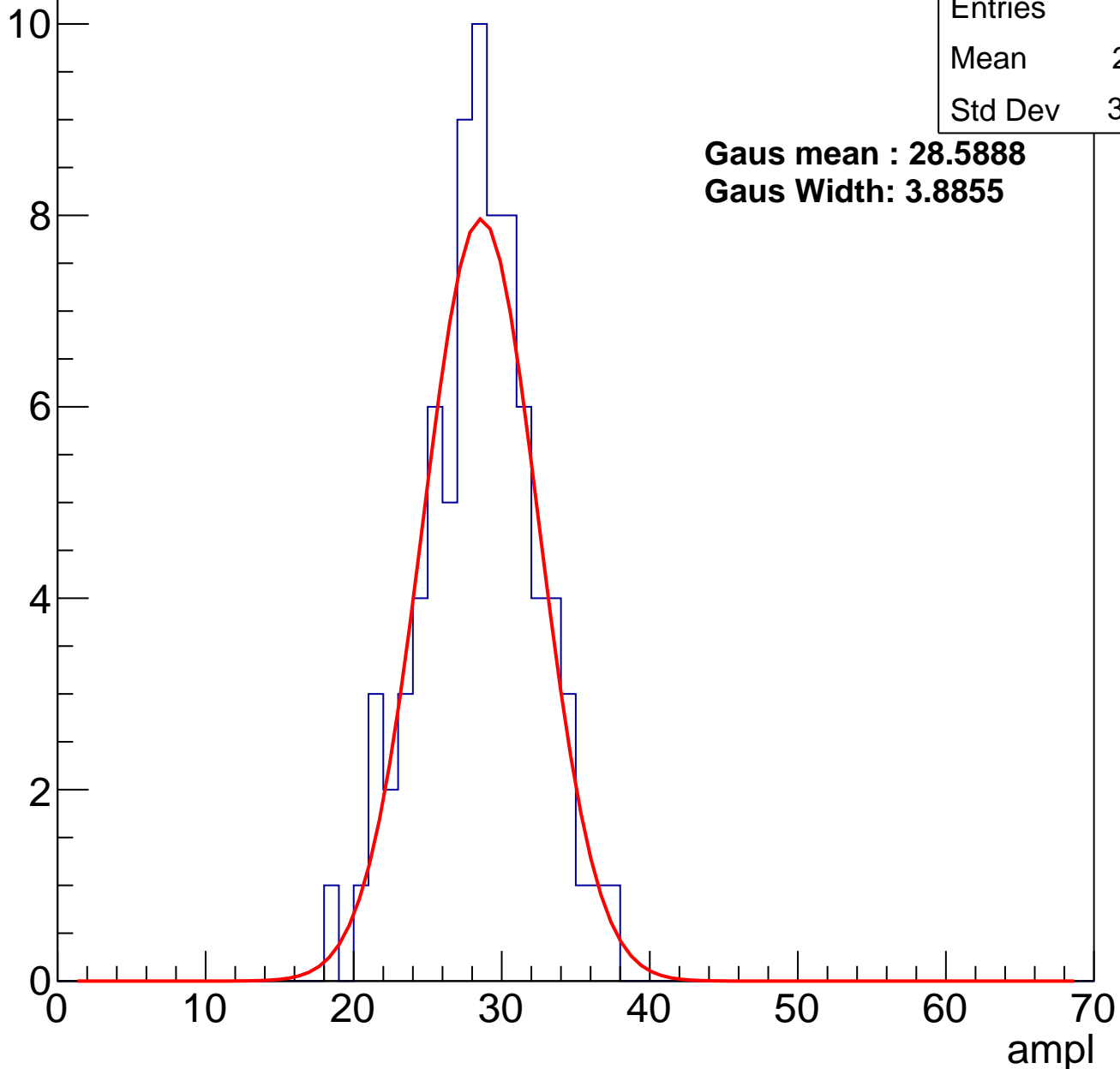
calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	28.01
Std Dev	3.816

Gaus mean : 28.5888

Gaus Width: 3.8855

Entry



B1L101S, U17-ch105, adc1

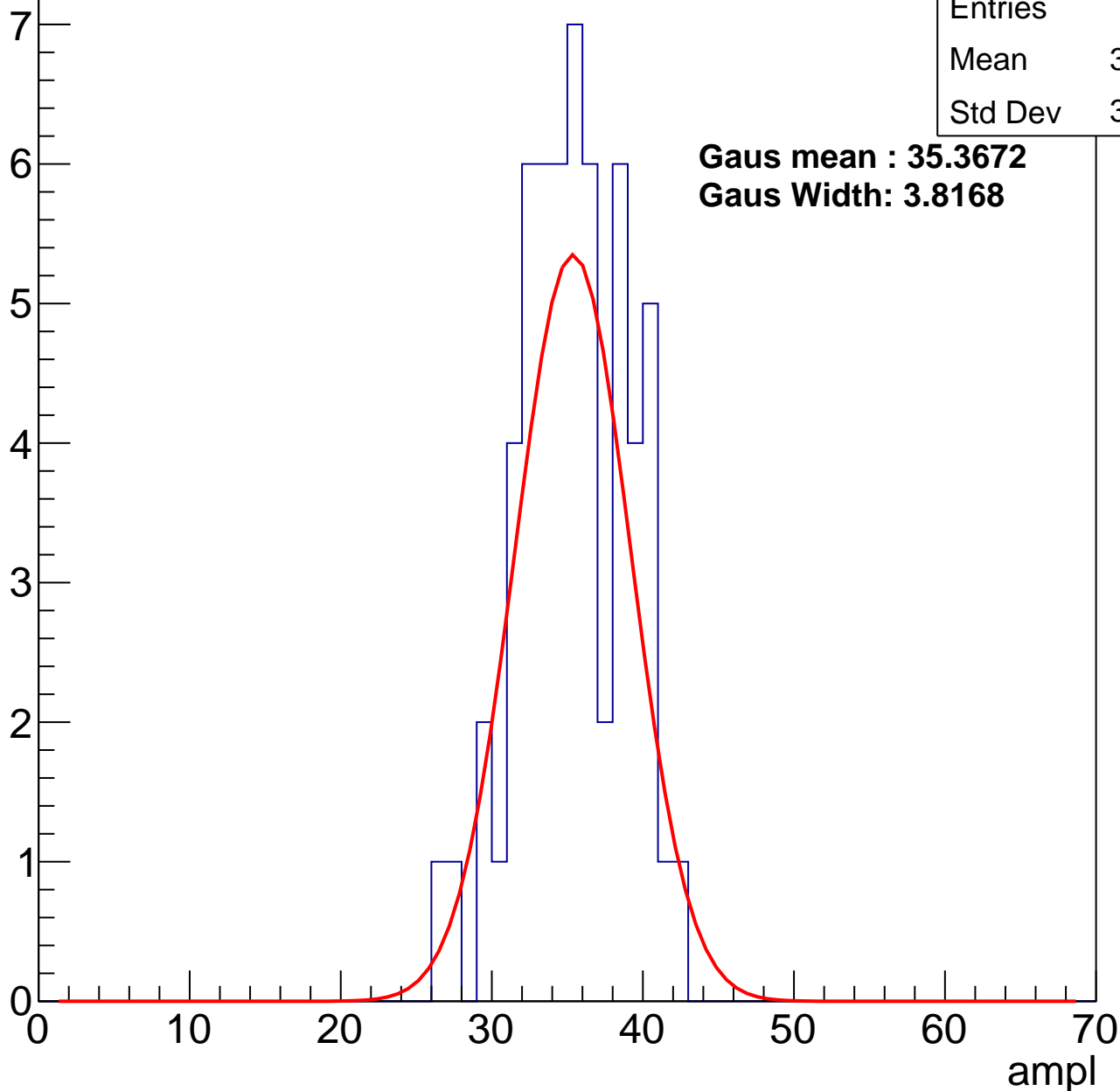
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.93
Std Dev	3.522

Gaus mean : 35.3672

Gaus Width: 3.8168



B1L101S, U17-ch105, adc2

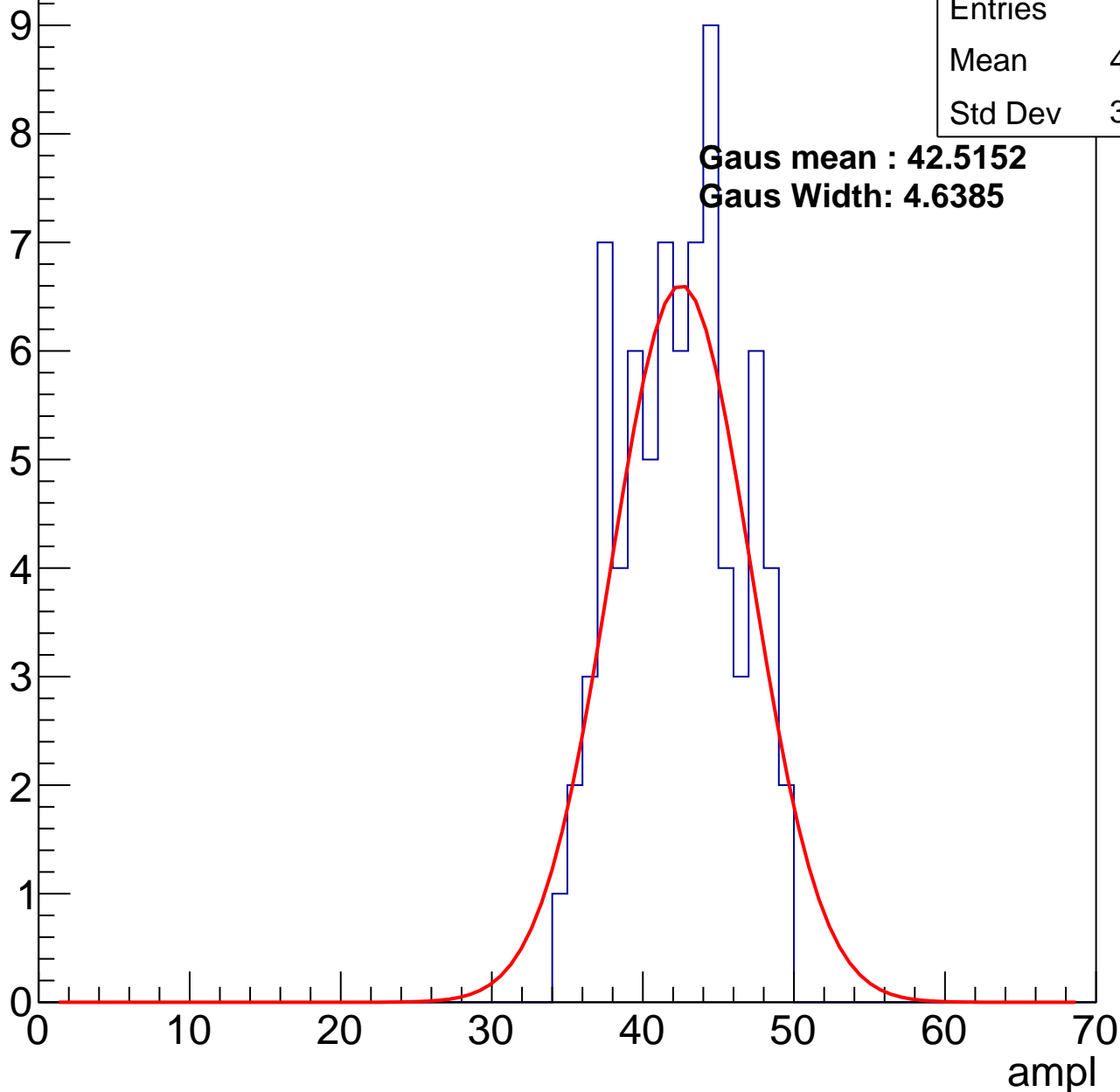
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	41.88
Std Dev	3.818

Gaus mean : 42.5152

Gaus Width: 4.6385

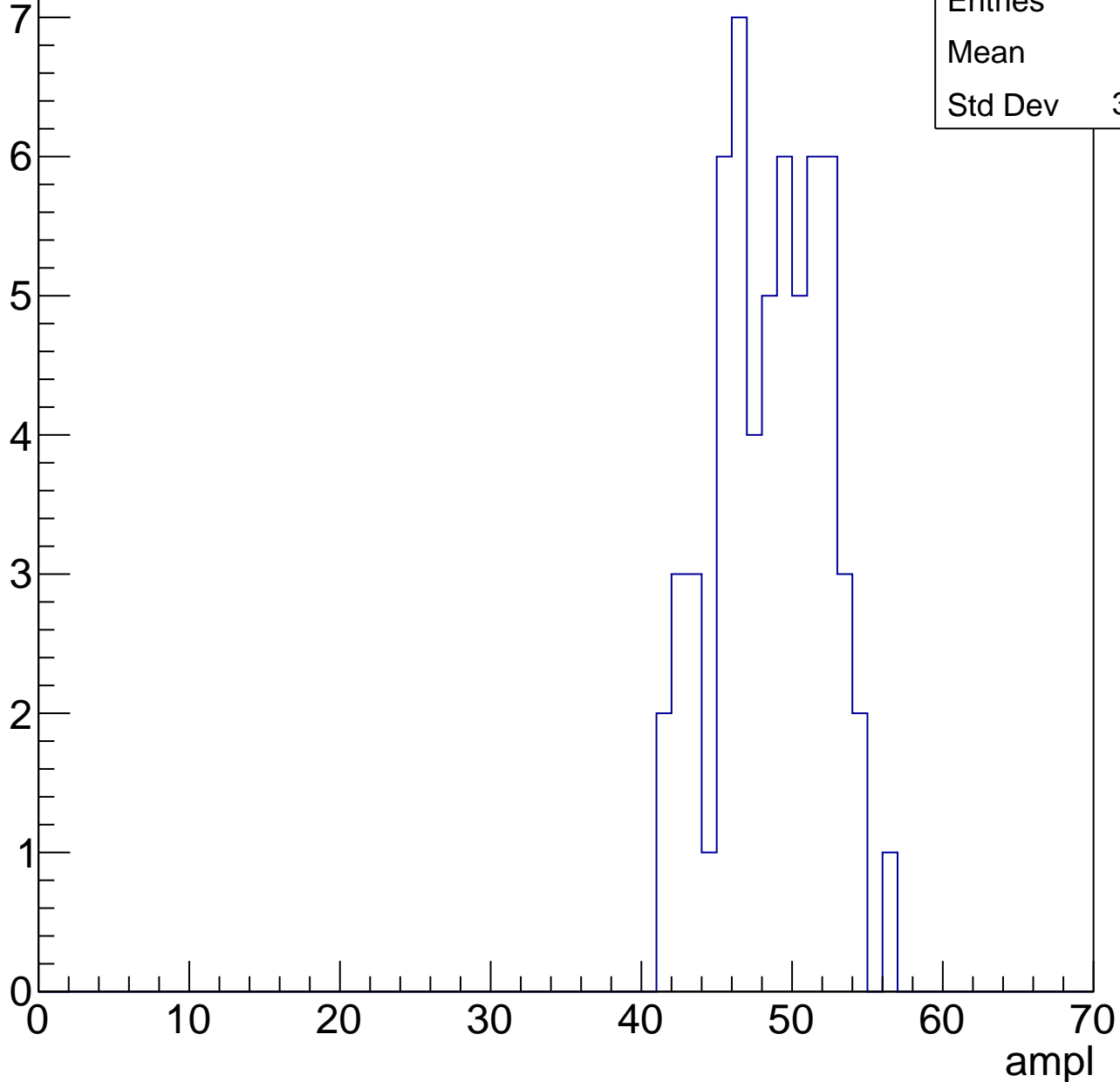


B1L101S, U17-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.1
Std Dev	3.581

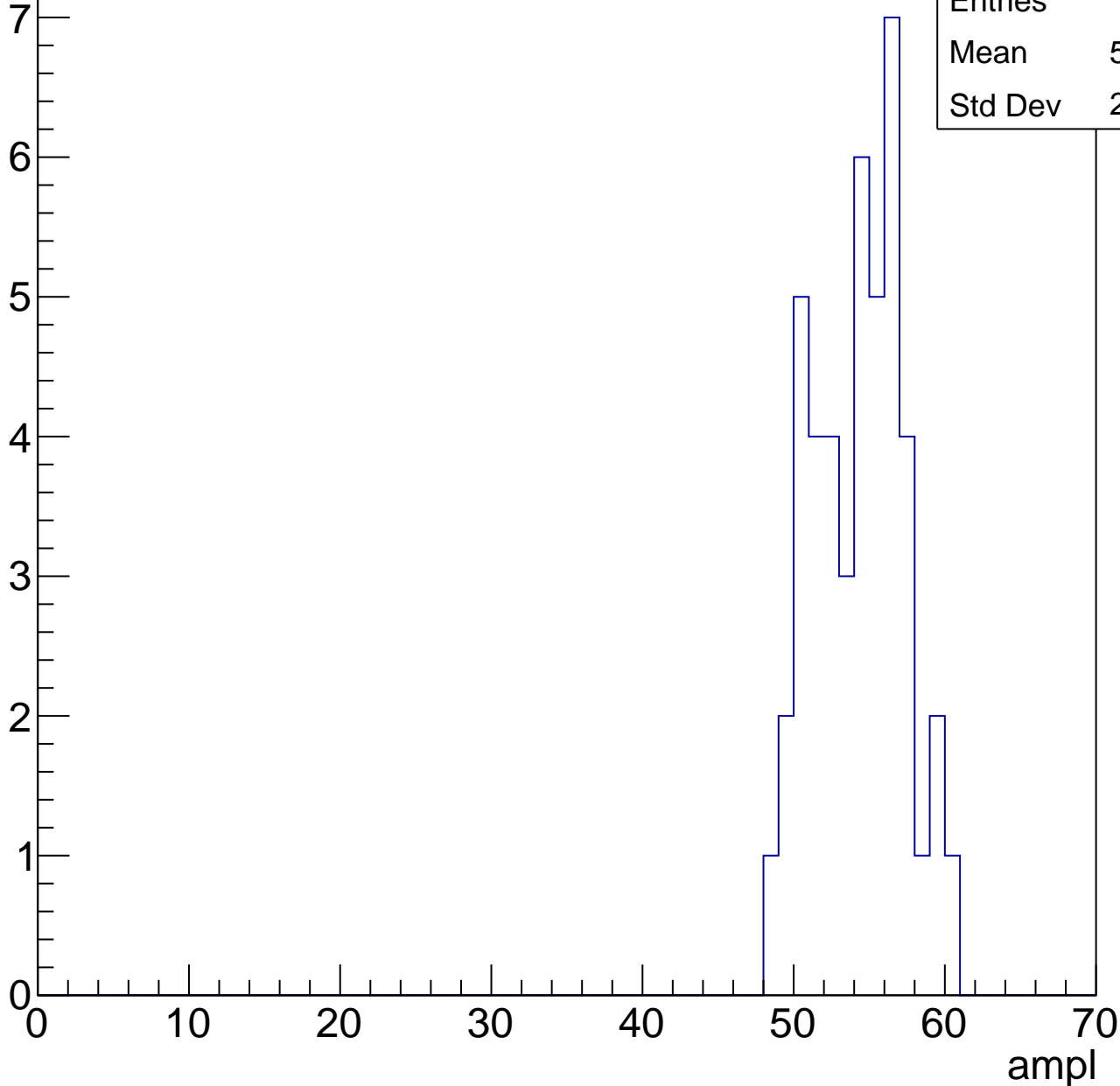


B1L101S, U17-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	53.82
Std Dev	2.946



B1L101S, U17-ch105, adc5

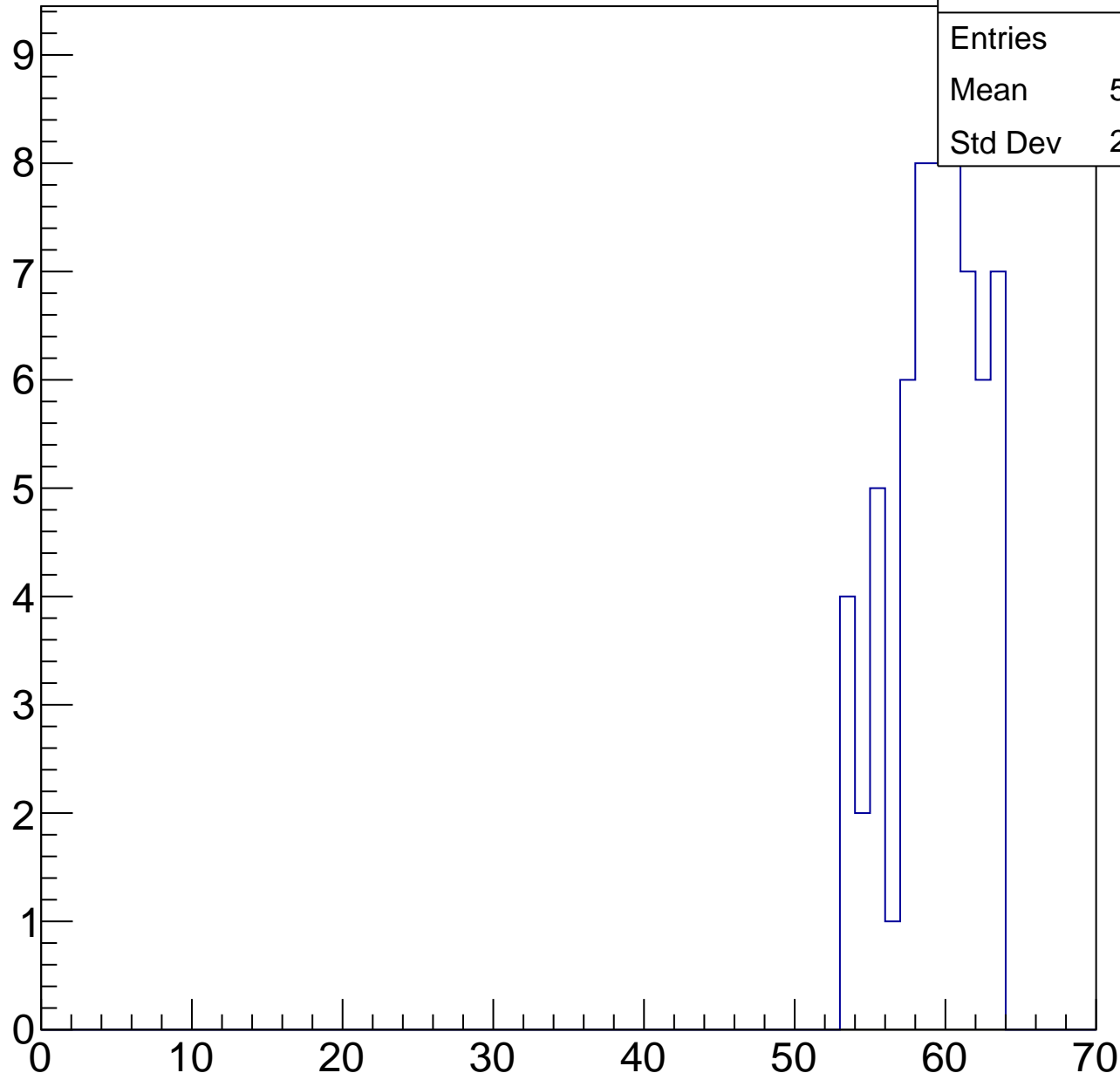
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.87
Std Dev	2.865

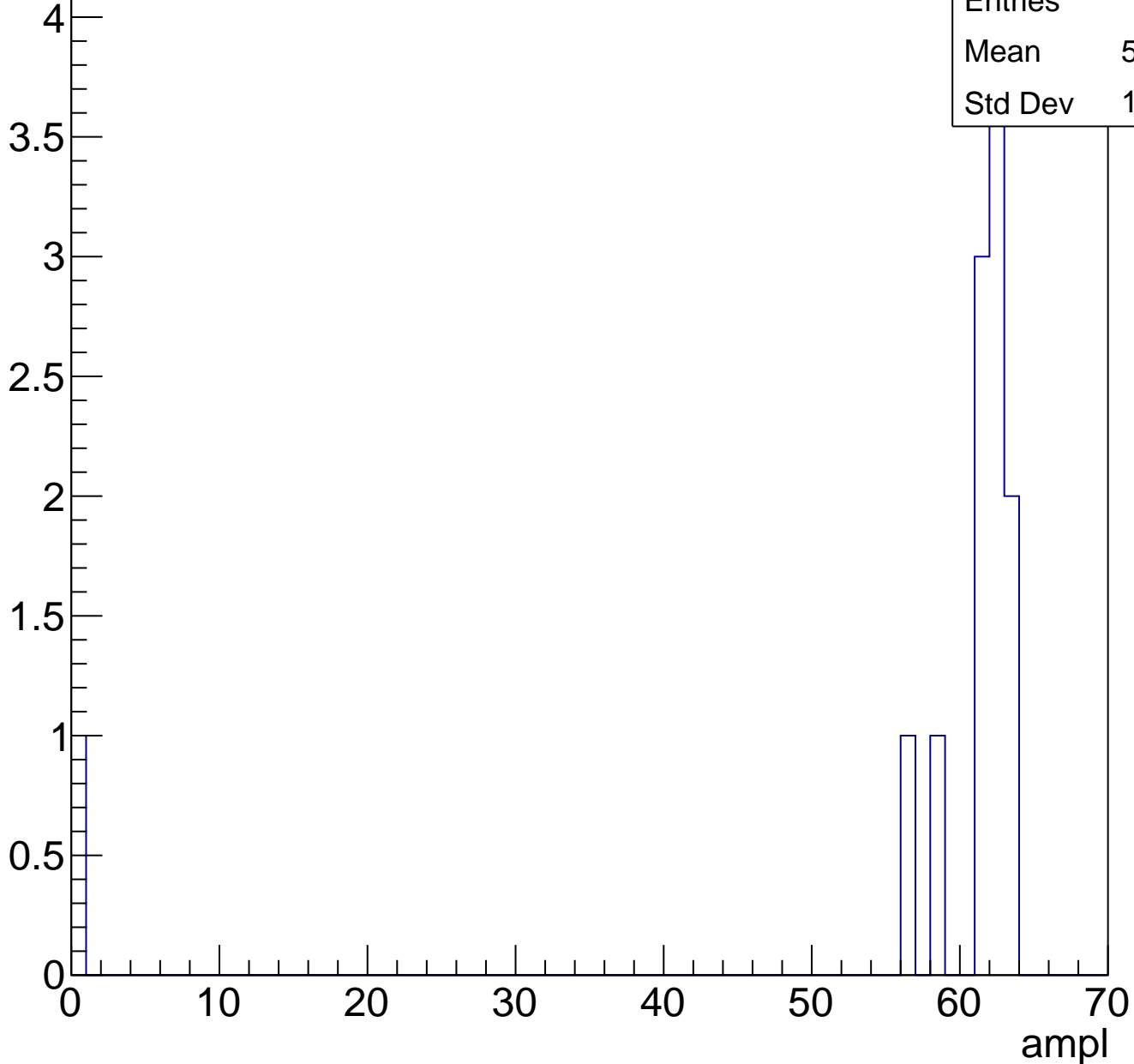
ampl



B1L101S, U17-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch106, adc0

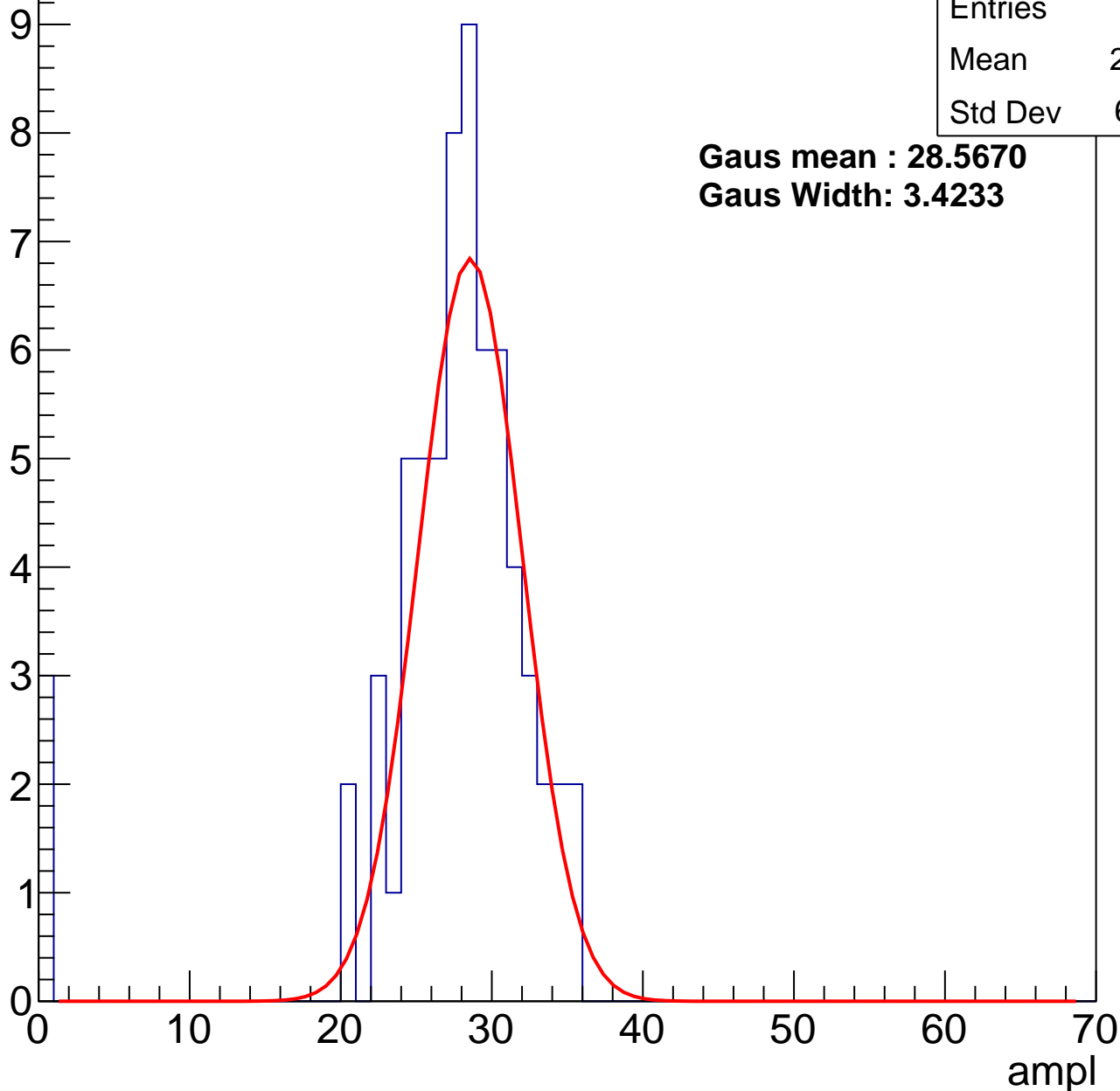
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	26.52
Std Dev	6.691

Gaus mean : 28.5670

Gaus Width: 3.4233



B1L101S, U17-ch106, adc1

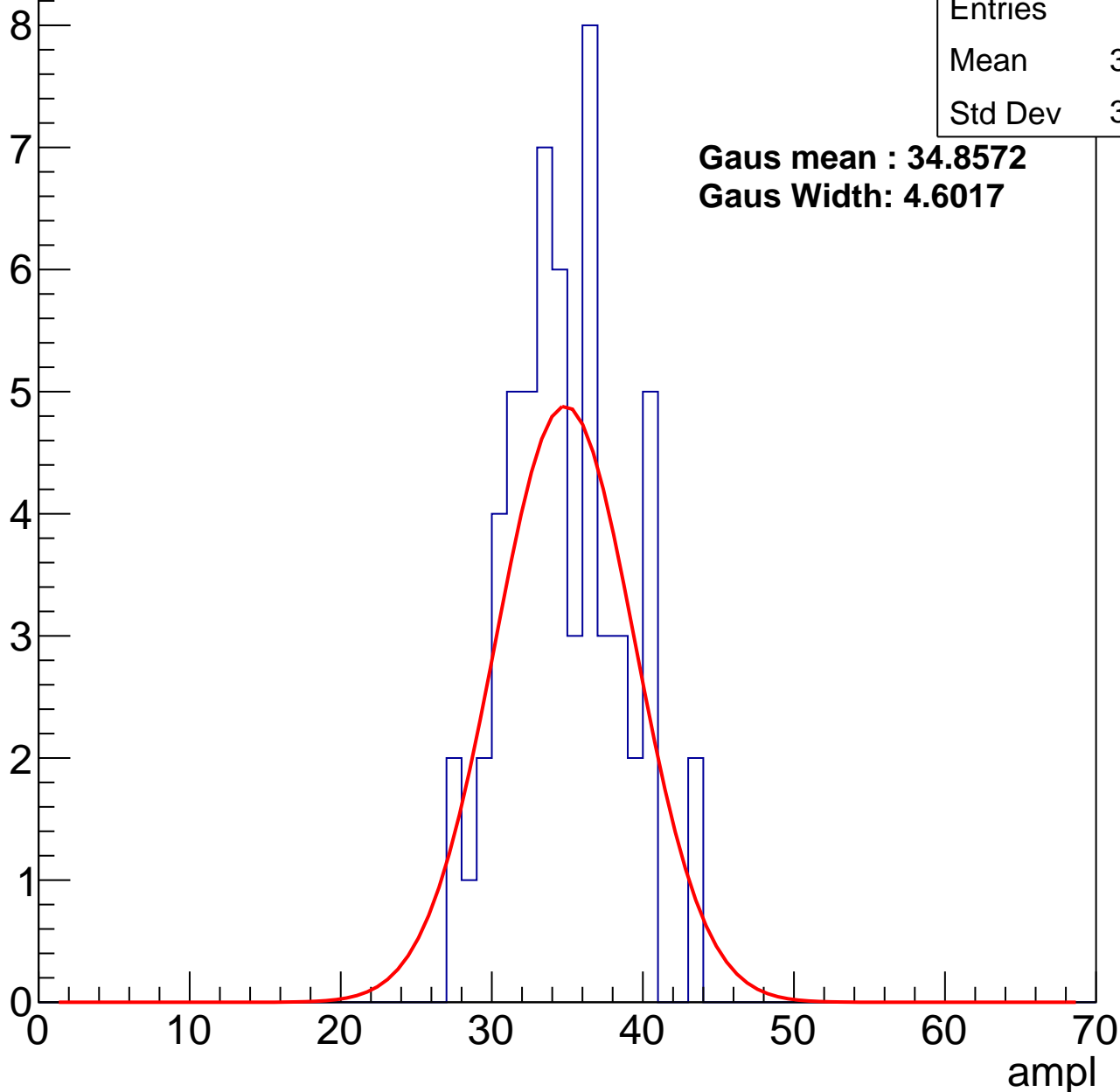
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	34.34
Std Dev	3.749

Gaus mean : 34.8572

Gaus Width: 4.6017



B1L101S, U17-ch106, adc2

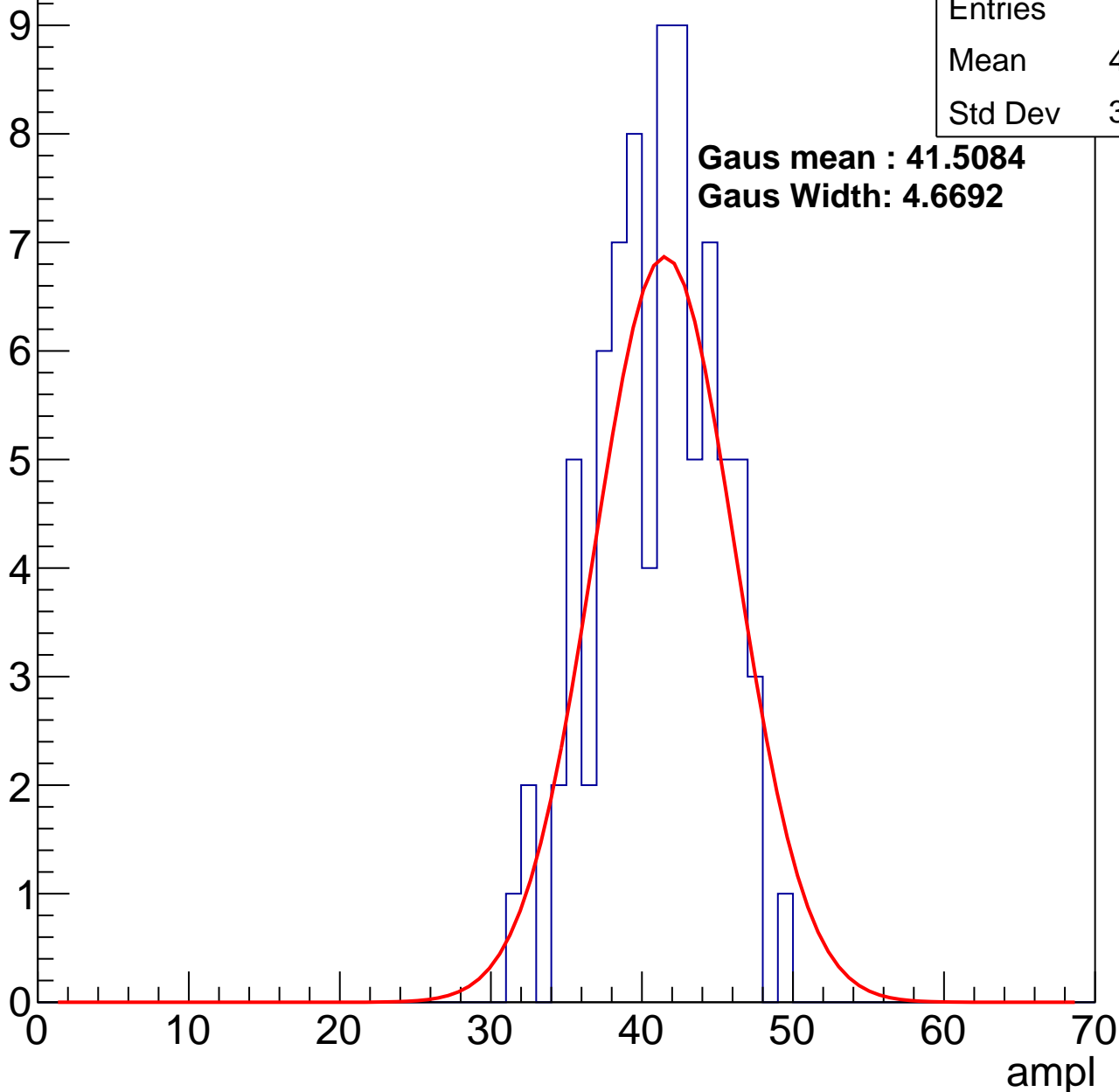
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	40.56
Std Dev	3.909

Gaus mean : 41.5084

Gaus Width: 4.6692

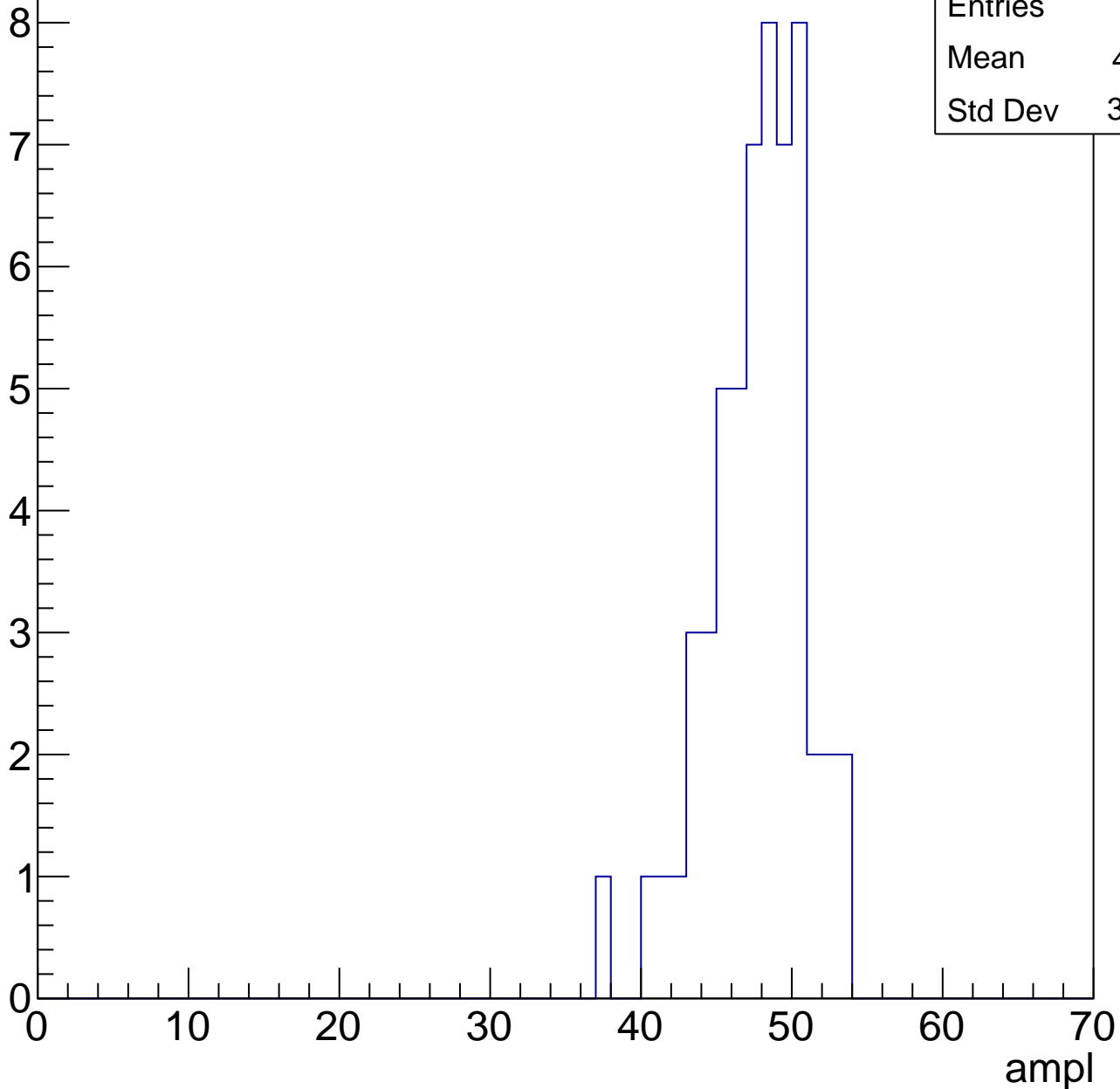


B1L101S, U17-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	47.21
Std Dev	3.189

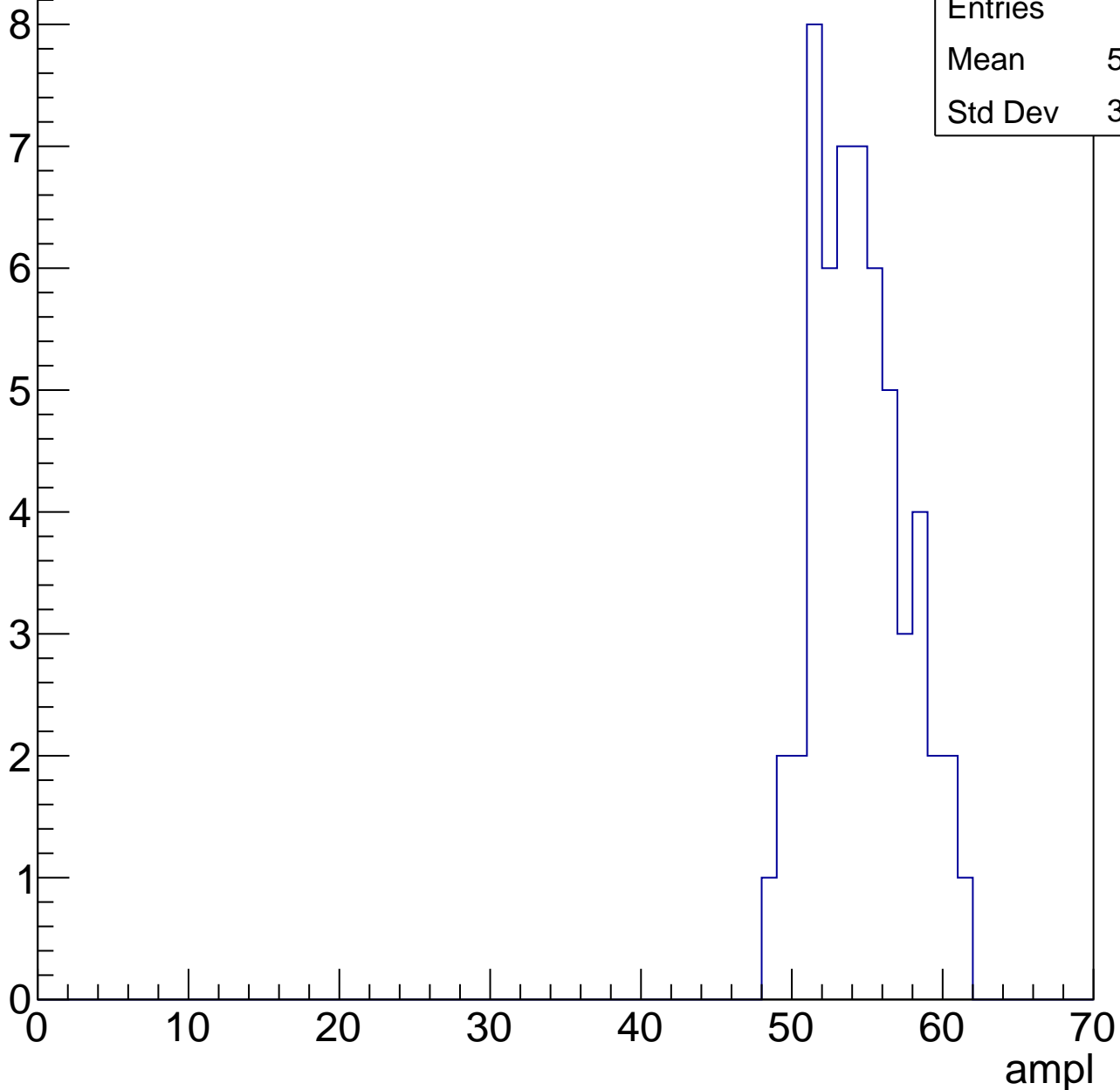


B1L101S, U17-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	54.05
Std Dev	3.014

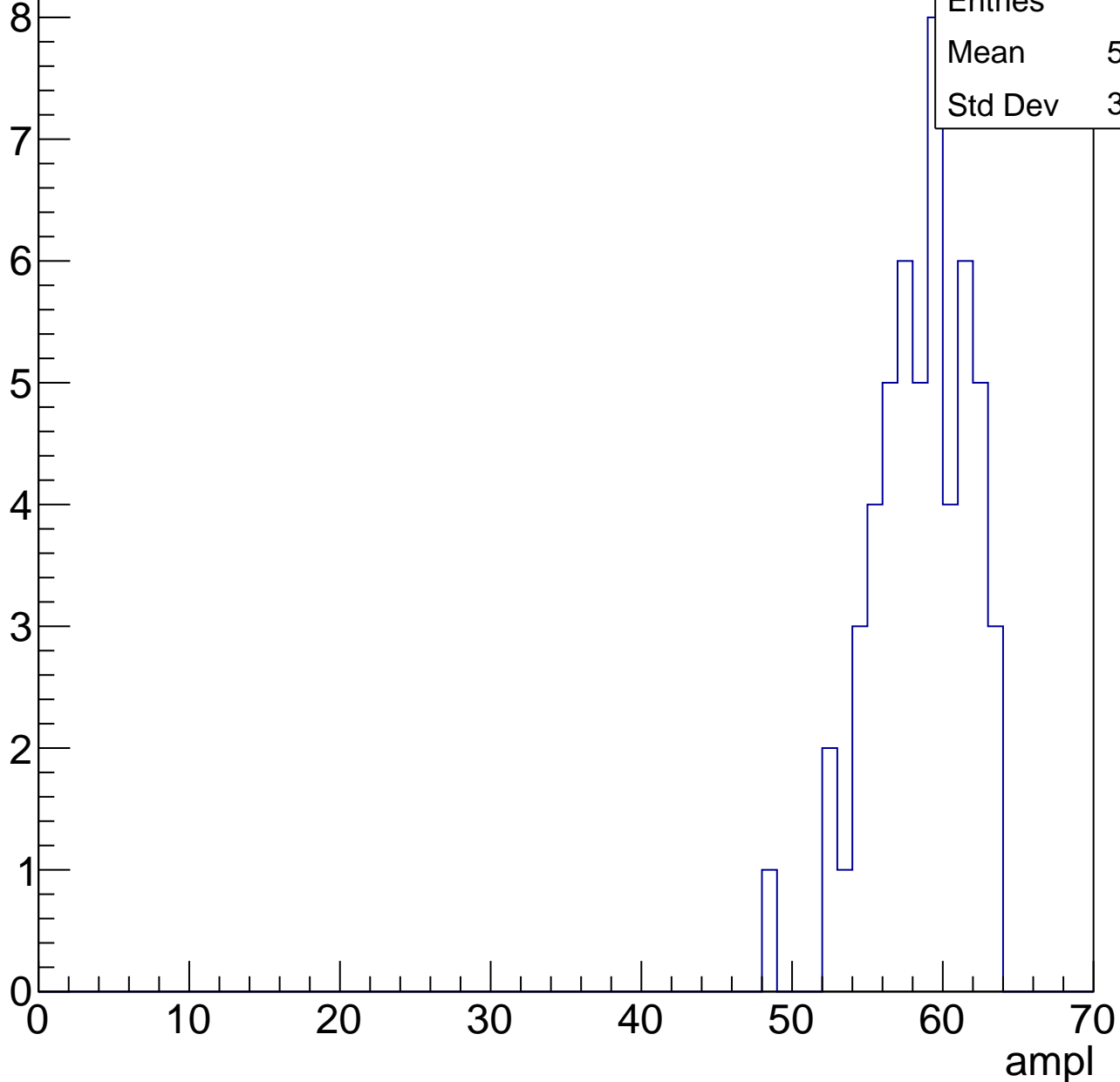


B1L101S, U17-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.04
Std Dev	3.192

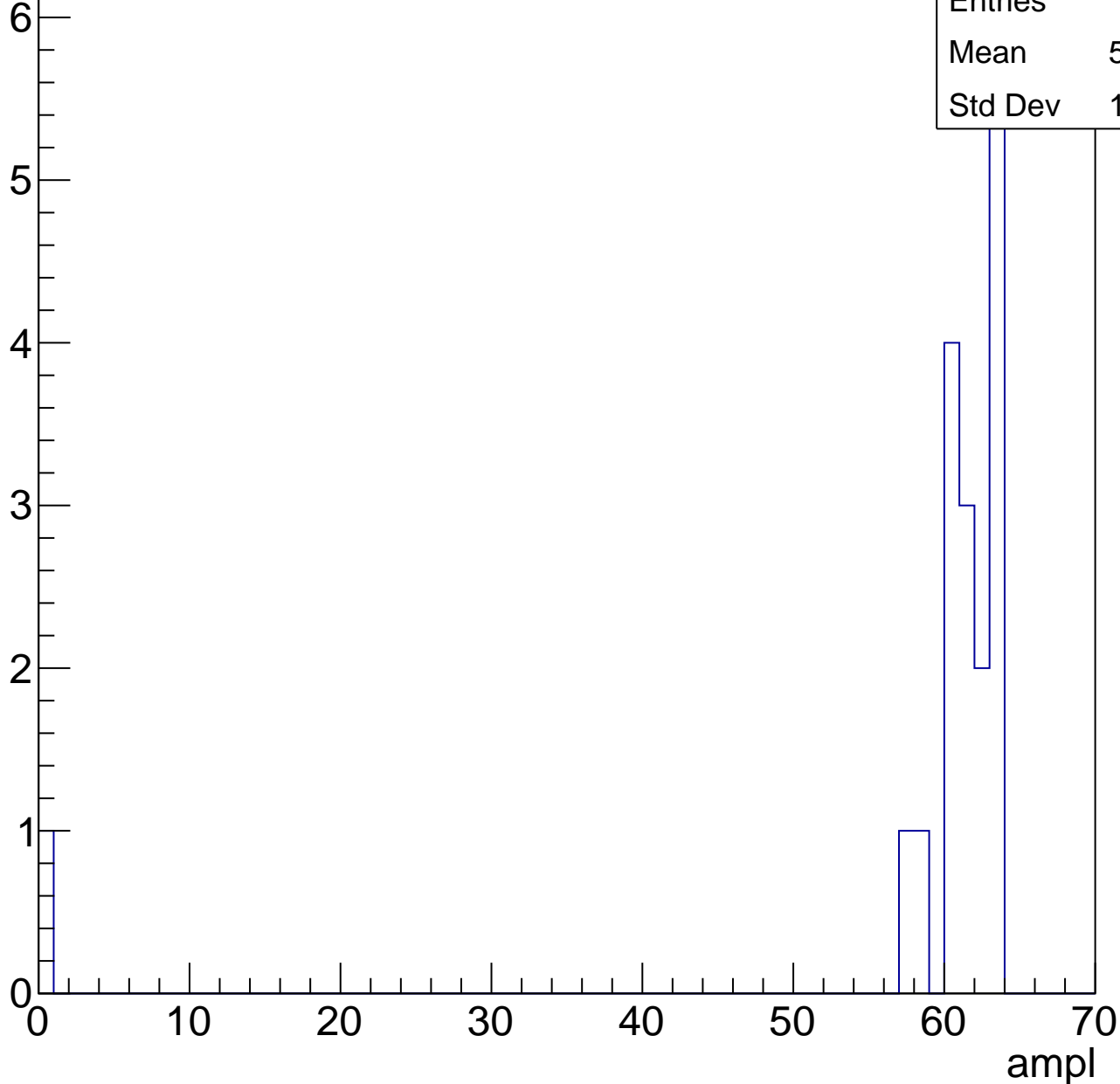


B1L101S, U17-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

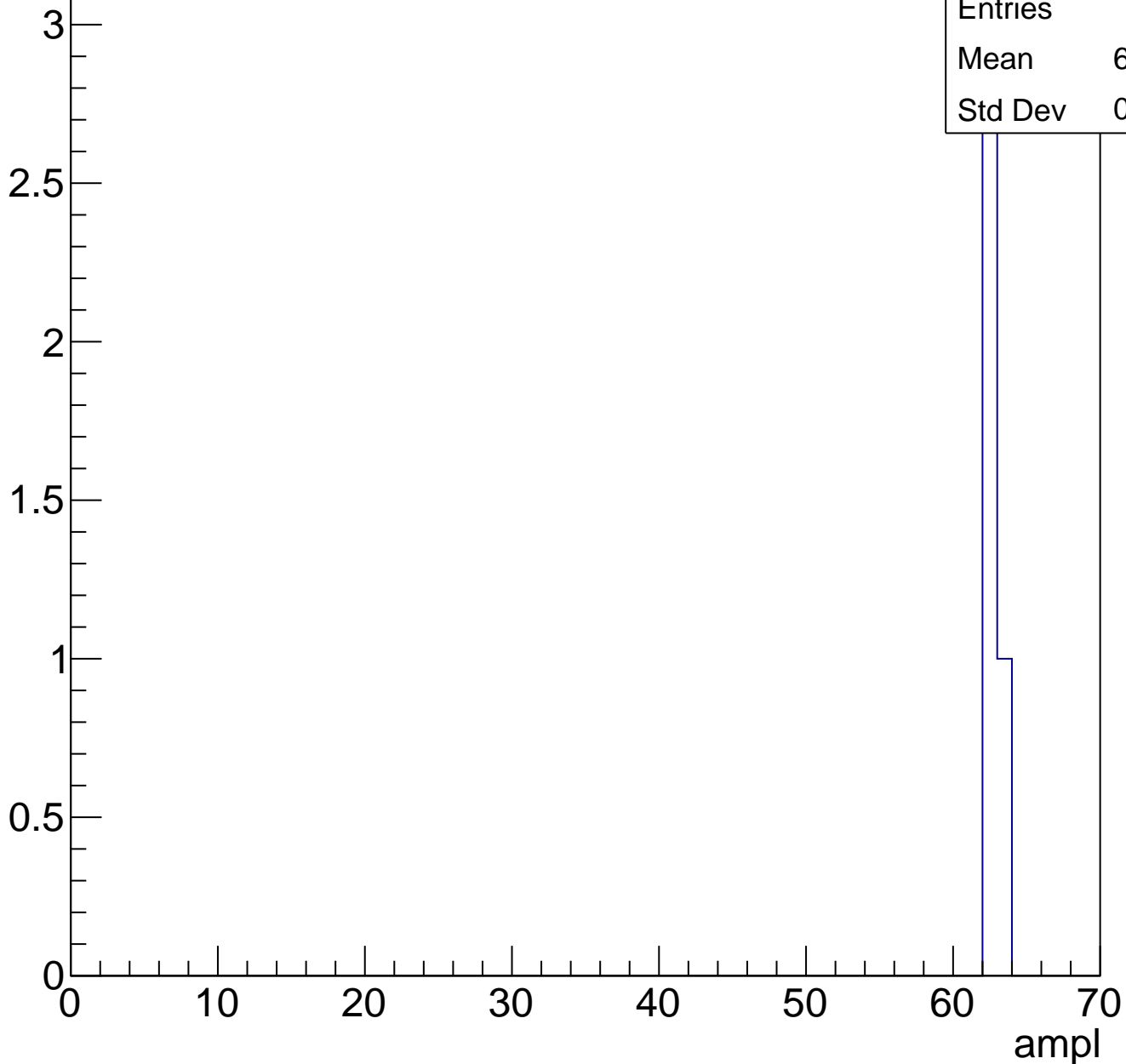
Entries	18
Mean	57.78
Std Dev	14.12



B1L101S, U17-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch107, adc0

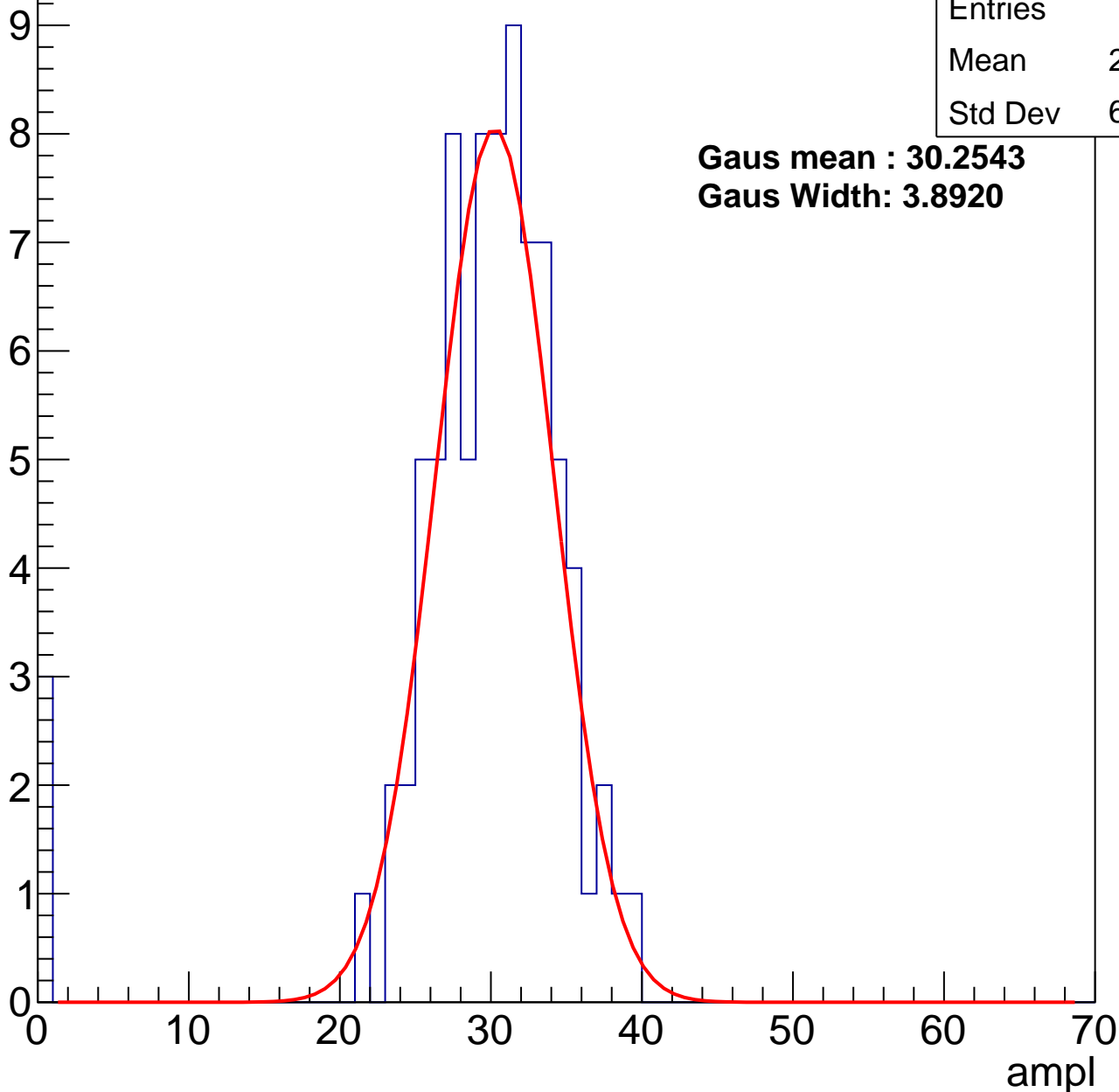
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	28.92
Std Dev	6.652

Gaus mean : 30.2543

Gaus Width: 3.8920



B1L101S, U17-ch107, adc1

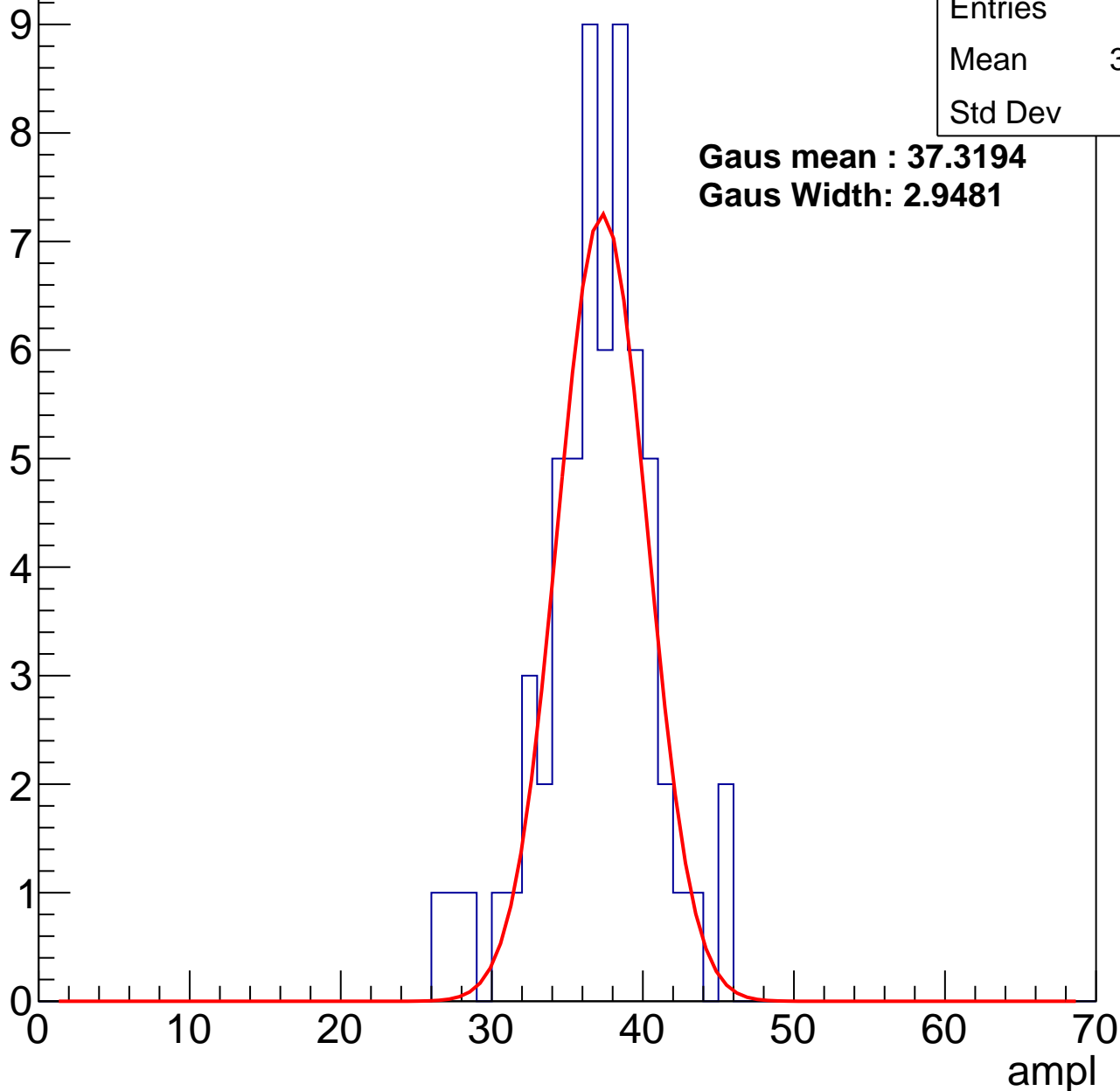
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.52
Std Dev	3.74

Gaus mean : 37.3194

Gaus Width: 2.9481



B1L101S, U17-ch107, adc2

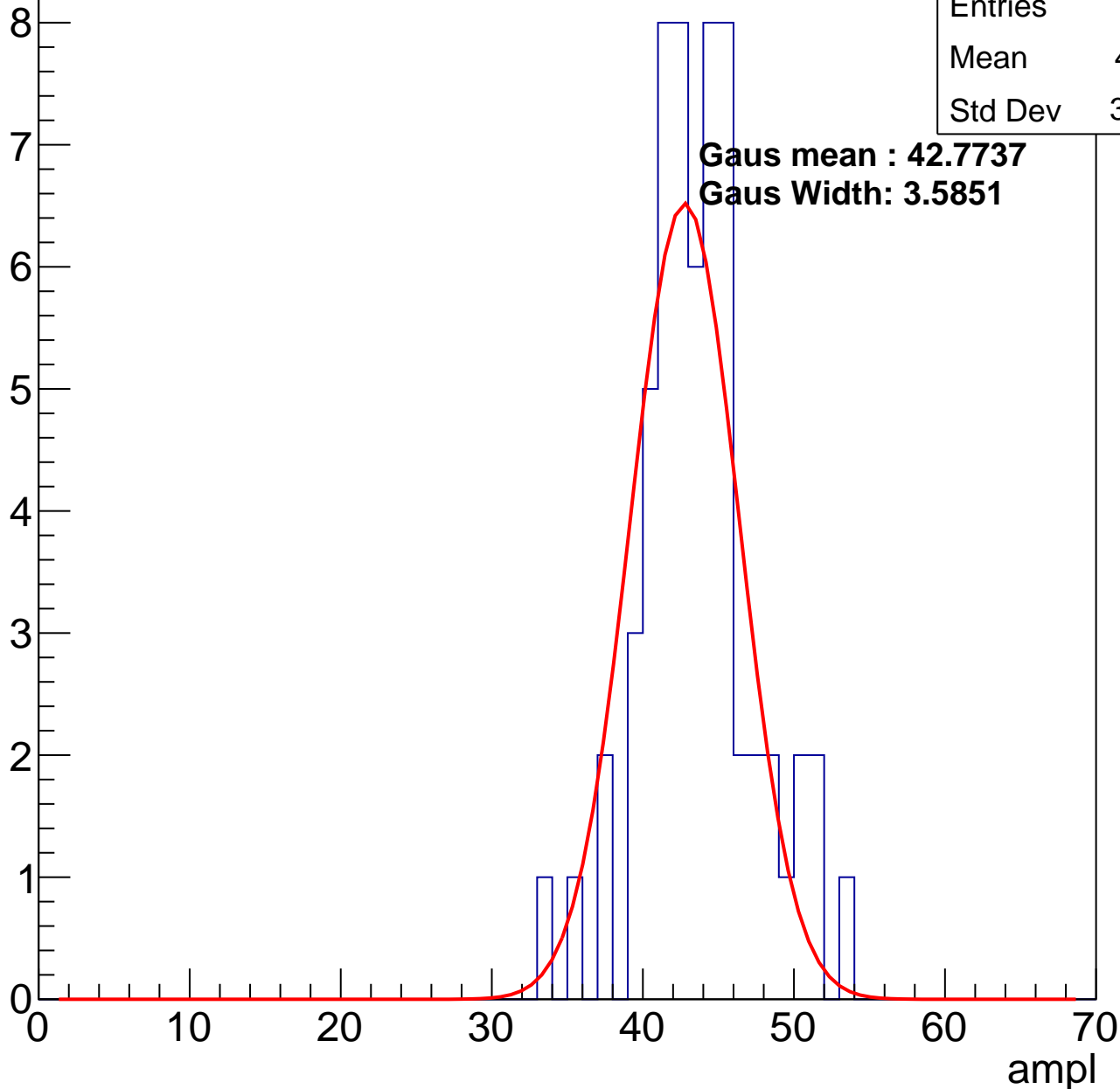
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.21
Std Dev	3.742

Gaus mean : 42.7737

Gaus Width: 3.5851

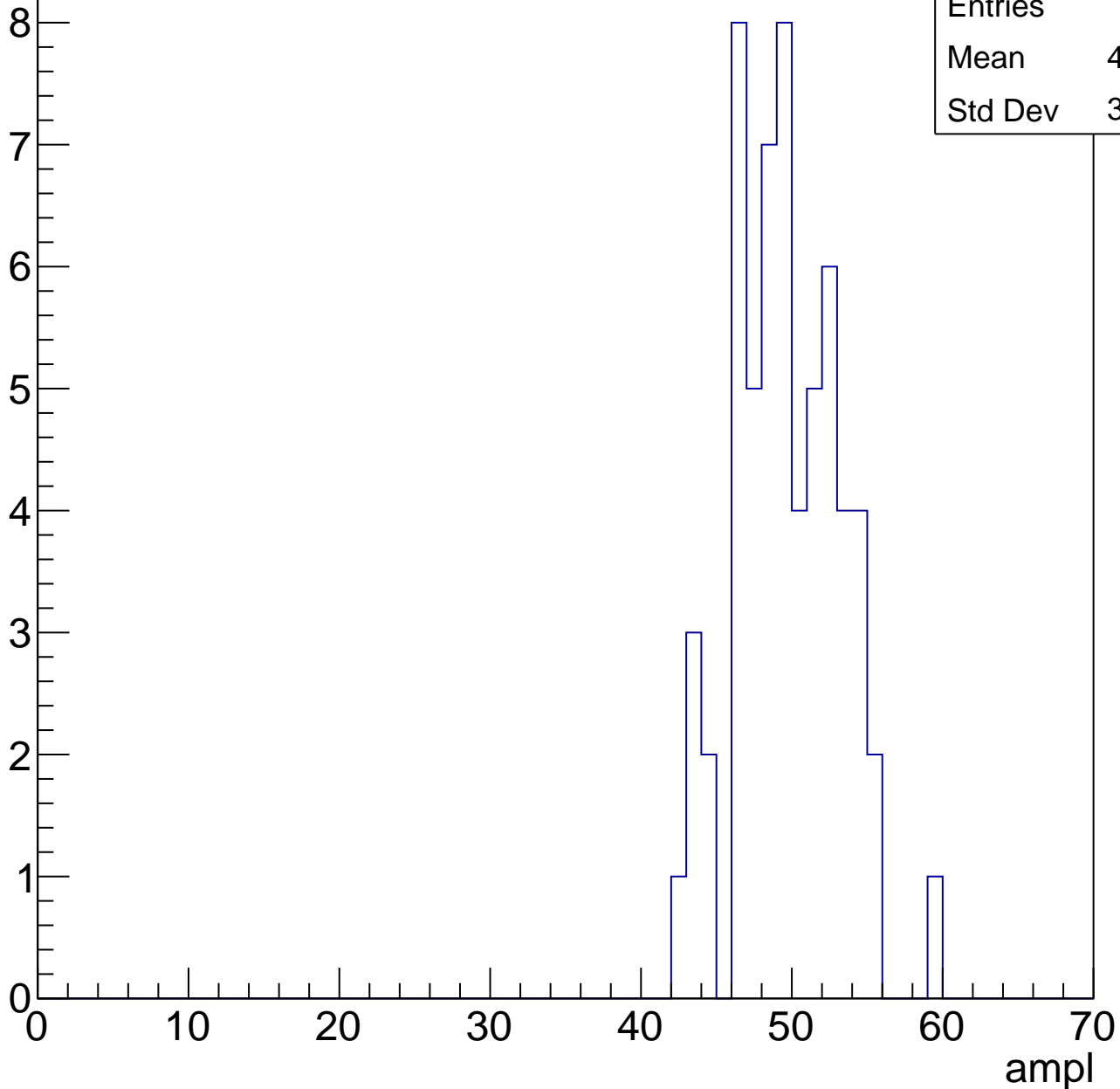


B1L101S, U17-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.23
Std Dev	3.456

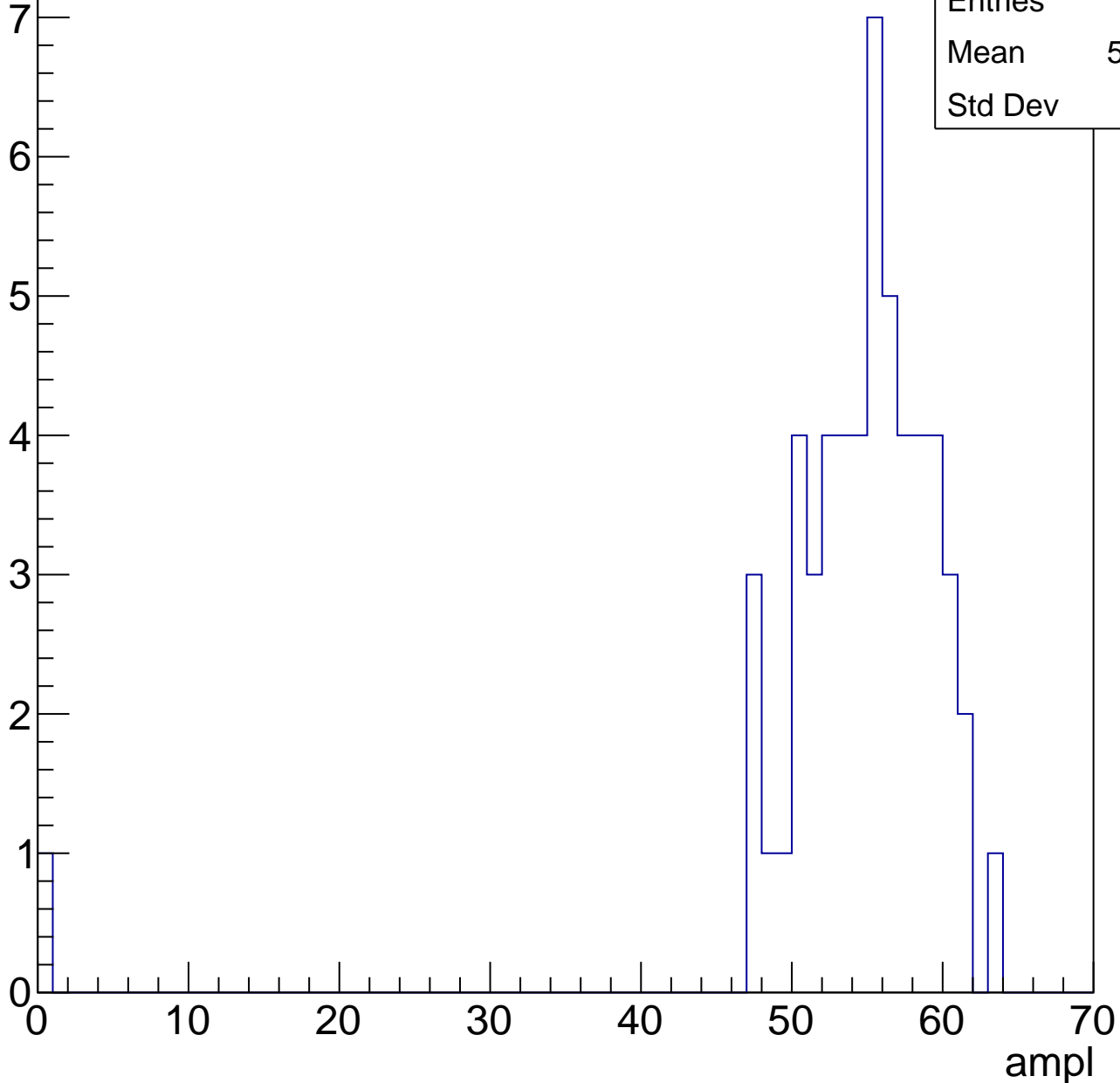


B1L101S, U17-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

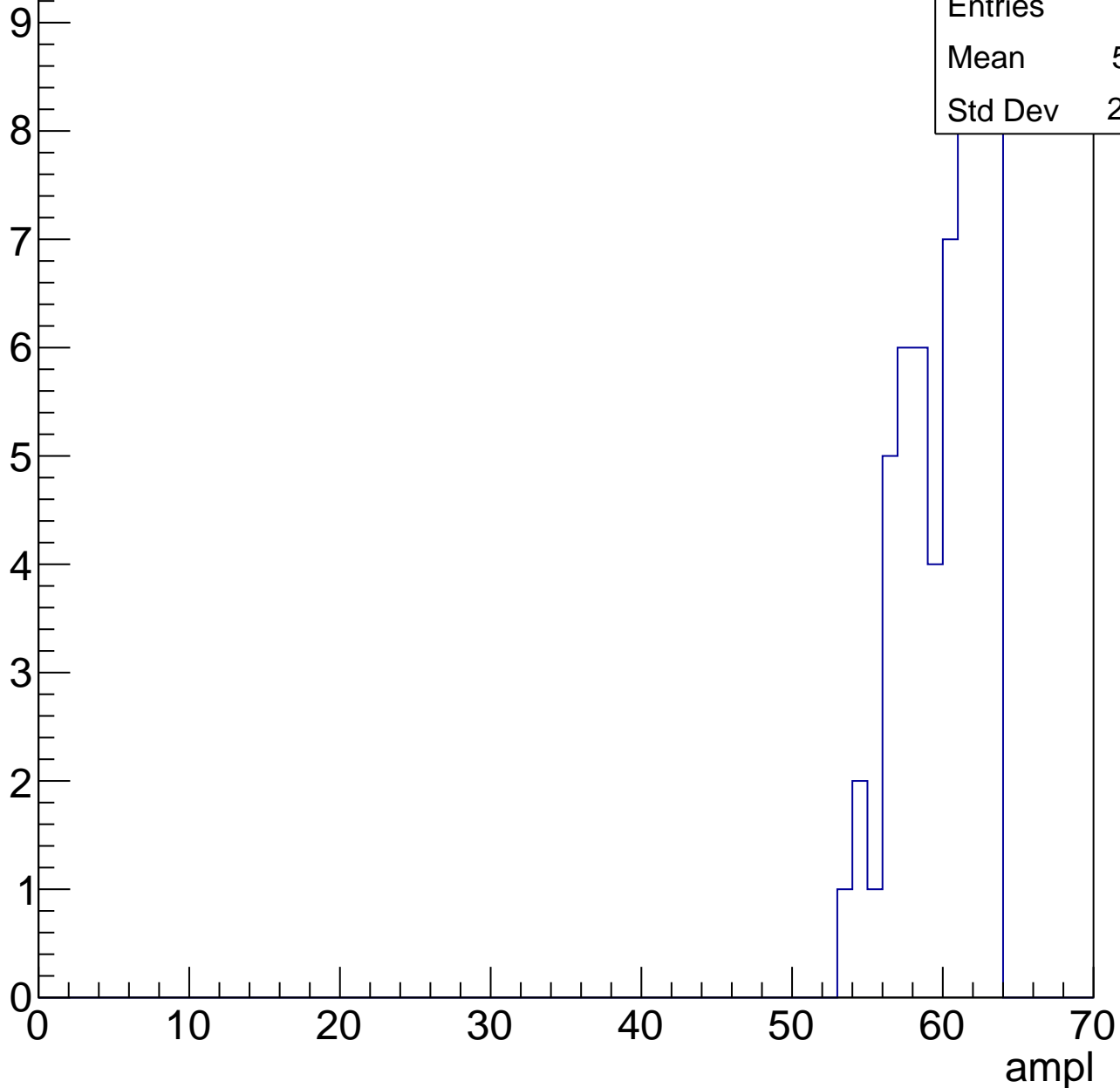
Entries	55
Mean	53.69
Std Dev	8.25



B1L101S, U17-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

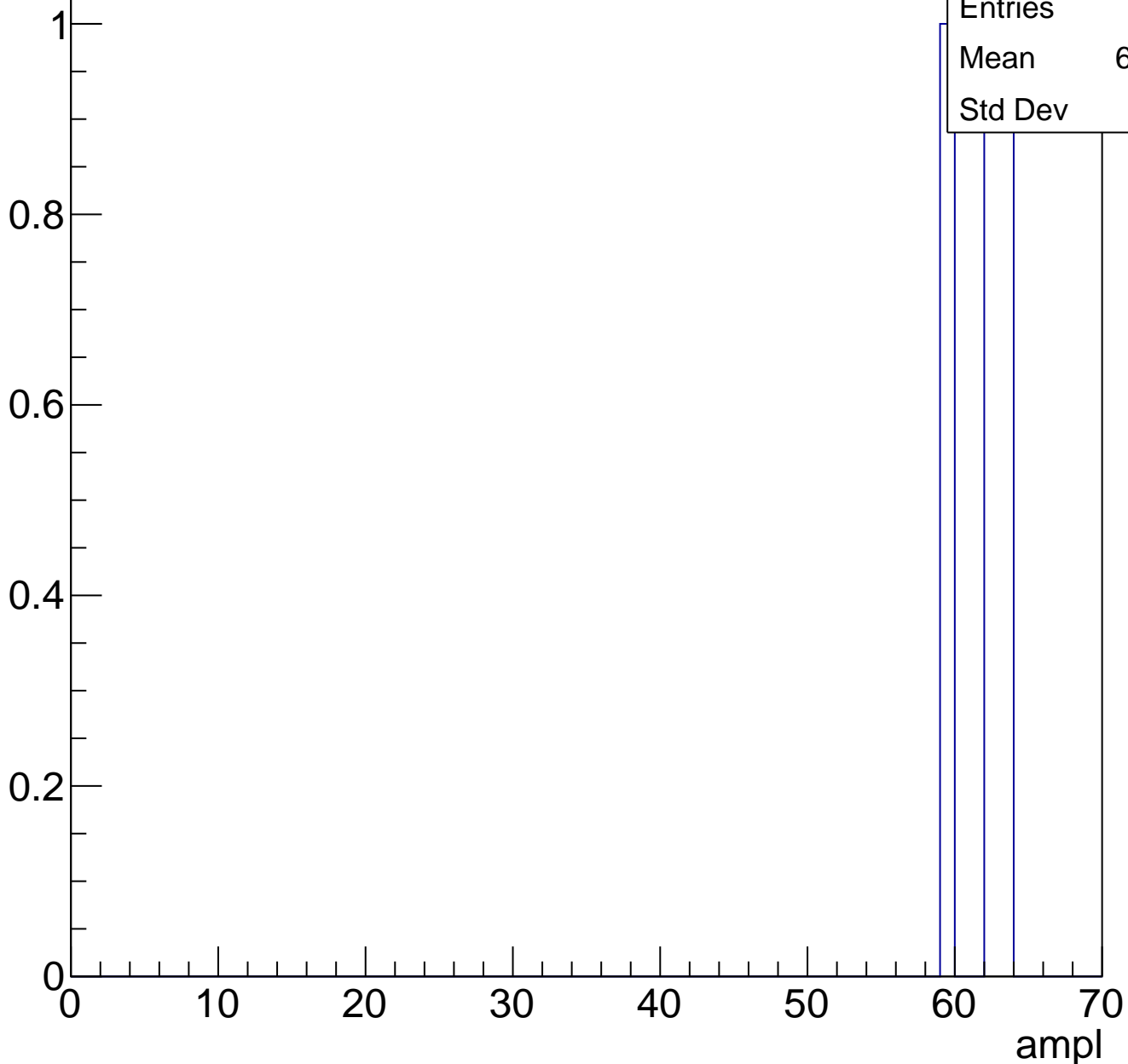
Entry



B1L101S, U17-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch108, adc0

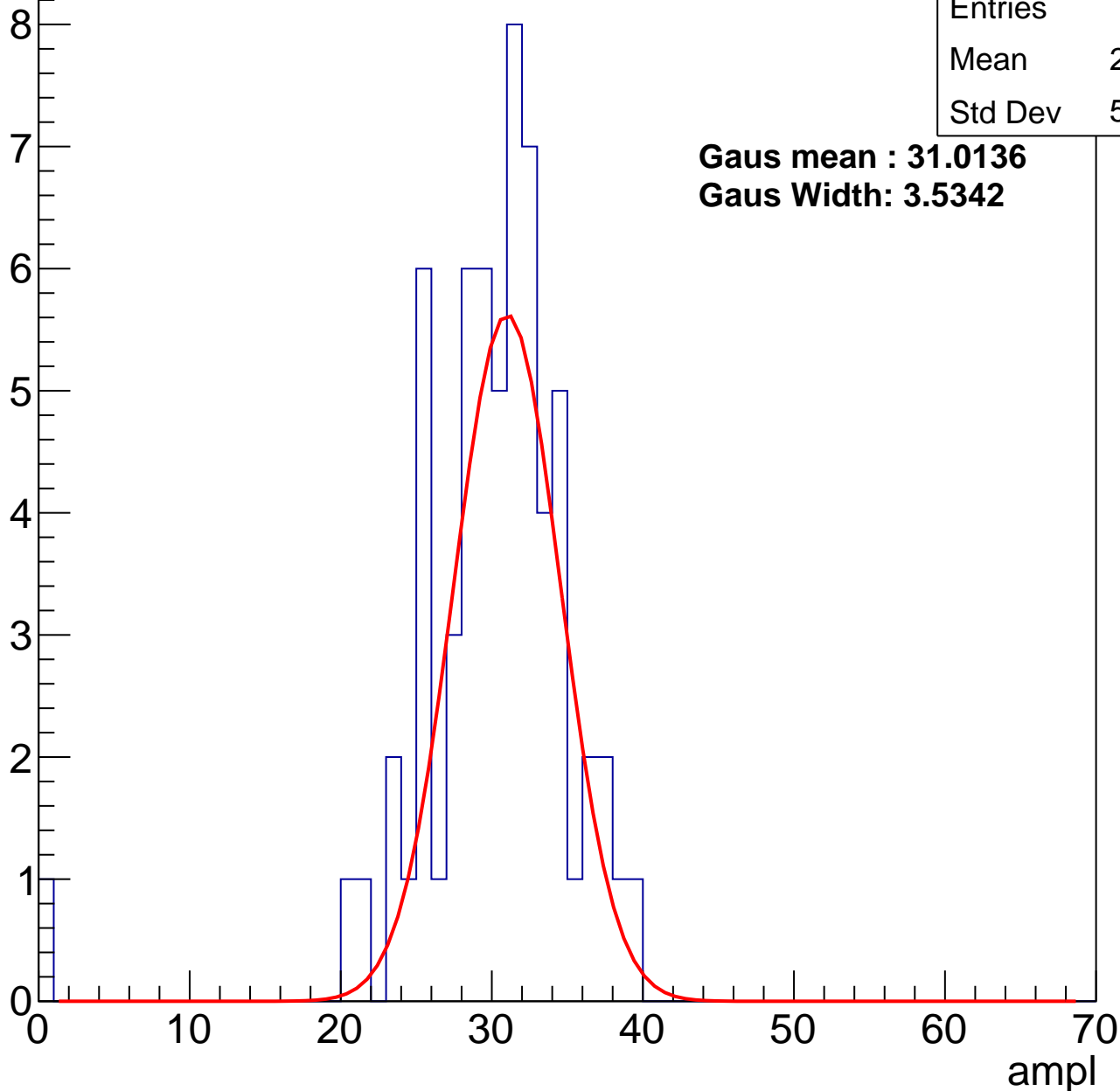
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.56
Std Dev	5.474

Gaus mean : 31.0136

Gaus Width: 3.5342



B1L101S, U17-ch108, adc1

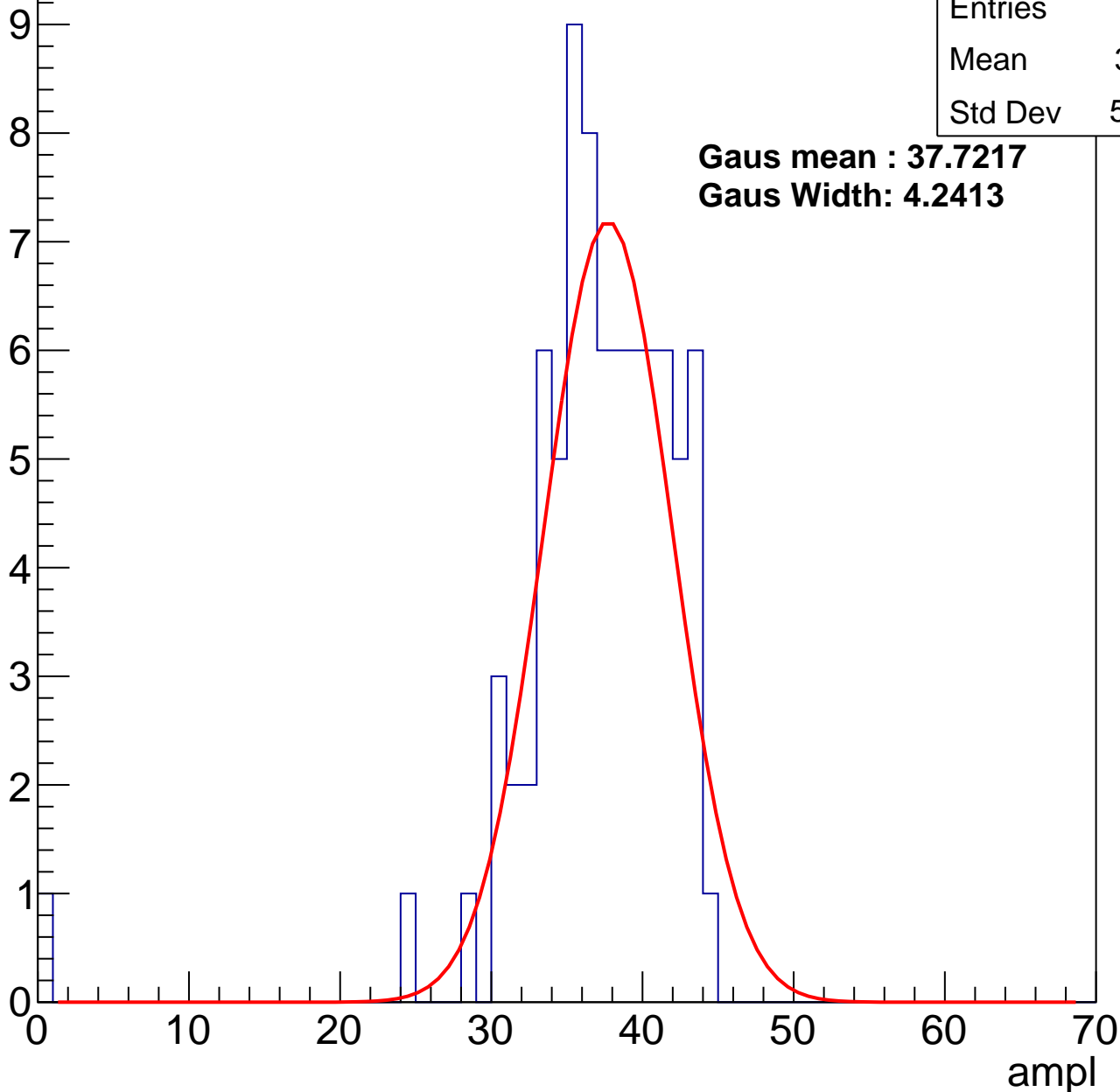
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.51
Std Dev	5.727

Gaus mean : 37.7217

Gaus Width: 4.2413



B1L101S, U17-ch108, adc2

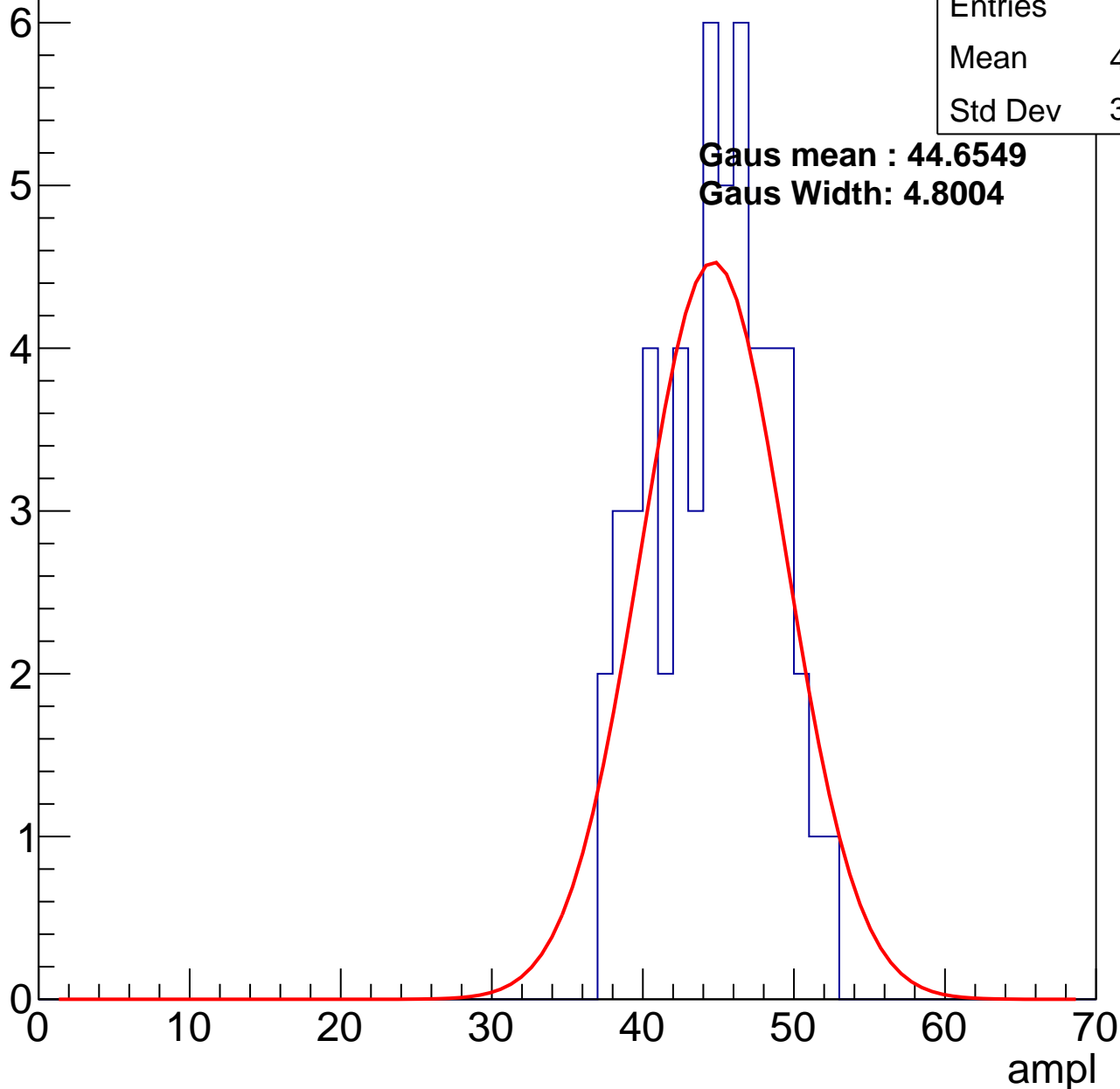
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.22
Std Dev	3.833

Gaus mean : 44.6549

Gaus Width: 4.8004

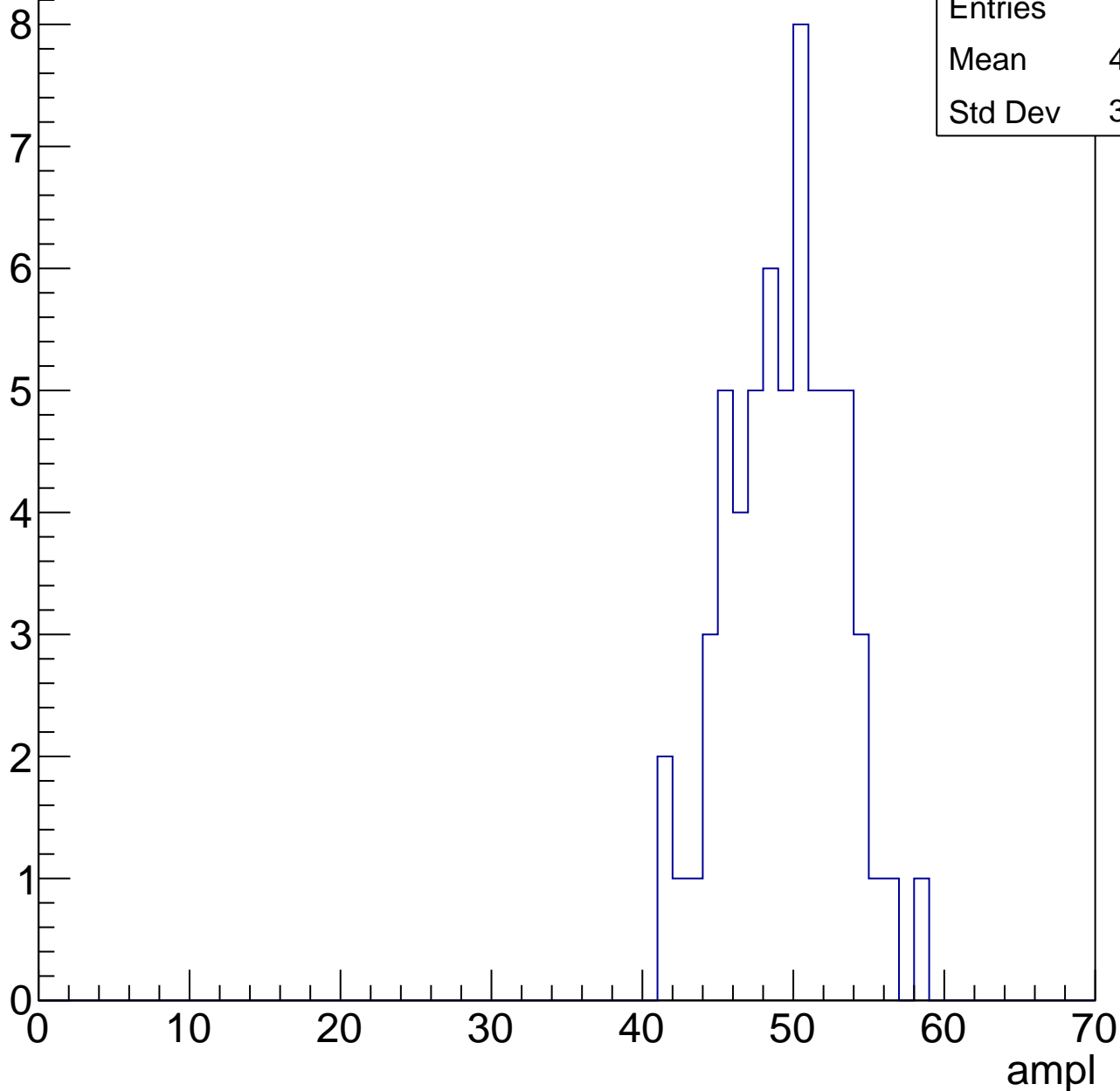


B1L101S, U17-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.97
Std Dev	3.675

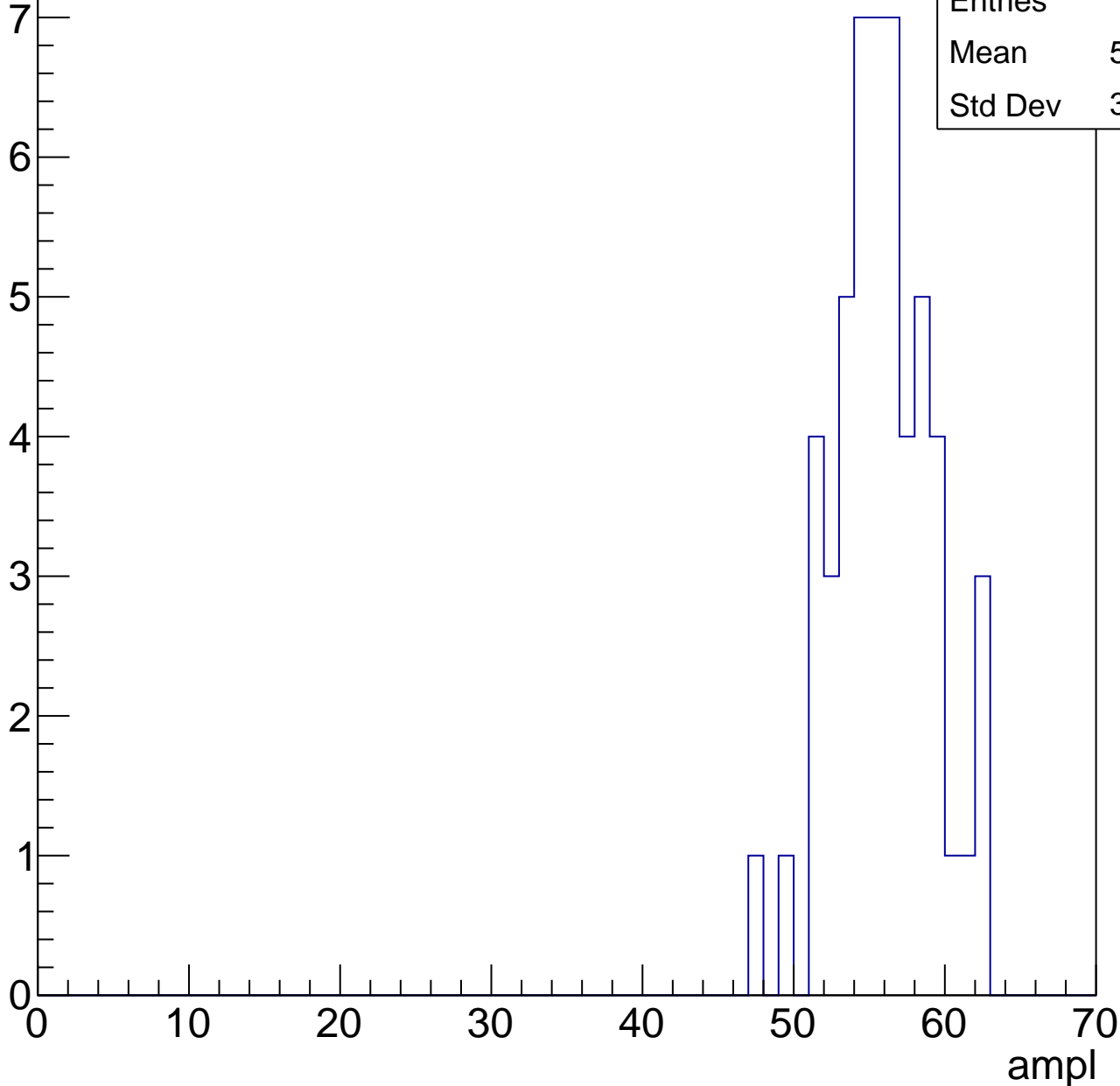


B1L101S, U17-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.42
Std Dev	3.218

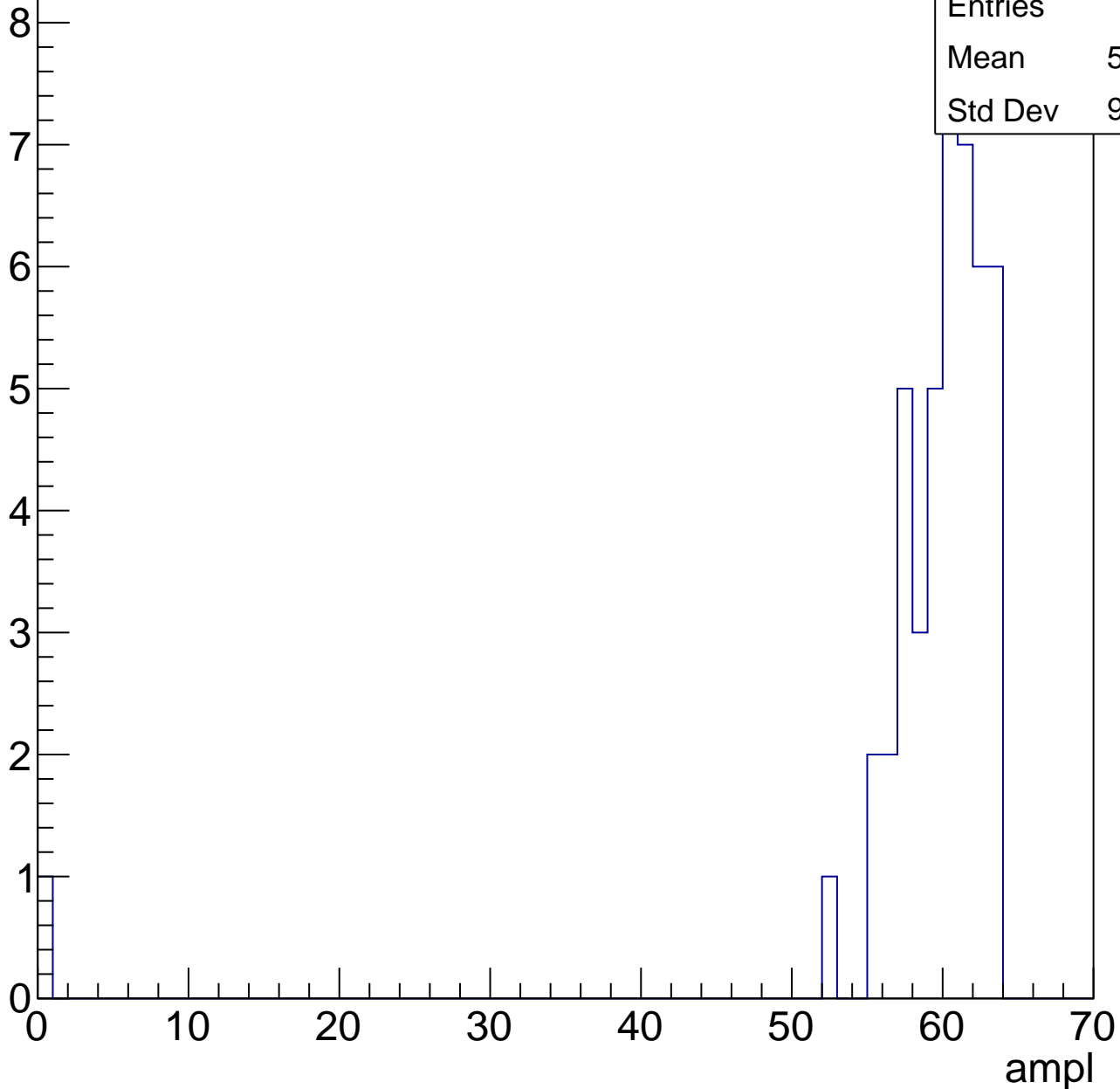


B1L101S, U17-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

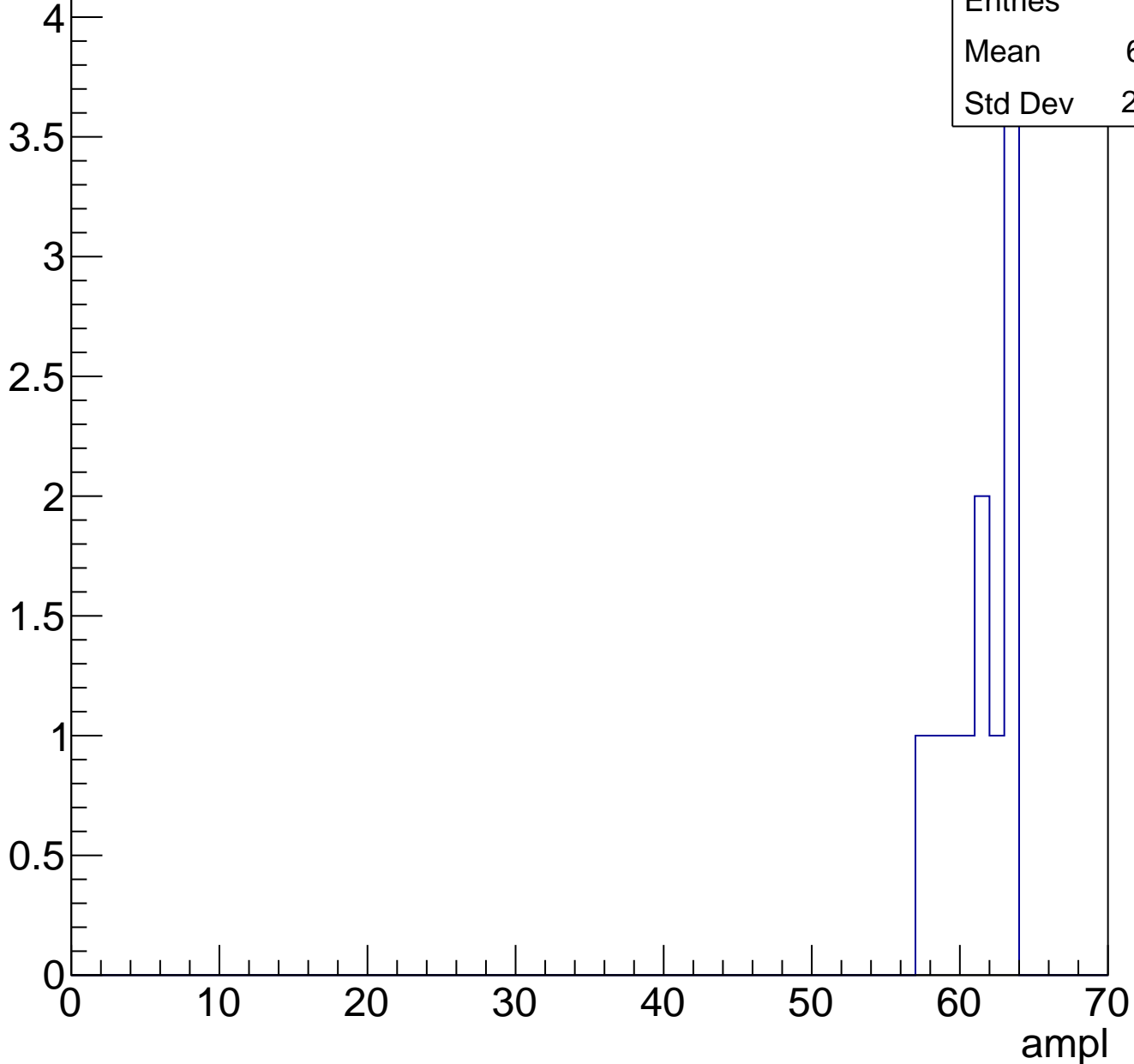
Entries	46
Mean	58.37
Std Dev	9.054



B1L101S, U17-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch109, adc0

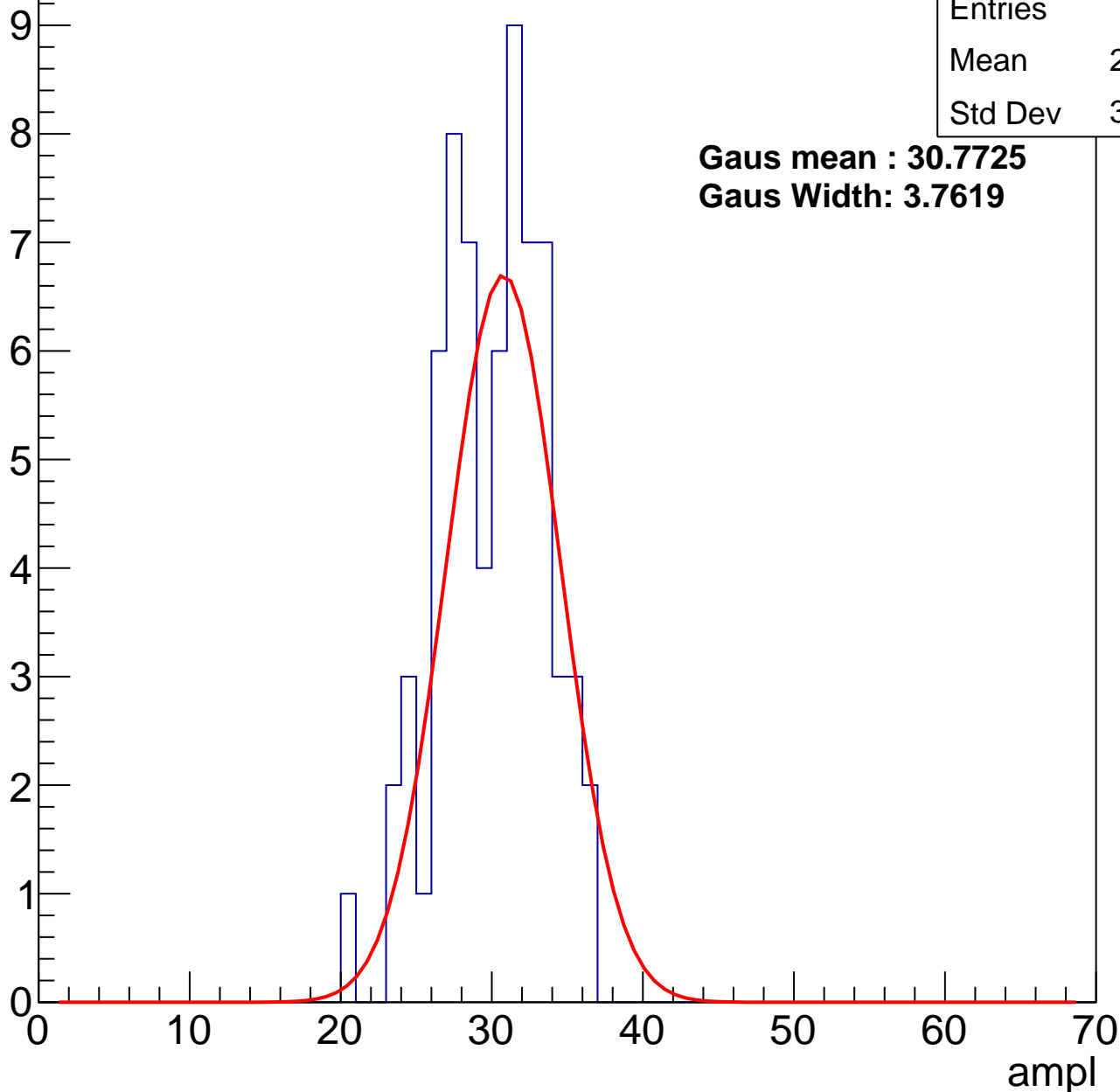
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.57
Std Dev	3.432

Gaus mean : 30.7725

Gaus Width: 3.7619



B1L101S, U17-ch109, adc1

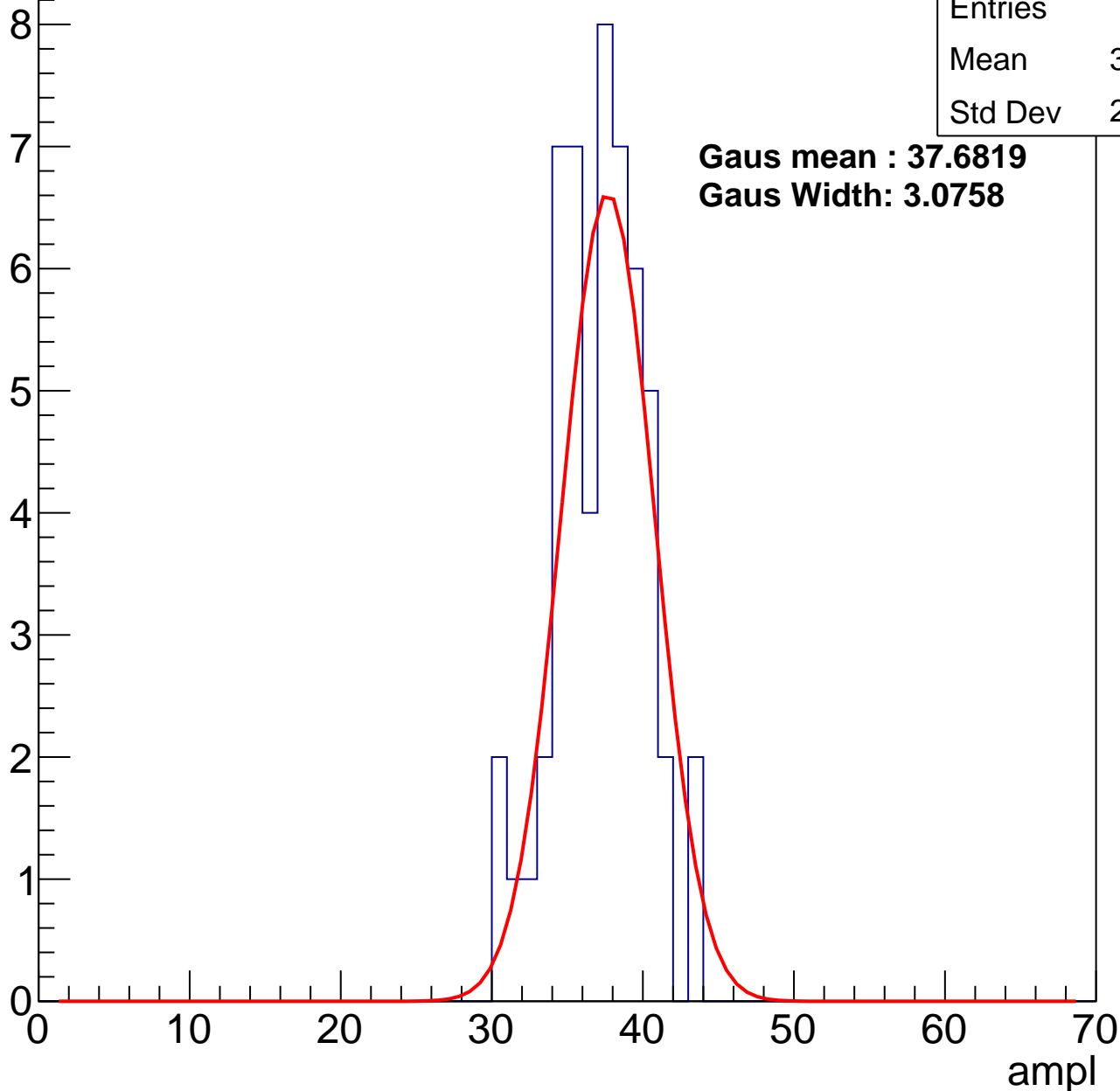
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.67
Std Dev	2.919

Gaus mean : 37.6819

Gaus Width: 3.0758



B1L101S, U17-ch109, adc2

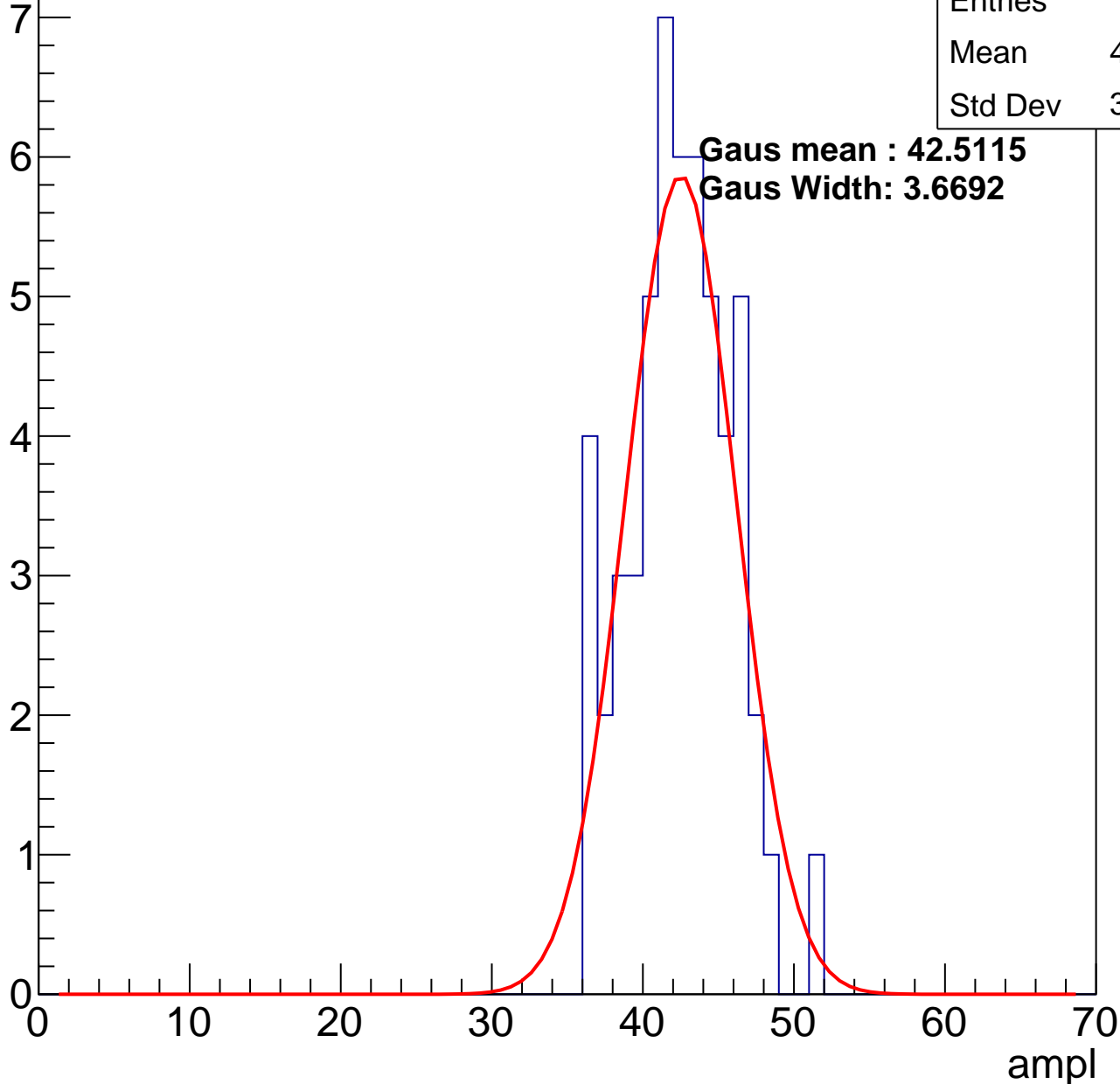
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	42.02
Std Dev	3.347

Gaus mean : 42.5115

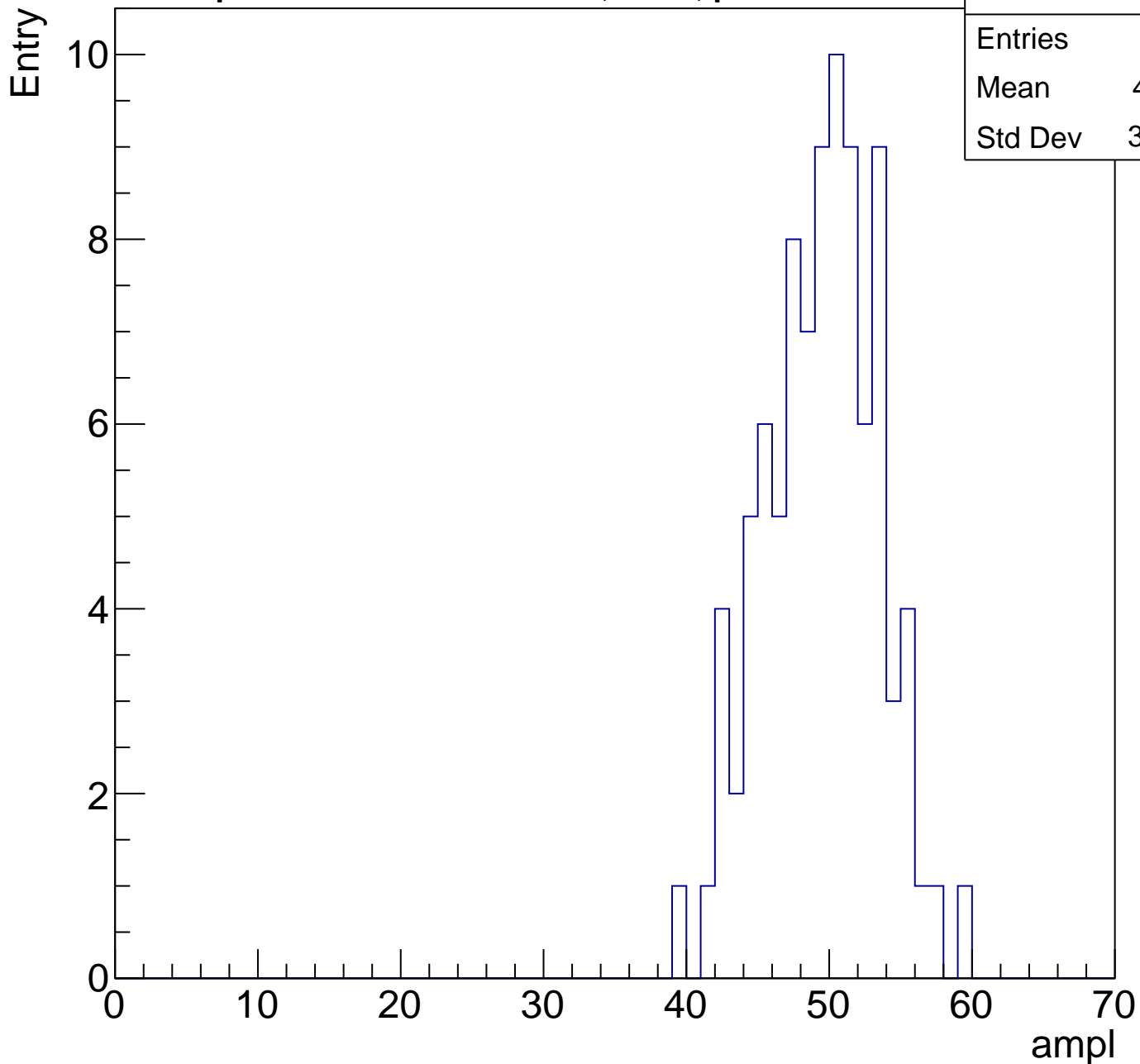
Gaus Width: 3.6692



B1L101S, U17-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	92
Mean	49.01
Std Dev	3.919

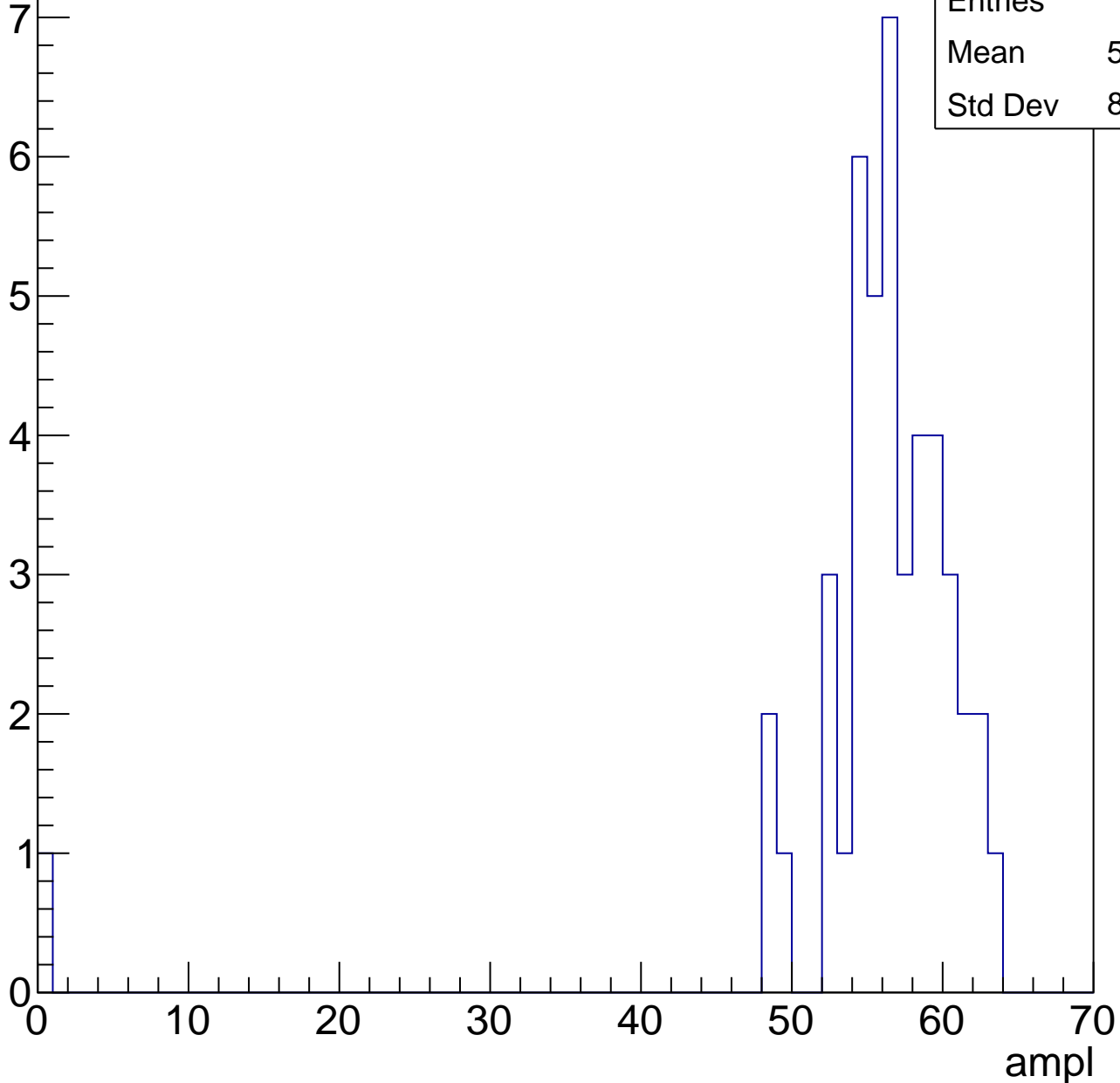


B1L101S, U17-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	54.96
Std Dev	8.974

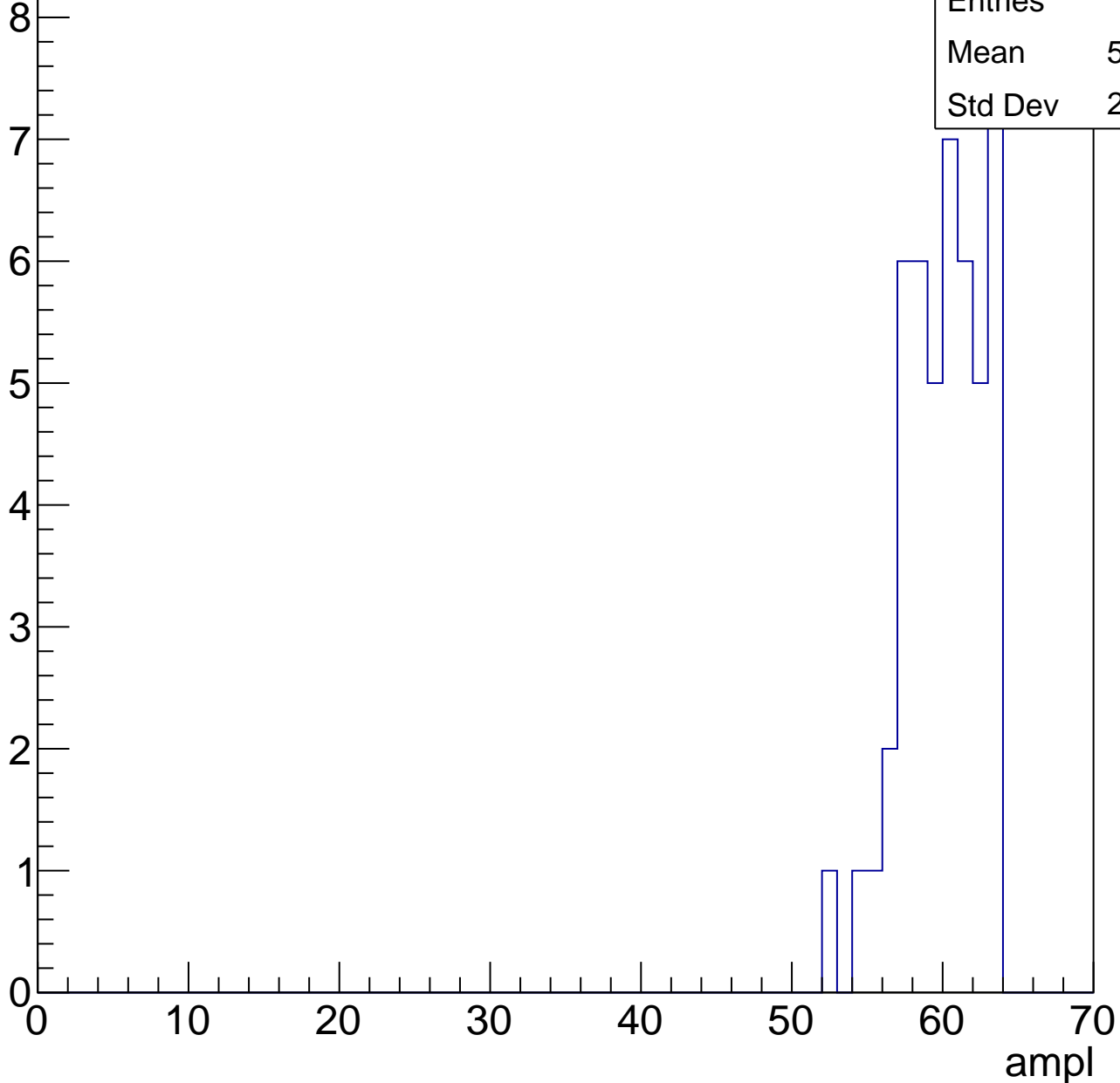


B1L101S, U17-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

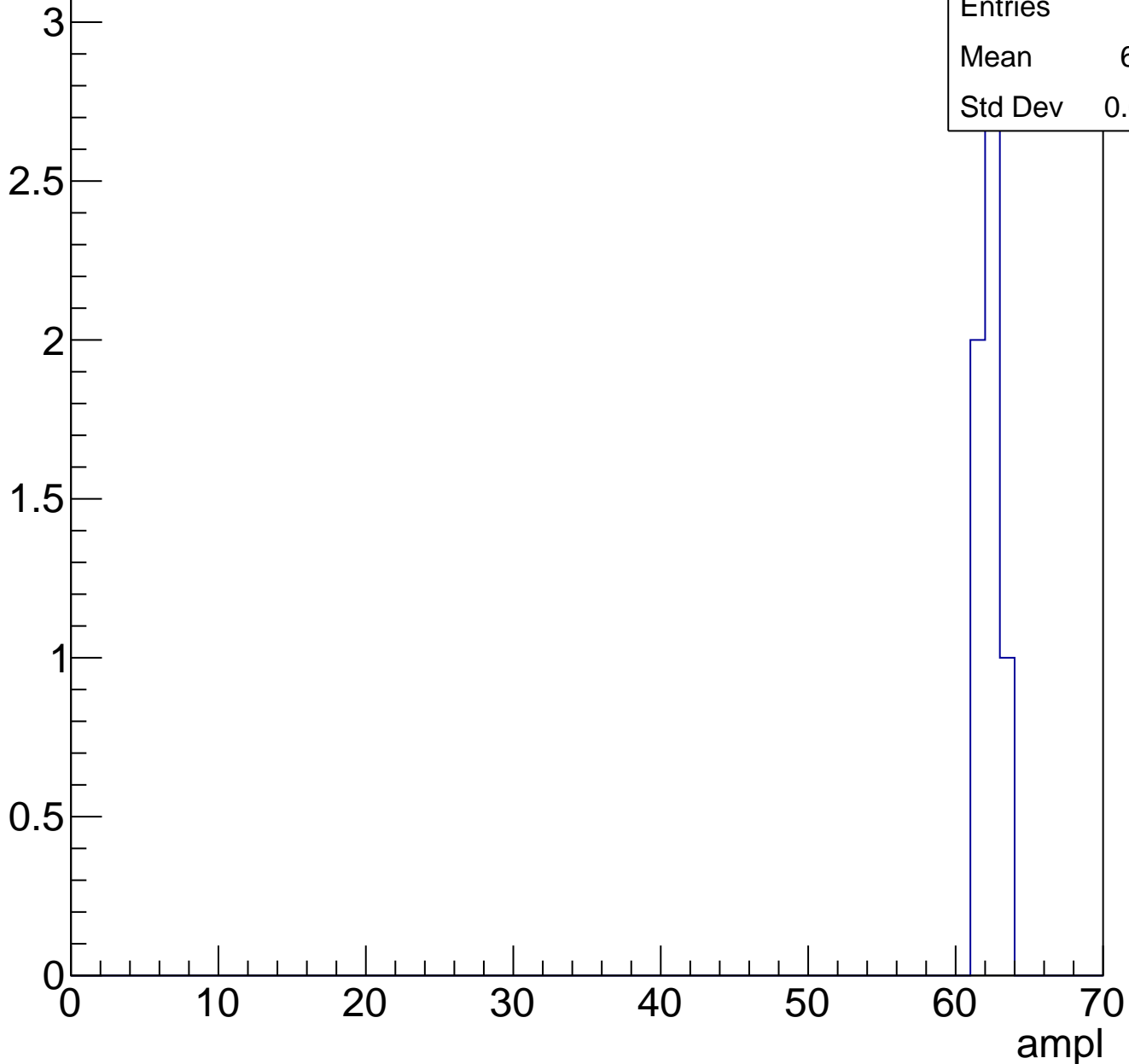
Entries	48
Mean	59.54
Std Dev	2.614



B1L101S, U17-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch110, adc0

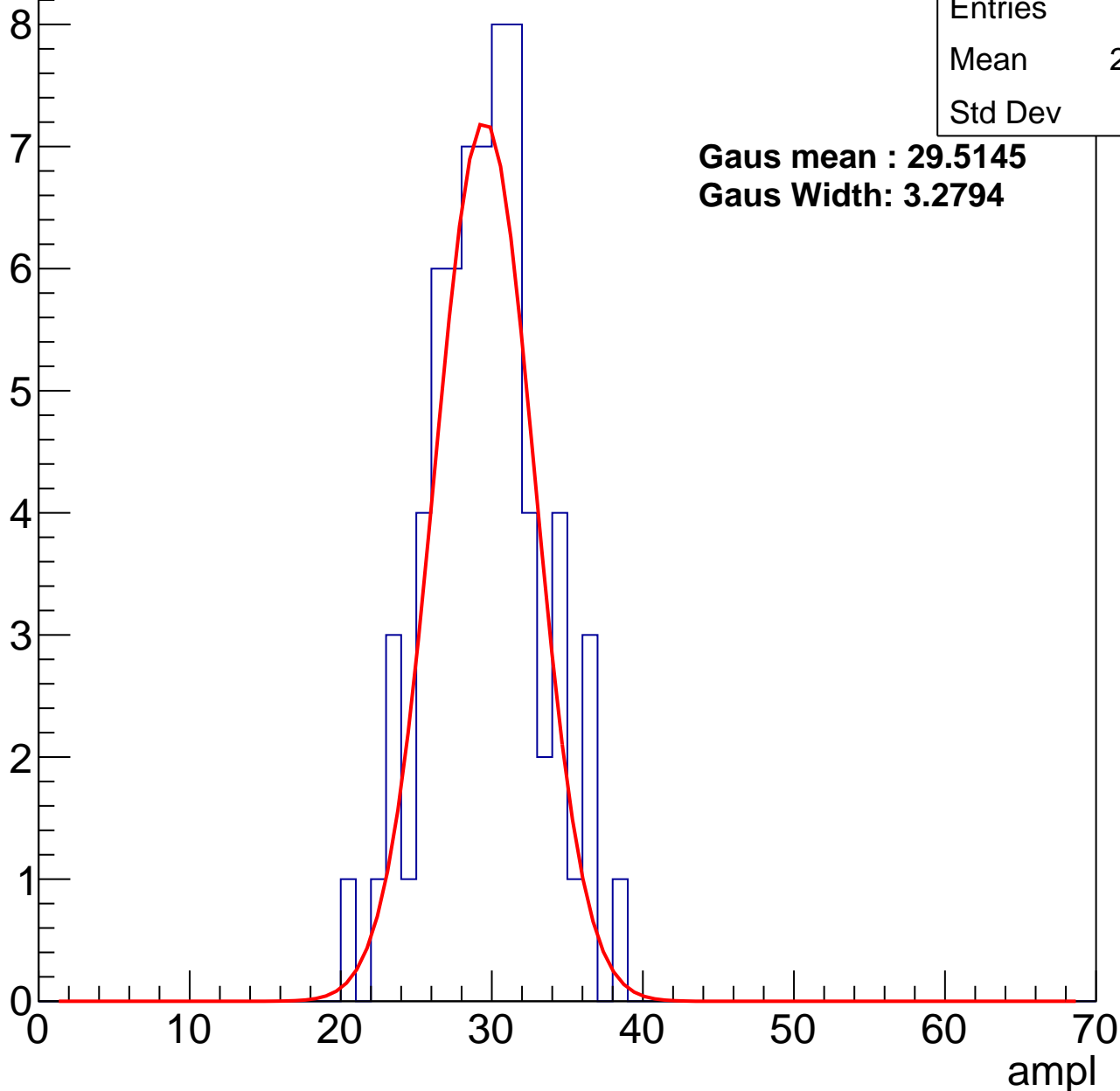
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.12
Std Dev	3.63

Gaus mean : 29.5145

Gaus Width: 3.2794



B1L101S, U17-ch110, adc1

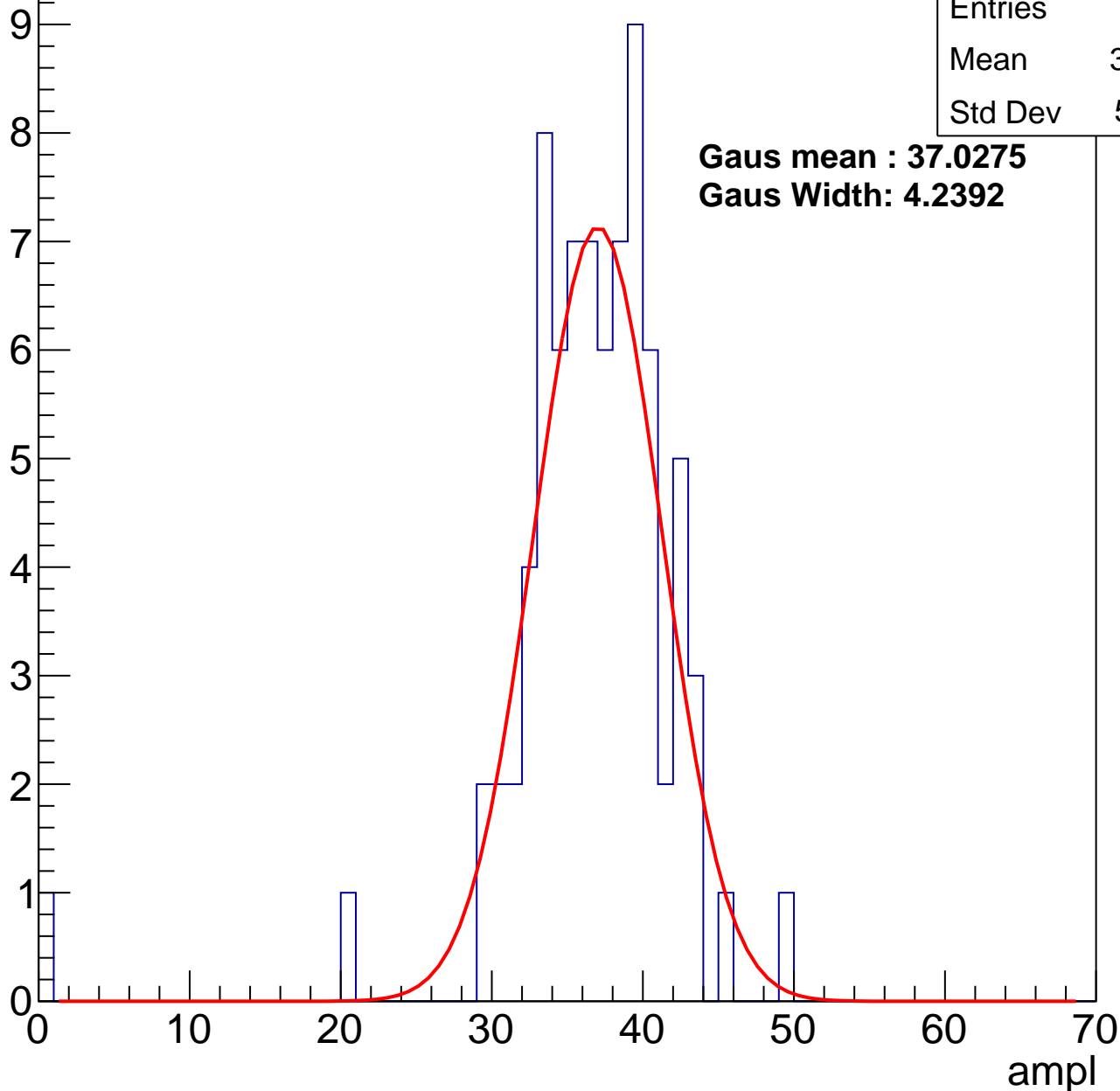
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.09
Std Dev	5.891

Gaus mean : 37.0275

Gaus Width: 4.2392



B1L101S, U17-ch110, adc2

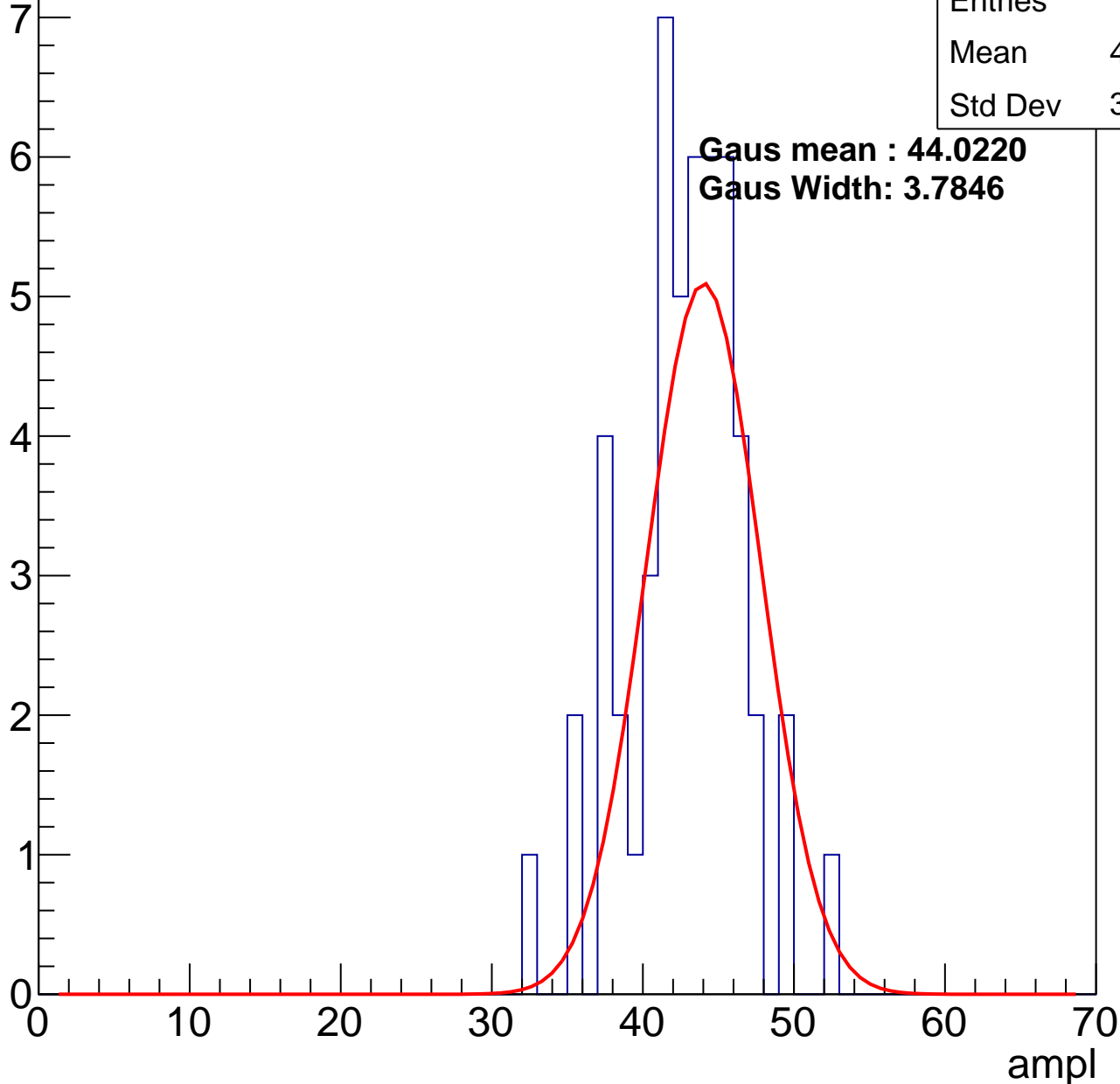
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	42.35
Std Dev	3.792

Gaus mean : 44.0220

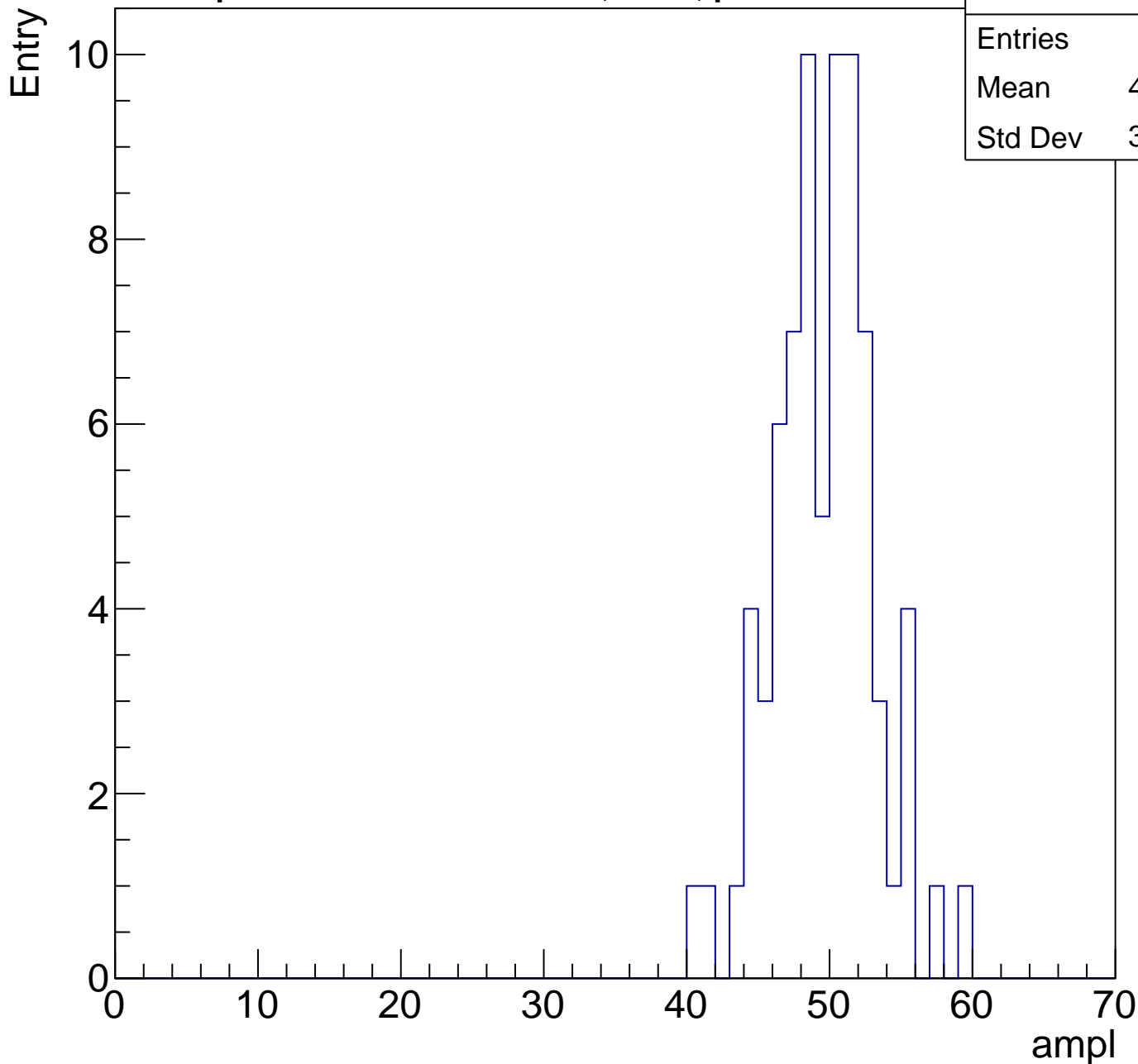
Gaus Width: 3.7846



B1L101S, U17-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	49.17
Std Dev	3.489

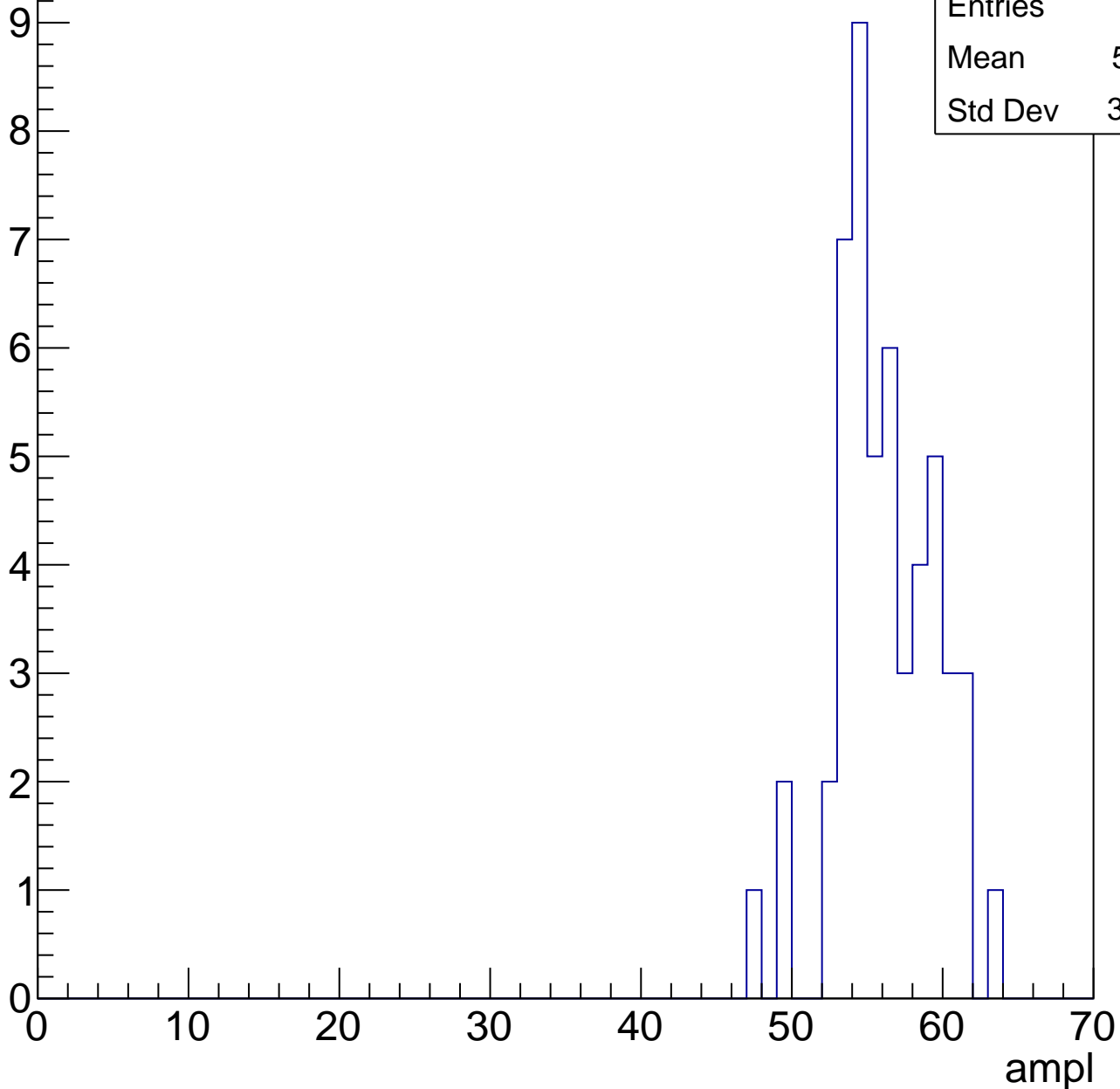


B1L101S, U17-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

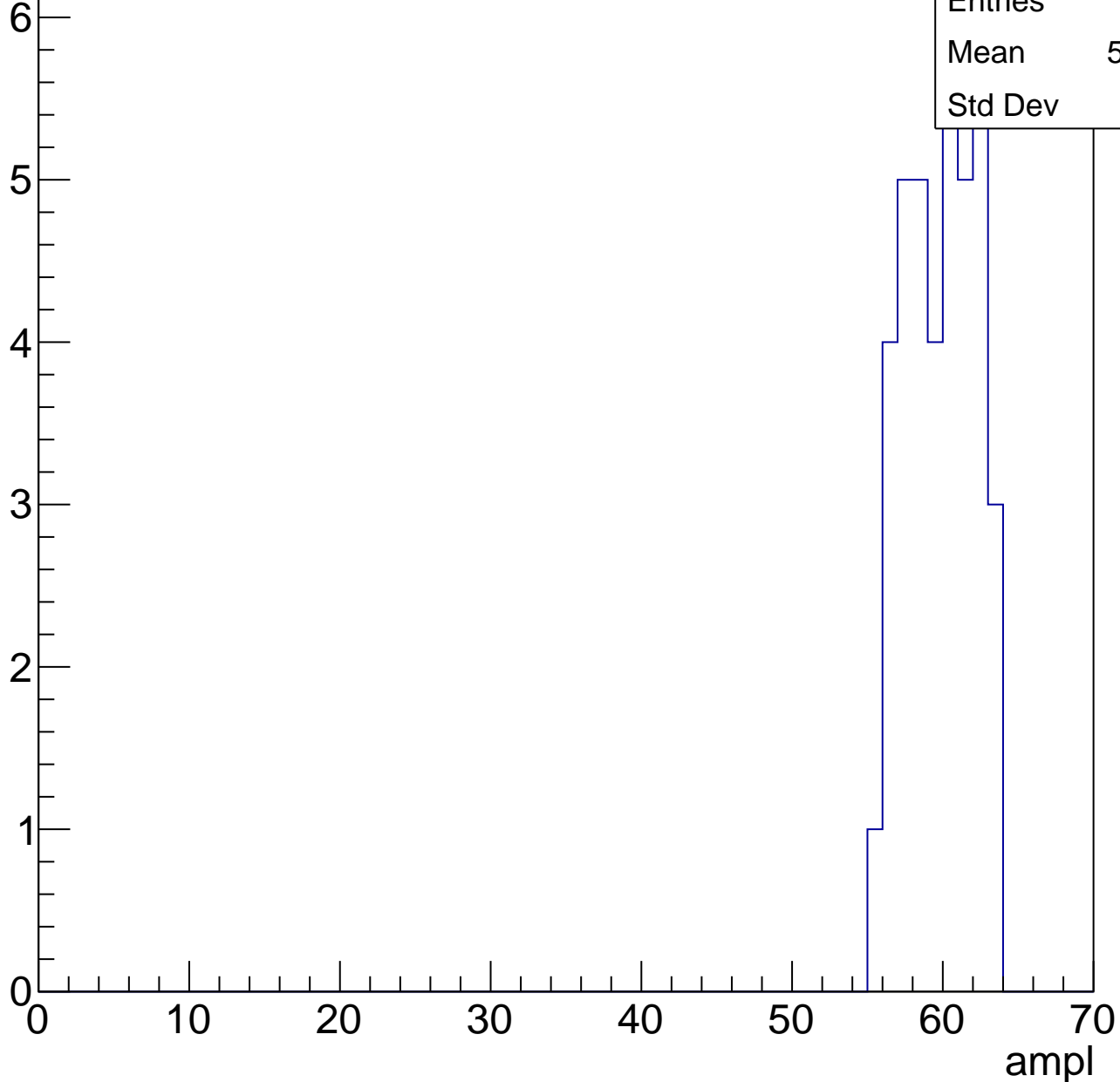
Entries	51
Mean	55.71
Std Dev	3.274



B1L101S, U17-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

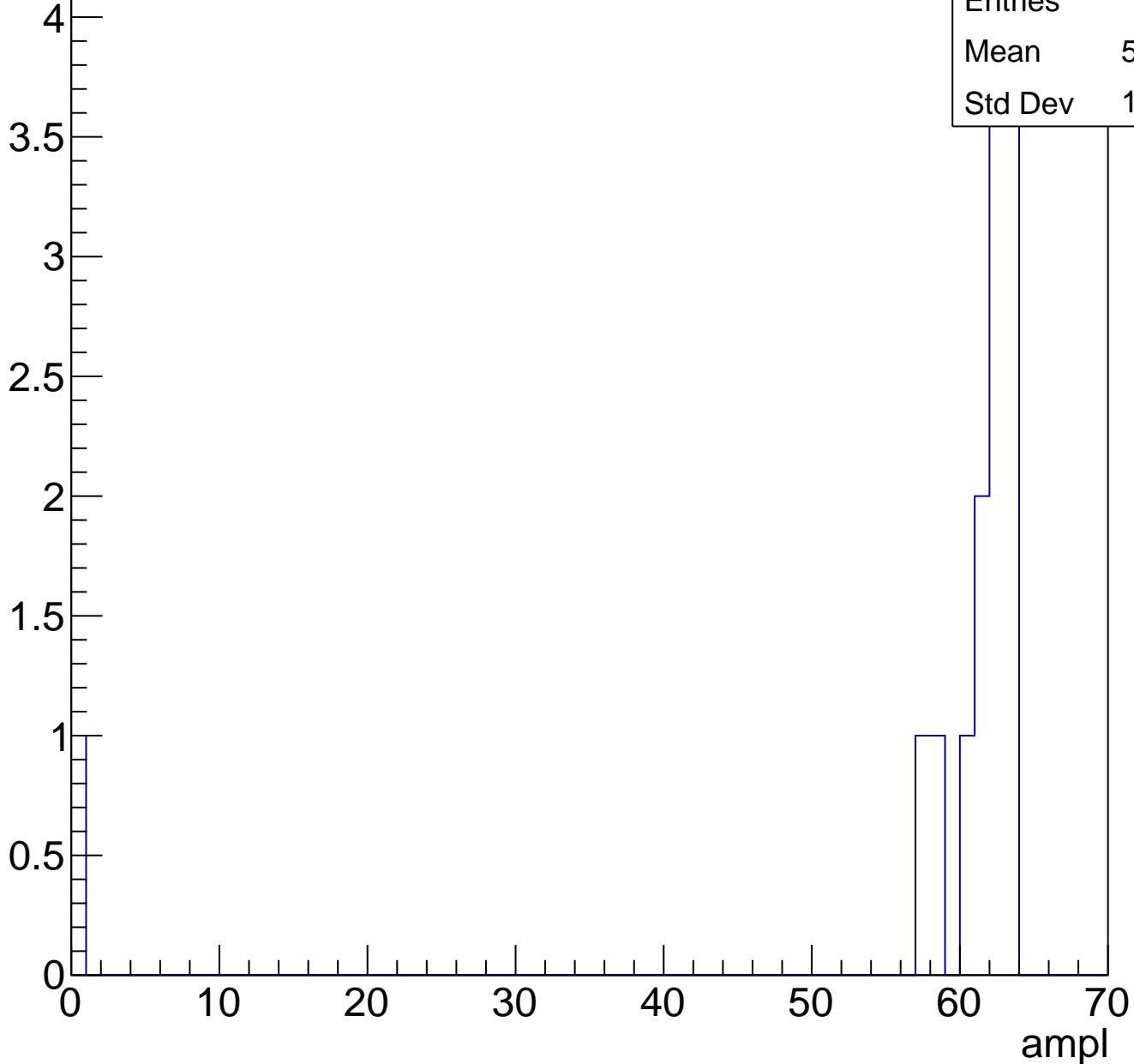


Entries	39
Mean	59.38
Std Dev	2.26

B1L101S, U17-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch111, adc0

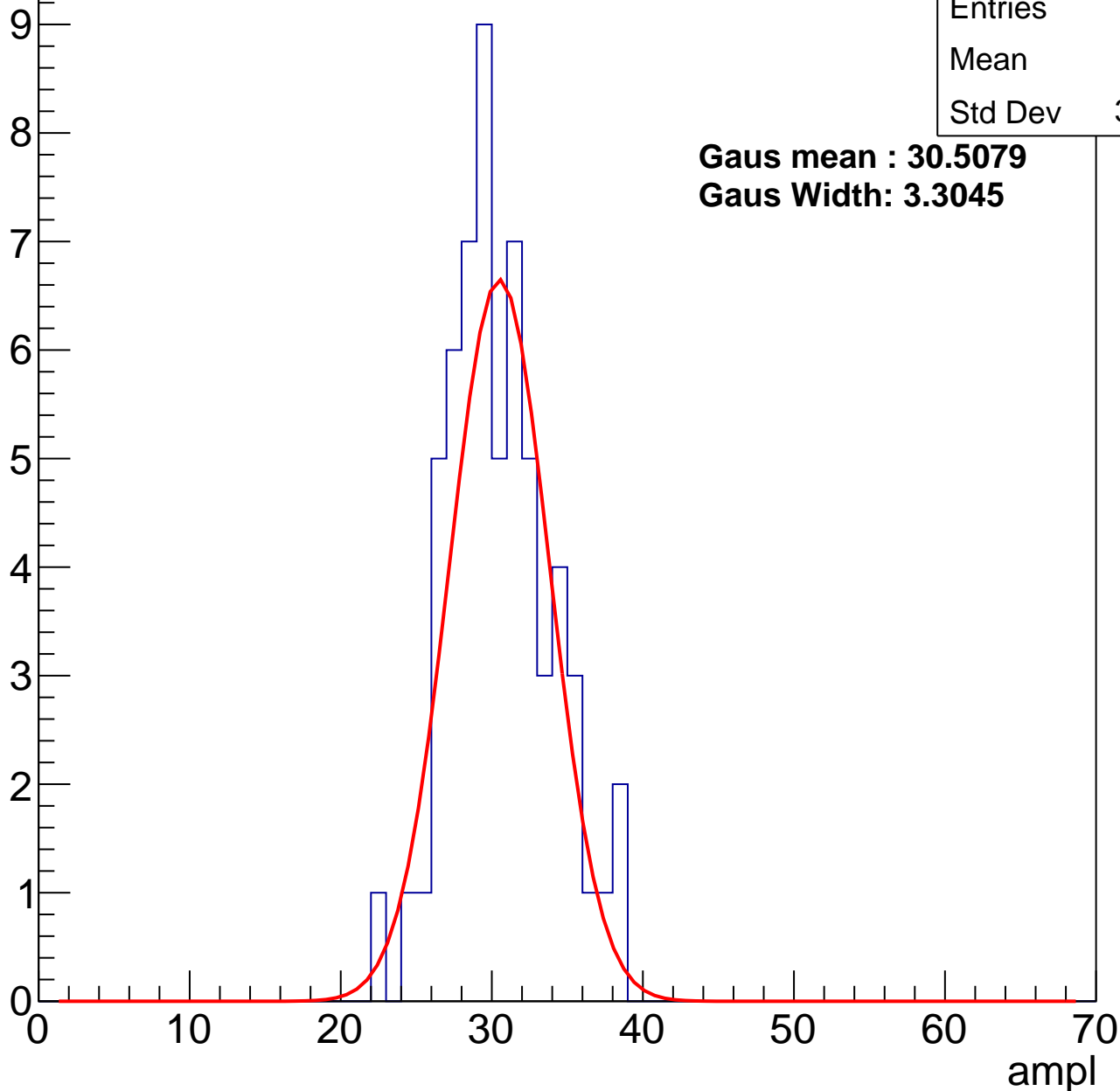
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	30.1
Std Dev	3.391

Gaus mean : 30.5079

Gaus Width: 3.3045



B1L101S, U17-ch111, adc1

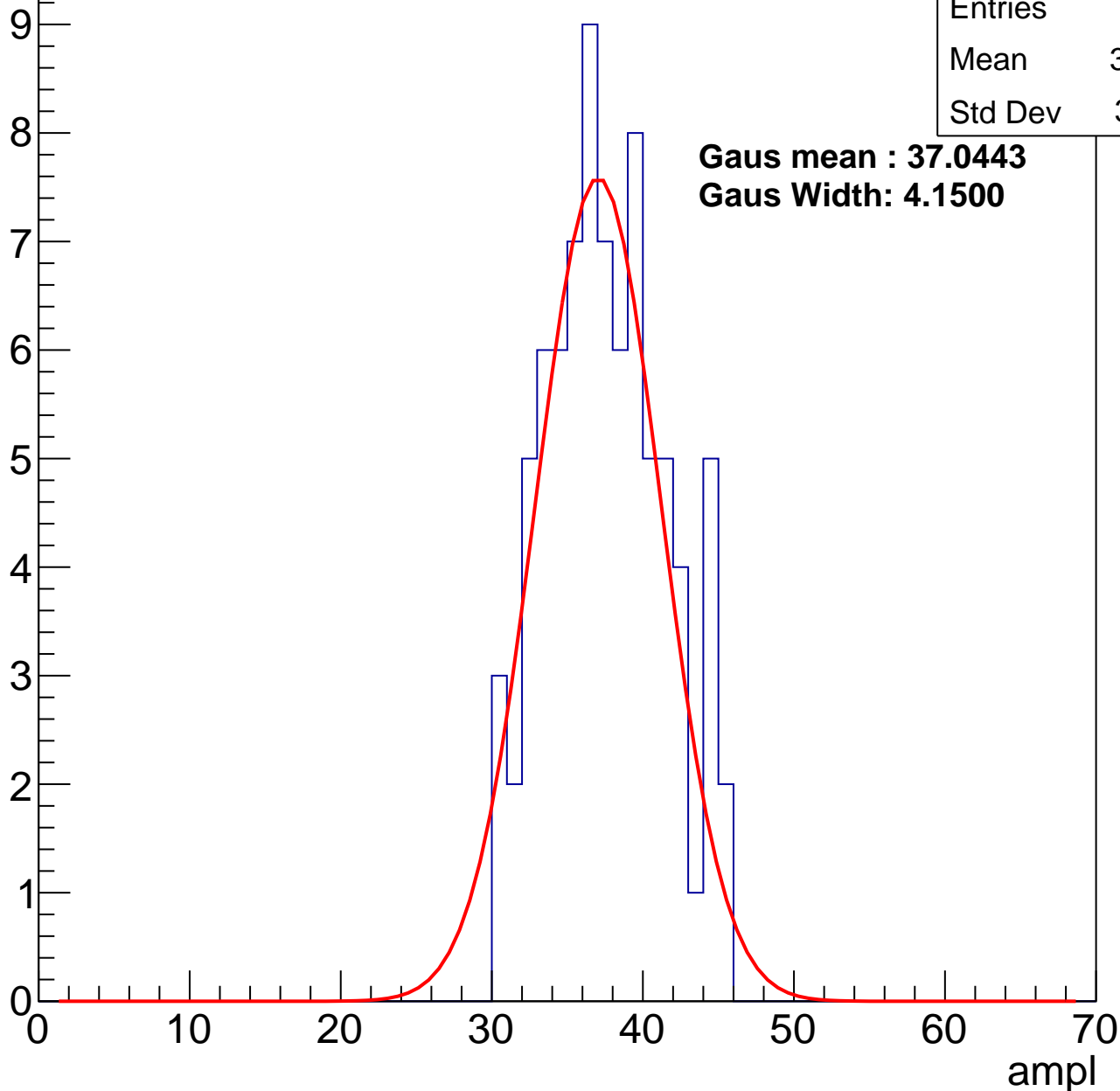
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.14
Std Dev	3.851

Gaus mean : 37.0443

Gaus Width: 4.1500



B1L101S, U17-ch111, adc2

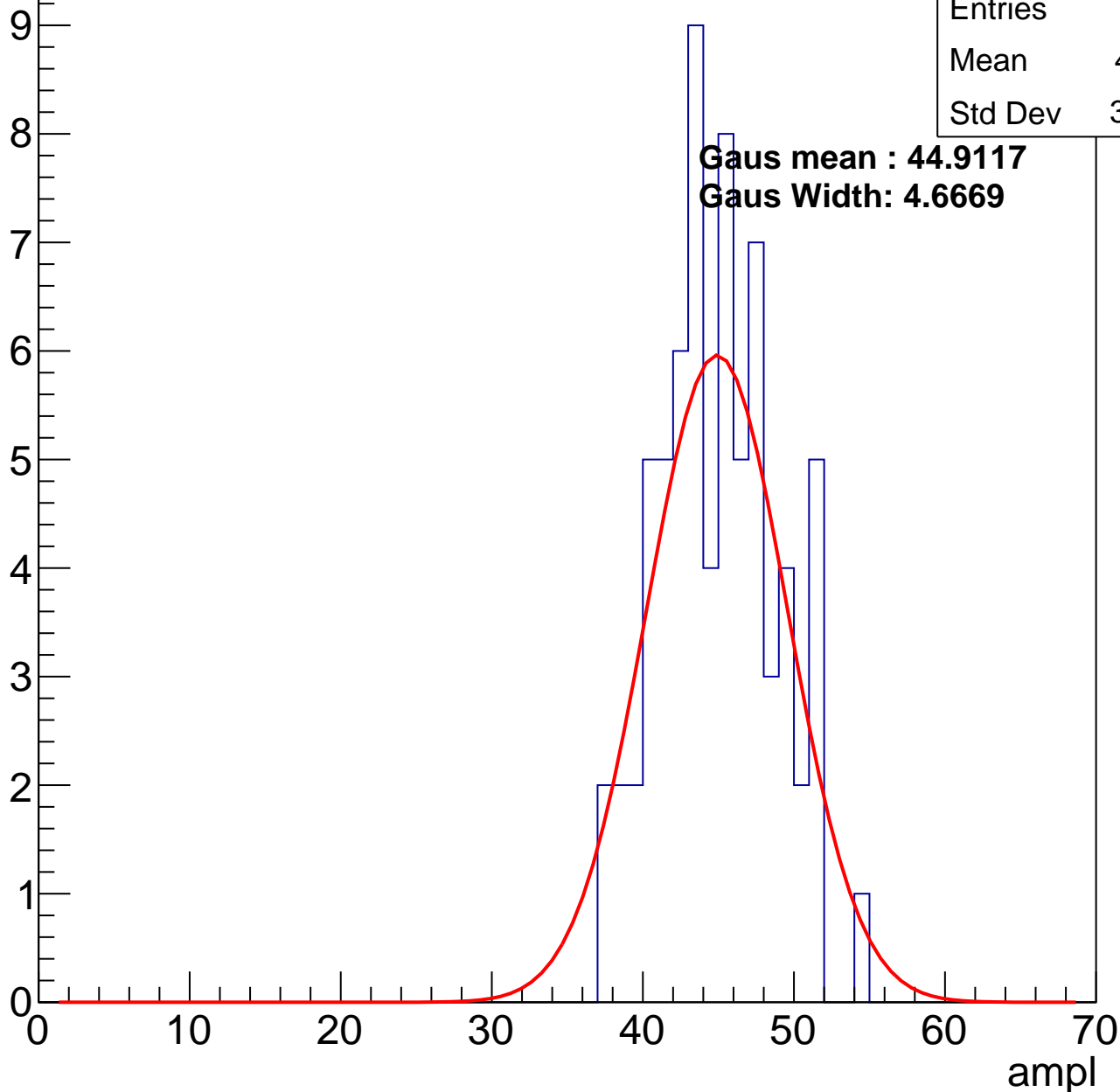
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	44.51
Std Dev	3.809

Gaus mean : 44.9117

Gaus Width: 4.6669

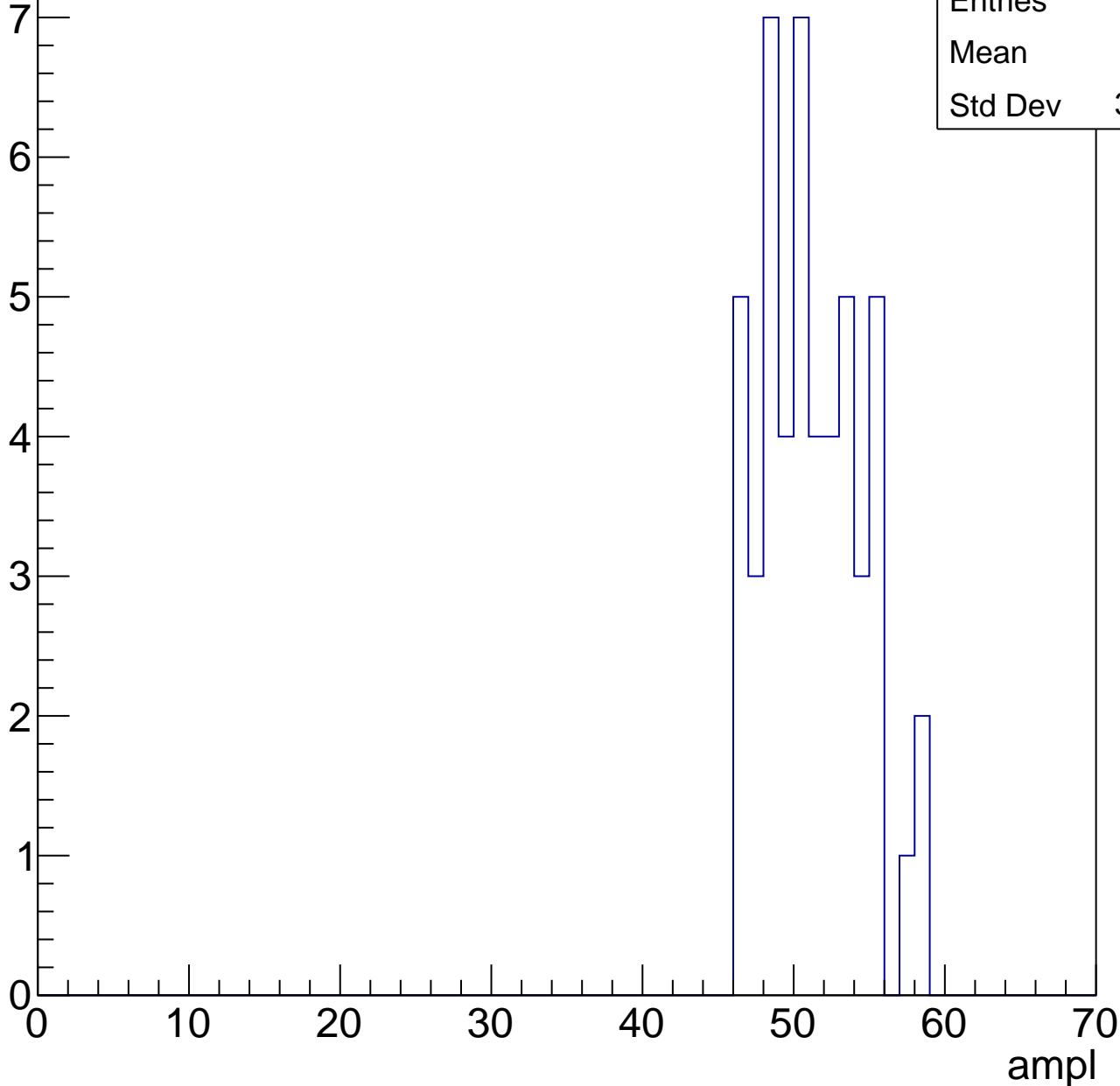


B1L101S, U17-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	50.8
Std Dev	3.231

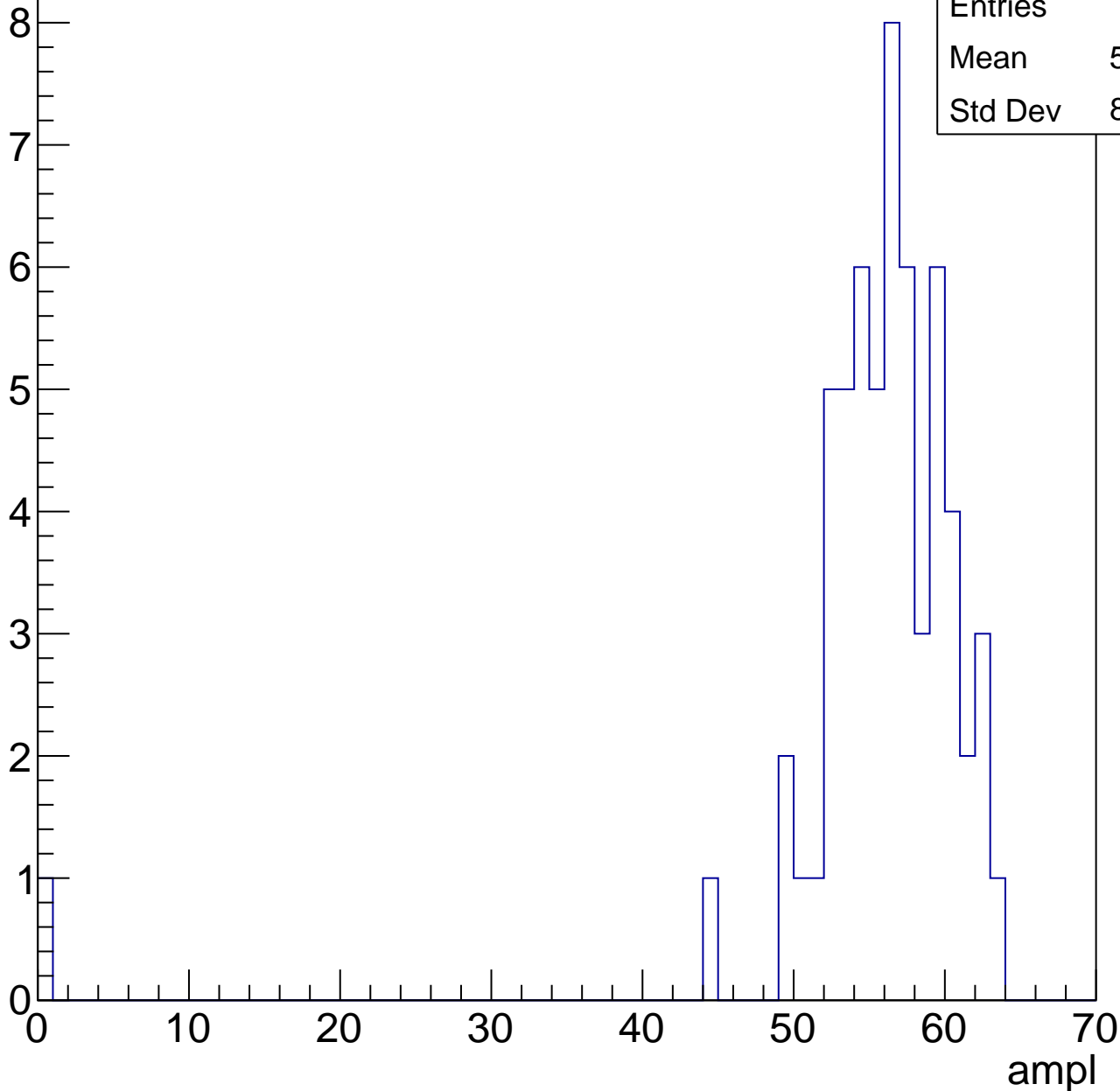


B1L101S, U17-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.93
Std Dev	8.029

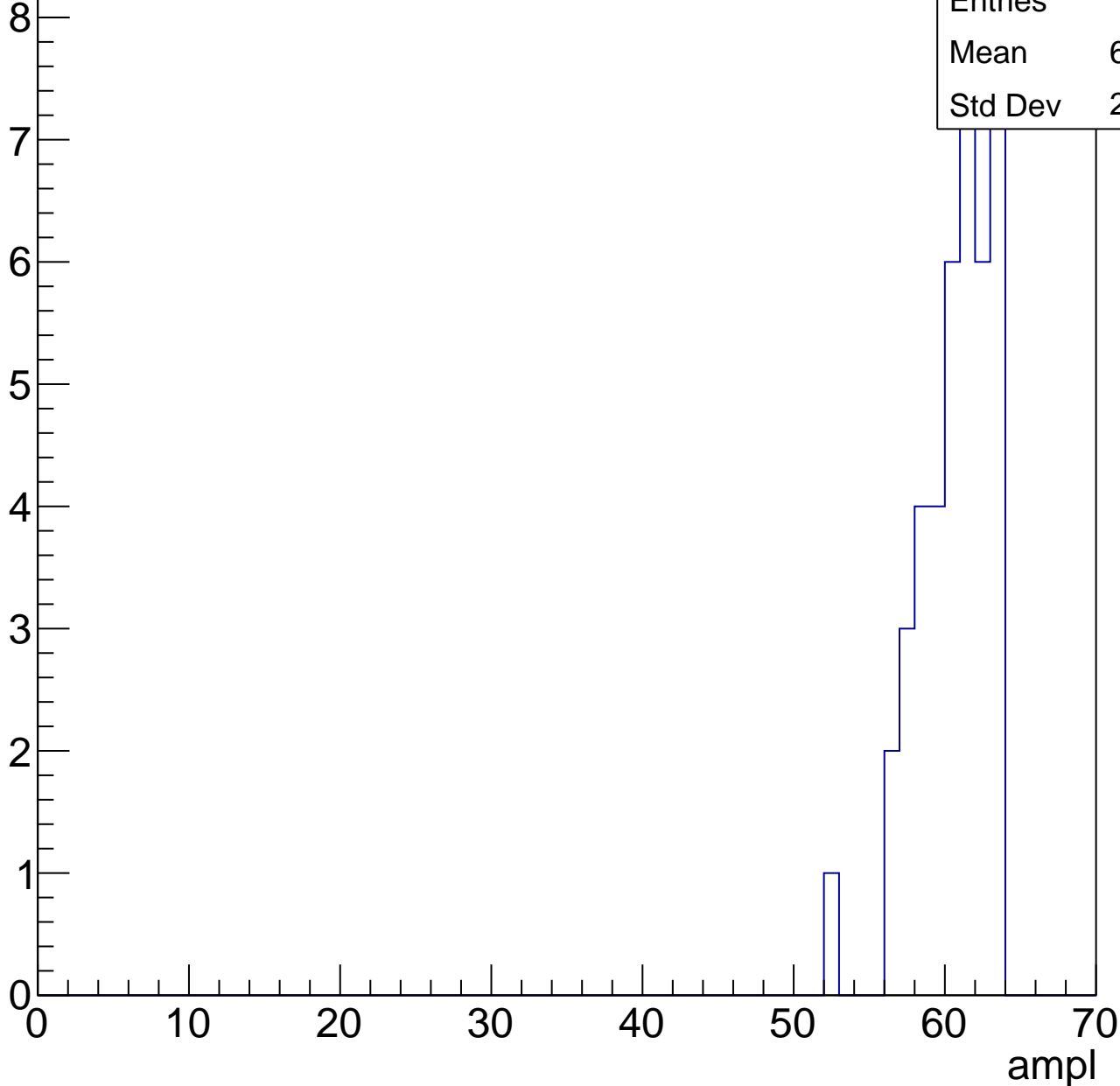


B1L101S, U17-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	60.17
Std Dev	2.419



B1L101S, U17-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61
Std Dev	2.16

ampl

B1L101S, U17-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch112, adc0

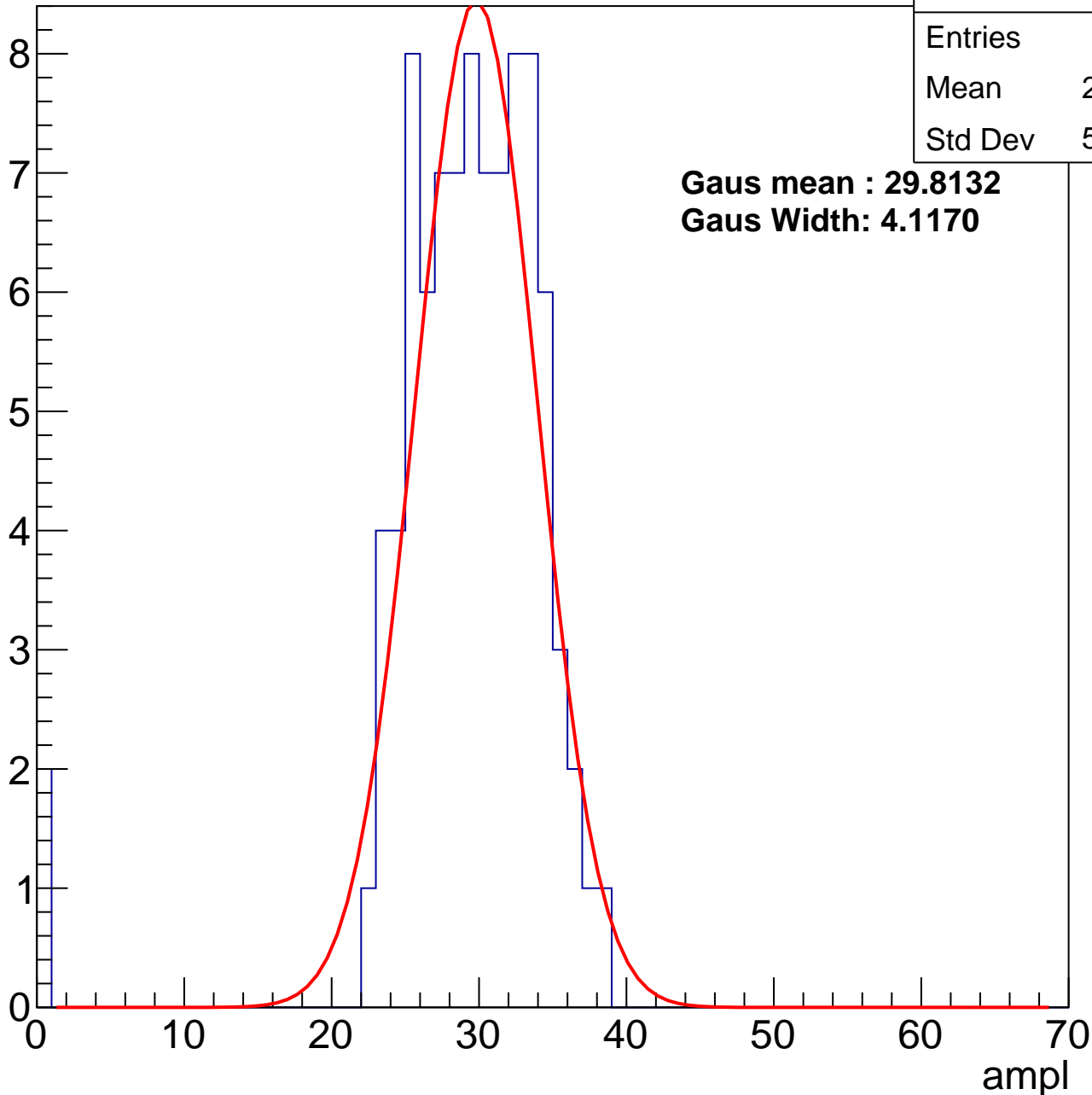
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	28.73
Std Dev	5.698

Gaus mean : 29.8132

Gaus Width: 4.1170



B1L101S, U17-ch112, adc1

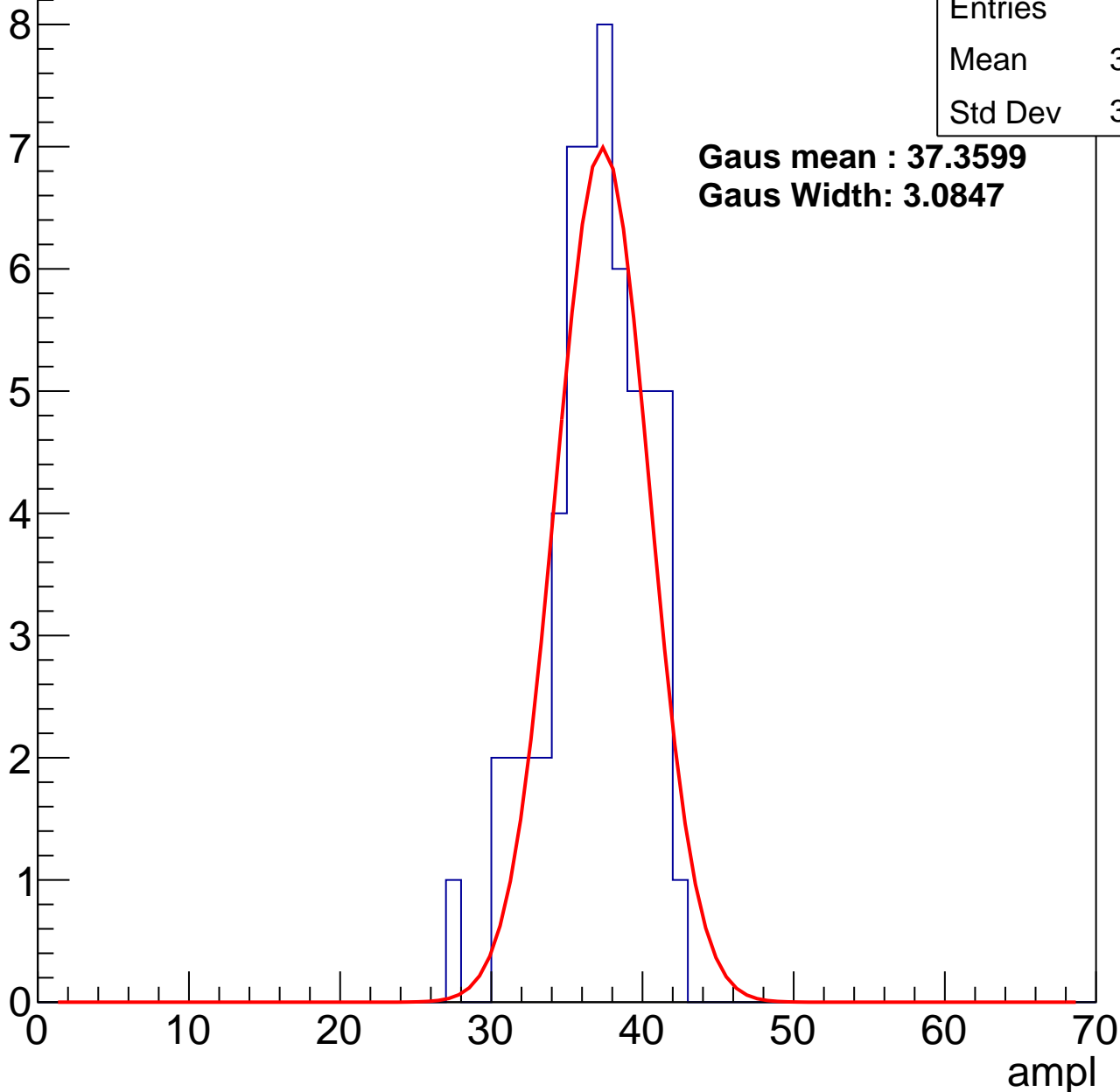
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.46
Std Dev	3.196

Gaus mean : 37.3599

Gaus Width: 3.0847



B1L101S, U17-ch112, adc2

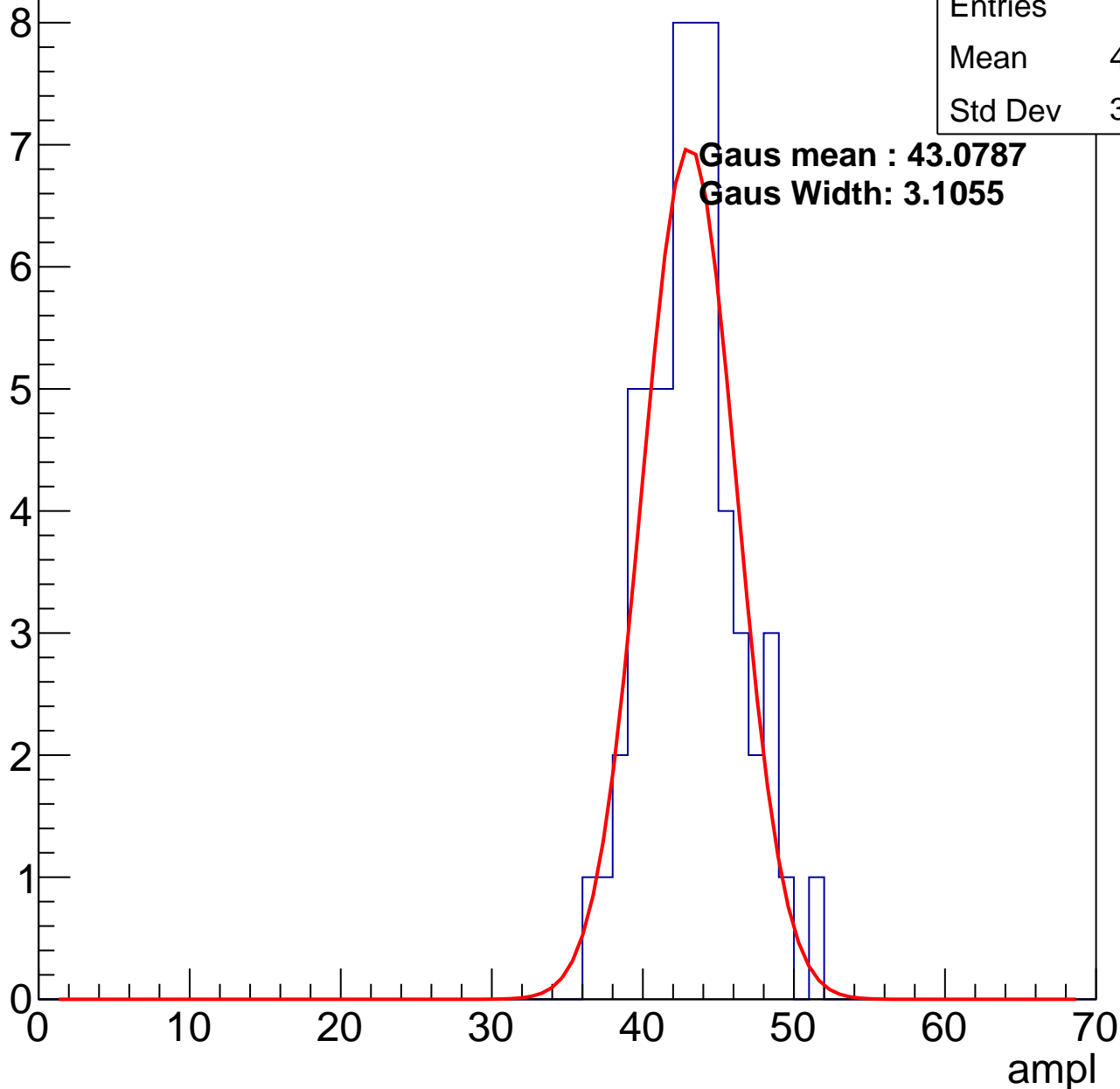
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.75
Std Dev	3.085

Gaus mean : 43.0787

Gaus Width: 3.1055

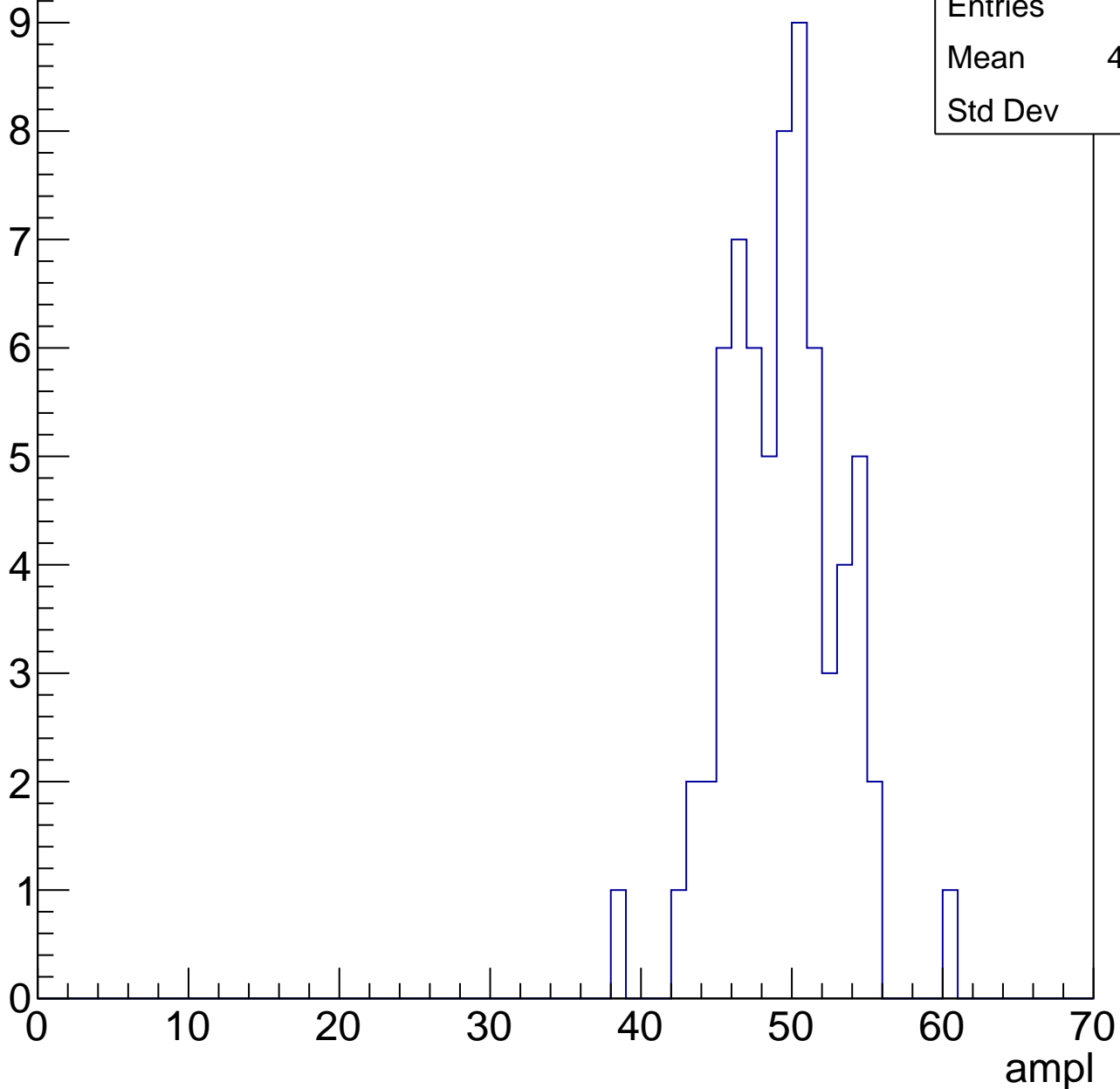


B1L101S, U17-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.88
Std Dev	3.68

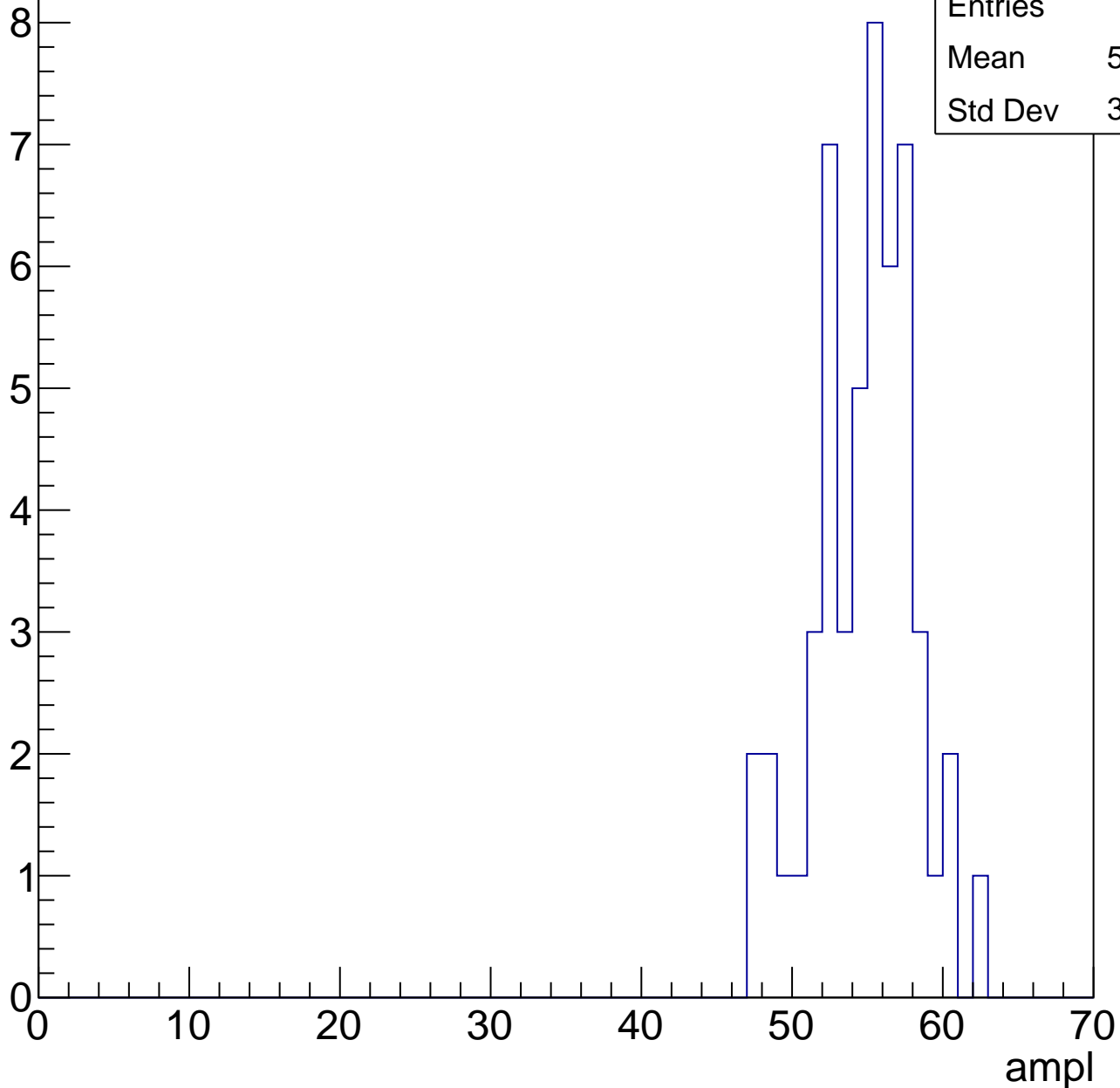


B1L101S, U17-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.33
Std Dev	3.303

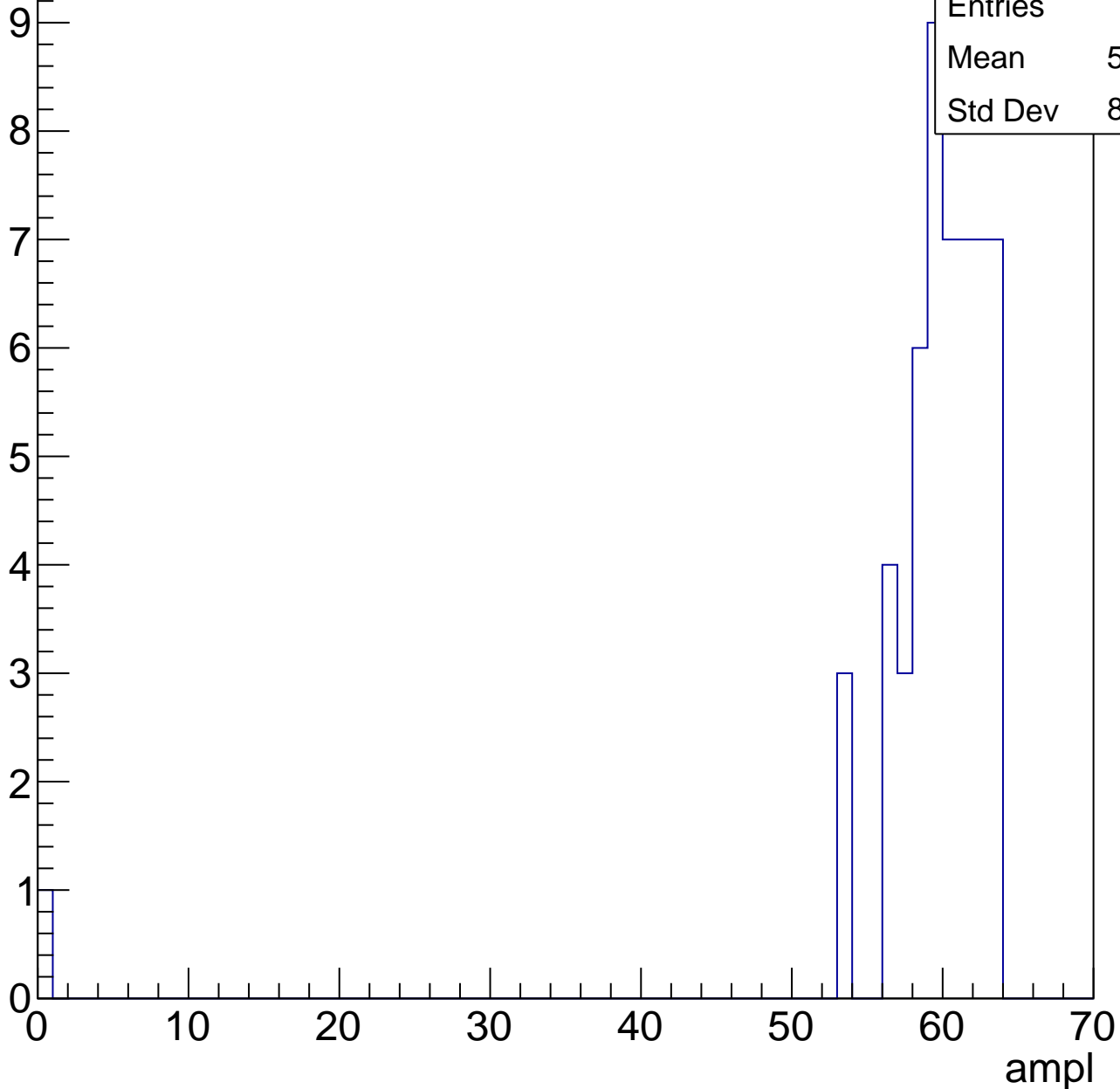


B1L101S, U17-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

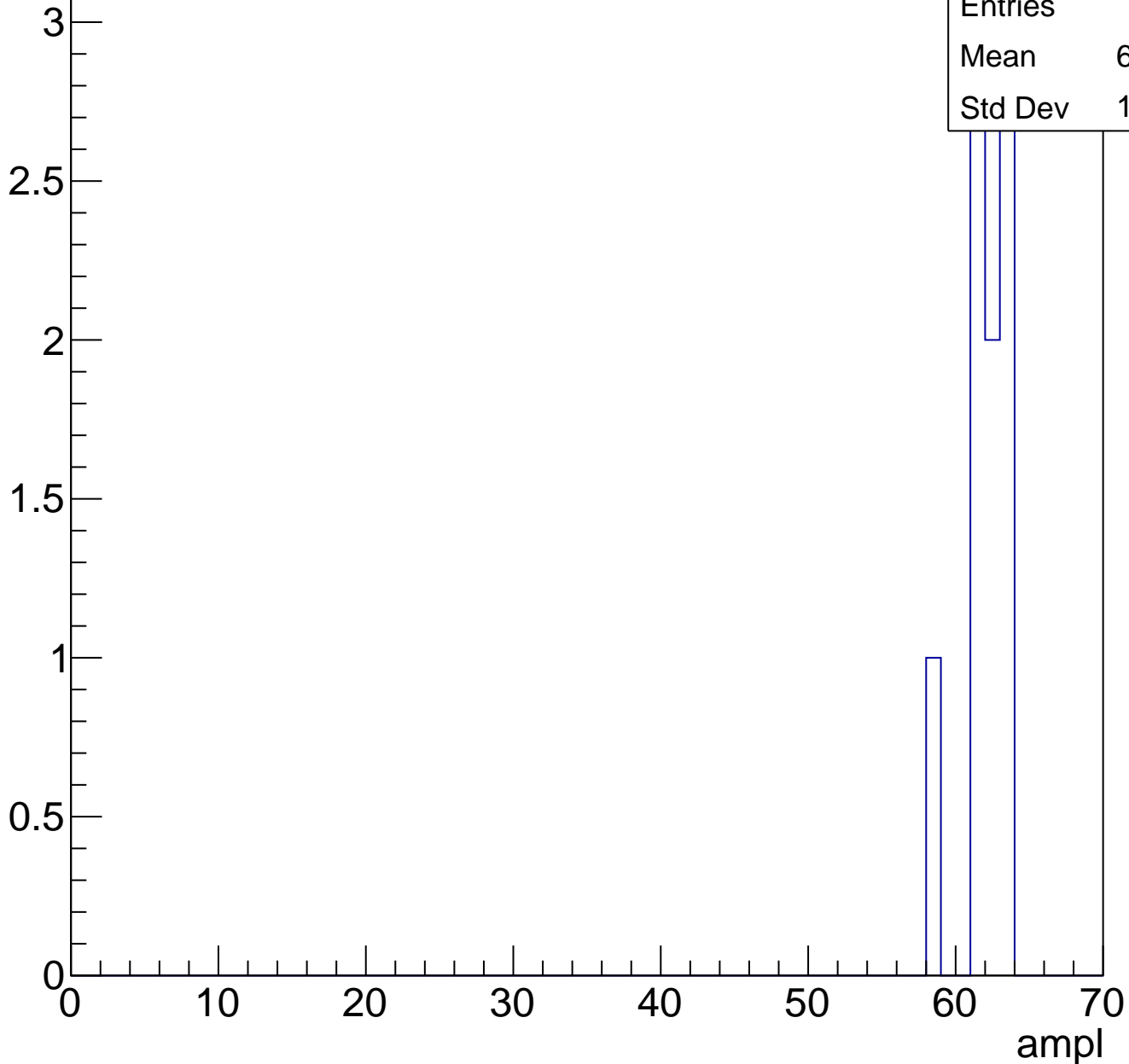
Entries	54
Mean	58.43
Std Dev	8.427



B1L101S, U17-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch113, adc0

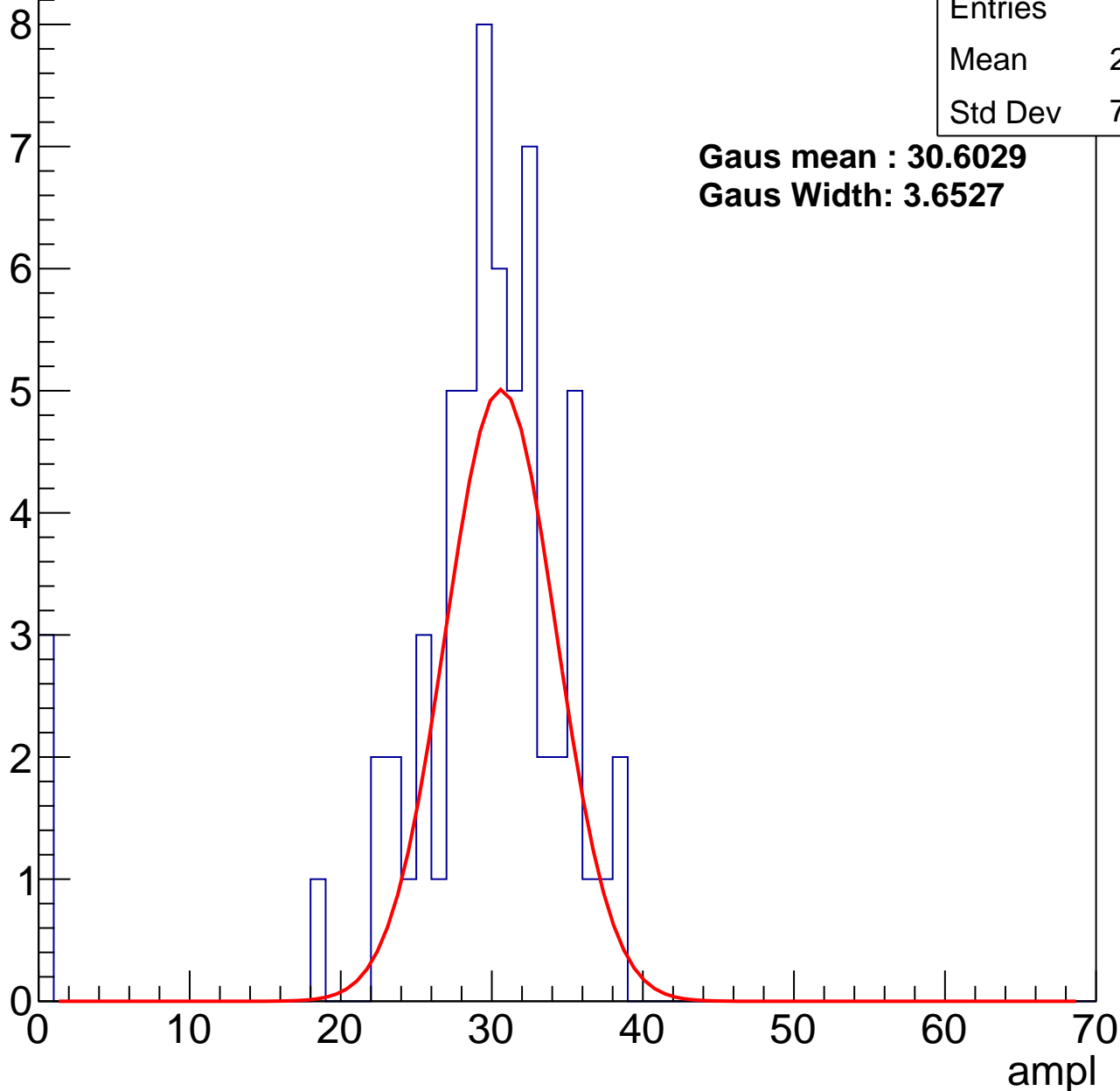
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.34
Std Dev	7.535

Gaus mean : 30.6029

Gaus Width: 3.6527



B1L101S, U17-ch113, adc1

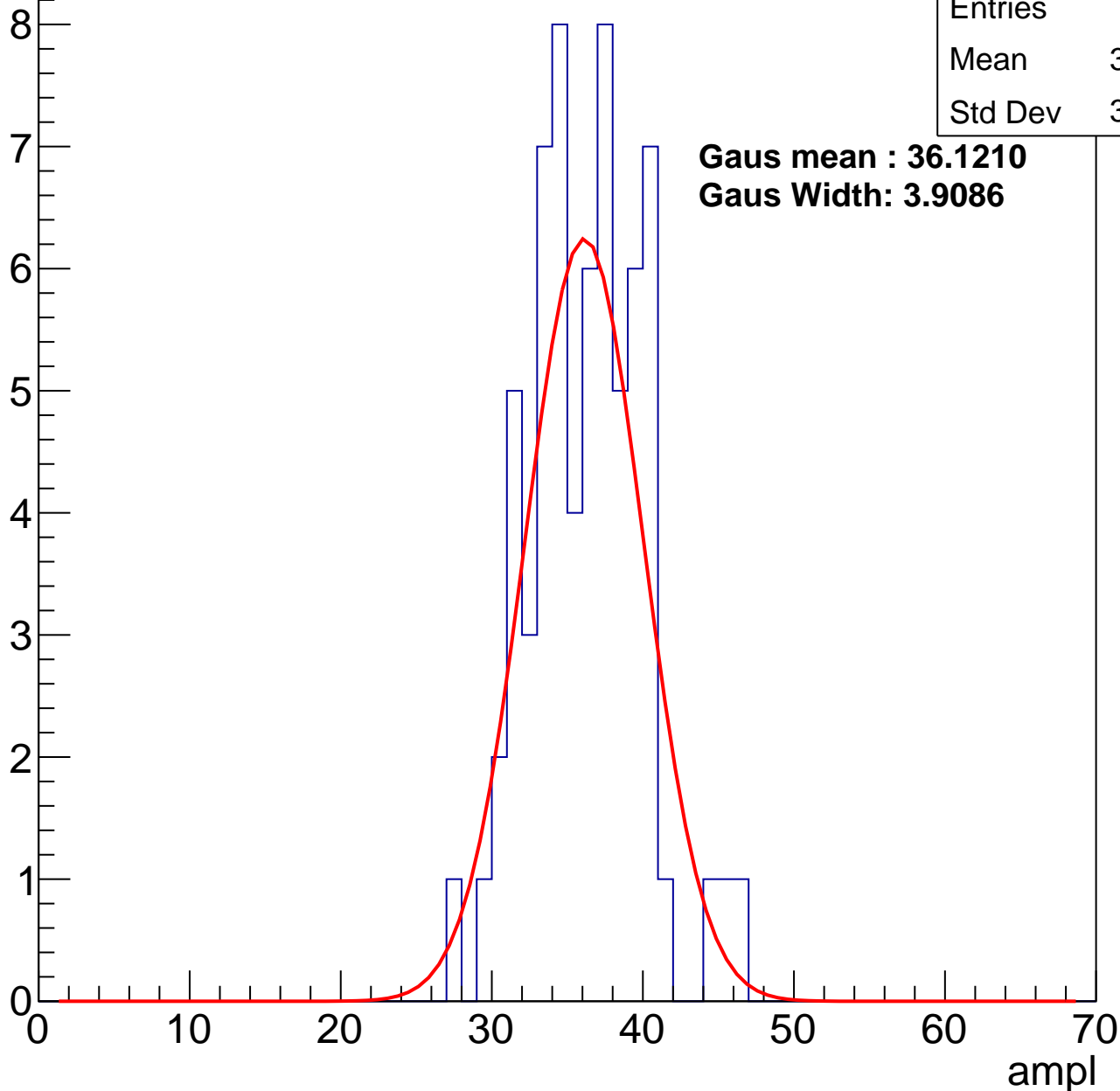
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.85
Std Dev	3.739

Gaus mean : 36.1210

Gaus Width: 3.9086



B1L101S, U17-ch113, adc2

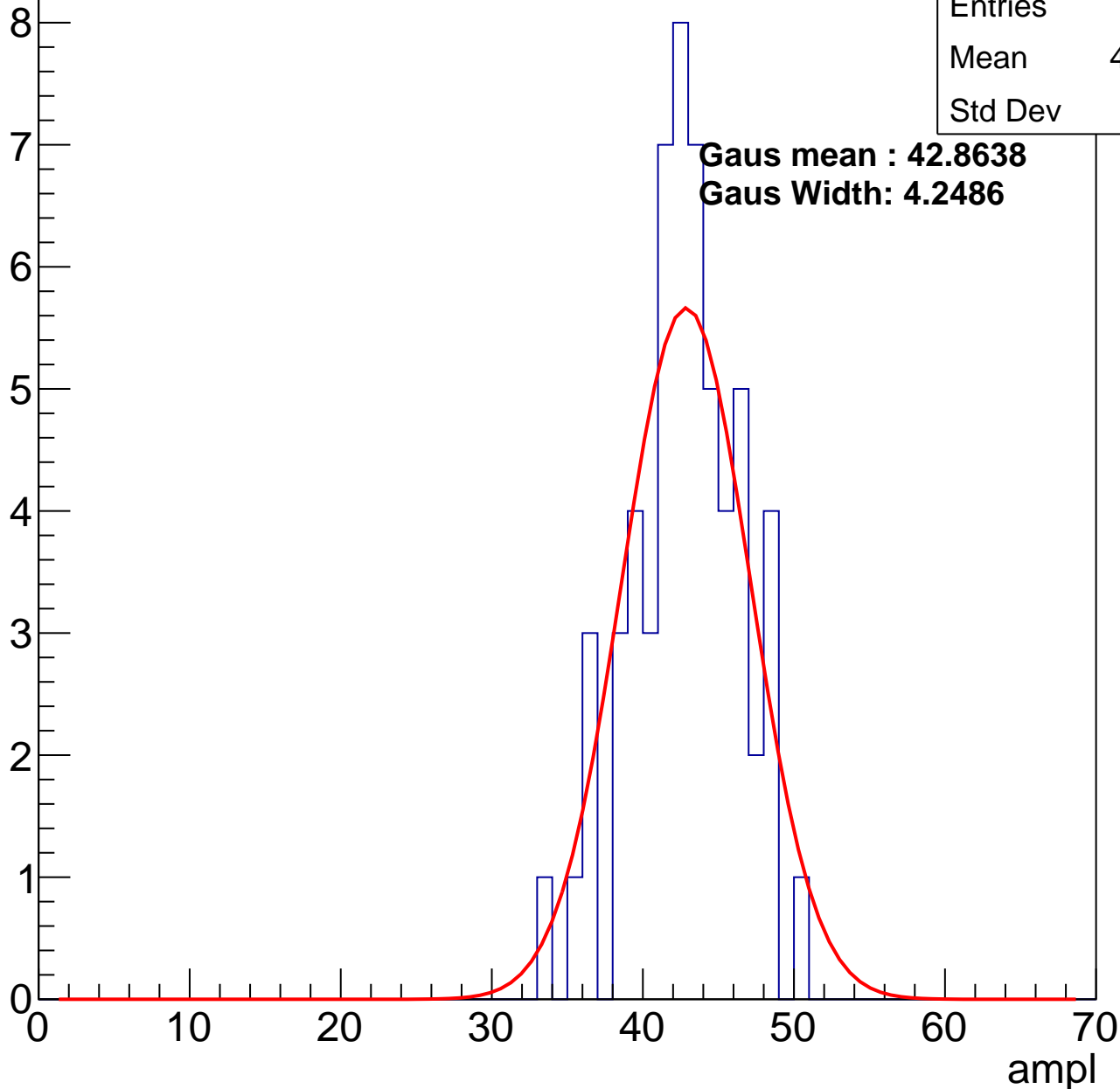
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.34
Std Dev	3.56

Gaus mean : 42.8638

Gaus Width: 4.2486

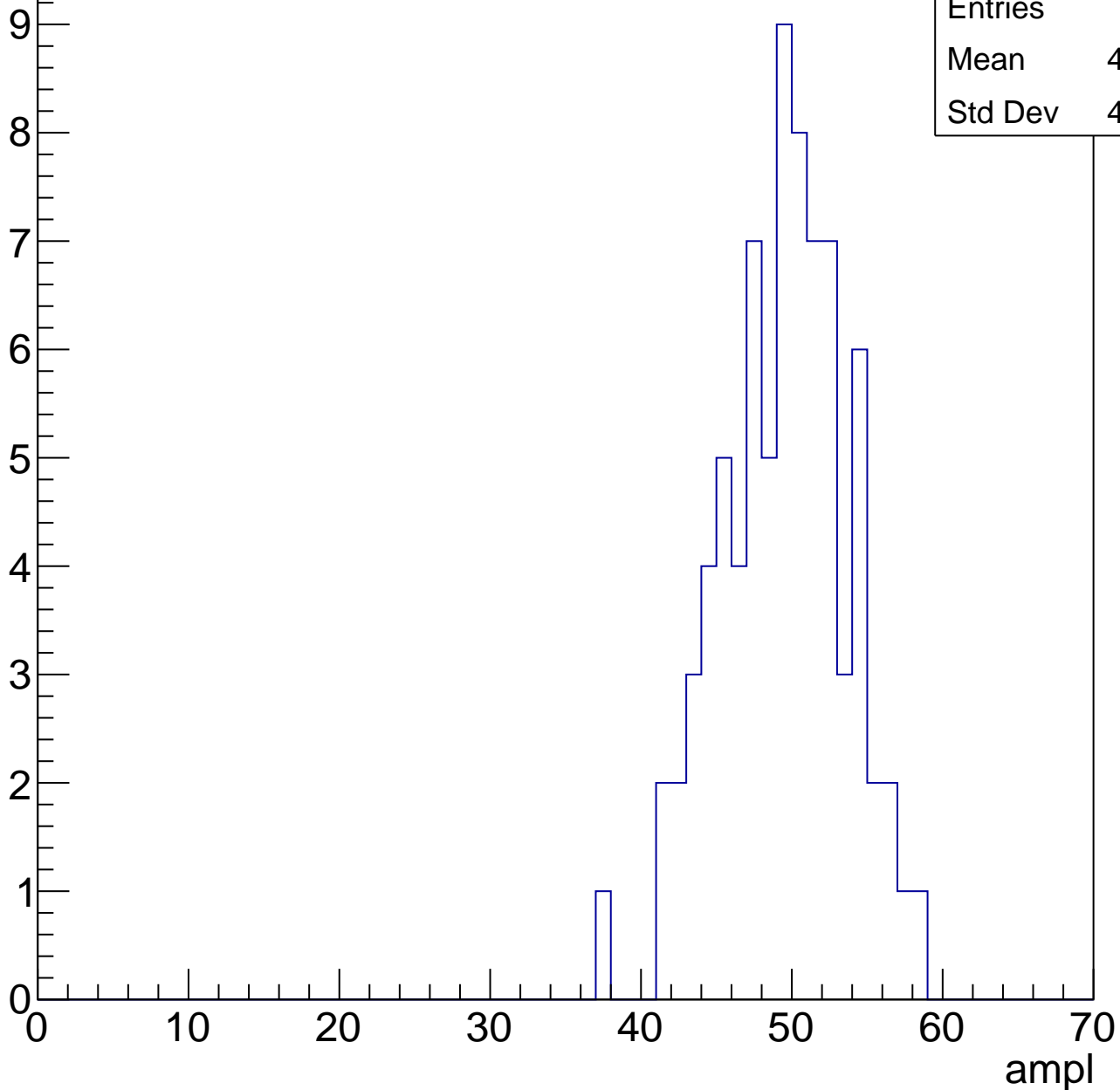


B1L101S, U17-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	48.96
Std Dev	4.117

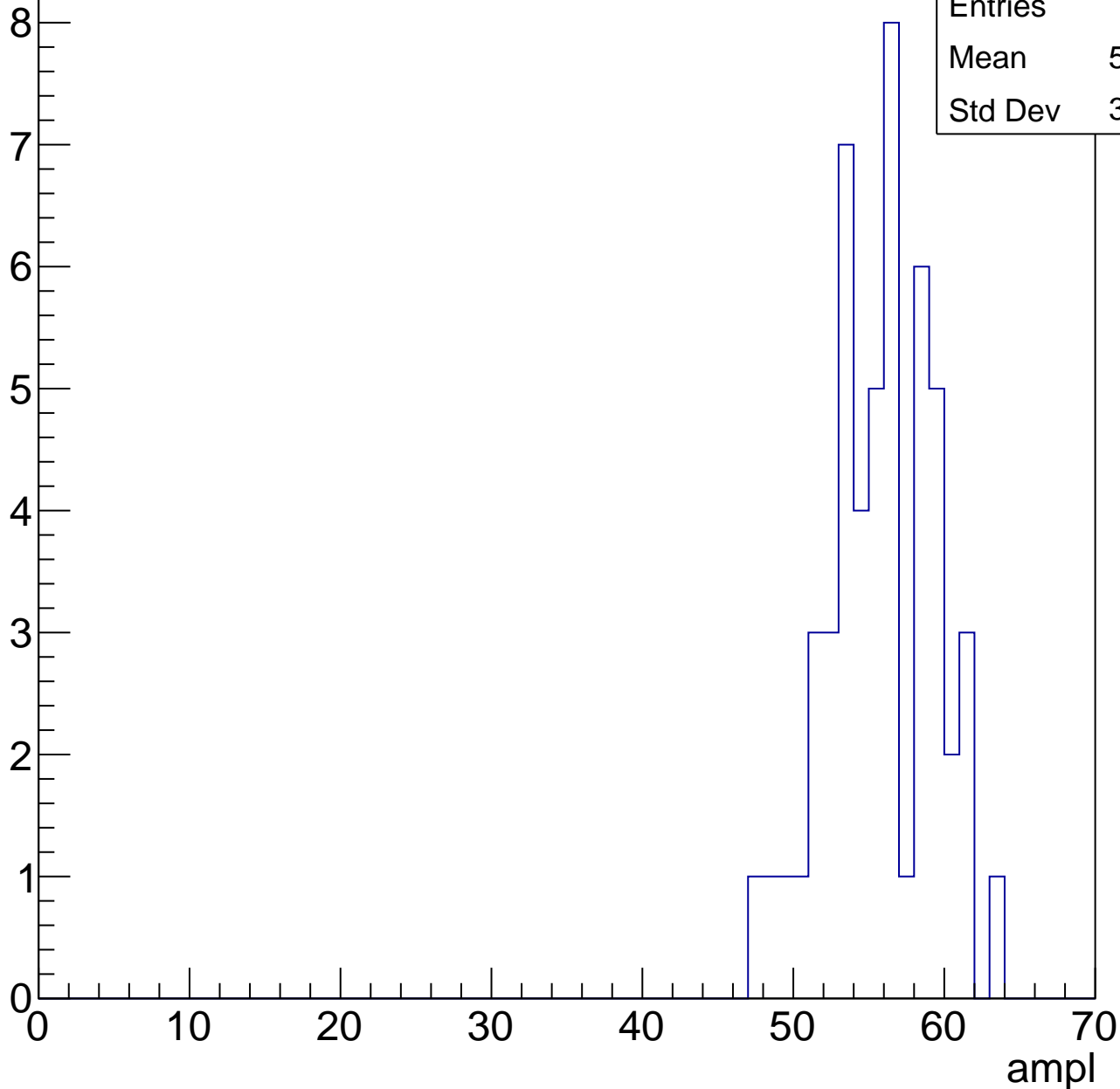


B1L101S, U17-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	55.37
Std Dev	3.514

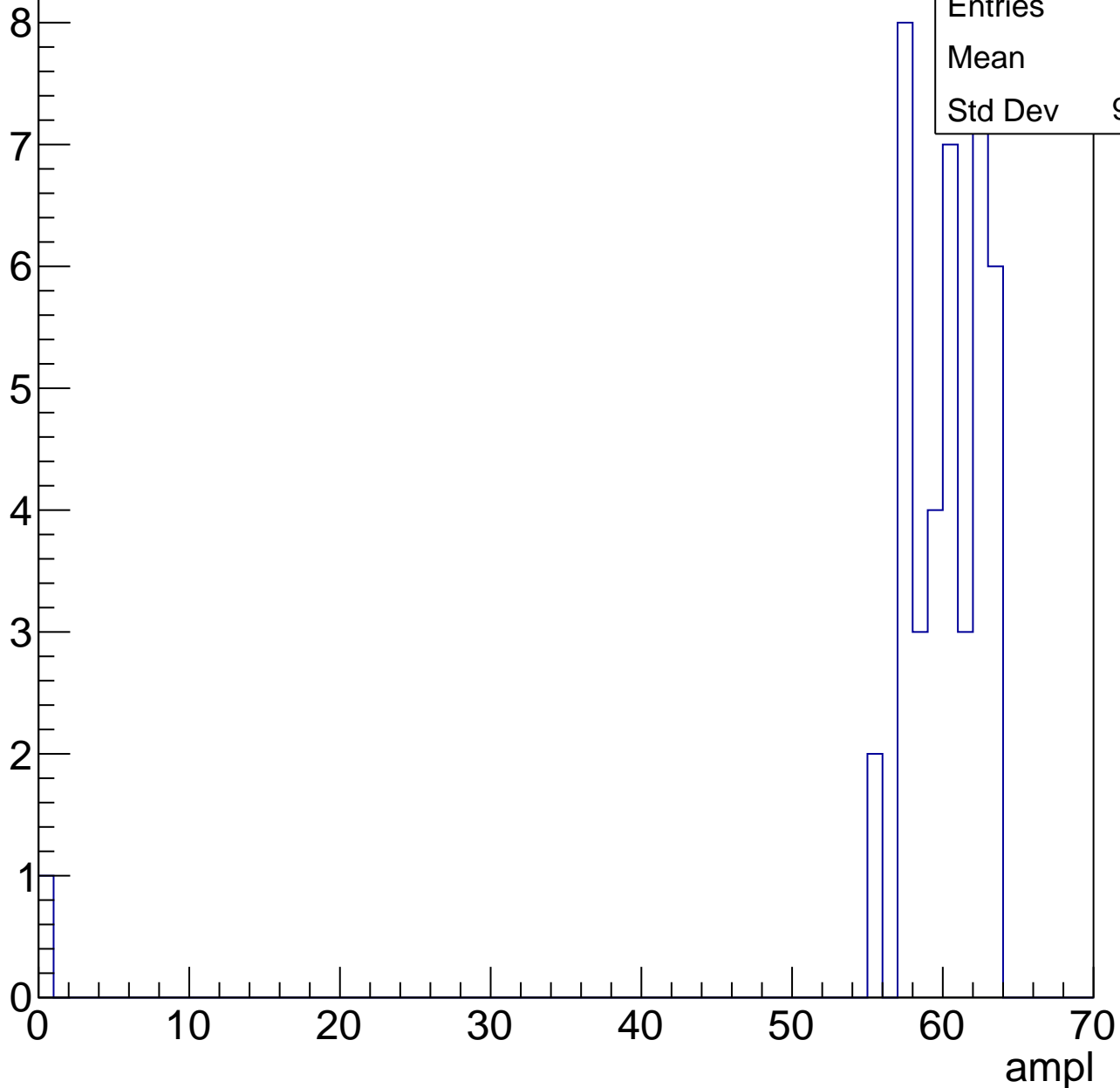


B1L101S, U17-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

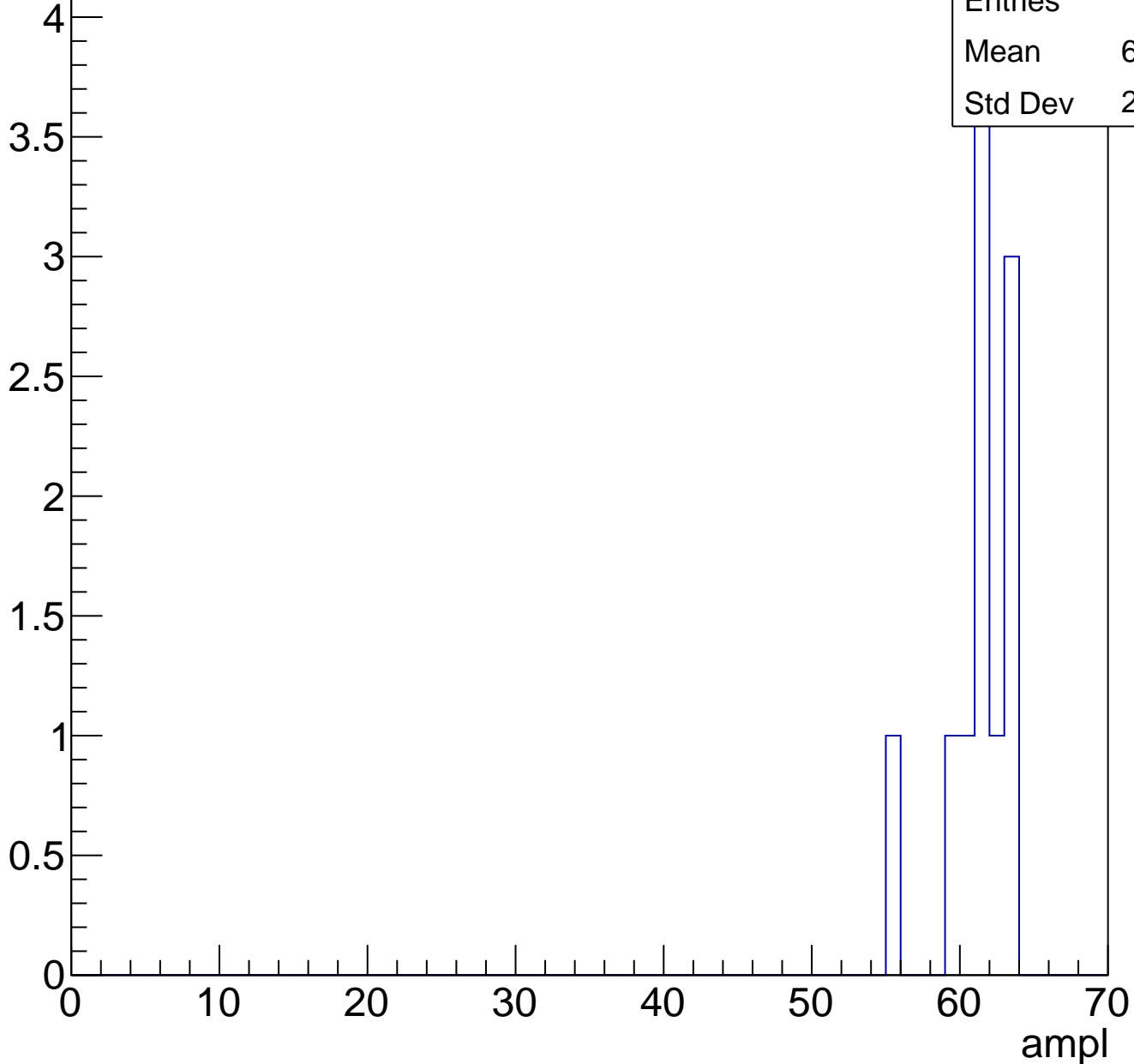
Entries	42
Mean	58.4
Std Dev	9.411



B1L101S, U17-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch114, adc0

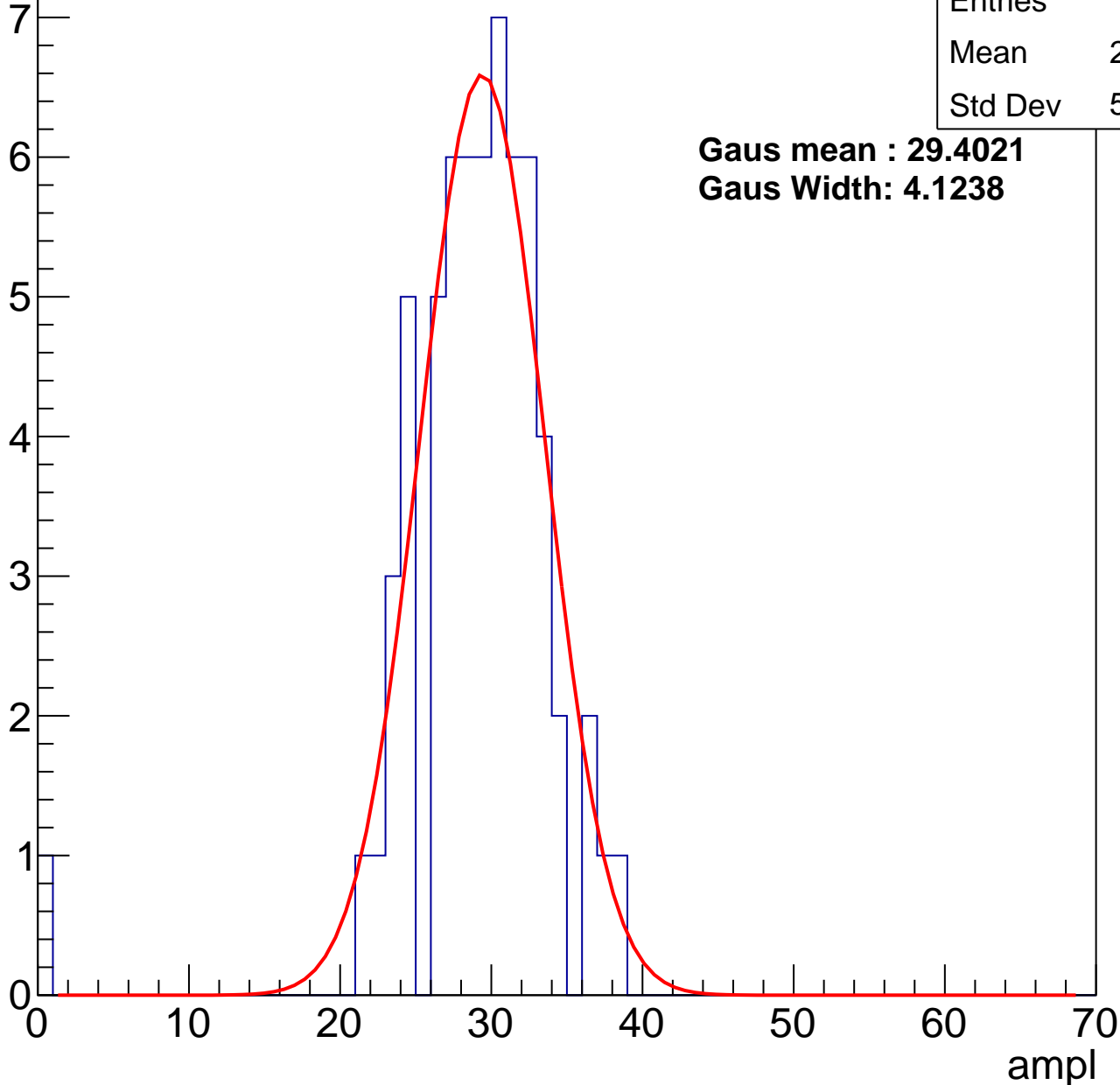
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.59
Std Dev	5.184

Gaus mean : 29.4021

Gaus Width: 4.1238



B1L101S, U17-ch114, adc1

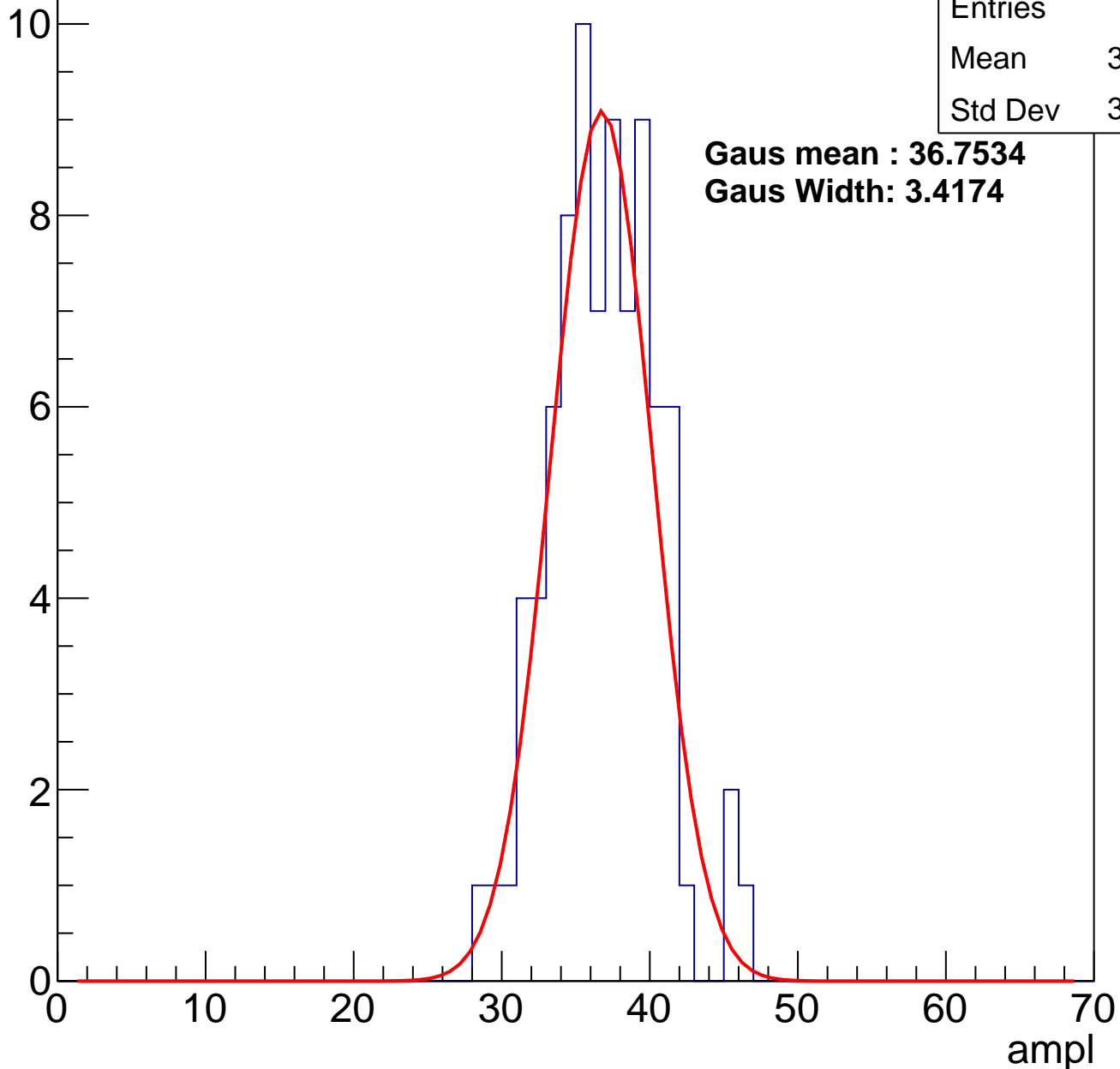
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	36.45
Std Dev	3.564

Gaus mean : 36.7534

Gaus Width: 3.4174

Entry



B1L101S, U17-ch114, adc2

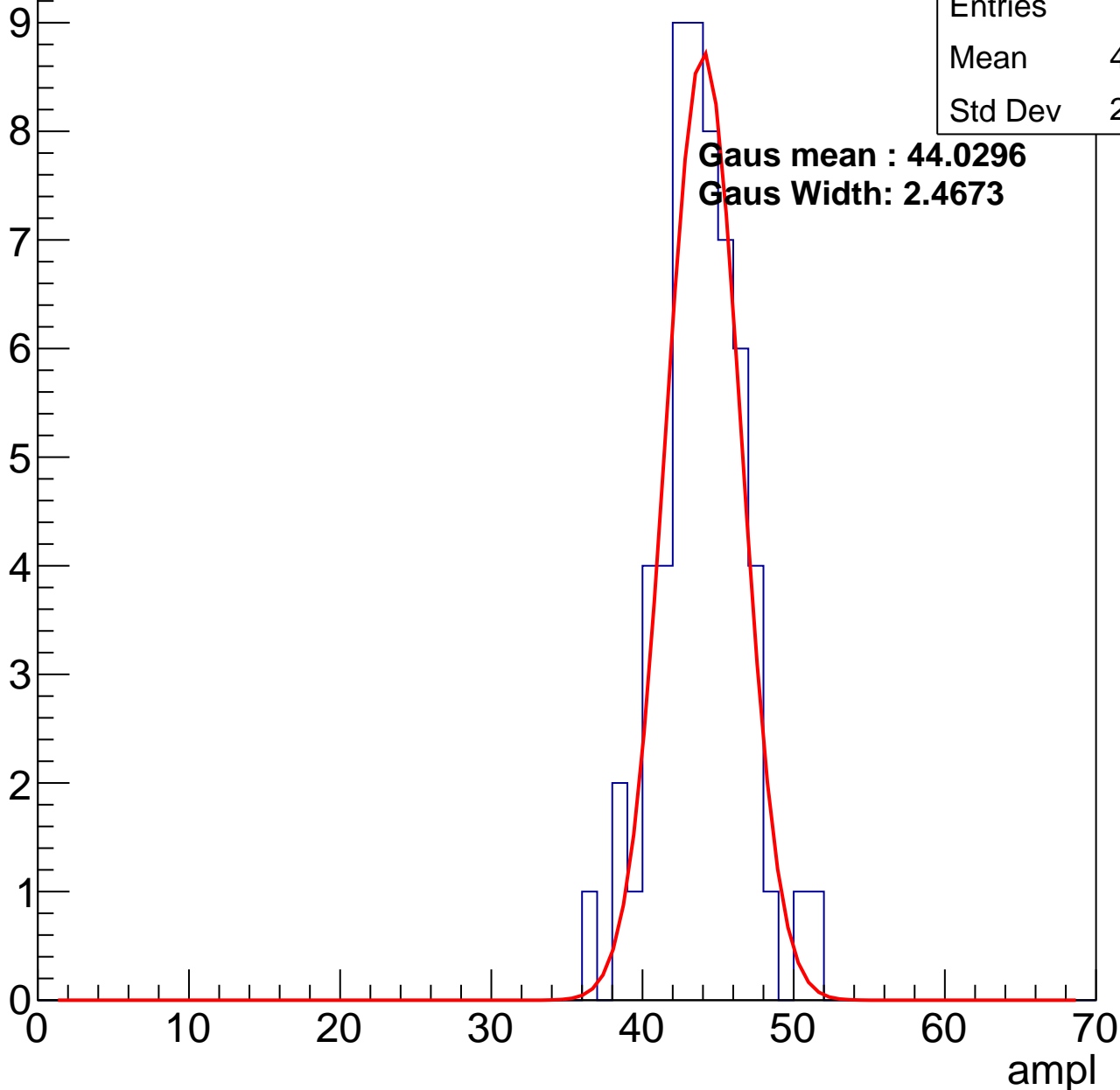
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.45
Std Dev	2.805

Gaus mean : 44.0296

Gaus Width: 2.4673

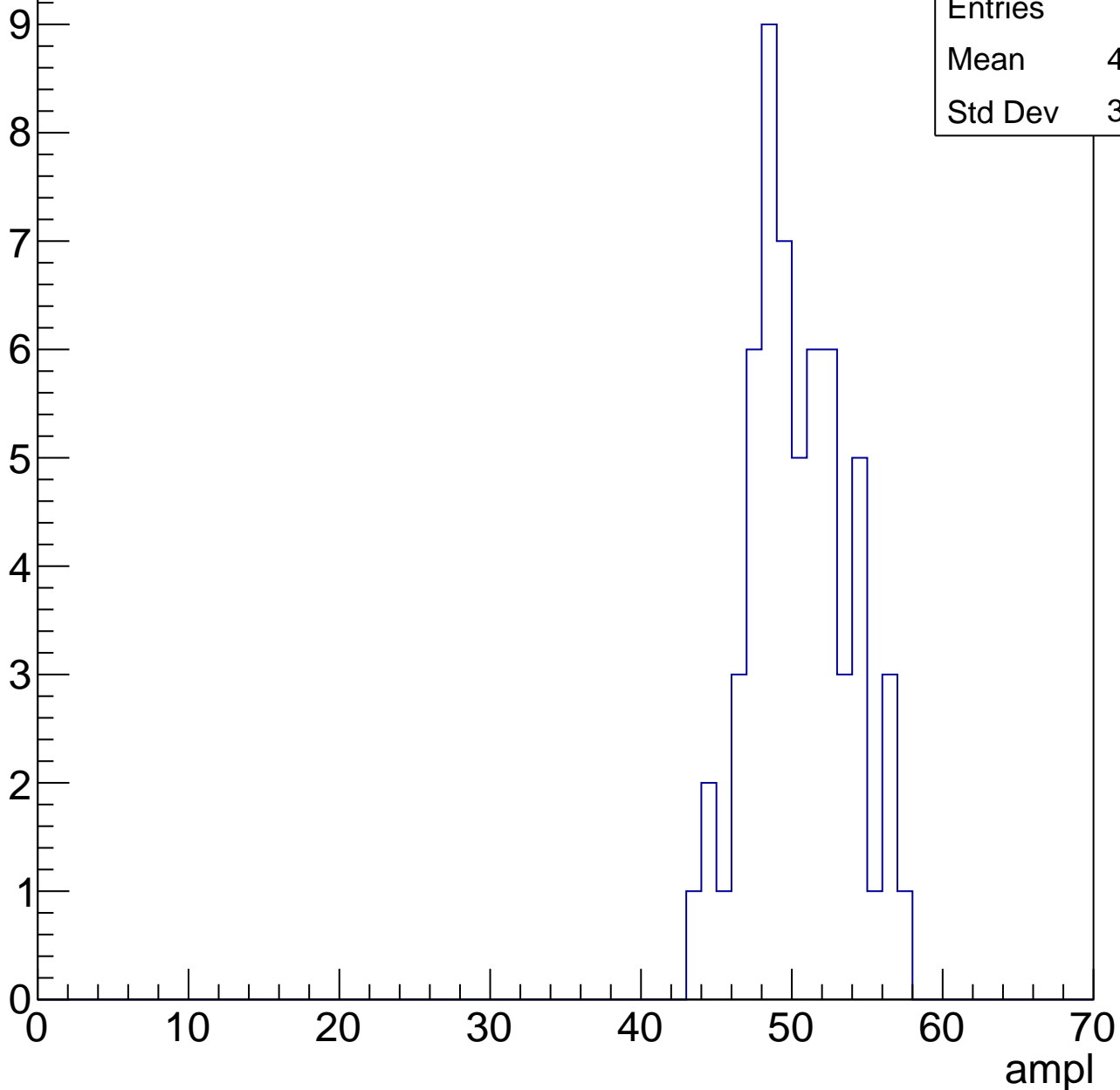


B1L101S, U17-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.97
Std Dev	3.215

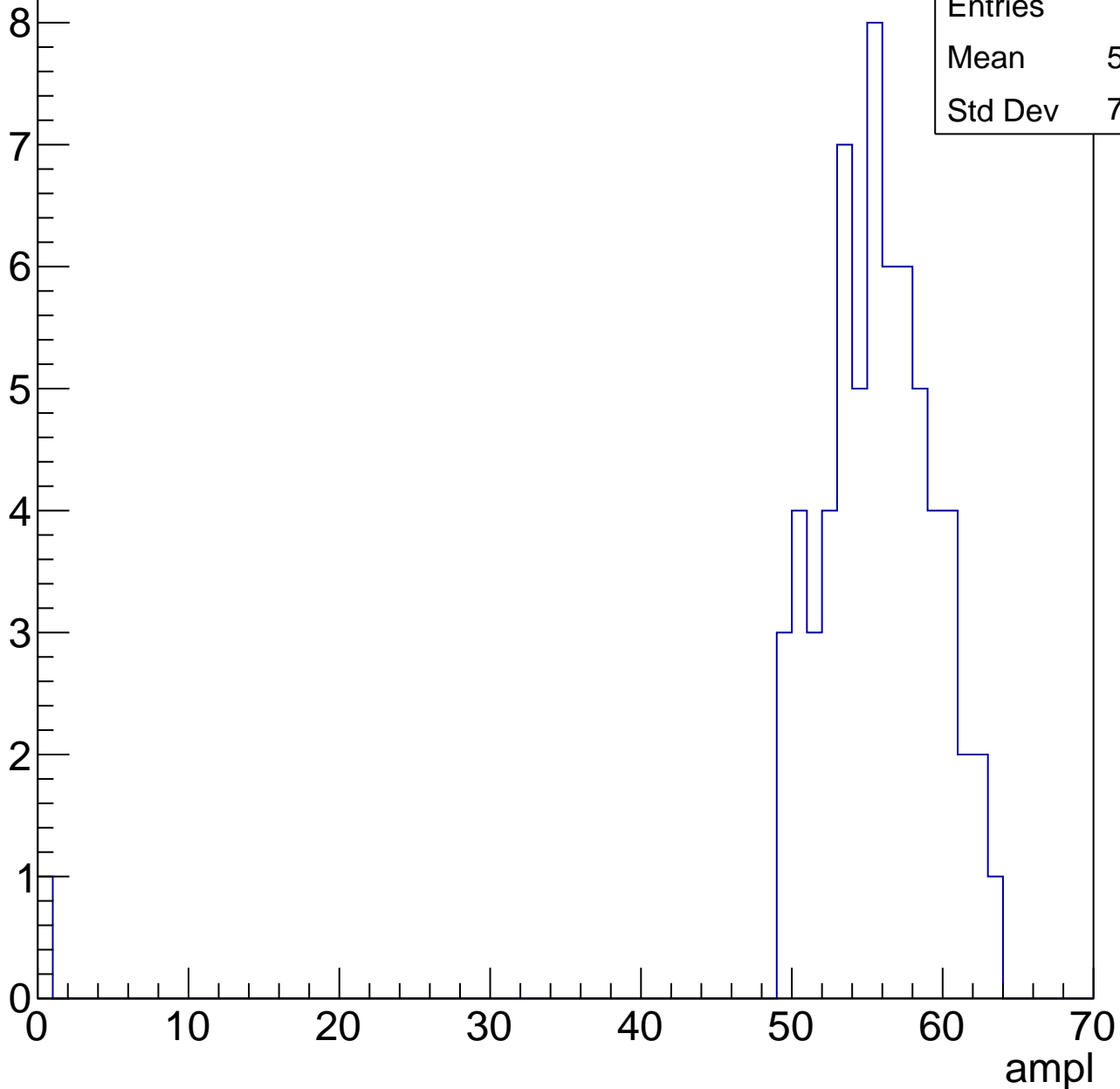


B1L101S, U17-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.49
Std Dev	7.652

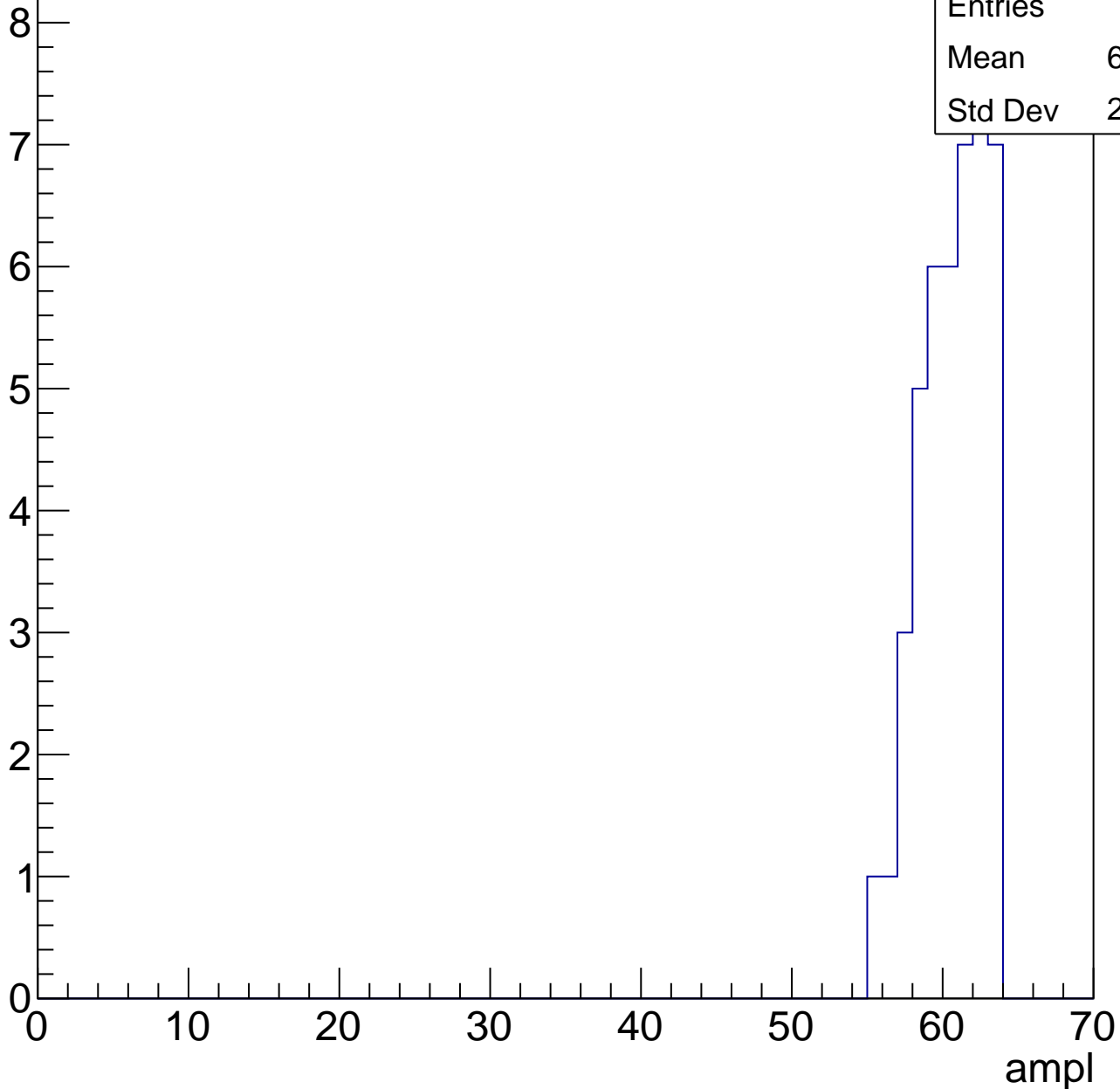


B1L101S, U17-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	60.23
Std Dev	2.098



B1L101S, U17-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

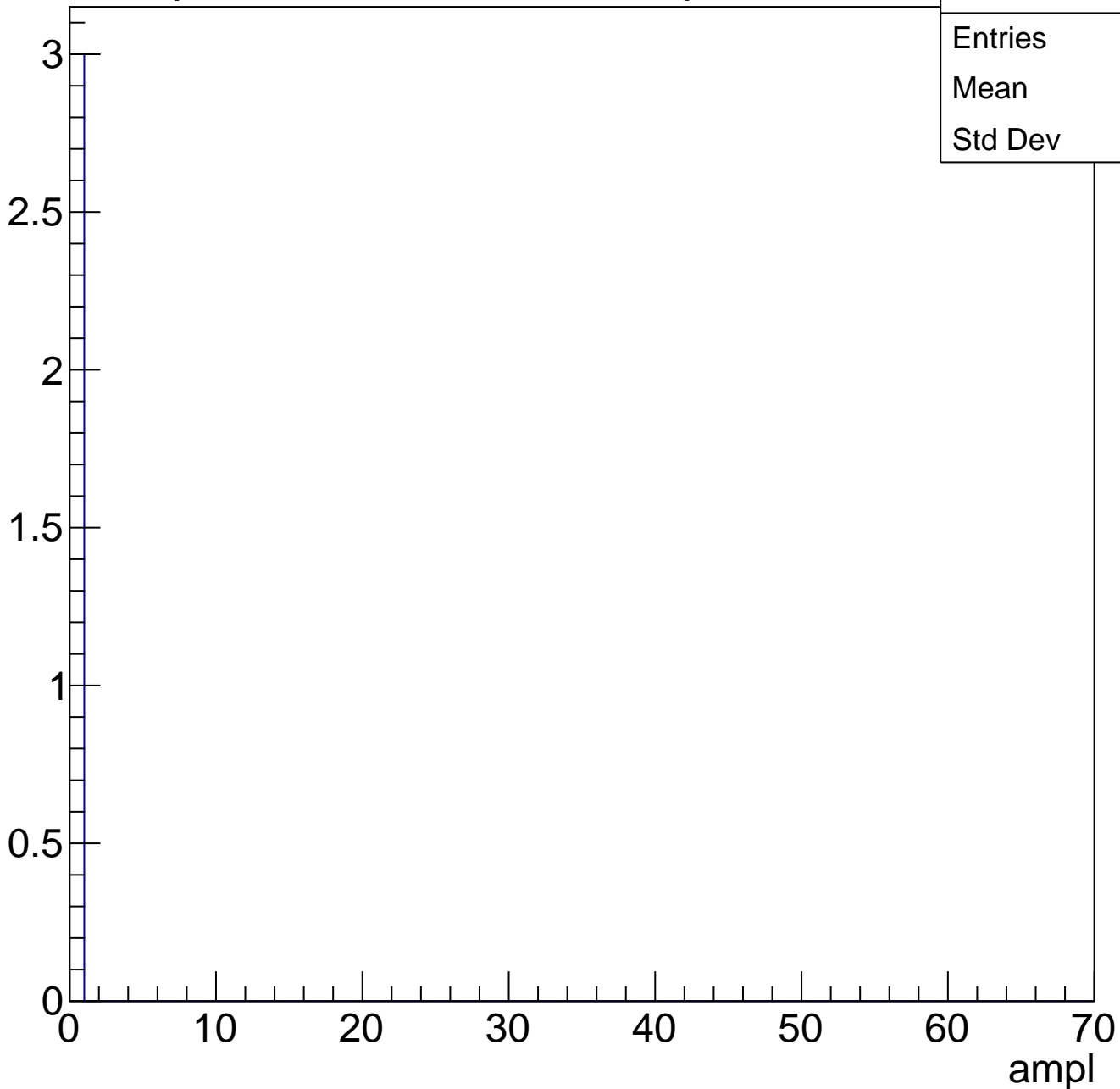
Entry



B1L101S, U17-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U17-ch115, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	29.7
Std Dev	3.738

Gaus mean : 30.6075

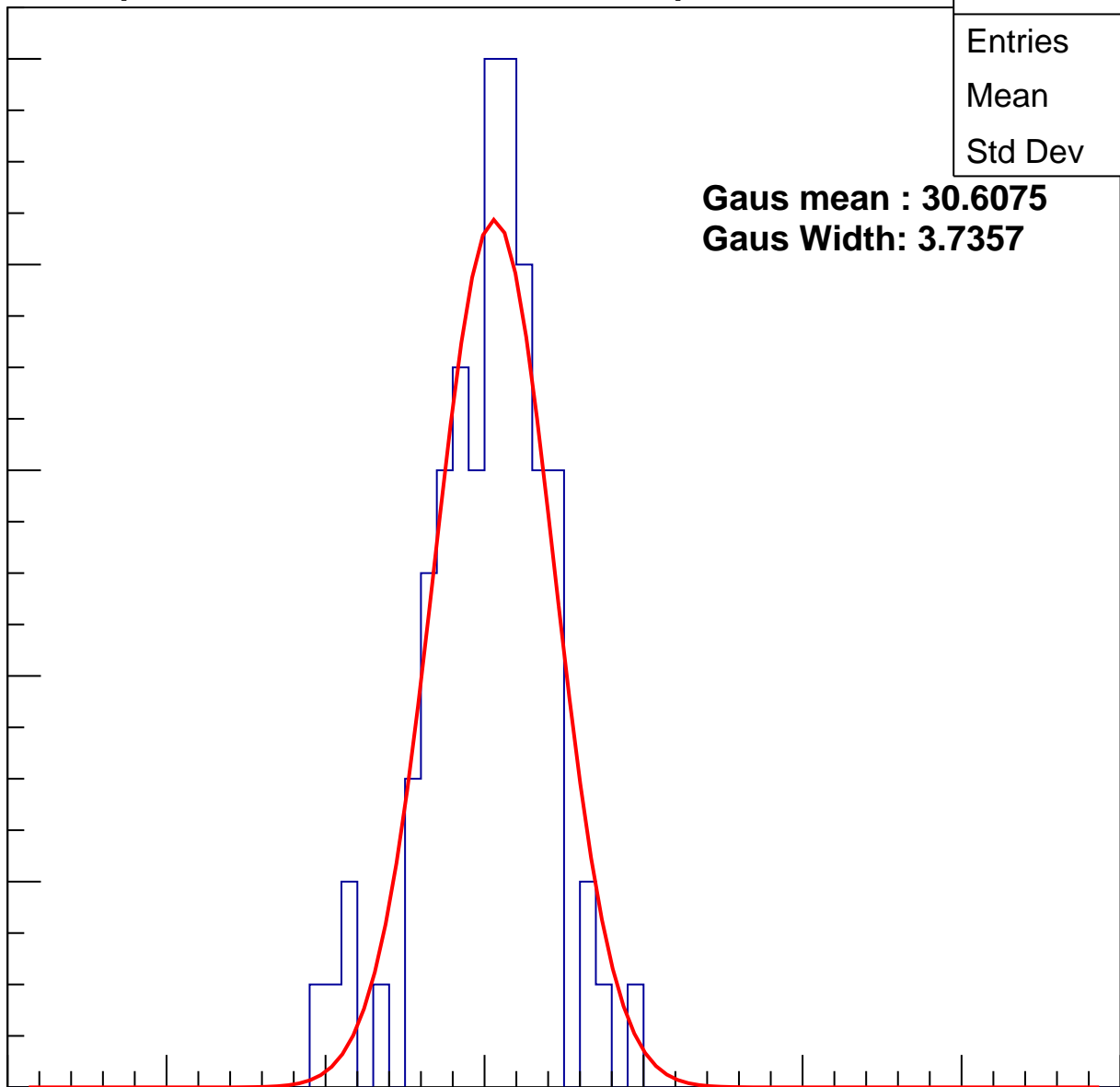
Gaus Width: 3.7357

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch115, adc1

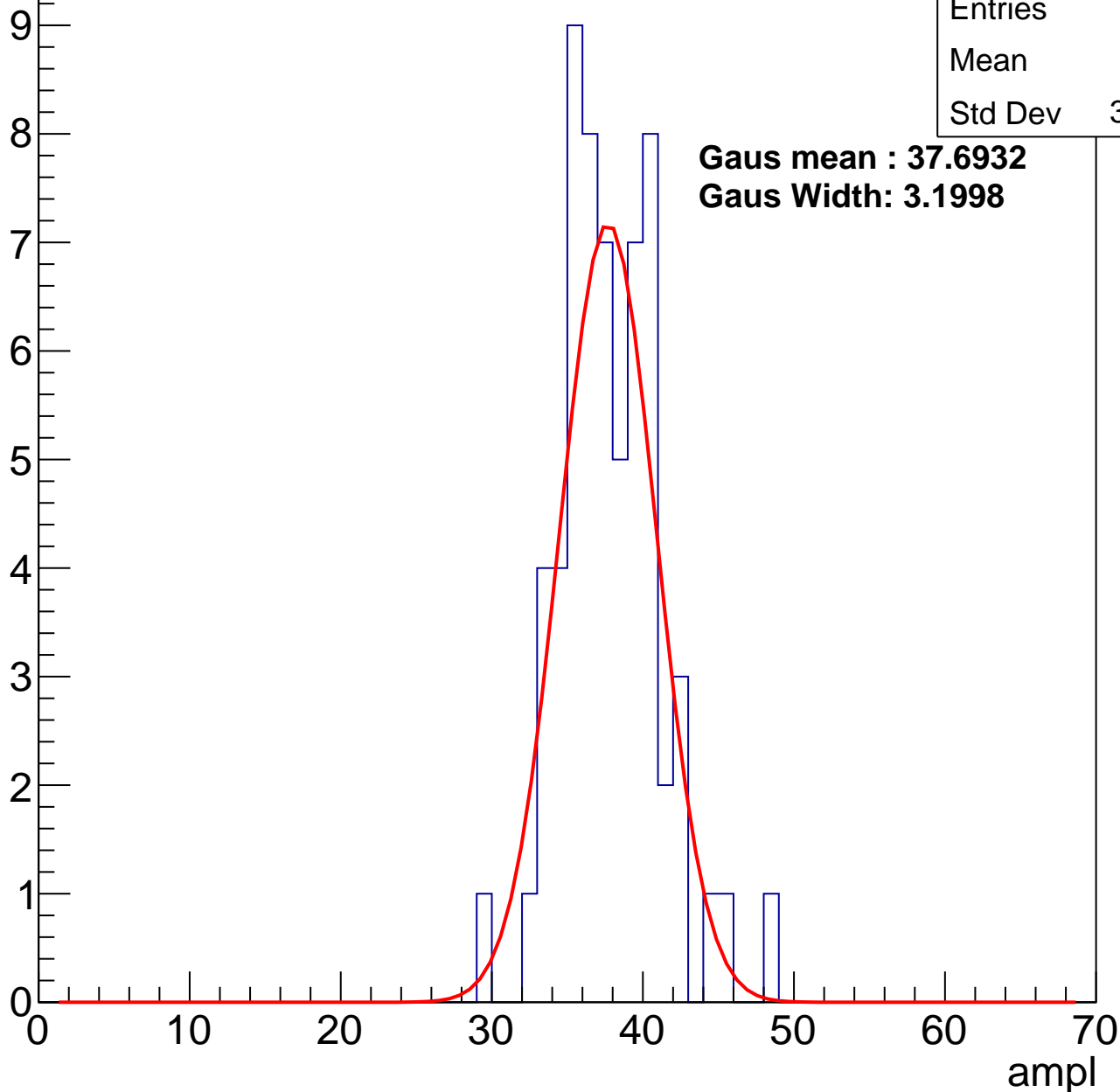
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.4
Std Dev	3.285

Gaus mean : 37.6932

Gaus Width: 3.1998



B1L101S, U17-ch115, adc2

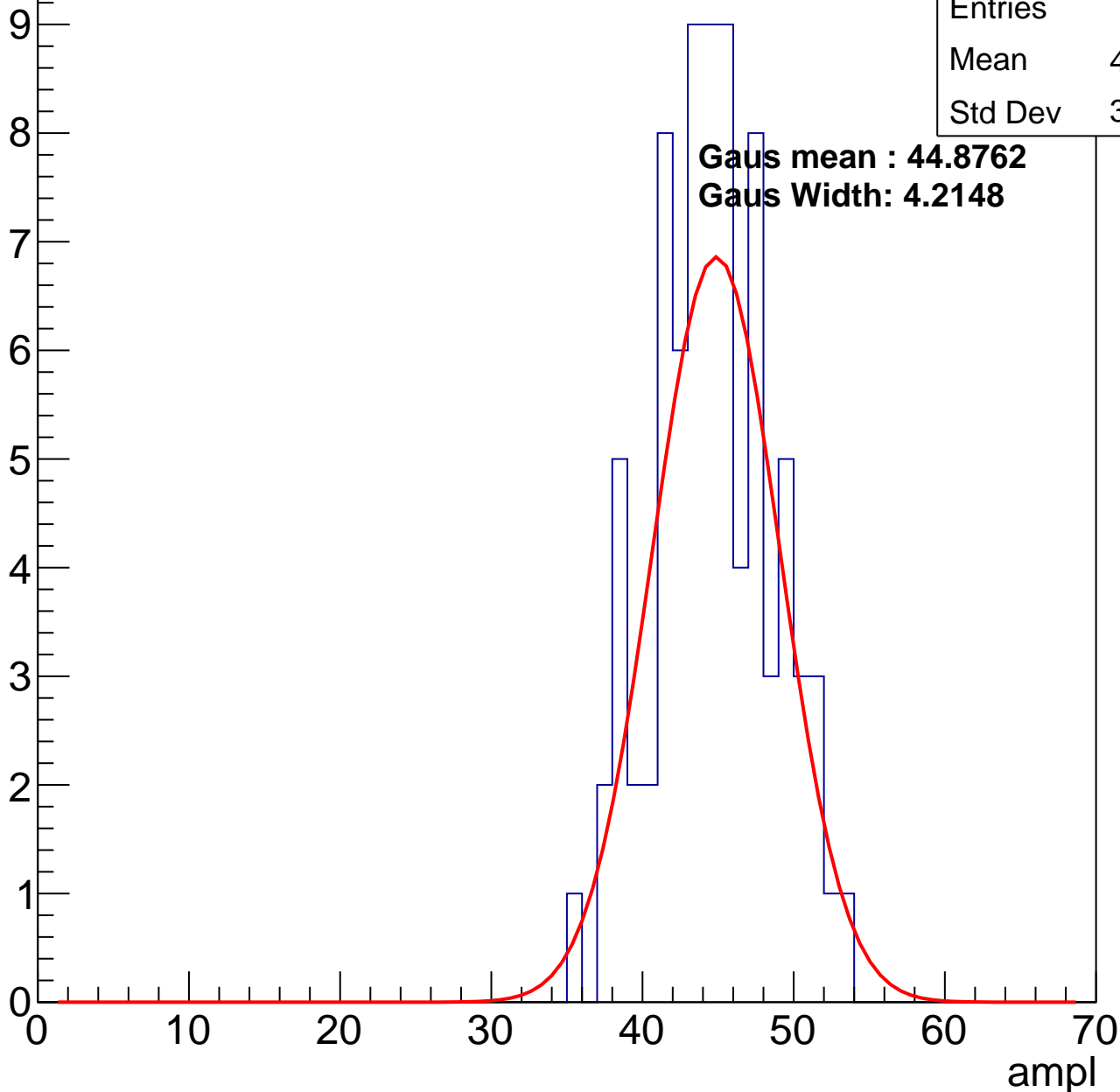
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	44.22
Std Dev	3.868

Gaus mean : 44.8762

Gaus Width: 4.2148

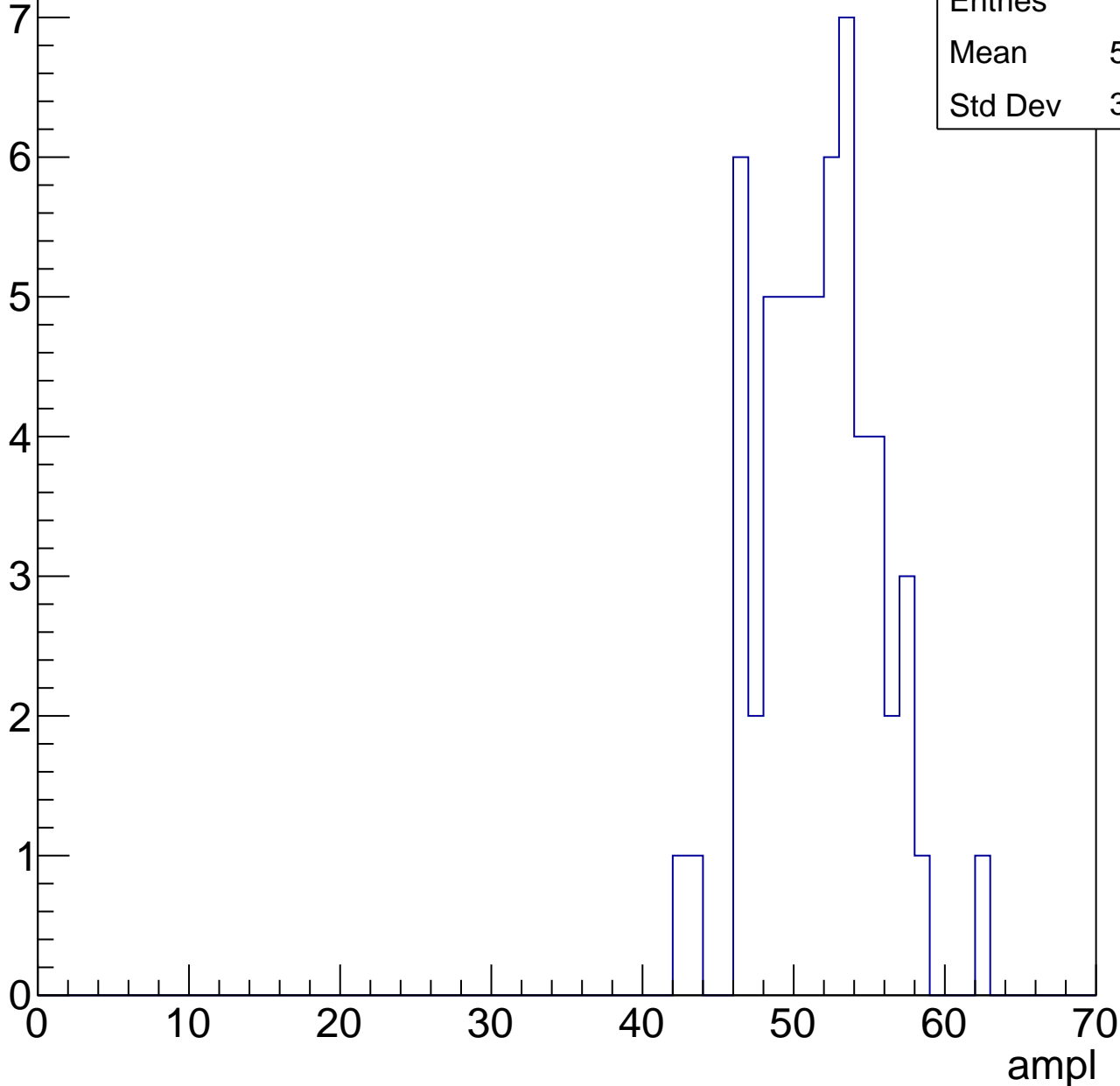


B1L101S, U17-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	51.16
Std Dev	3.845

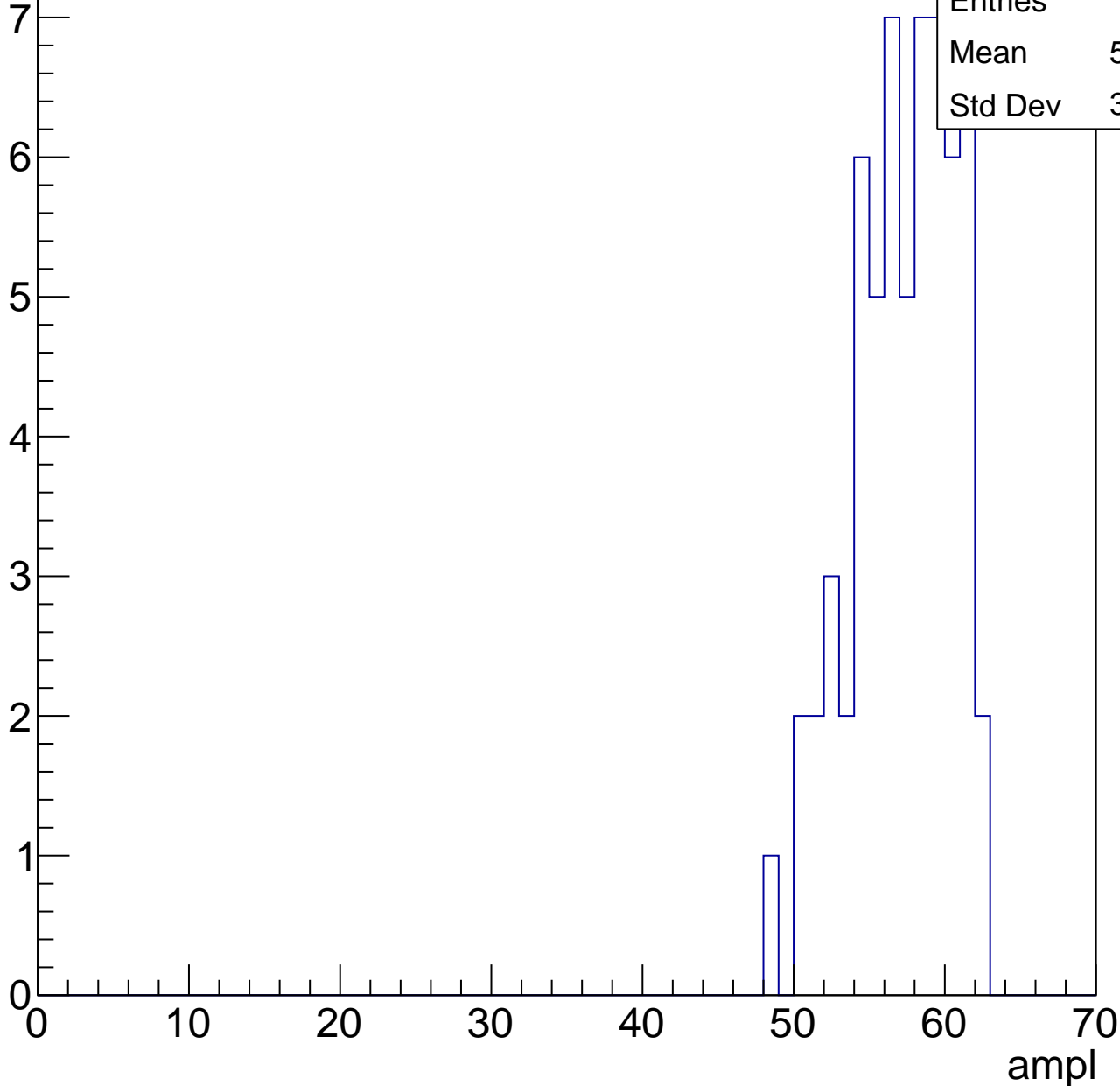


B1L101S, U17-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.74
Std Dev	3.326

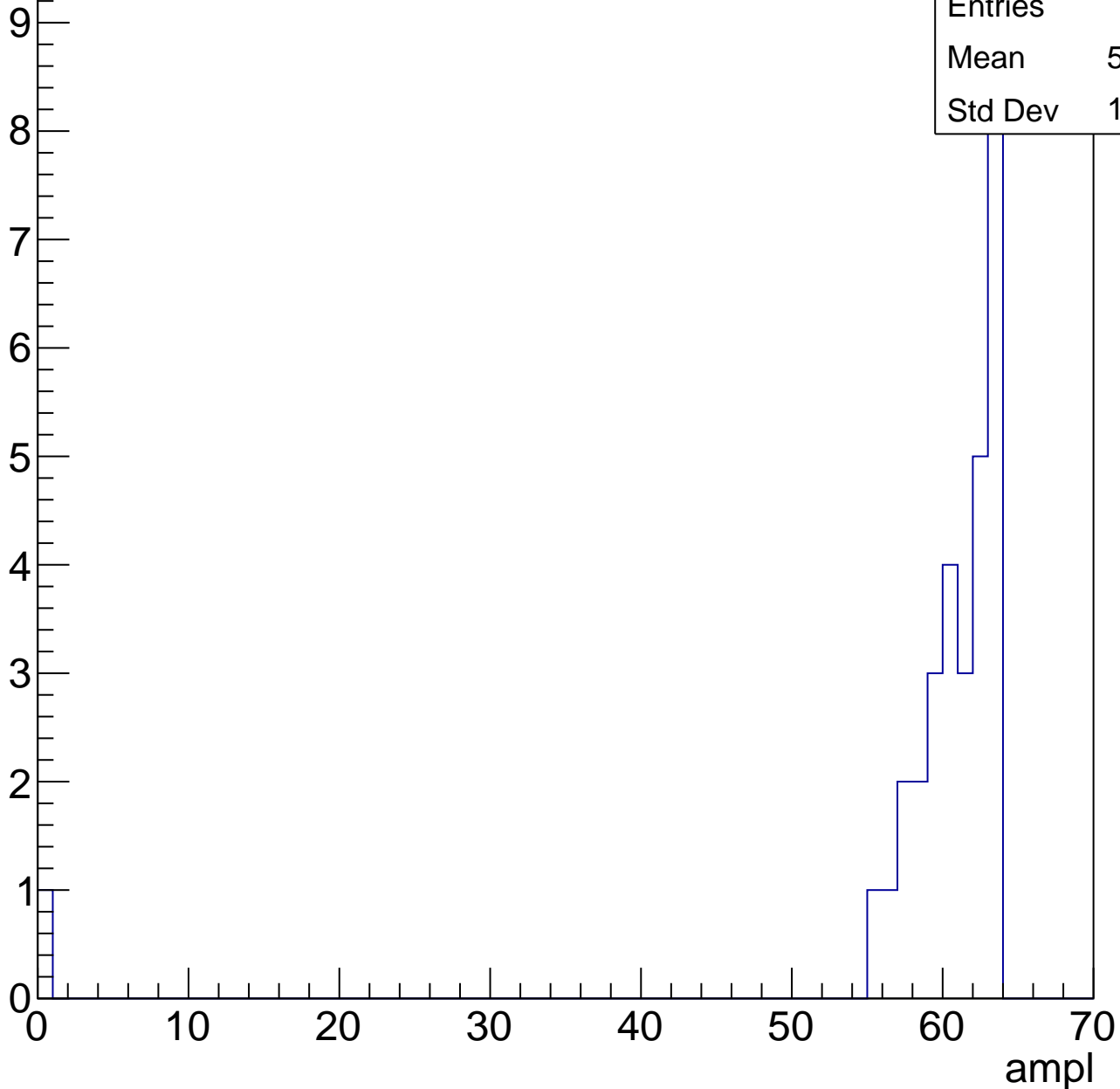


B1L101S, U17-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.65
Std Dev	10.95



B1L101S, U17-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L101S, U17-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch116, adc0

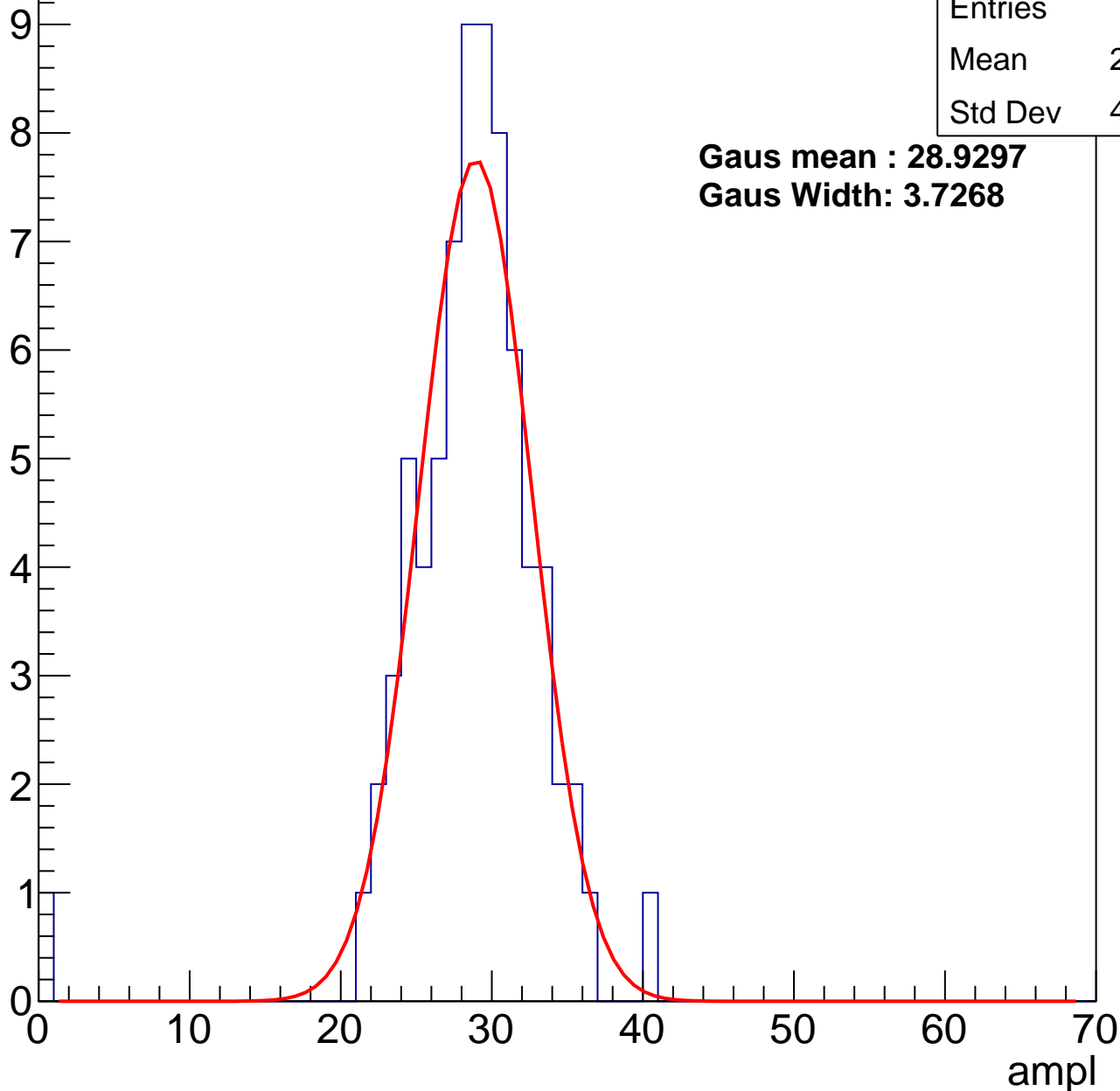
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.19
Std Dev	4.873

Gaus mean : 28.9297

Gaus Width: 3.7268



B1L101S, U17-ch116, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	36.67
Std Dev	4.042

Gaus mean : 36.8118

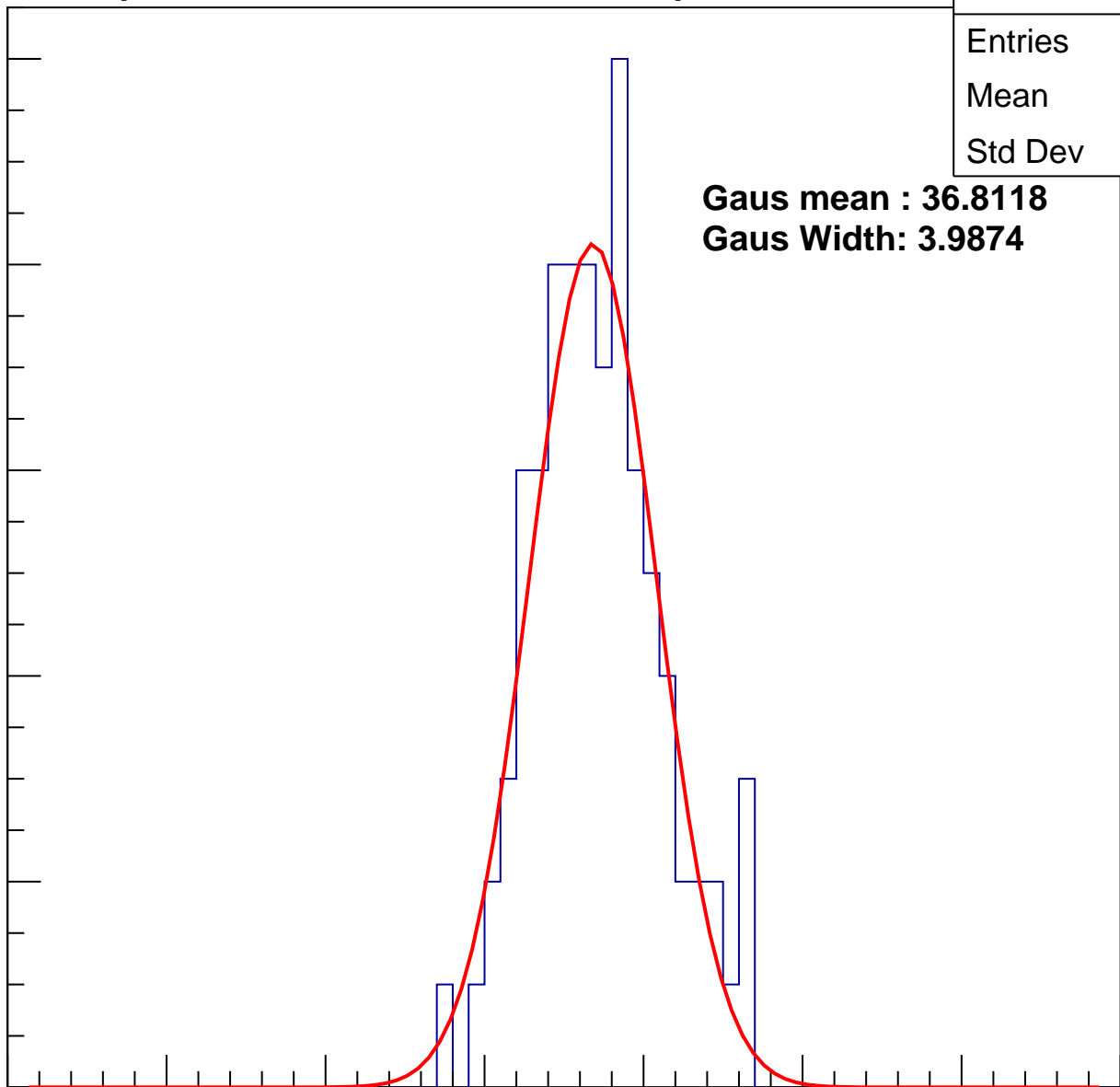
Gaus Width: 3.9874

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch116, adc2

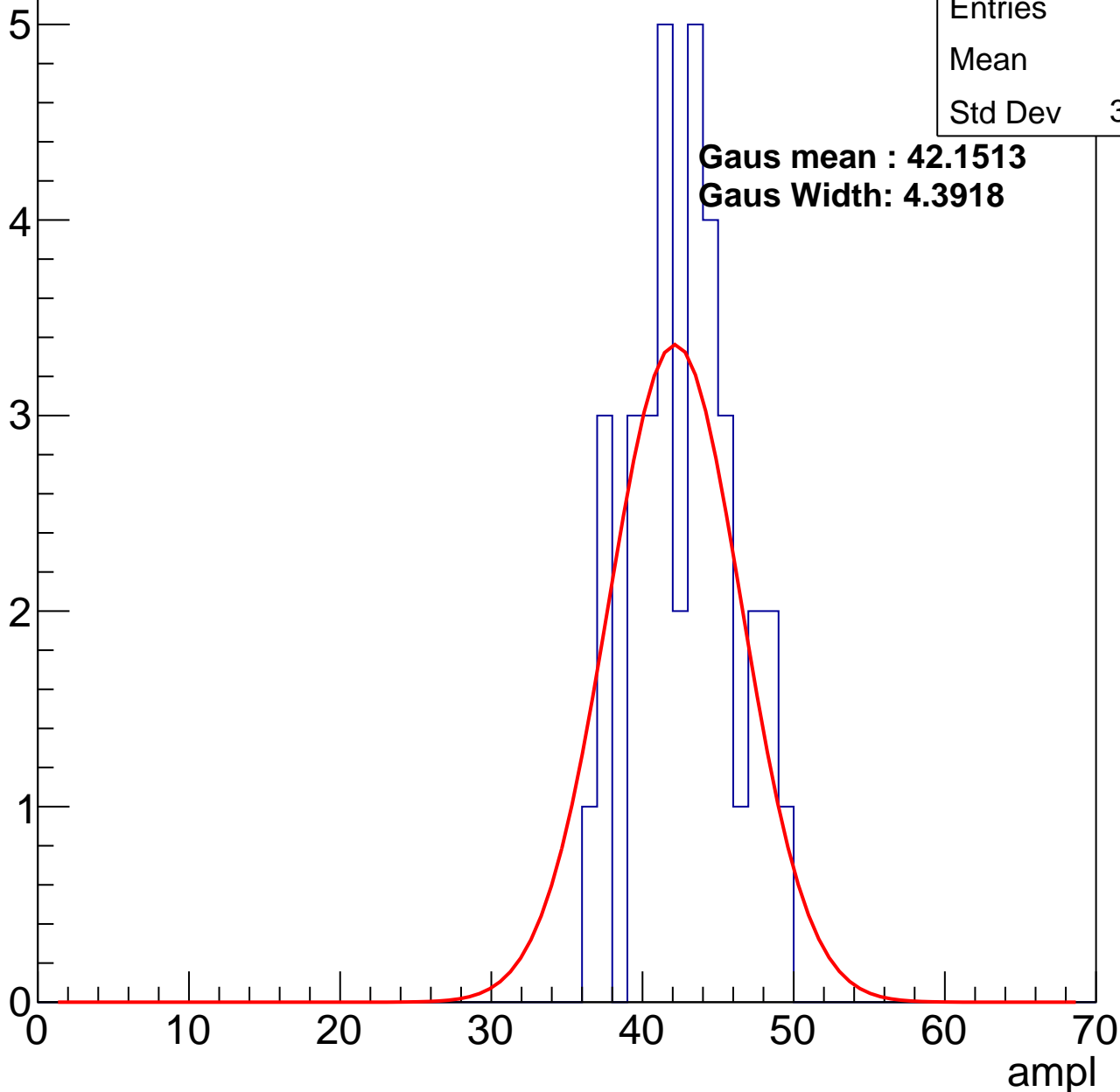
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	42.4
Std Dev	3.314

Gaus mean : 42.1513

Gaus Width: 4.3918

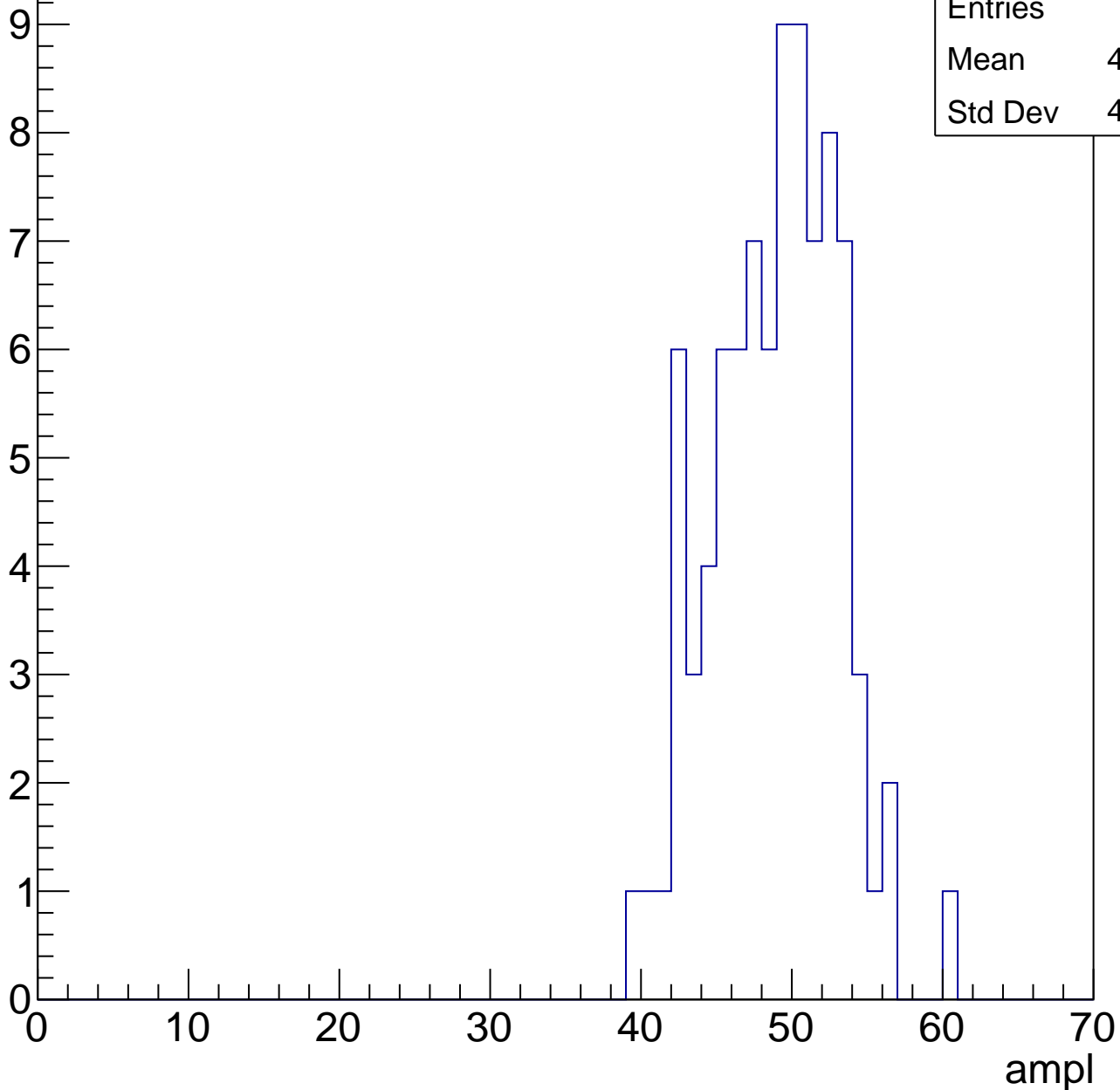


B1L101S, U17-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	48.45
Std Dev	4.067

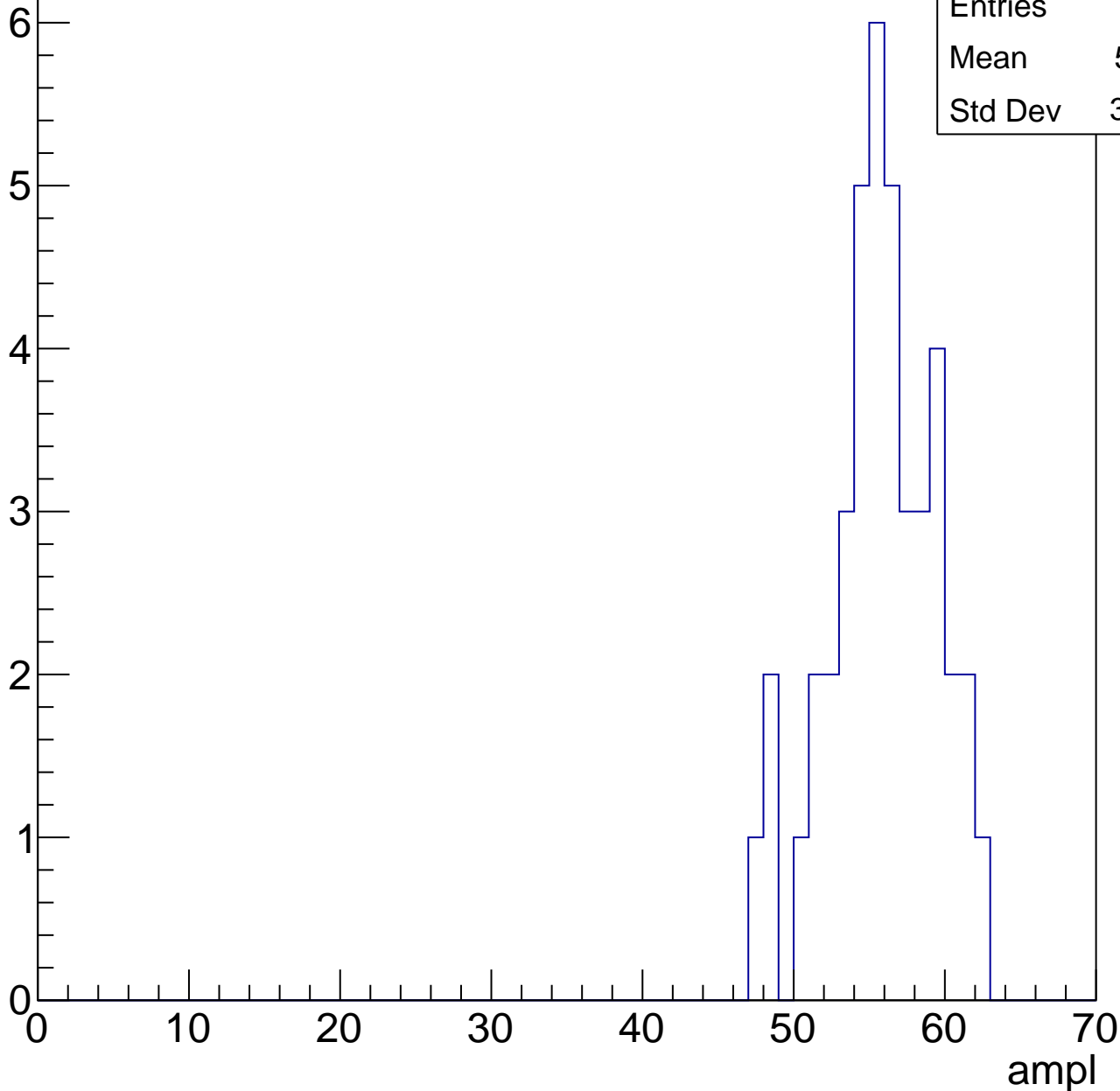


B1L101S, U17-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	55.31
Std Dev	3.549

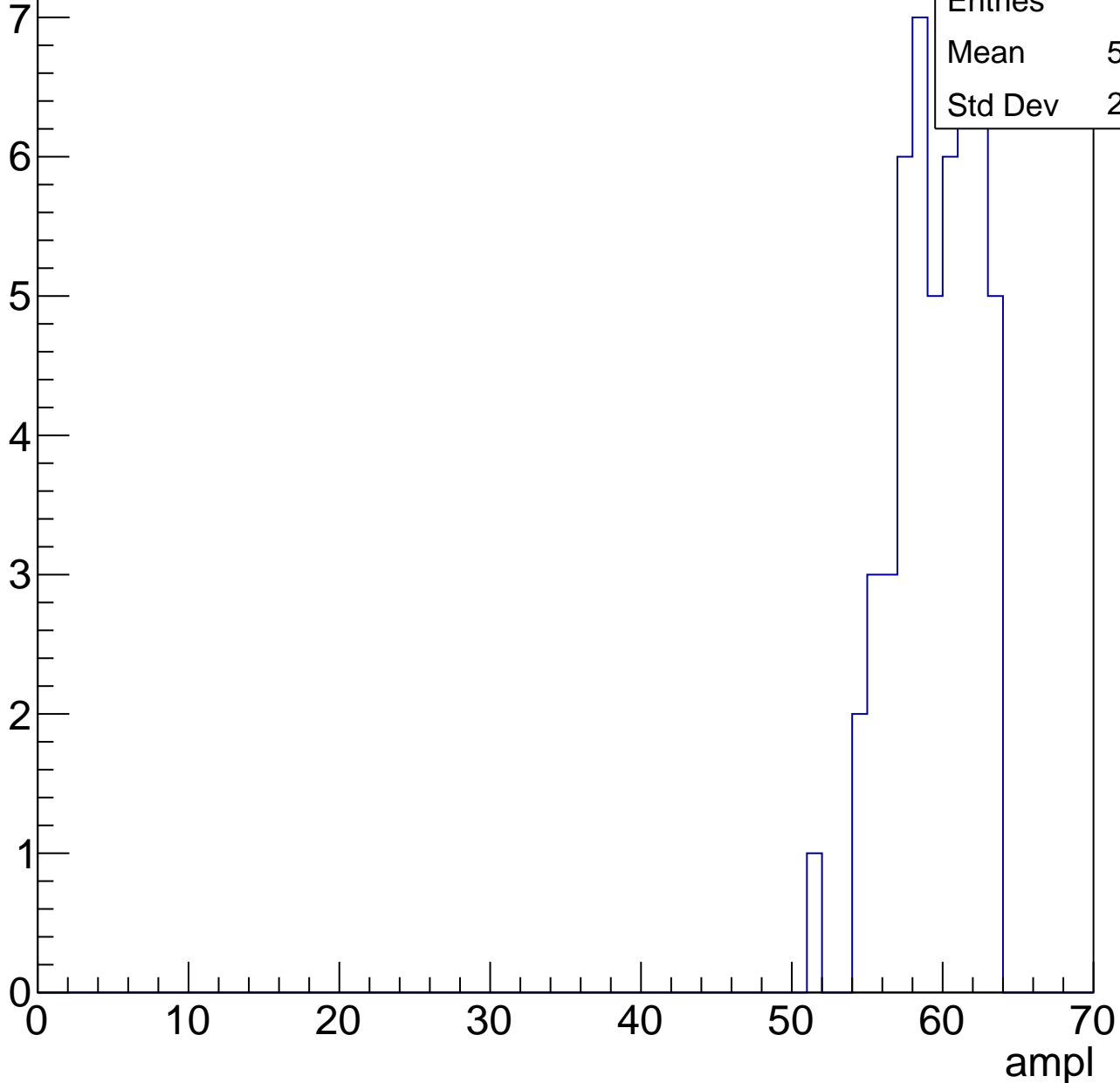


B1L101S, U17-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

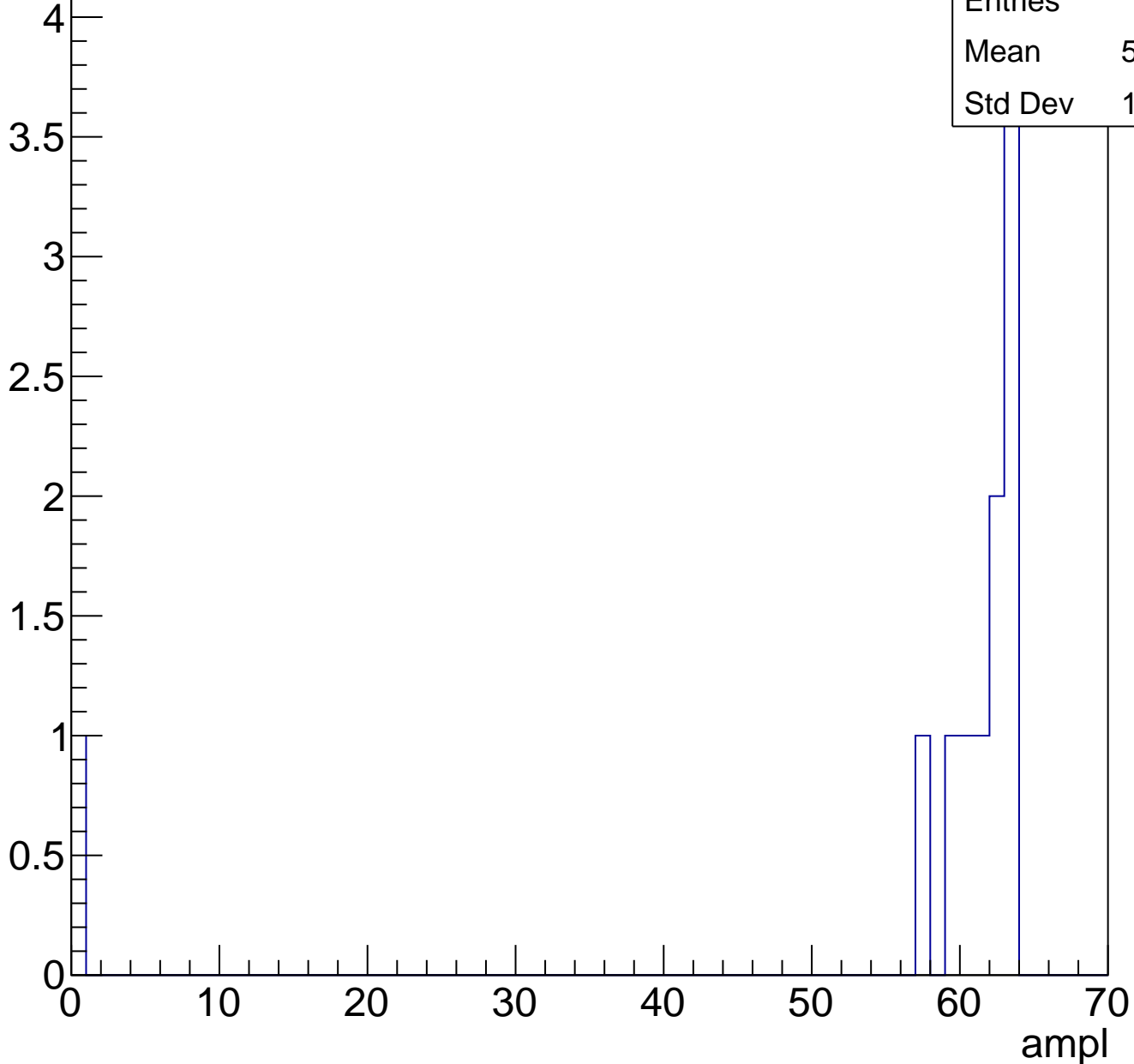
Entries	52
Mean	59.06
Std Dev	2.762



B1L101S, U17-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

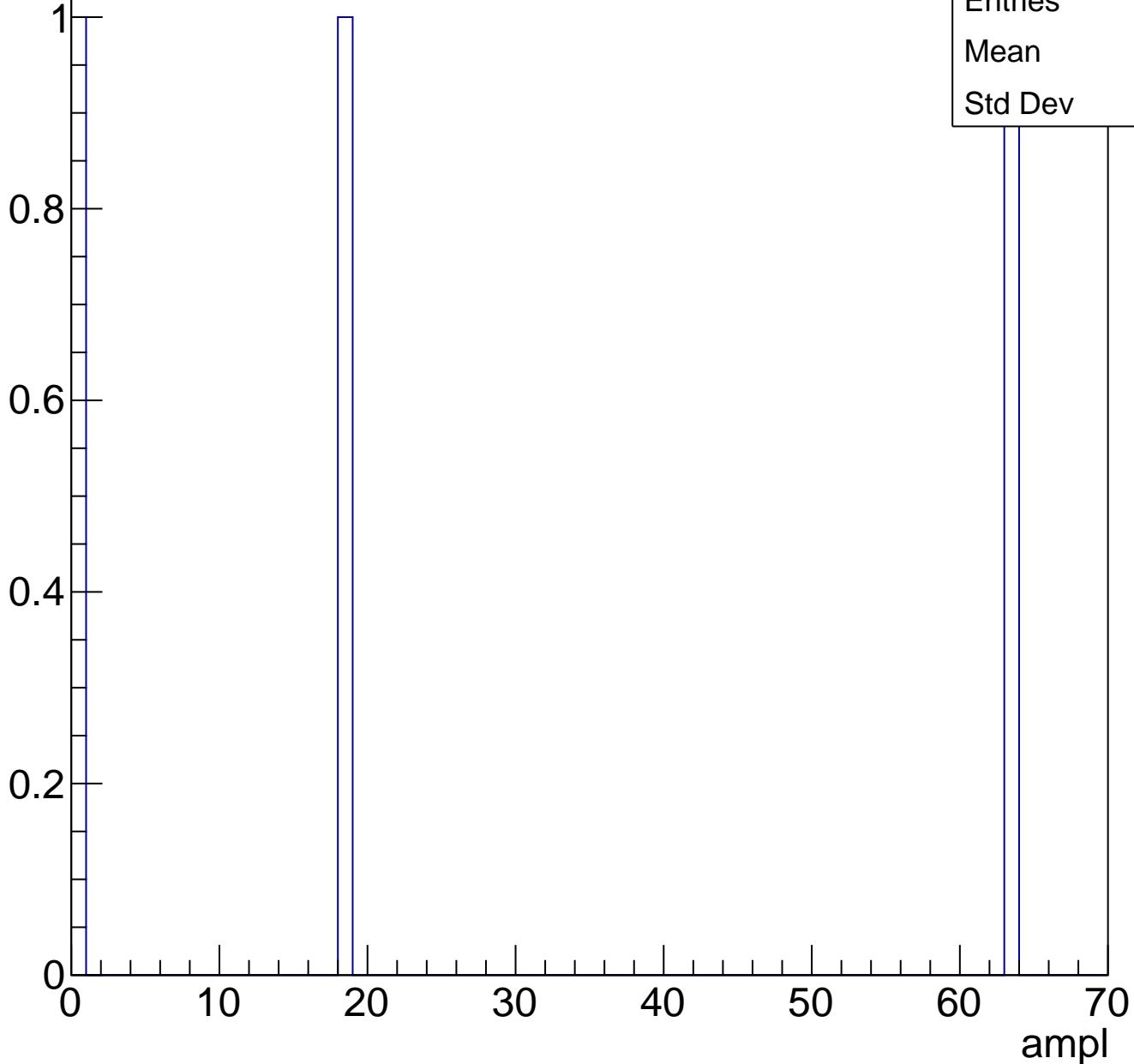
Entry



B1L101S, U17-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27
Std Dev	26.5

B1L101S, U17-ch117, adc0

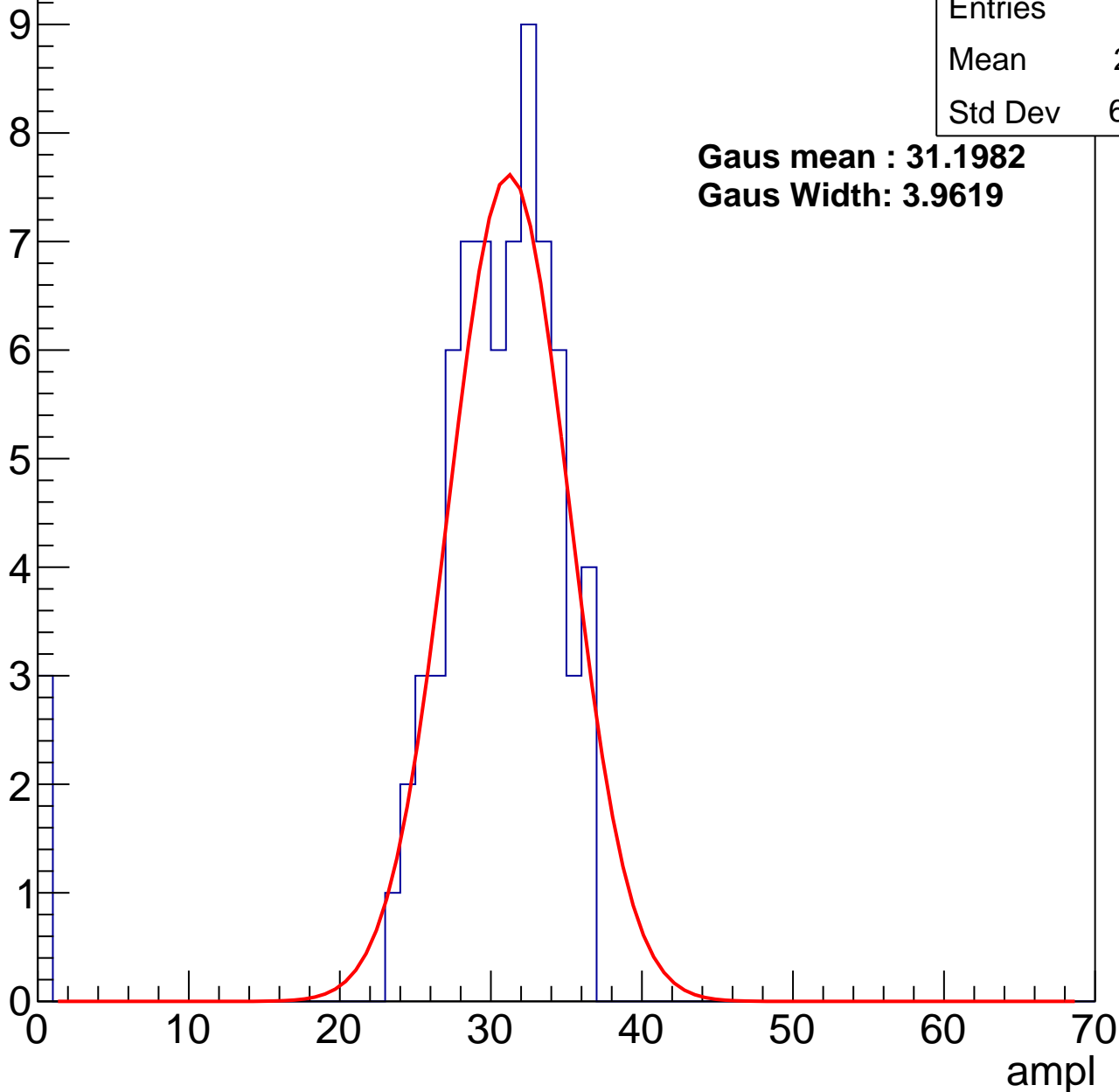
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.11
Std Dev	6.773

Gaus mean : 31.1982

Gaus Width: 3.9619



B1L101S, U17-ch117, adc1

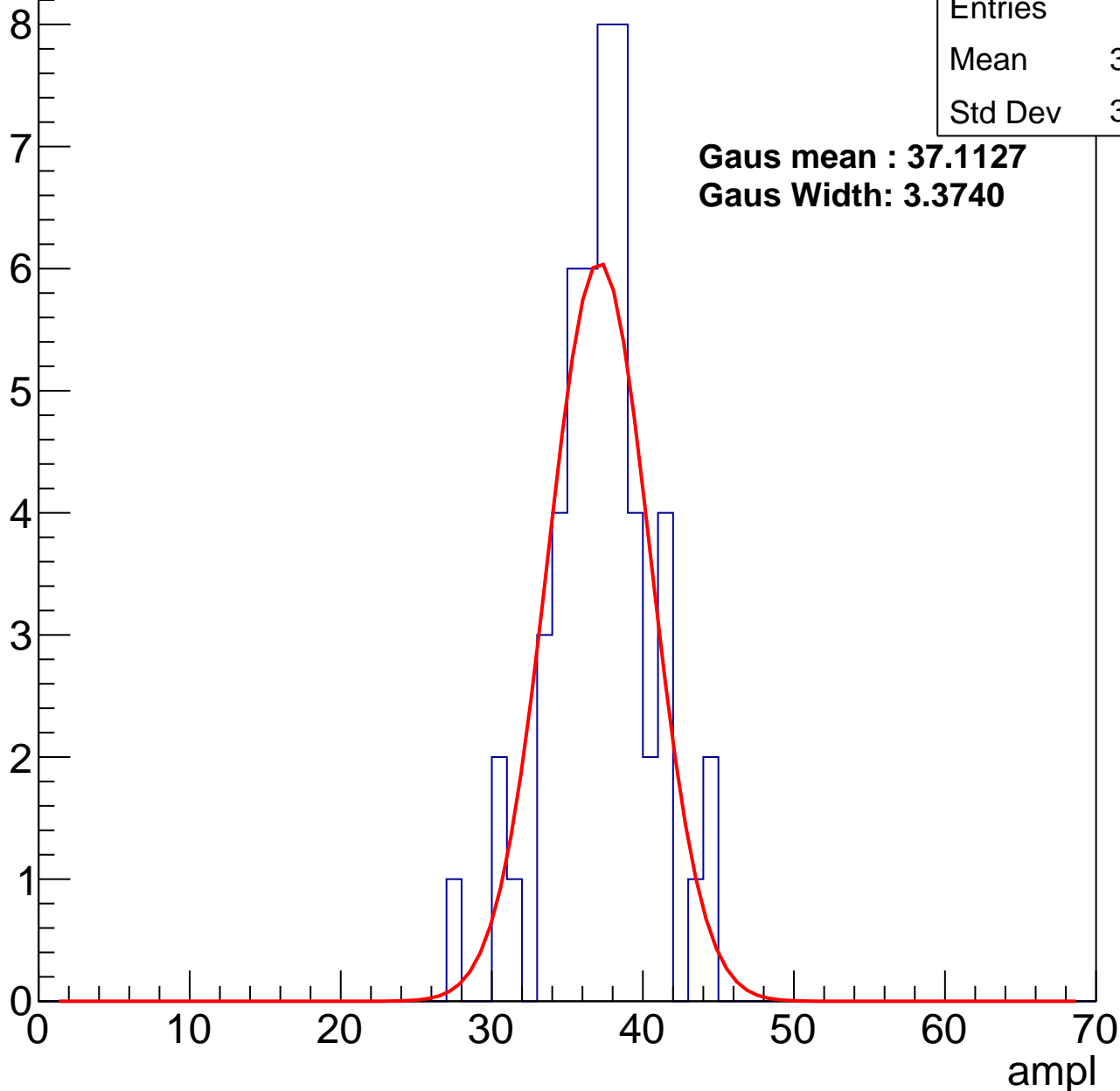
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	36.73
Std Dev	3.352

Gaus mean : 37.1127

Gaus Width: 3.3740



B1L101S, U17-ch117, adc2

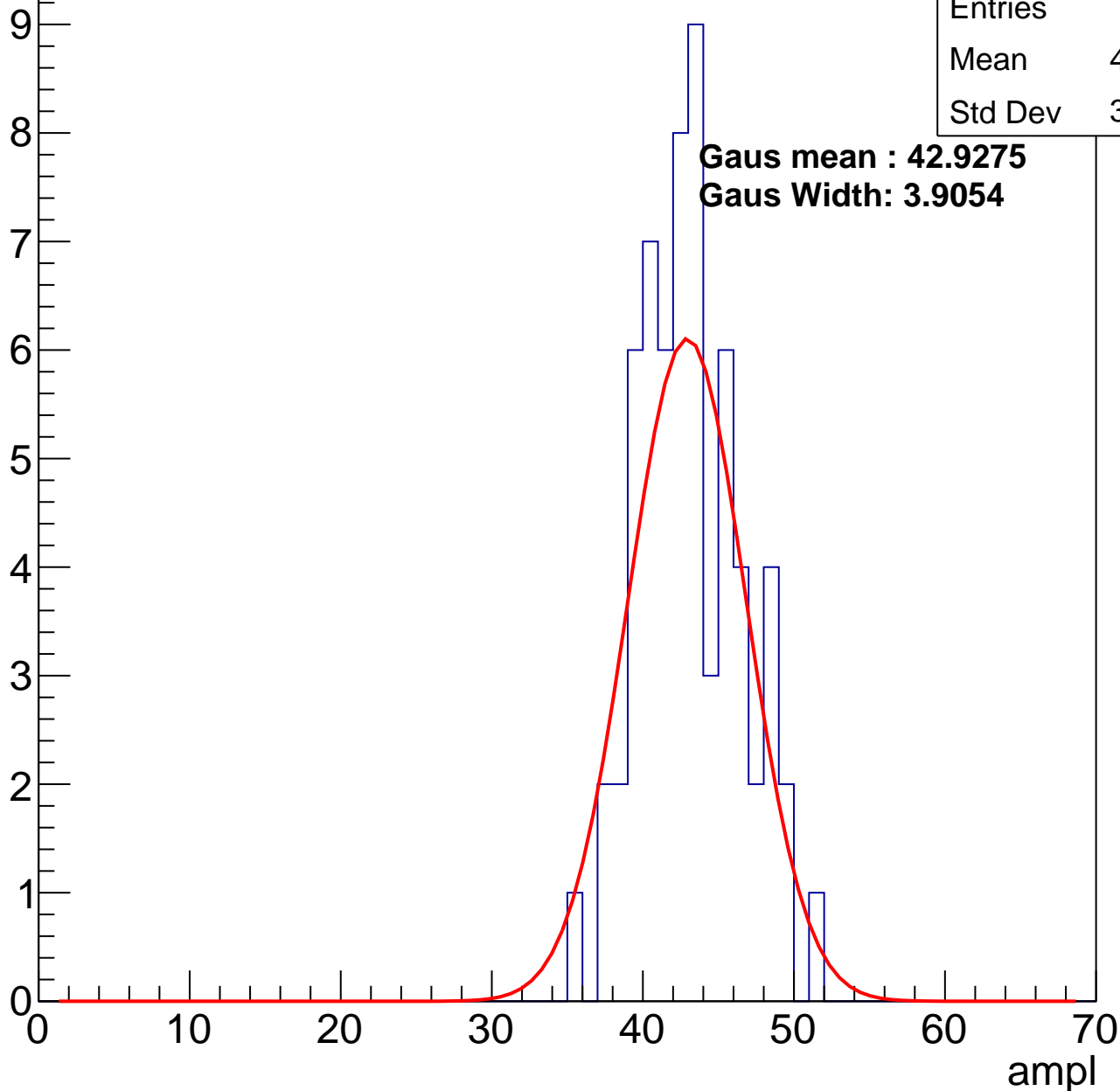
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	42.68
Std Dev	3.337

Gaus mean : 42.9275

Gaus Width: 3.9054

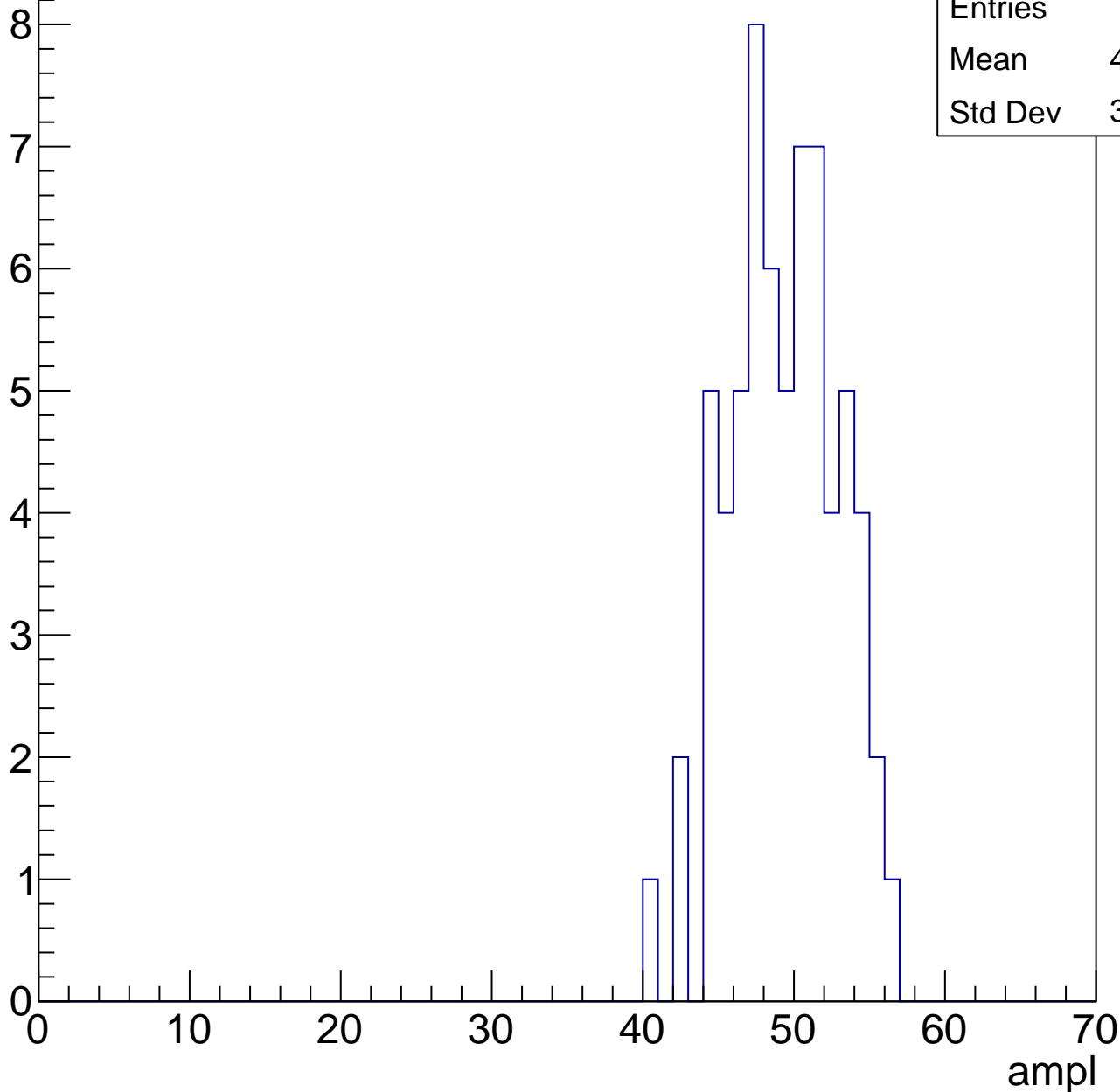


B1L101S, U17-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	48.86
Std Dev	3.529

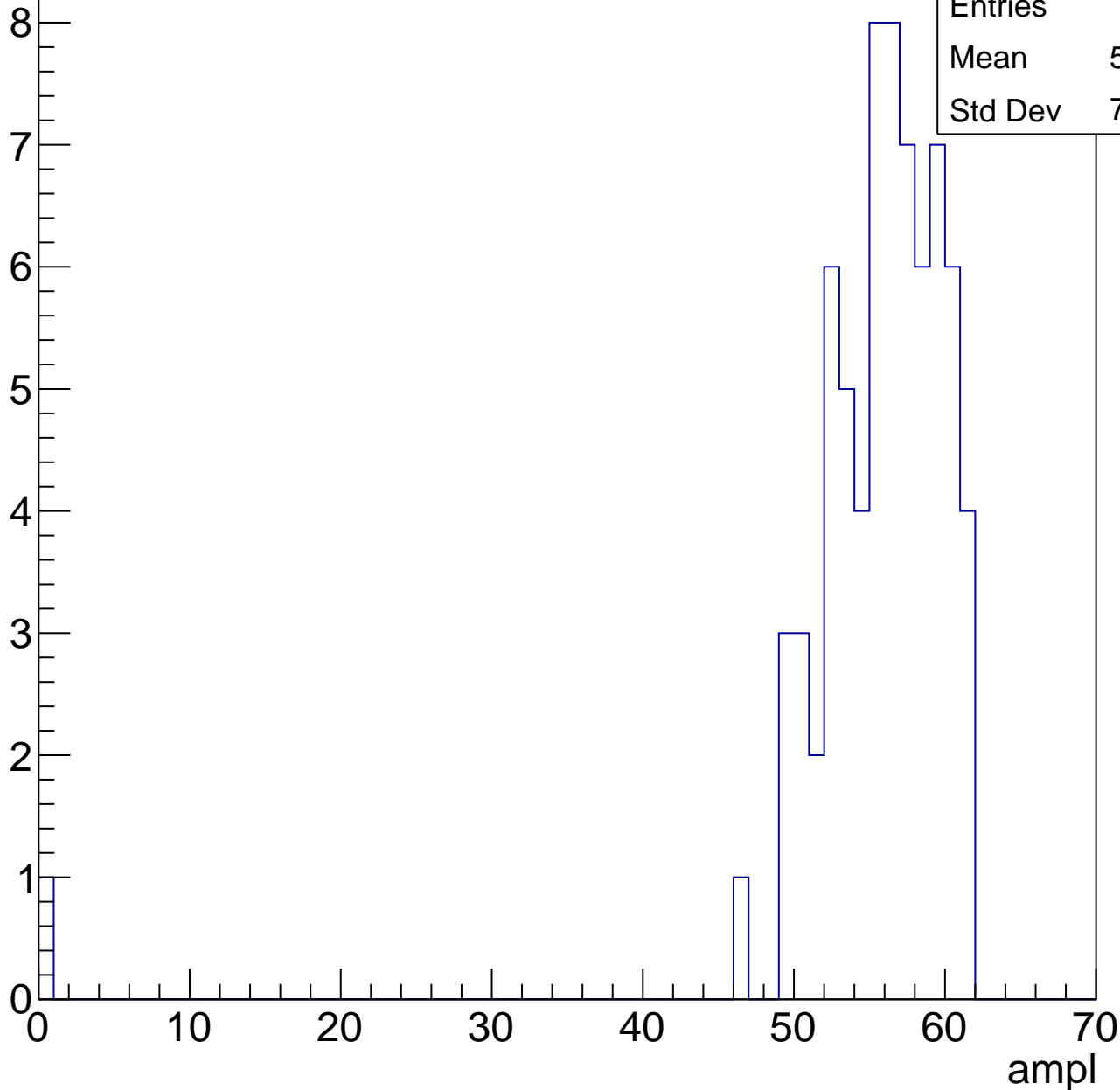


B1L101S, U17-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	54.79
Std Dev	7.404

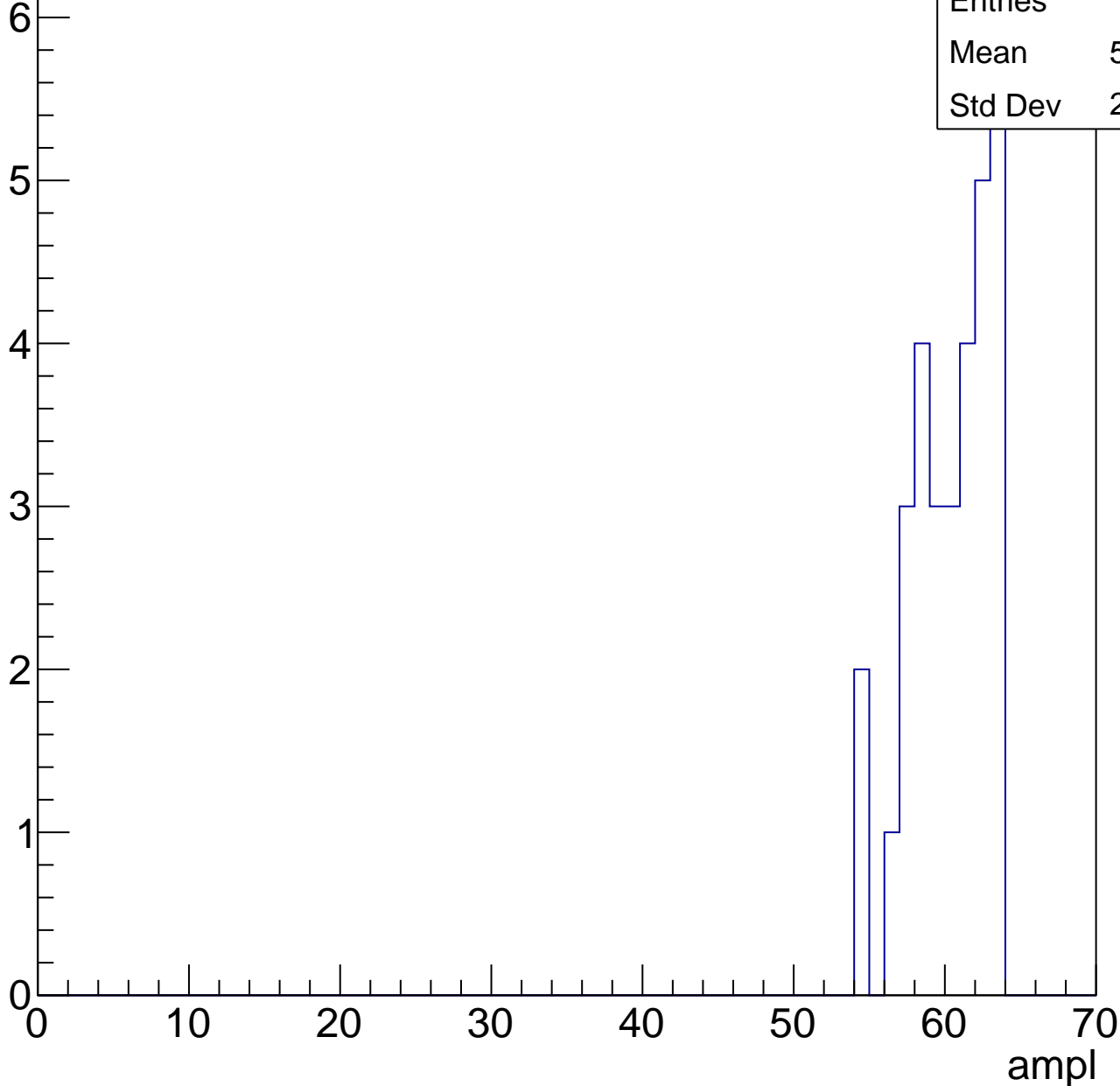


B1L101S, U17-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	59.87
Std Dev	2.612

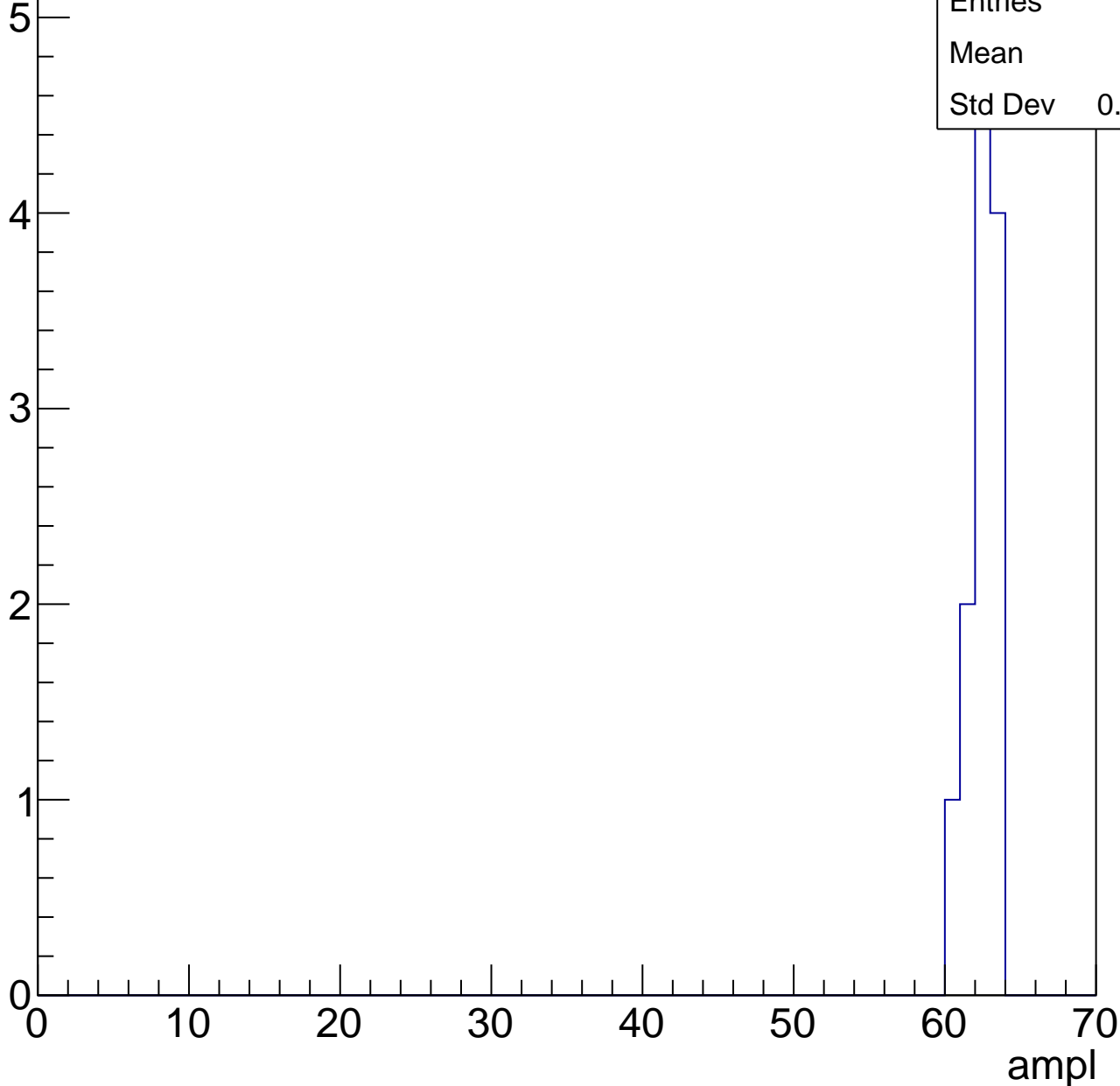


B1L101S, U17-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	62
Std Dev	0.9129



B1L101S, U17-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch118, adc0

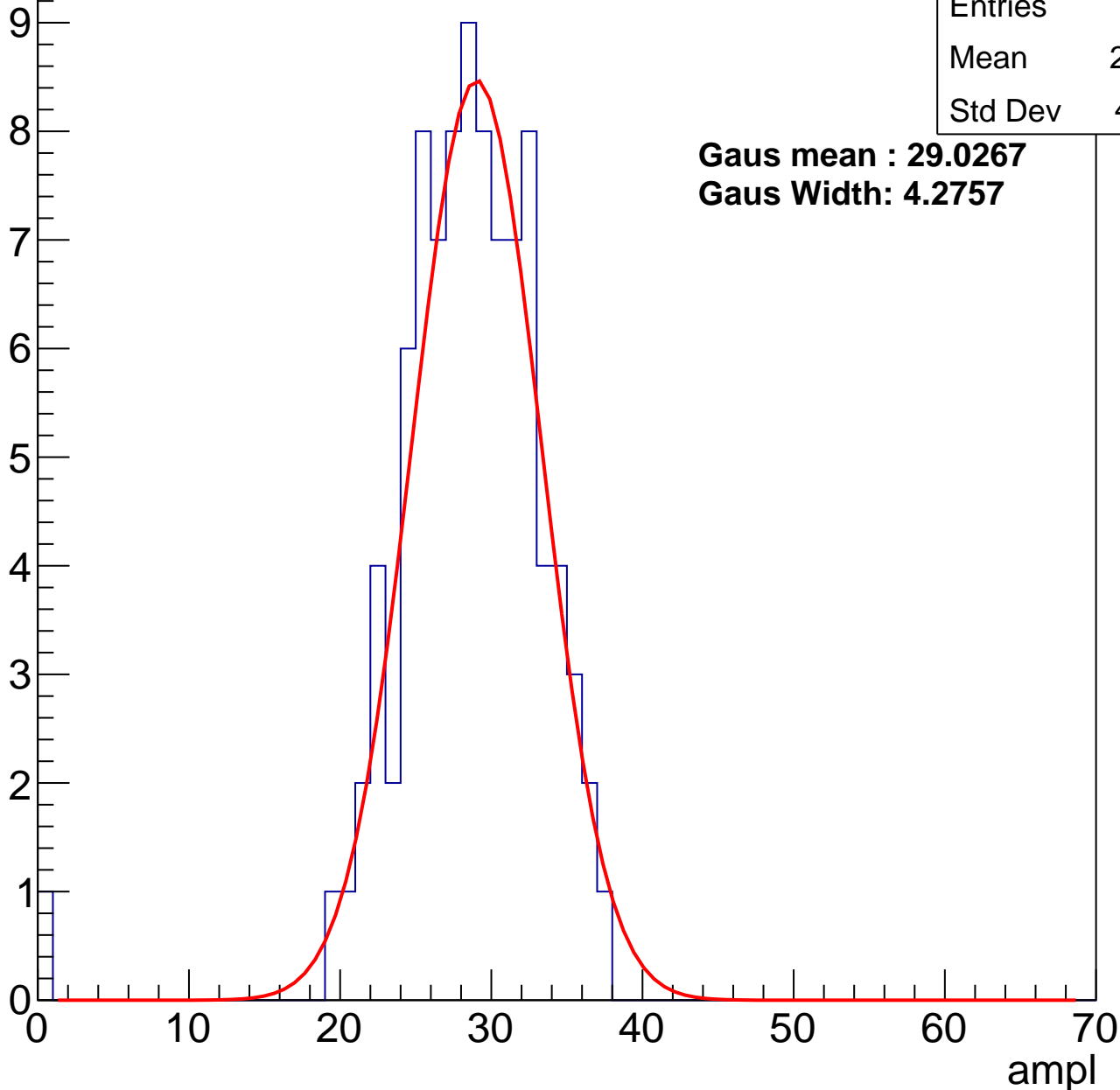
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	28.02
Std Dev	4.921

Gaus mean : 29.0267

Gaus Width: 4.2757



B1L101S, U17-ch118, adc1

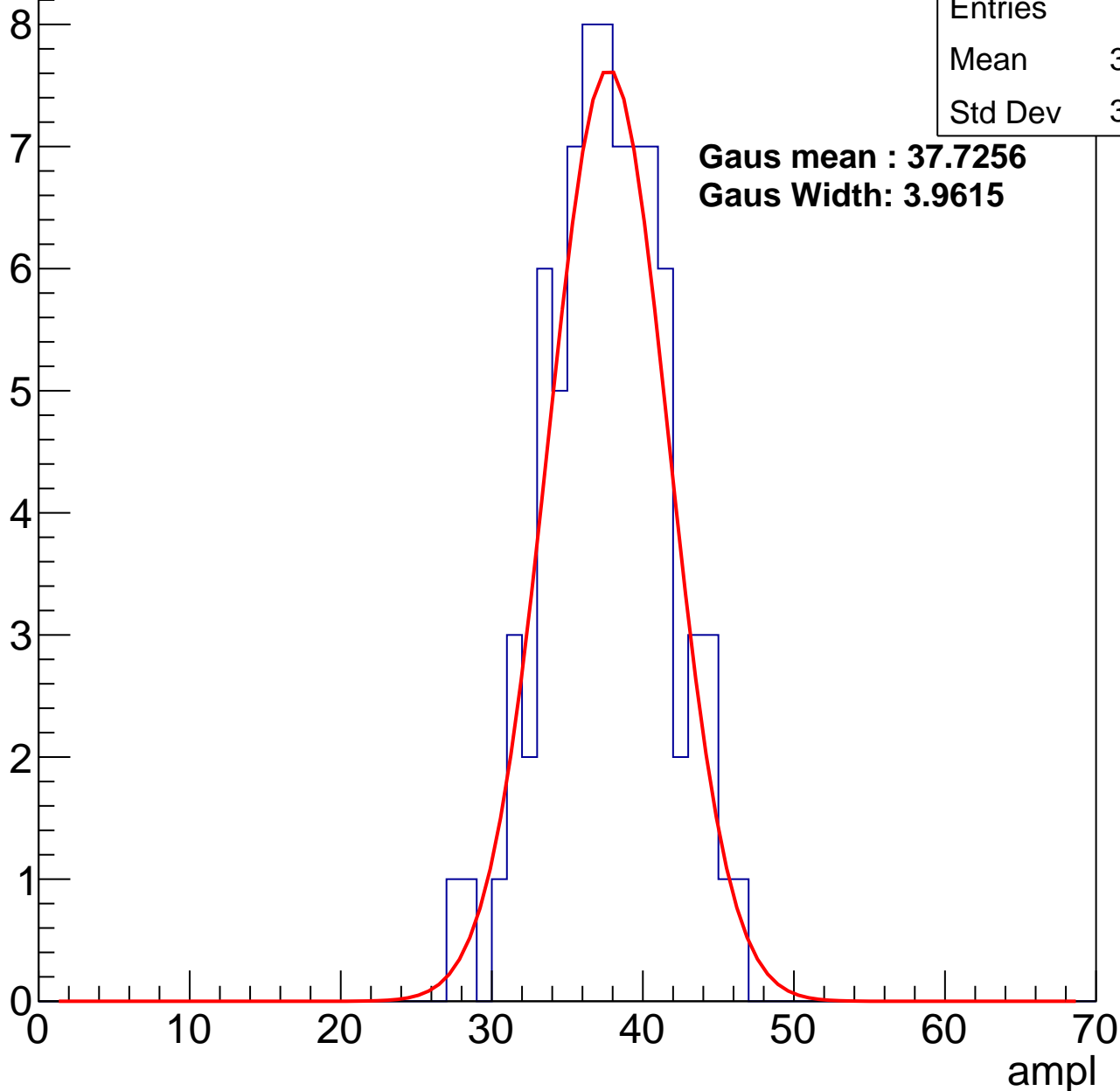
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	37.22
Std Dev	3.906

Gaus mean : 37.7256

Gaus Width: 3.9615



B1L101S, U17-ch118, adc2

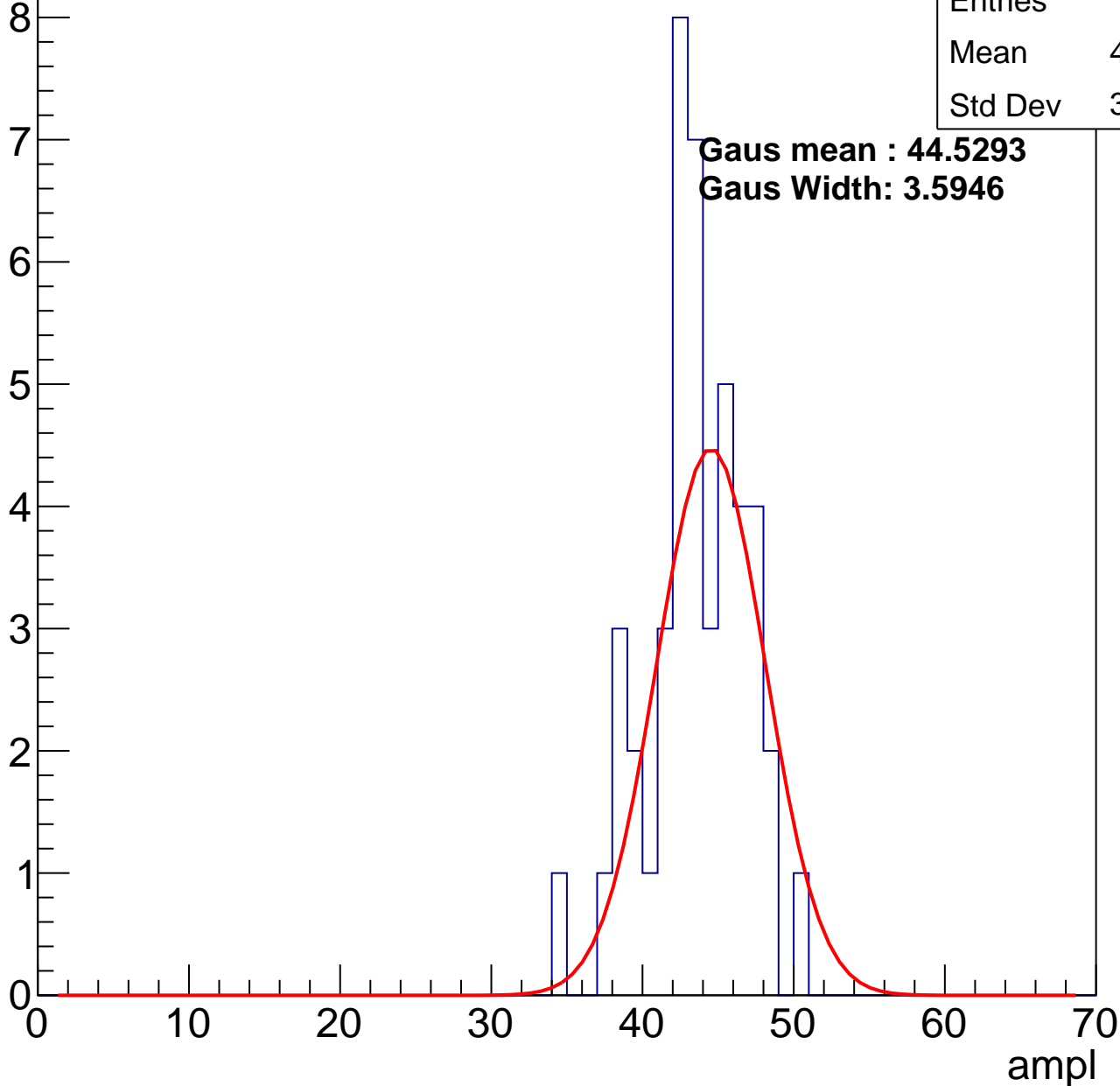
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	43.07
Std Dev	3.248

Gaus mean : 44.5293

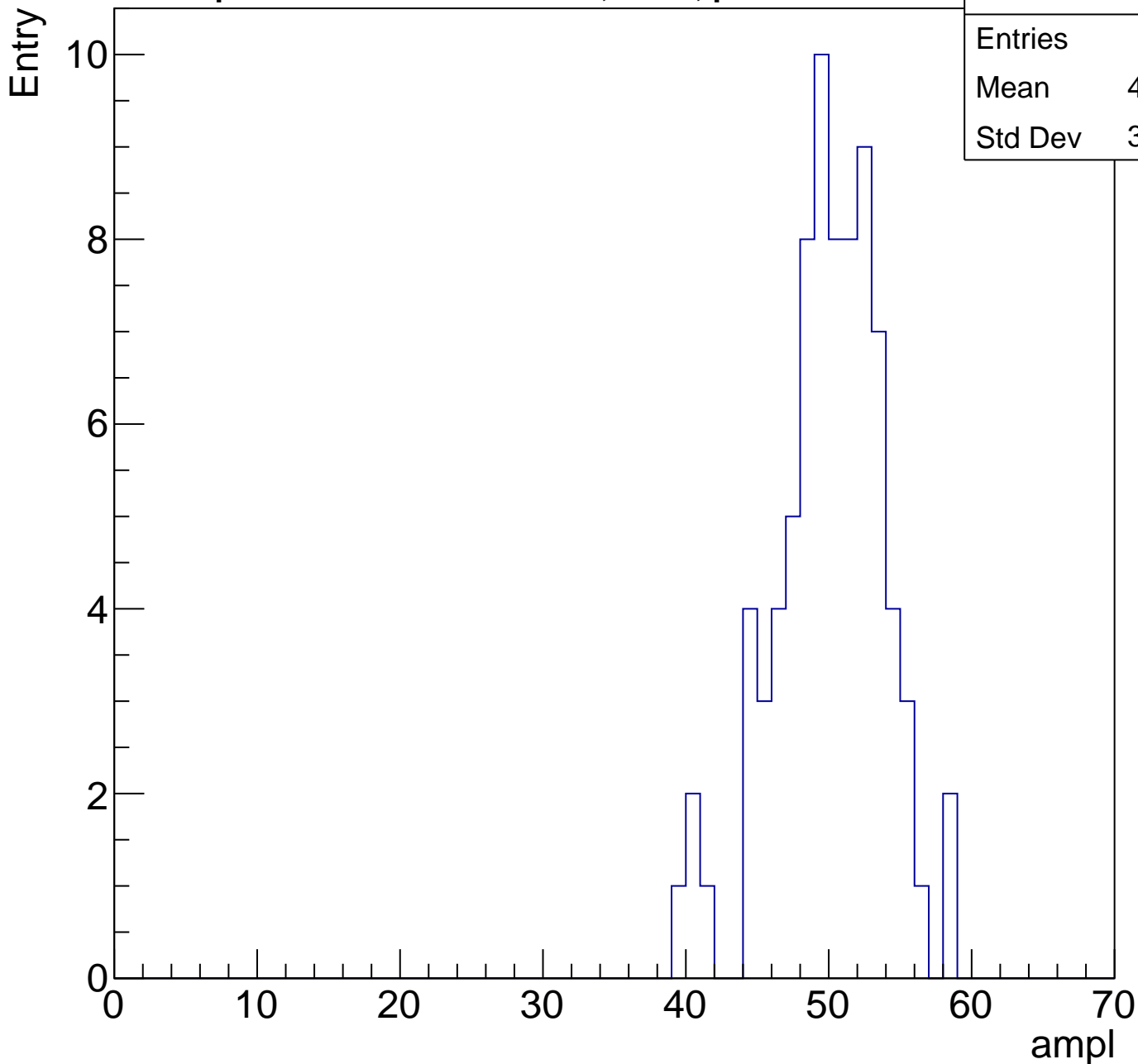
Gaus Width: 3.5946



B1L101S, U17-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	49.55
Std Dev	3.817

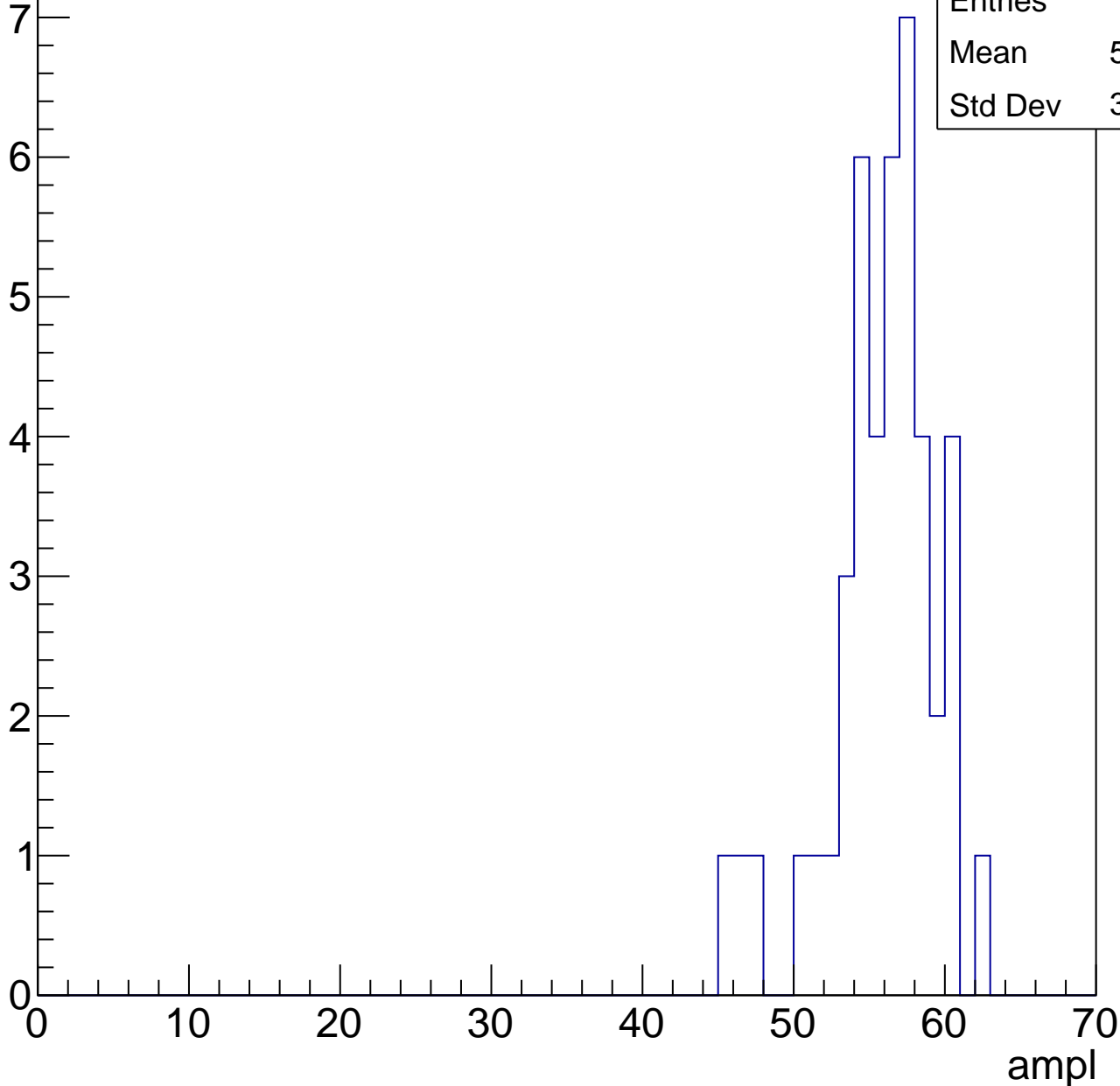


B1L101S, U17-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

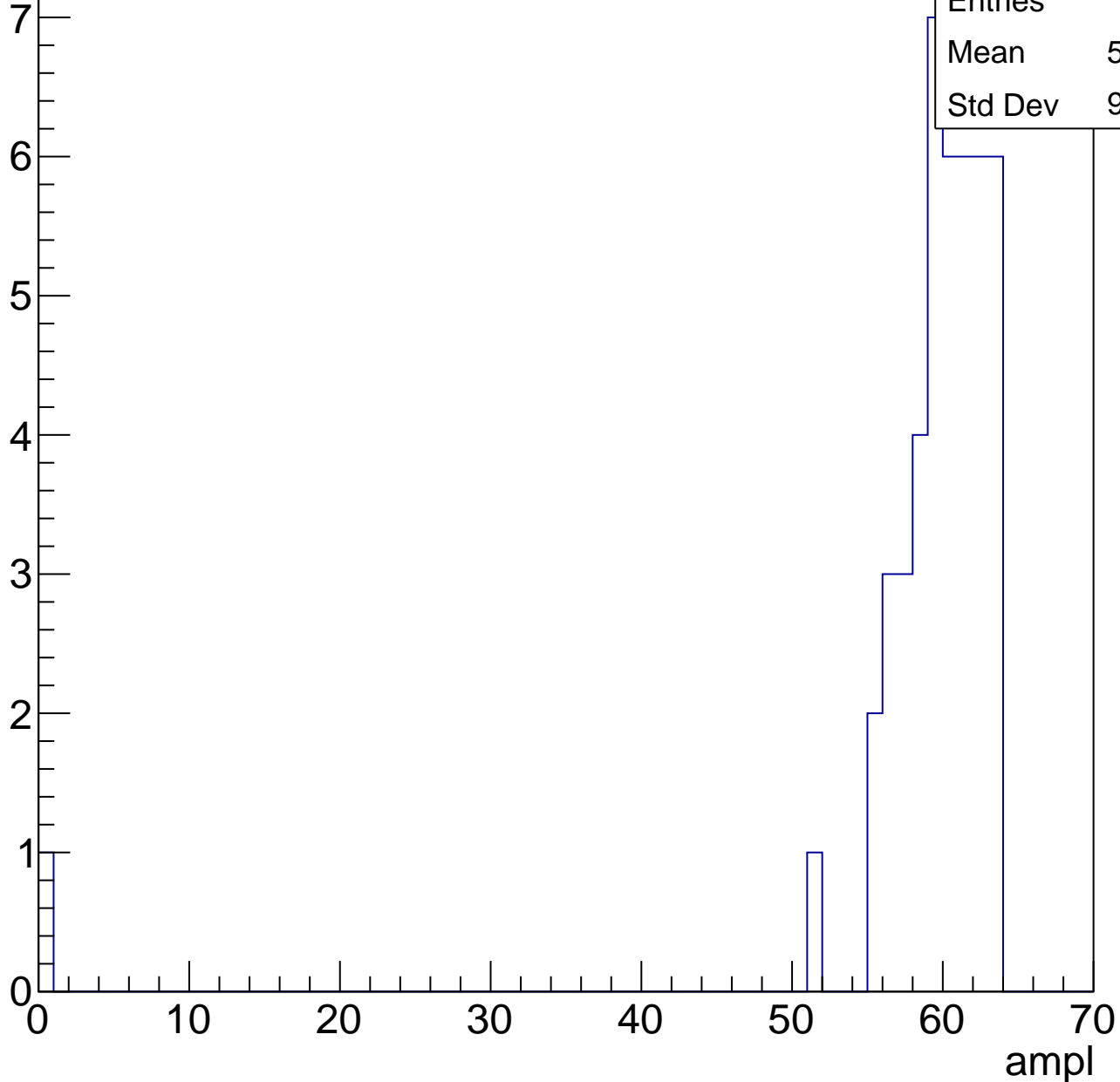
Entries	43
Mean	55.37
Std Dev	3.602



B1L101S, U17-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

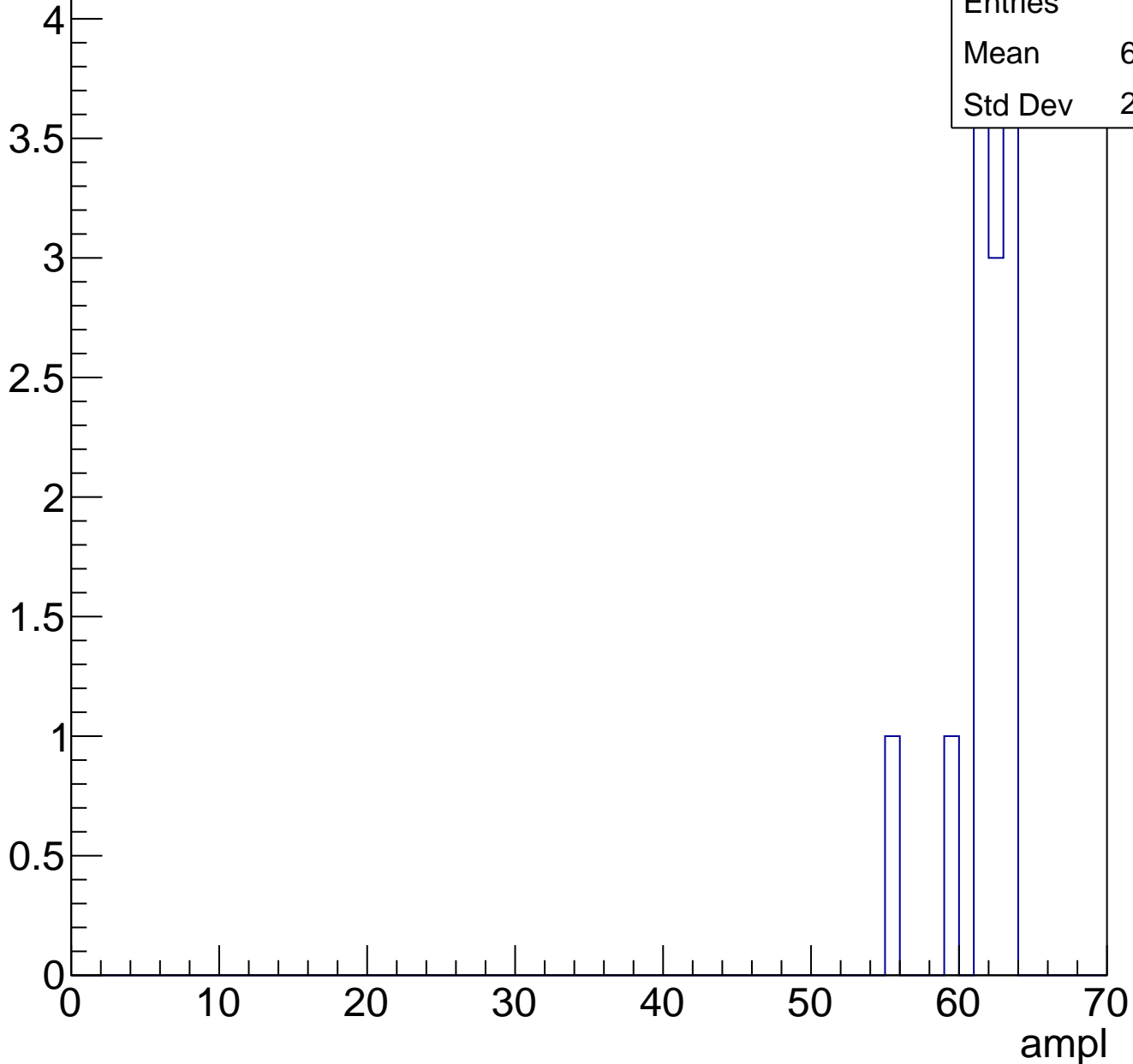
Entry



B1L101S, U17-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U17-ch119, adc0

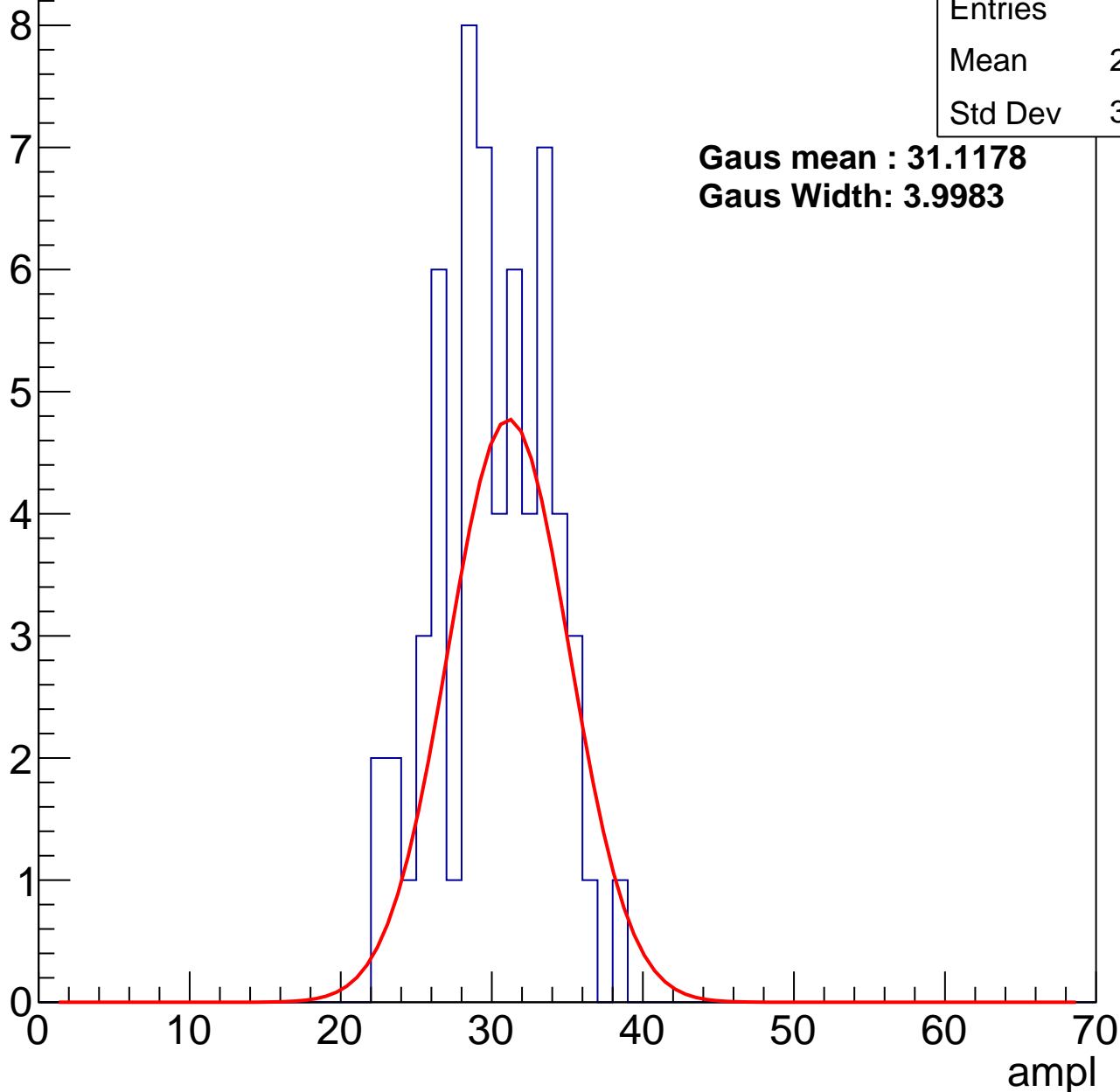
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.65
Std Dev	3.646

Gaus mean : 31.1178

Gaus Width: 3.9983



B1L101S, U17-ch119, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	36.55
Std Dev	5.43

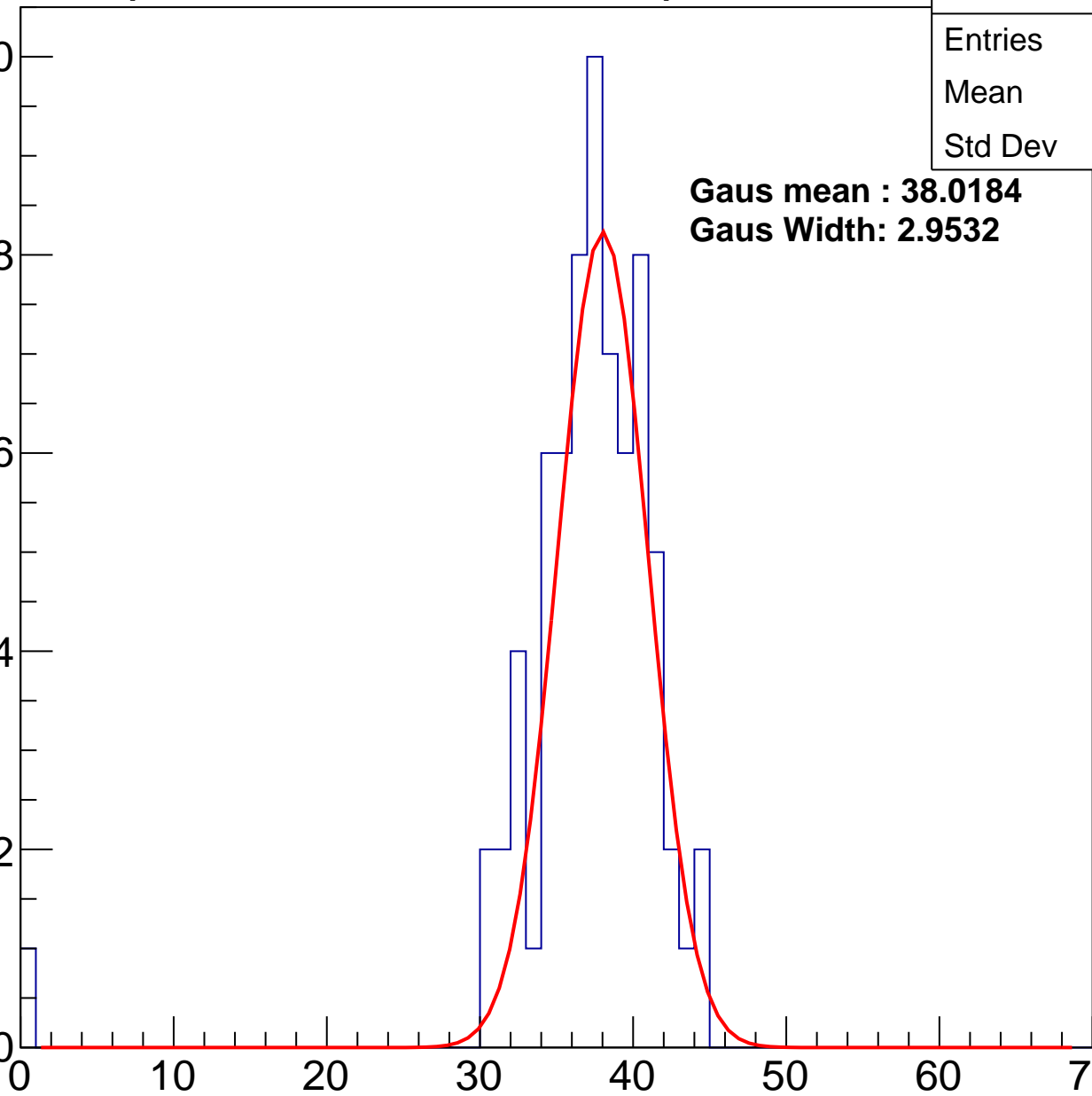
Gaus mean : 38.0184

Gaus Width: 2.9532

Entry

10
8
6
4
2
0

ampl



B1L101S, U17-ch119, adc2

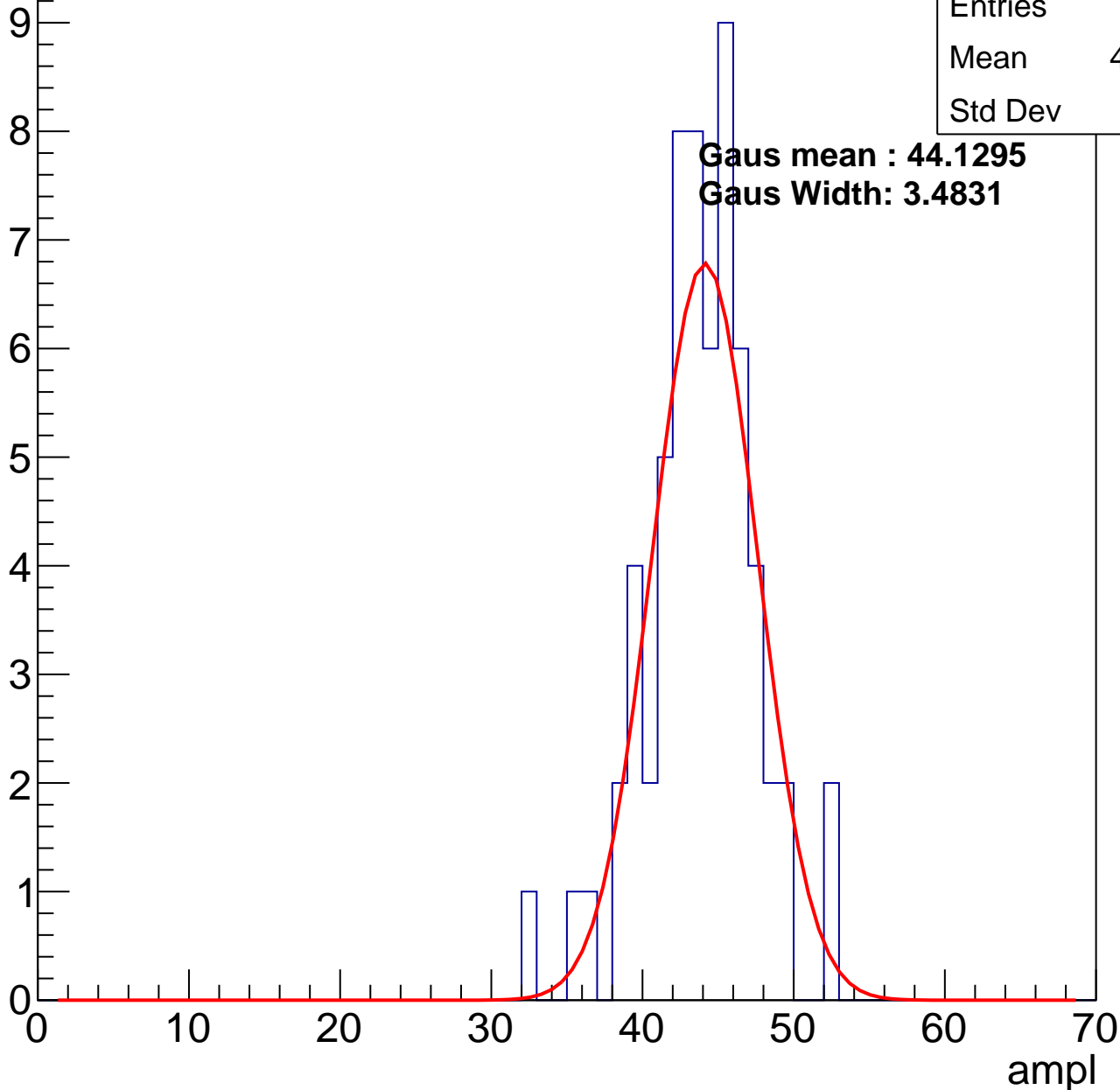
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.35
Std Dev	3.63

Gaus mean : 44.1295

Gaus Width: 3.4831



B1L101S, U17-ch119, adc3

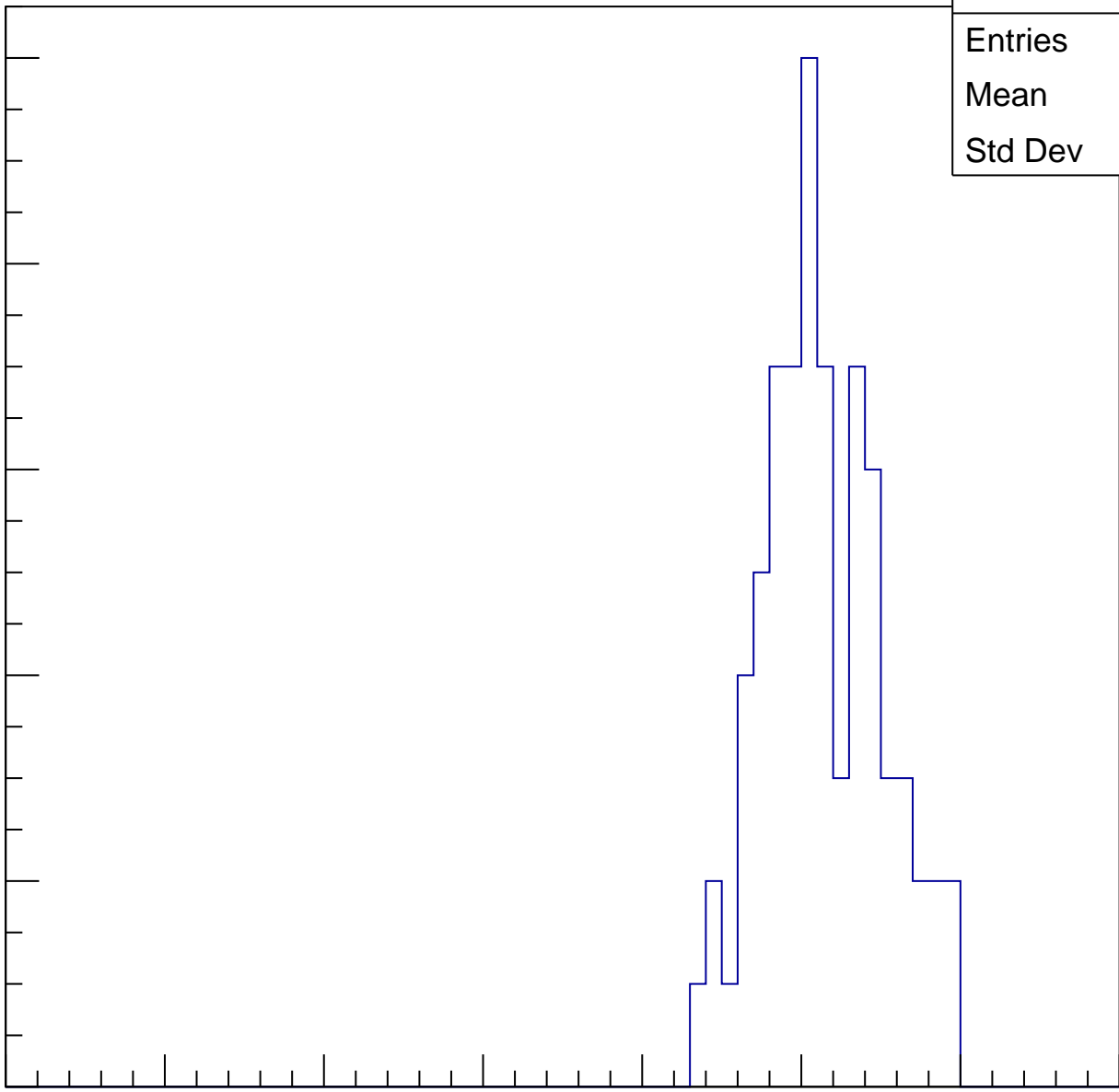
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	50.88
Std Dev	3.697

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

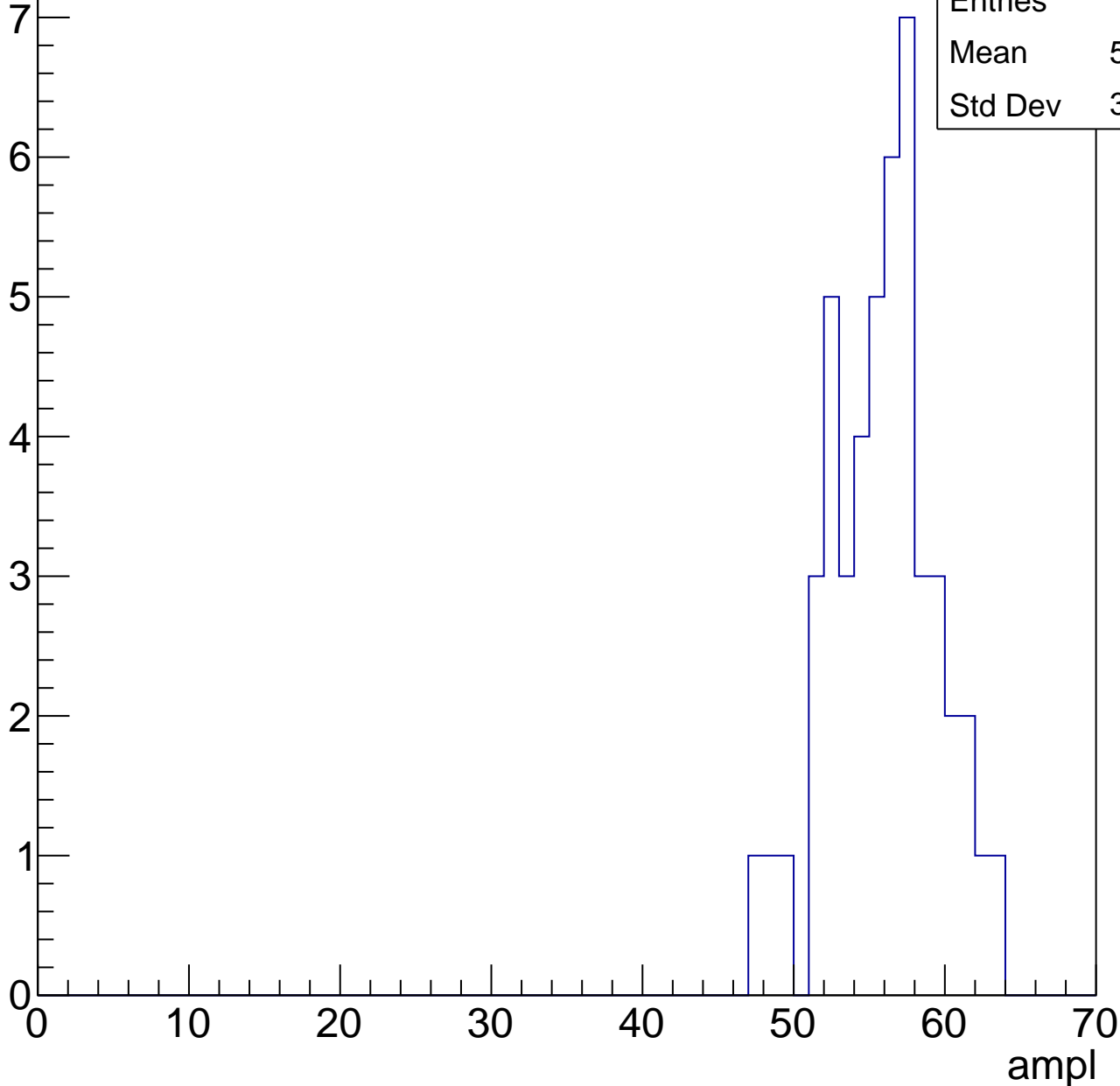


B1L101S, U17-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

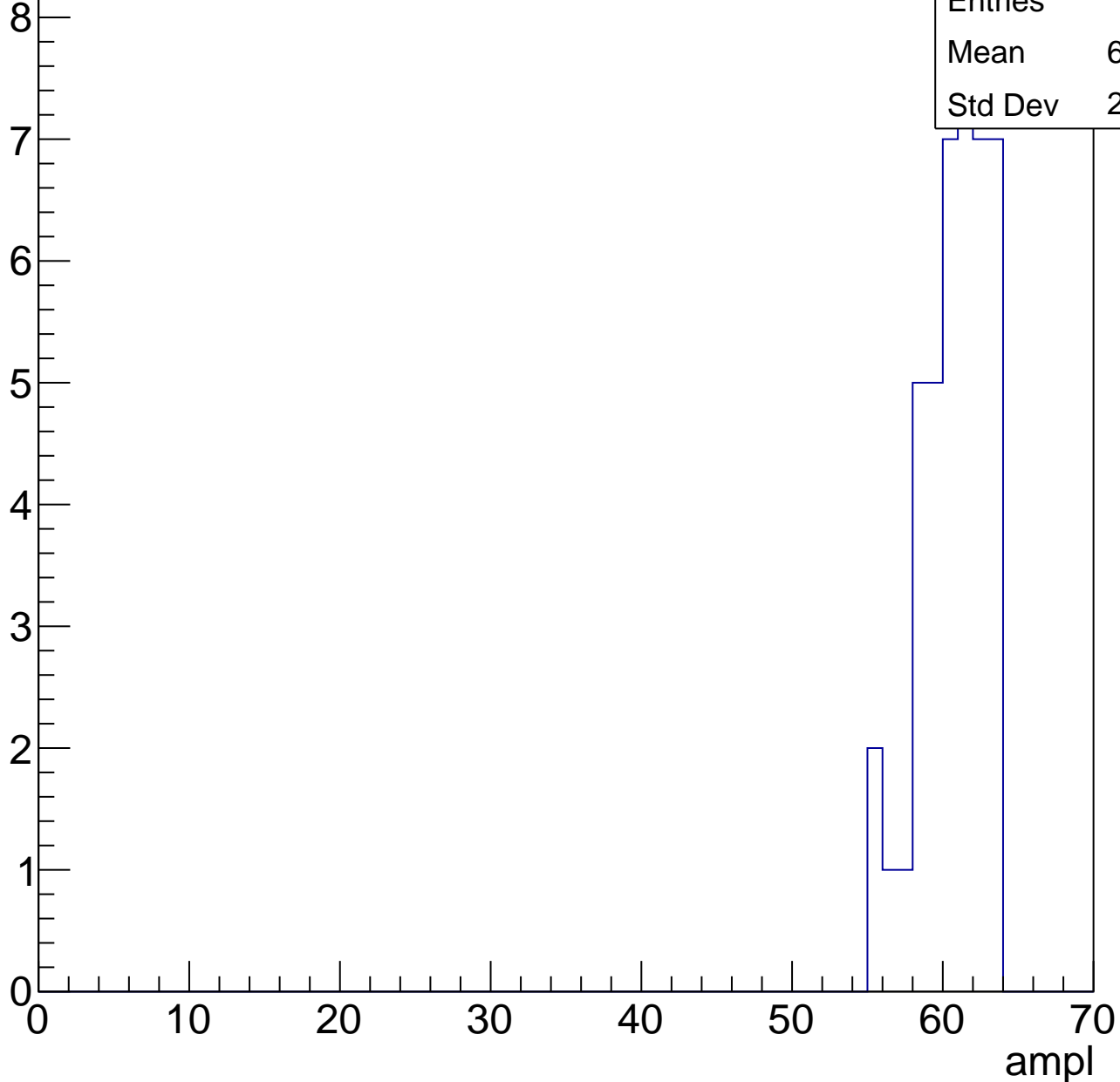
Entries	48
Mean	55.42
Std Dev	3.517



B1L101S, U17-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

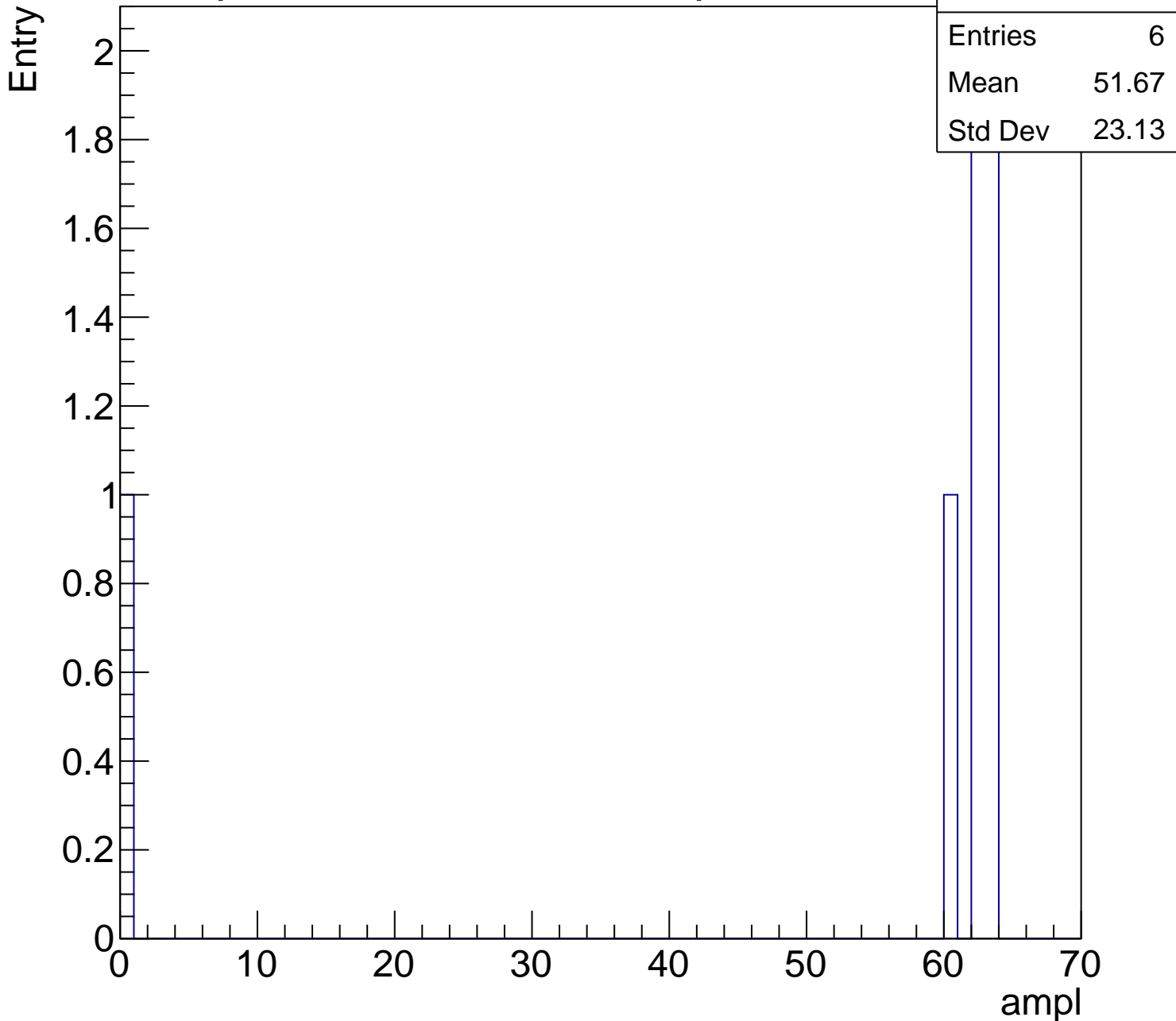
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.13

0 10 20 30 40 50 60 70

ampl



B1L101S, U17-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch120, adc0

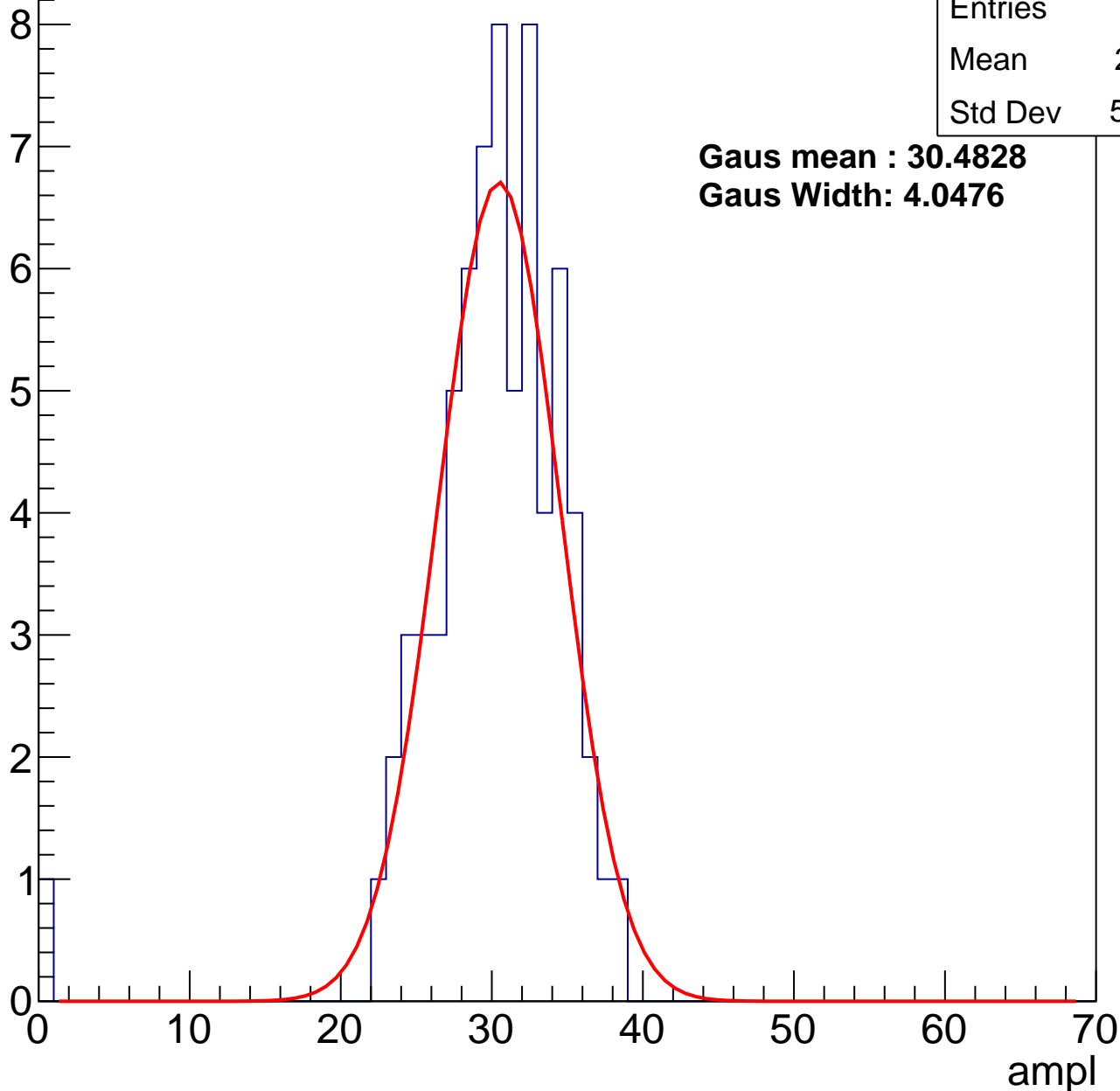
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.61
Std Dev	5.089

Gaus mean : 30.4828

Gaus Width: 4.0476



B1L101S, U17-ch120, adc1

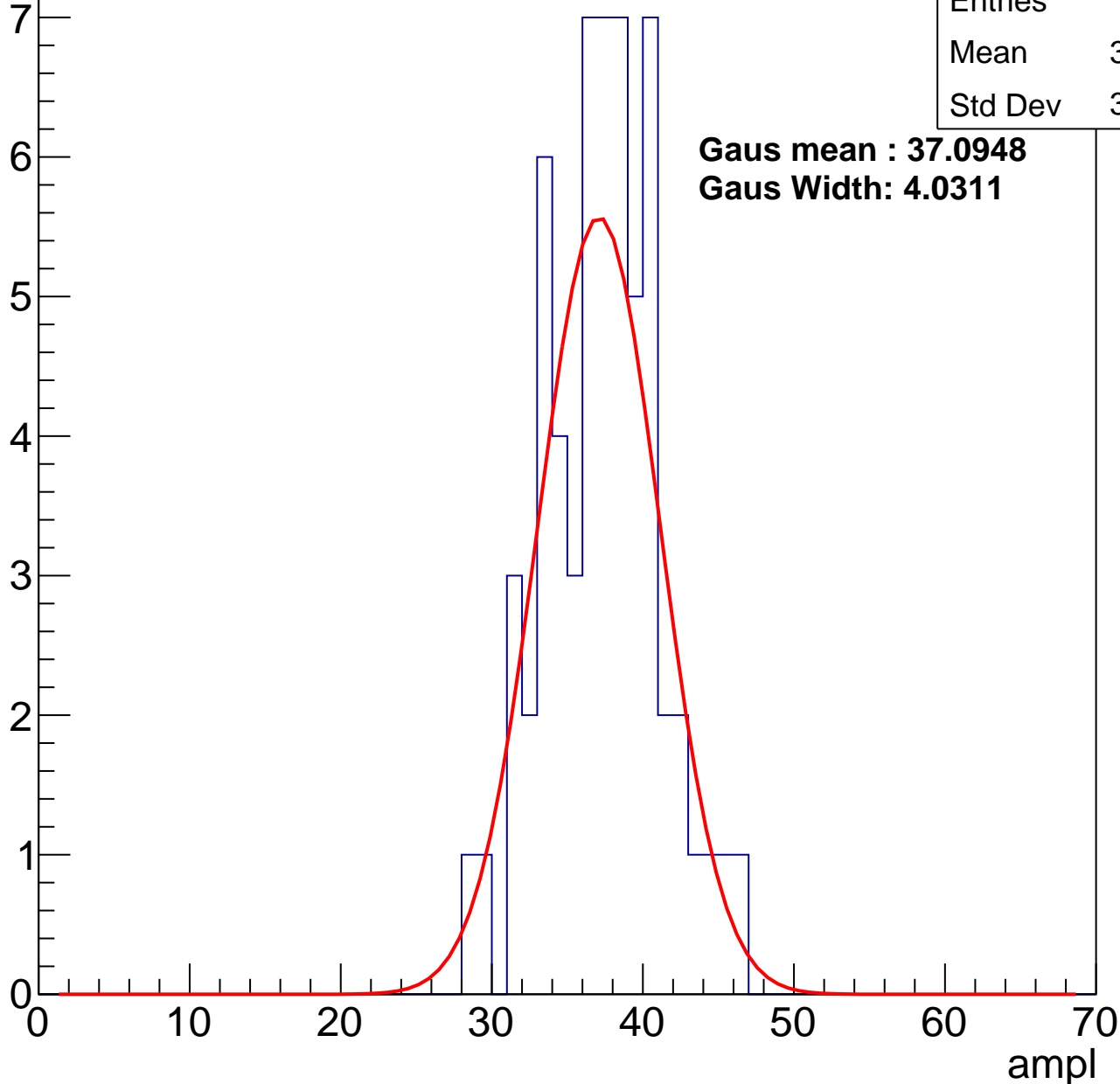
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.87
Std Dev	3.744

Gaus mean : 37.0948

Gaus Width: 4.0311



B1L101S, U17-ch120, adc2

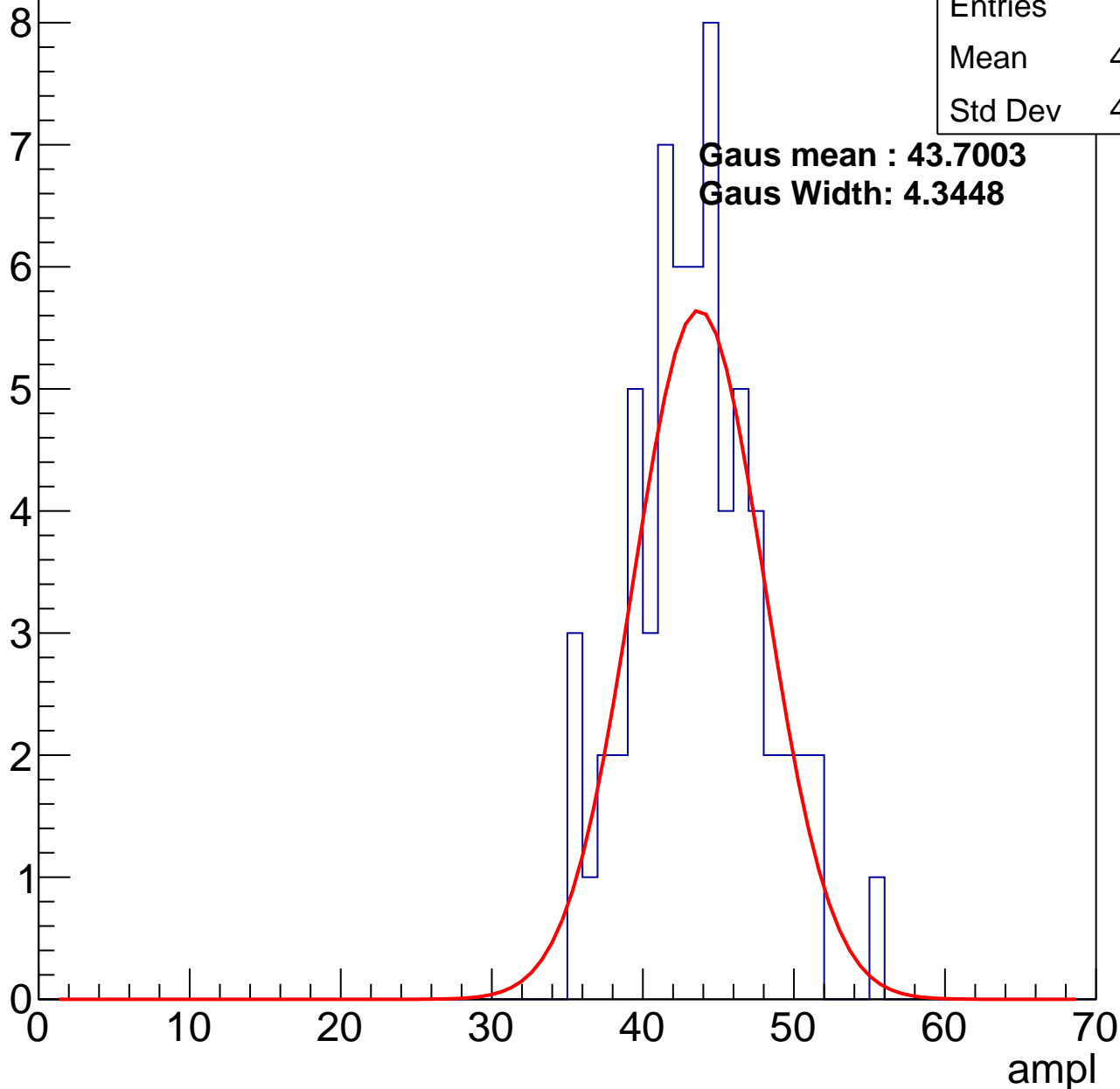
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.14
Std Dev	4.165

Gaus mean : 43.7003

Gaus Width: 4.3448

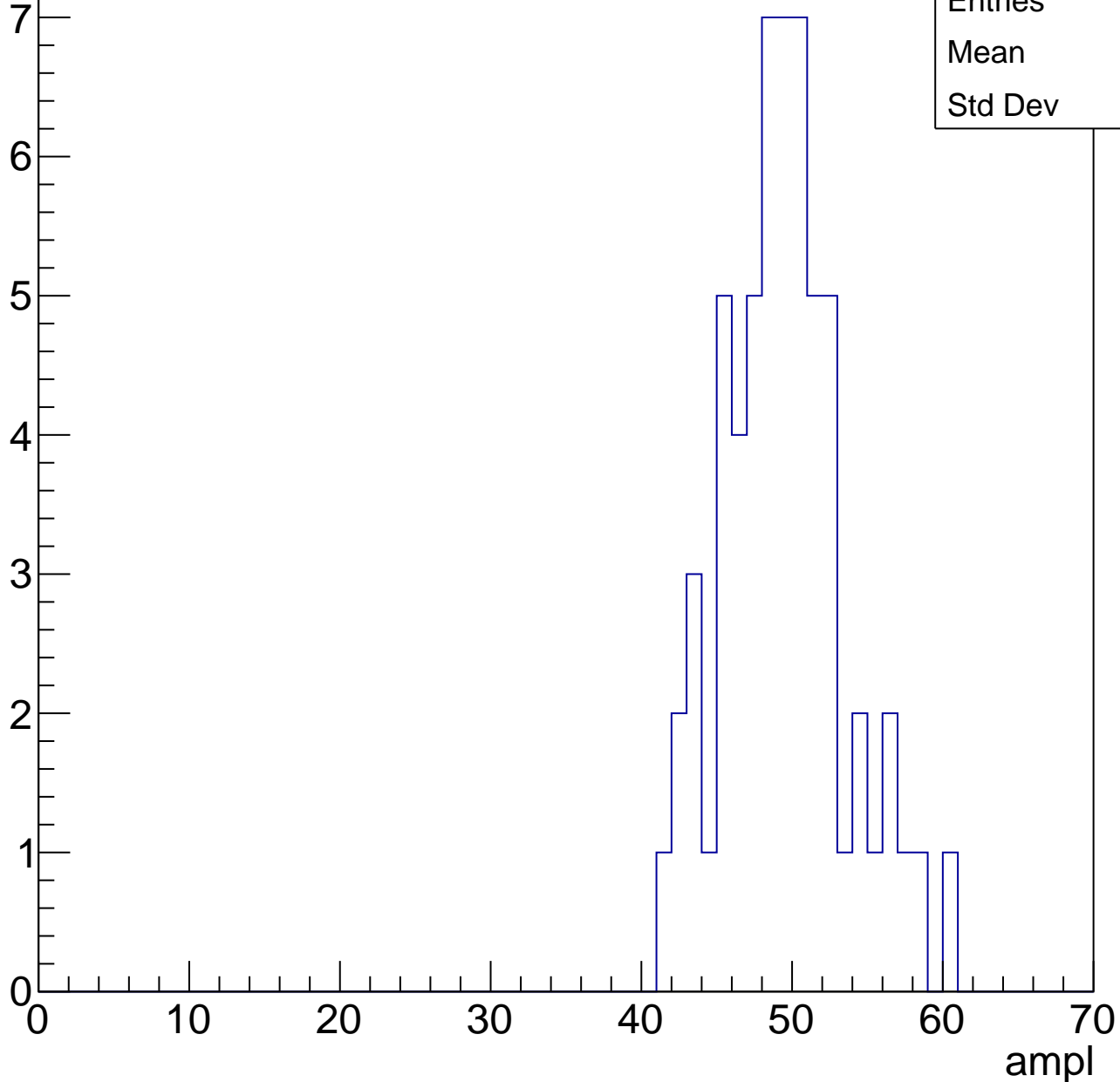


B1L101S, U17-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

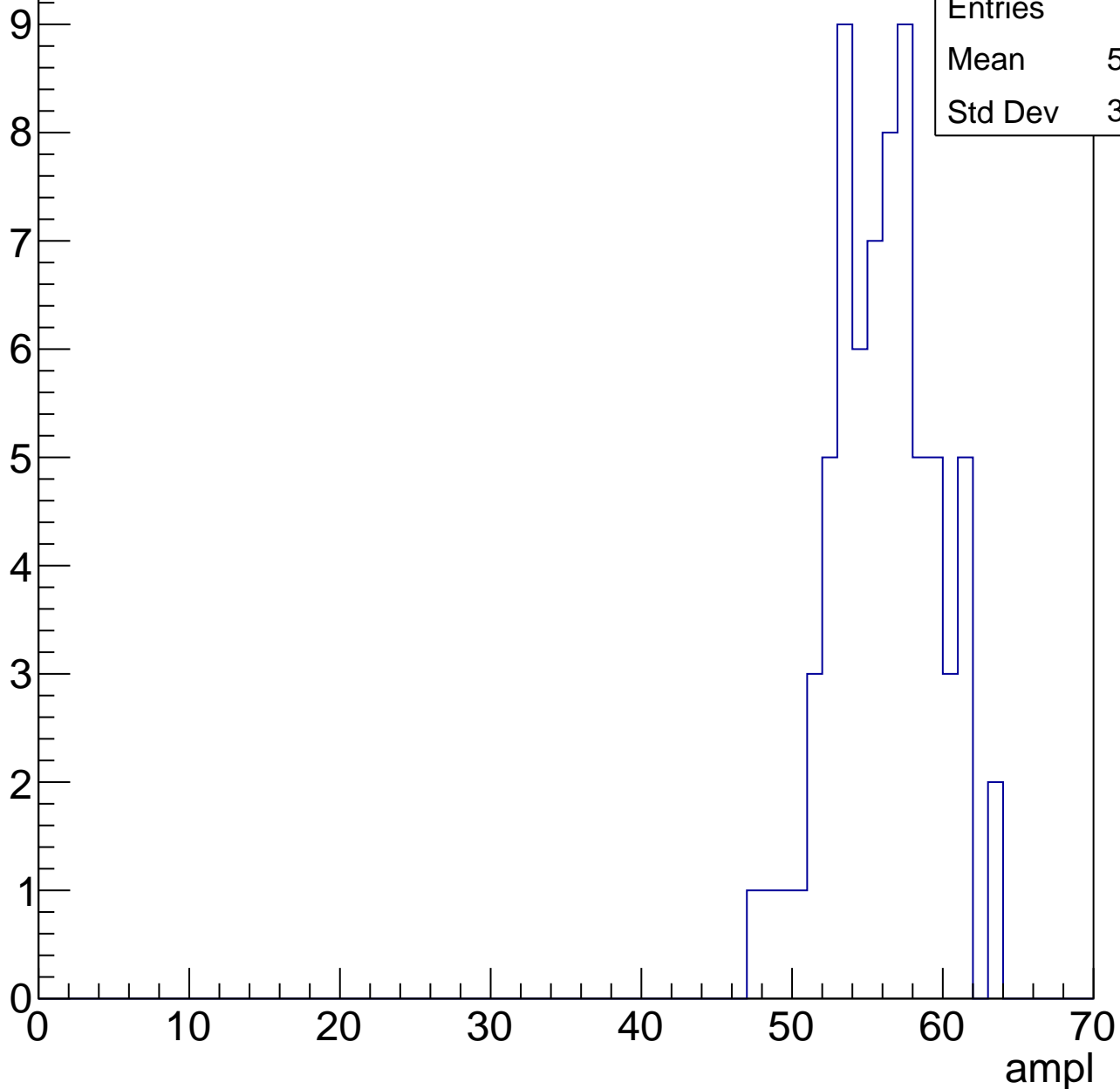
Entries	61
Mean	49
Std Dev	4



B1L101S, U17-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

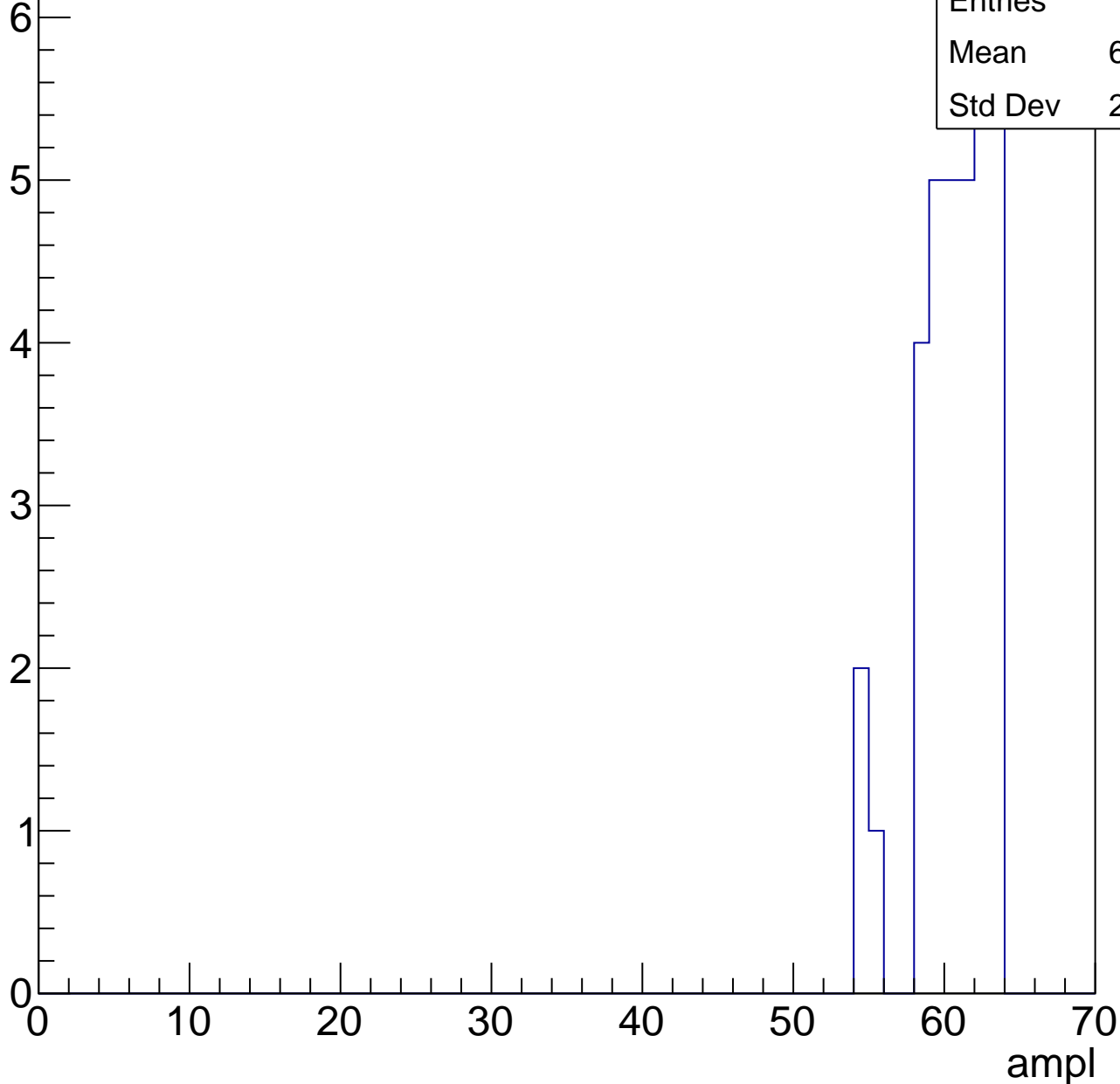


B1L101S, U17-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

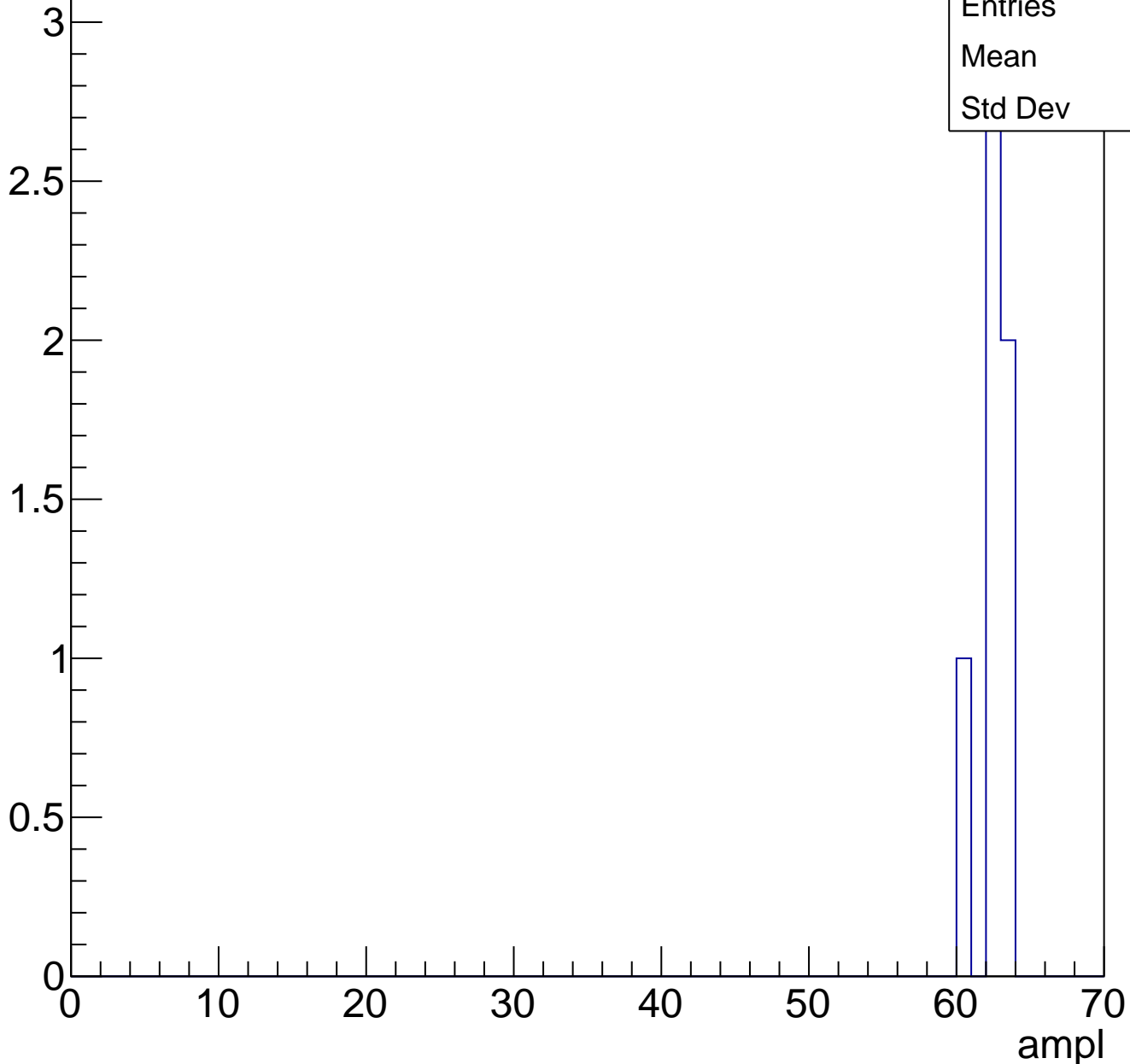
Entries	34
Mean	60.15
Std Dev	2.427



B1L101S, U17-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch121, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	27.59
Std Dev	7.306

Gaus mean : 29.7087

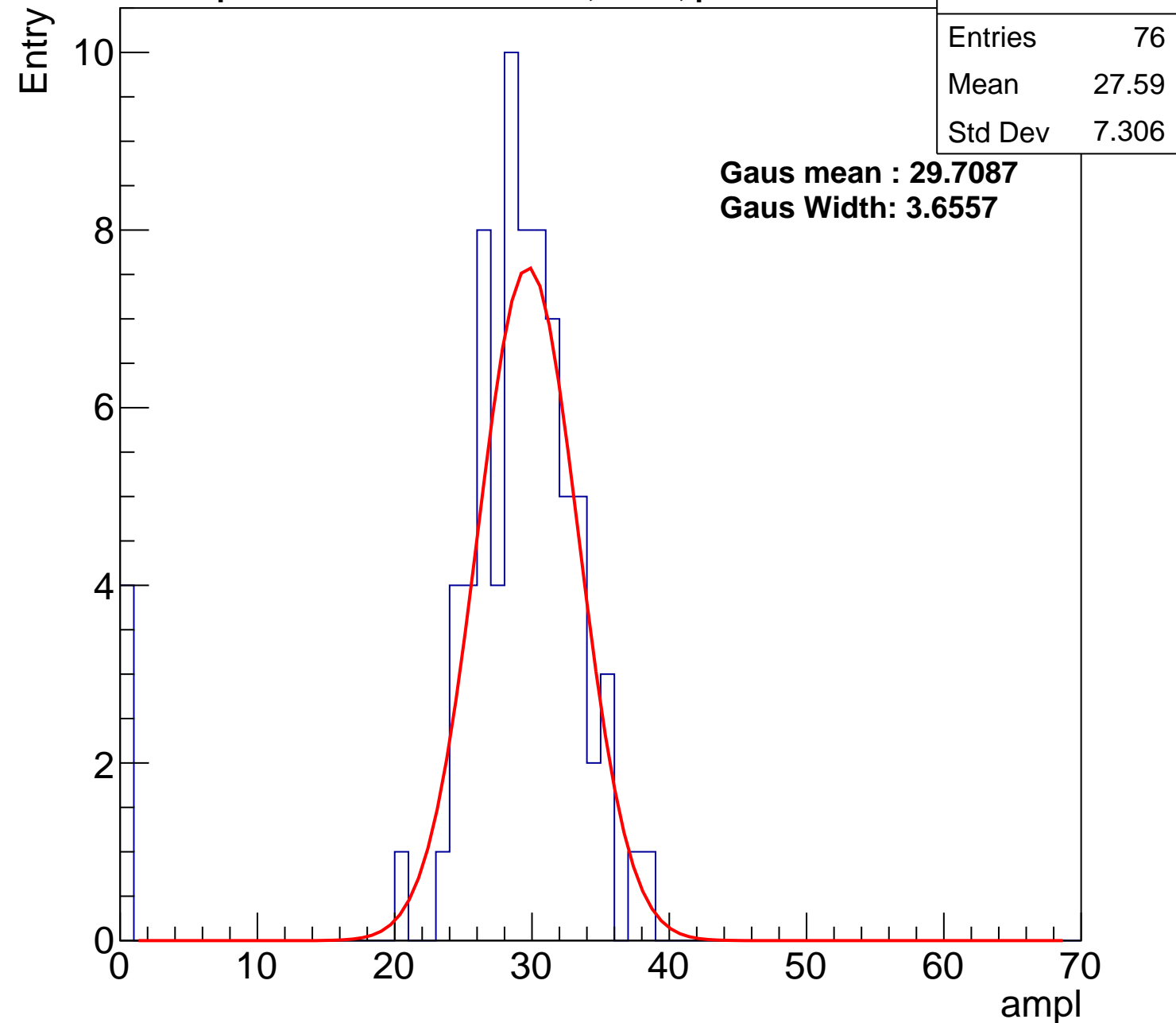
Gaus Width: 3.6557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch121, adc1

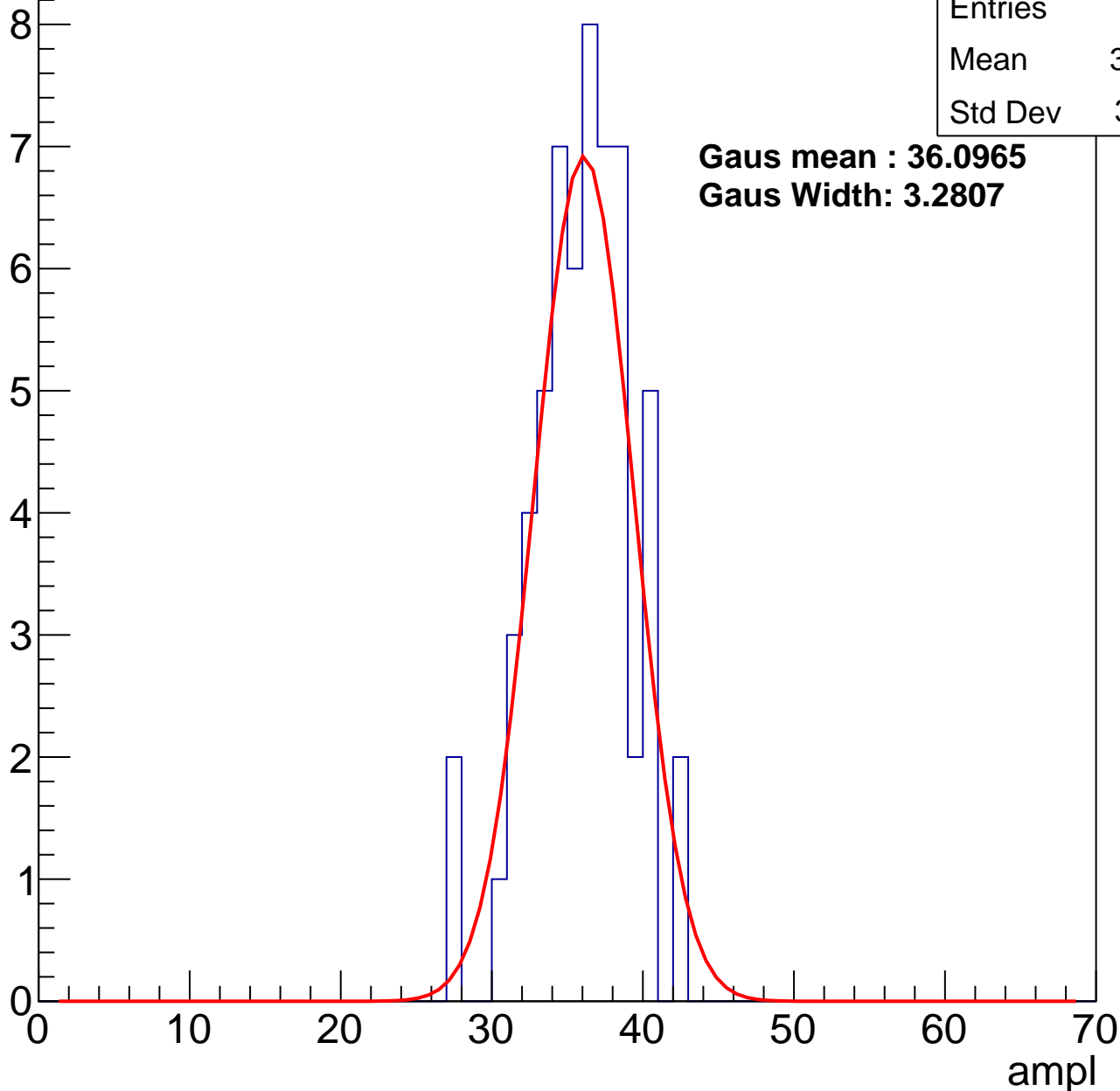
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.47
Std Dev	3.201

Gaus mean : 36.0965

Gaus Width: 3.2807



B1L101S, U17-ch121, adc2

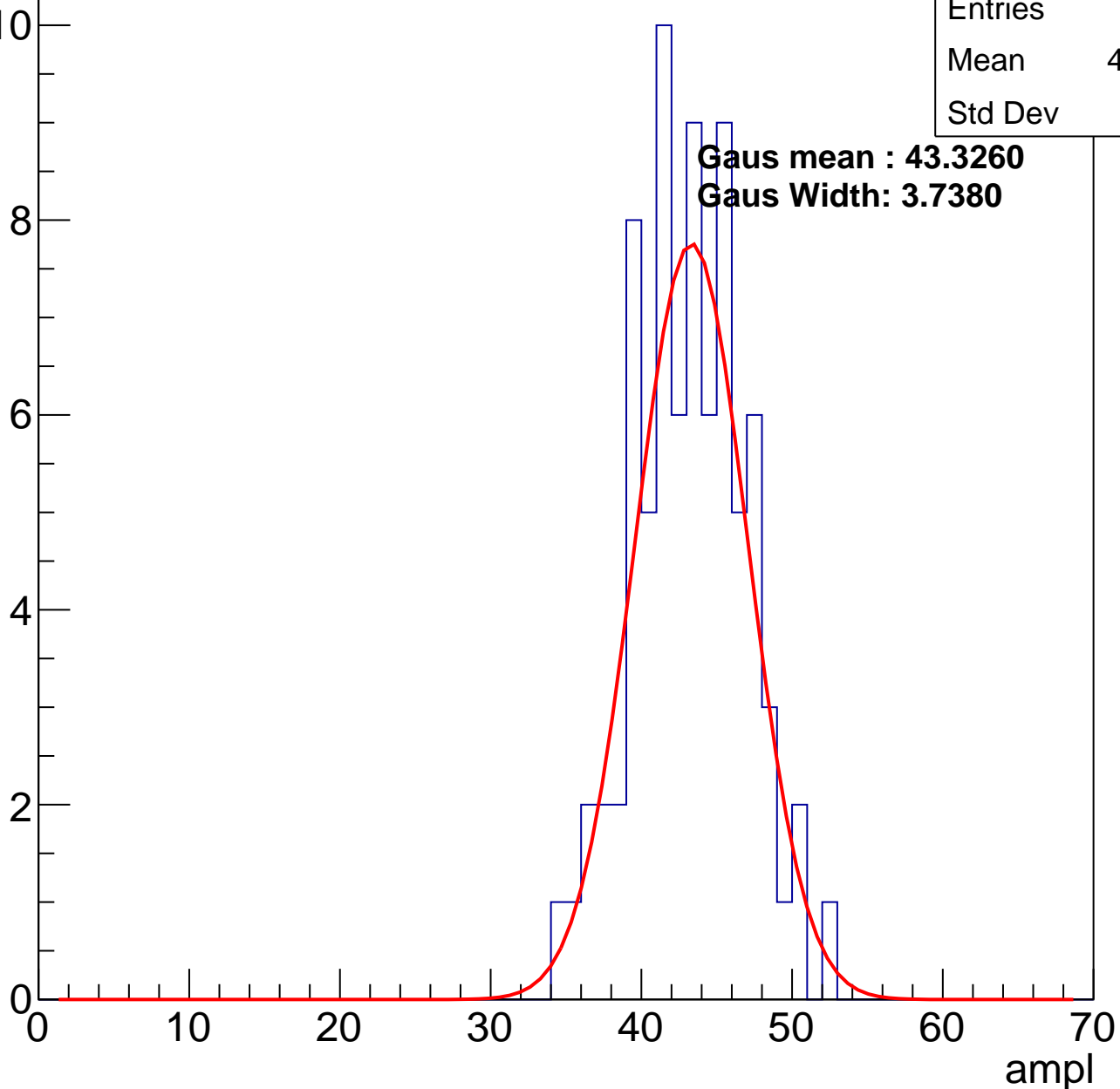
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	42.76
Std Dev	3.65

Gaus mean : 43.3260

Gaus Width: 3.7380

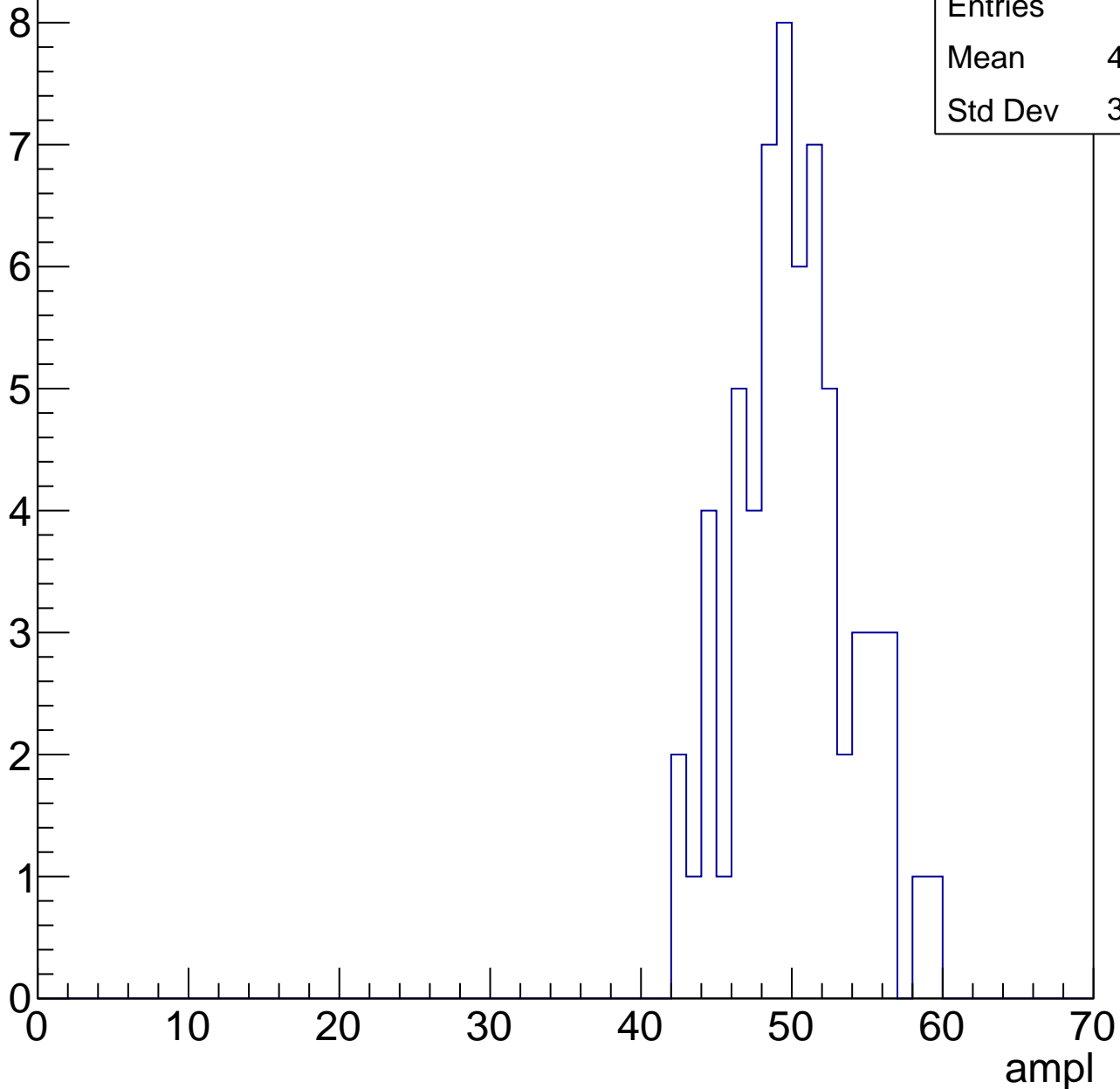


B1L101S, U17-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.67
Std Dev	3.805

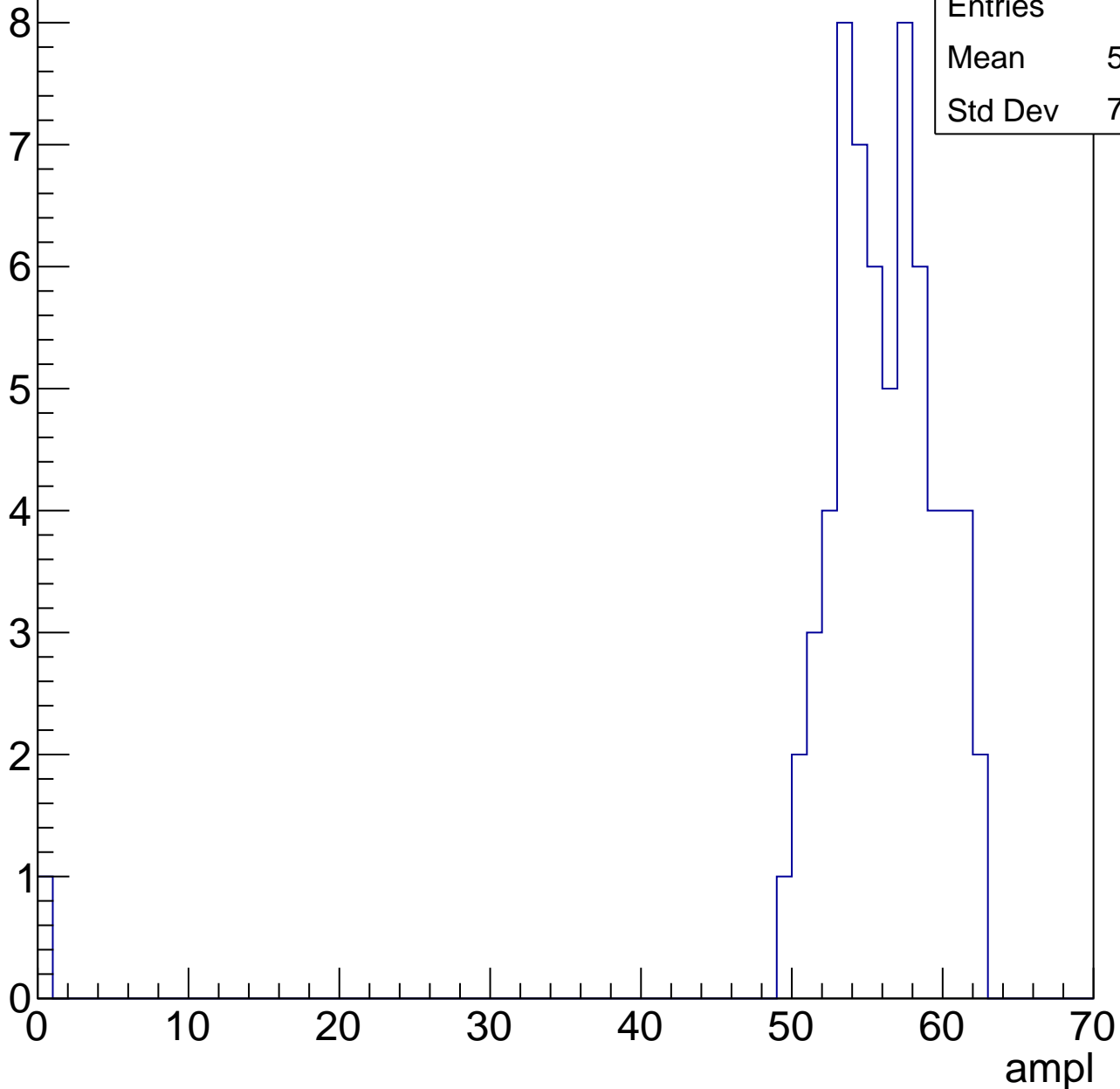


B1L101S, U17-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.92
Std Dev	7.576

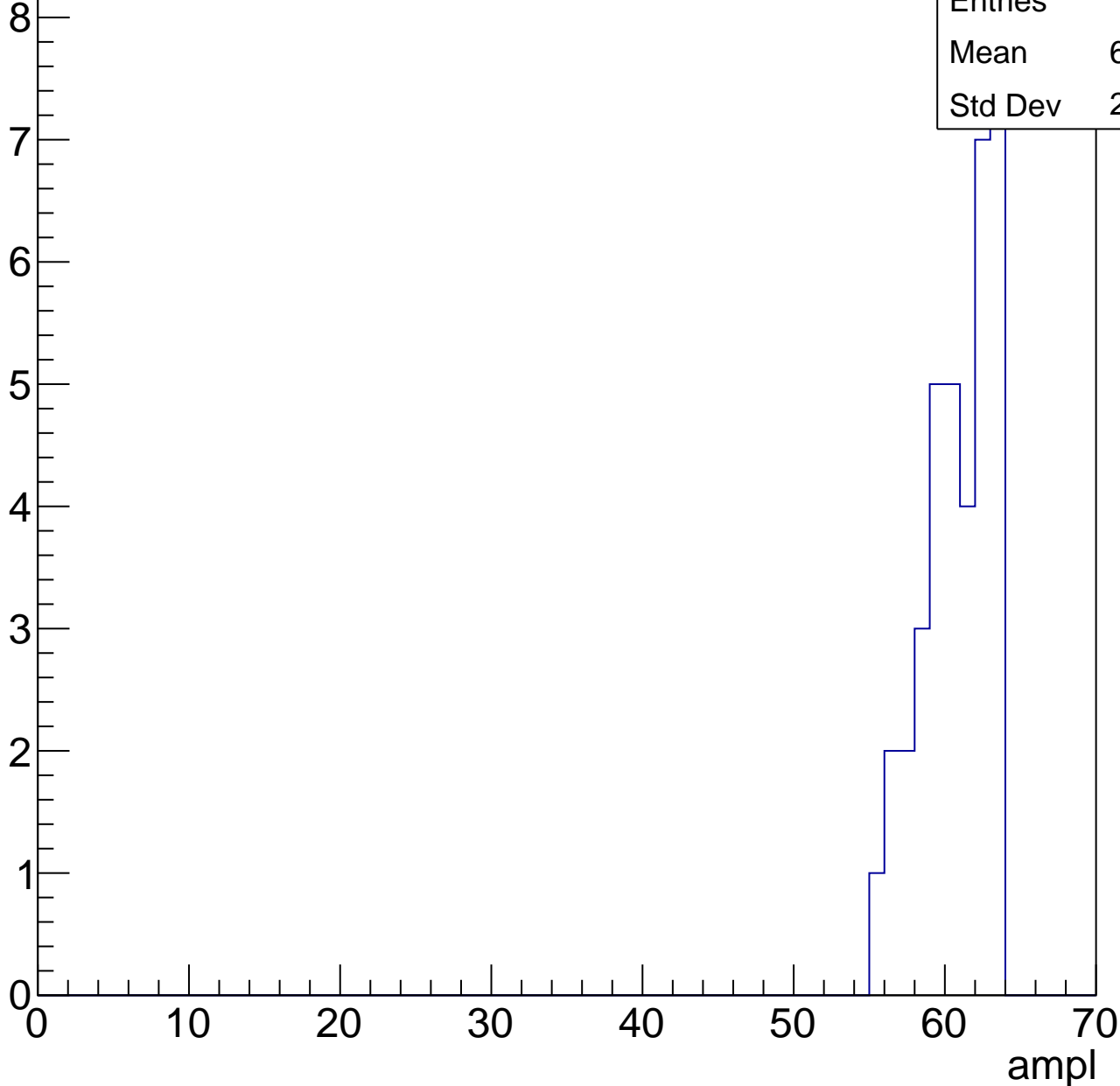


B1L101S, U17-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	60.32
Std Dev	2.279



B1L101S, U17-ch121, adc6

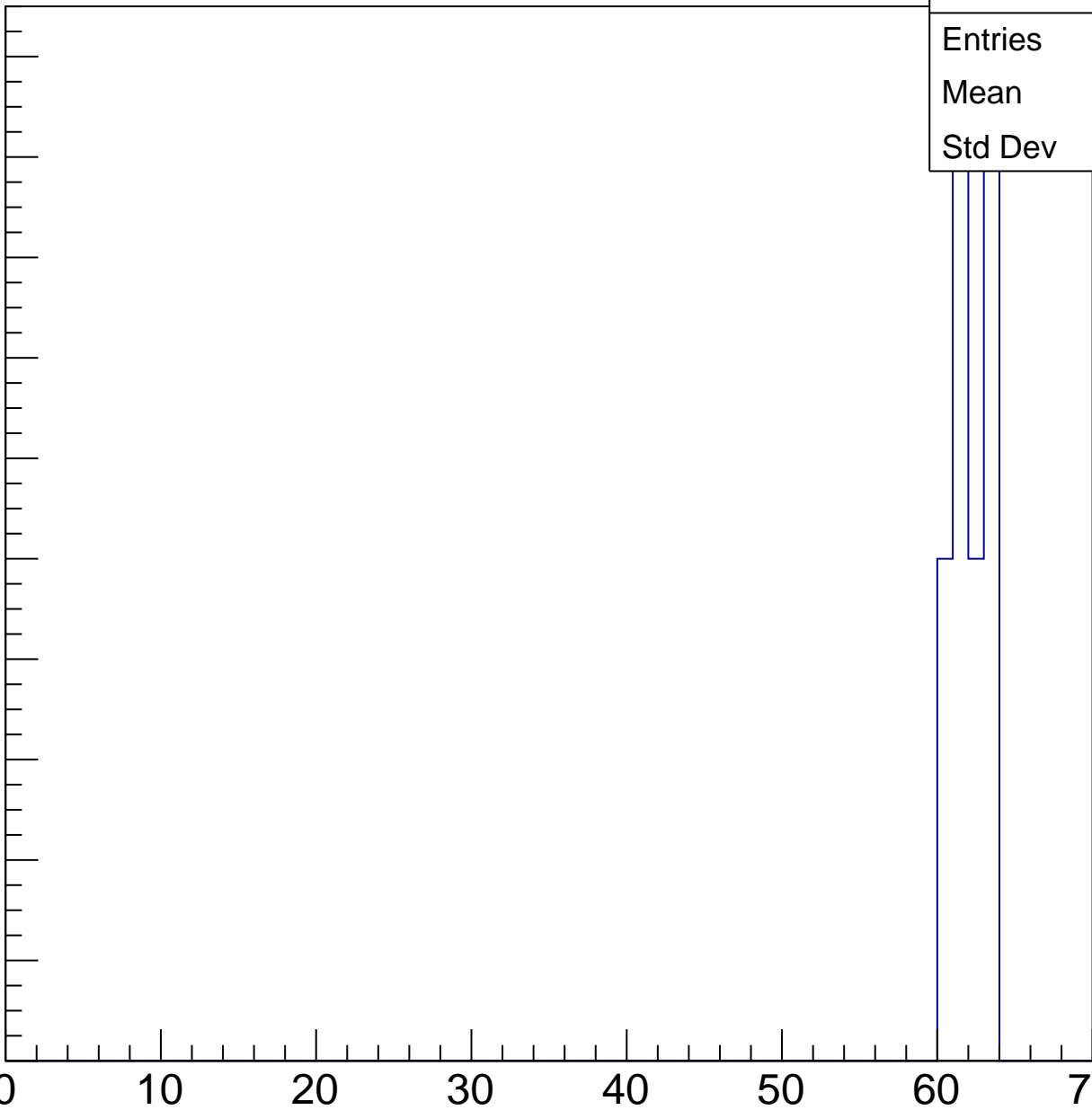
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl



B1L101S, U17-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch122, adc0

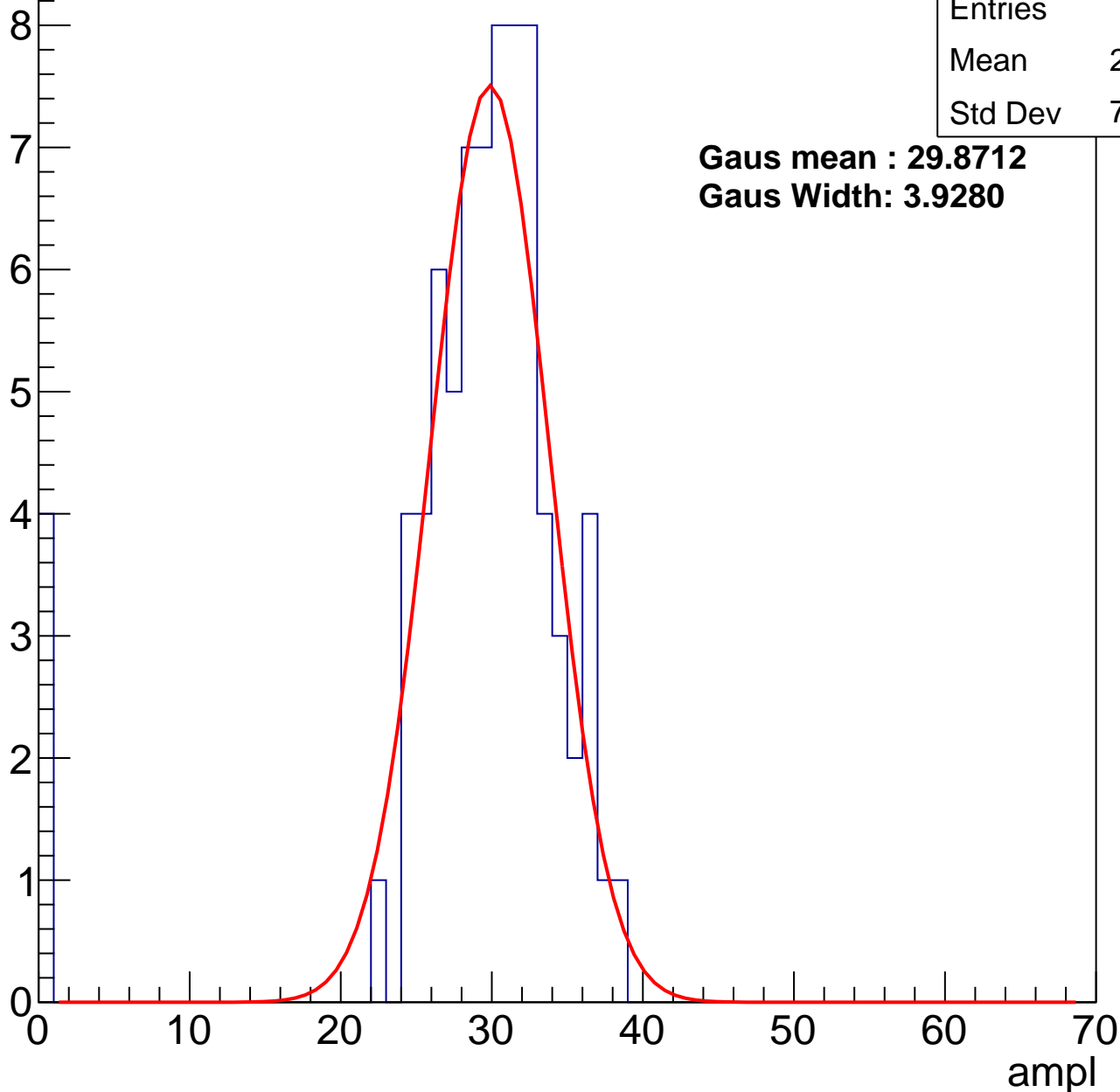
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.25
Std Dev	7.454

Gaus mean : 29.8712

Gaus Width: 3.9280



B1L101S, U17-ch122, adc1

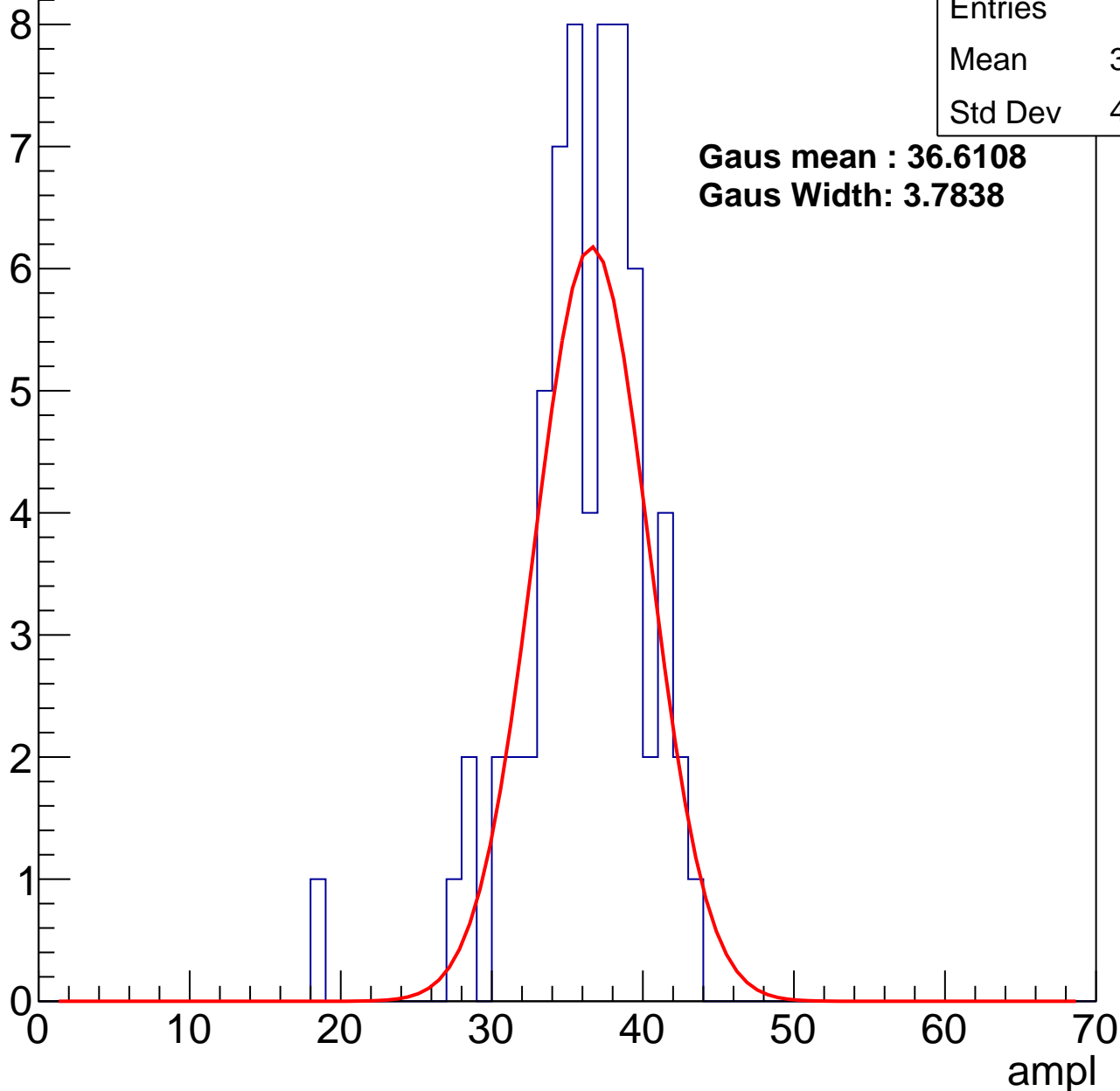
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	35.68
Std Dev	4.129

Gaus mean : 36.6108

Gaus Width: 3.7838



B1L101S, U17-ch122, adc2

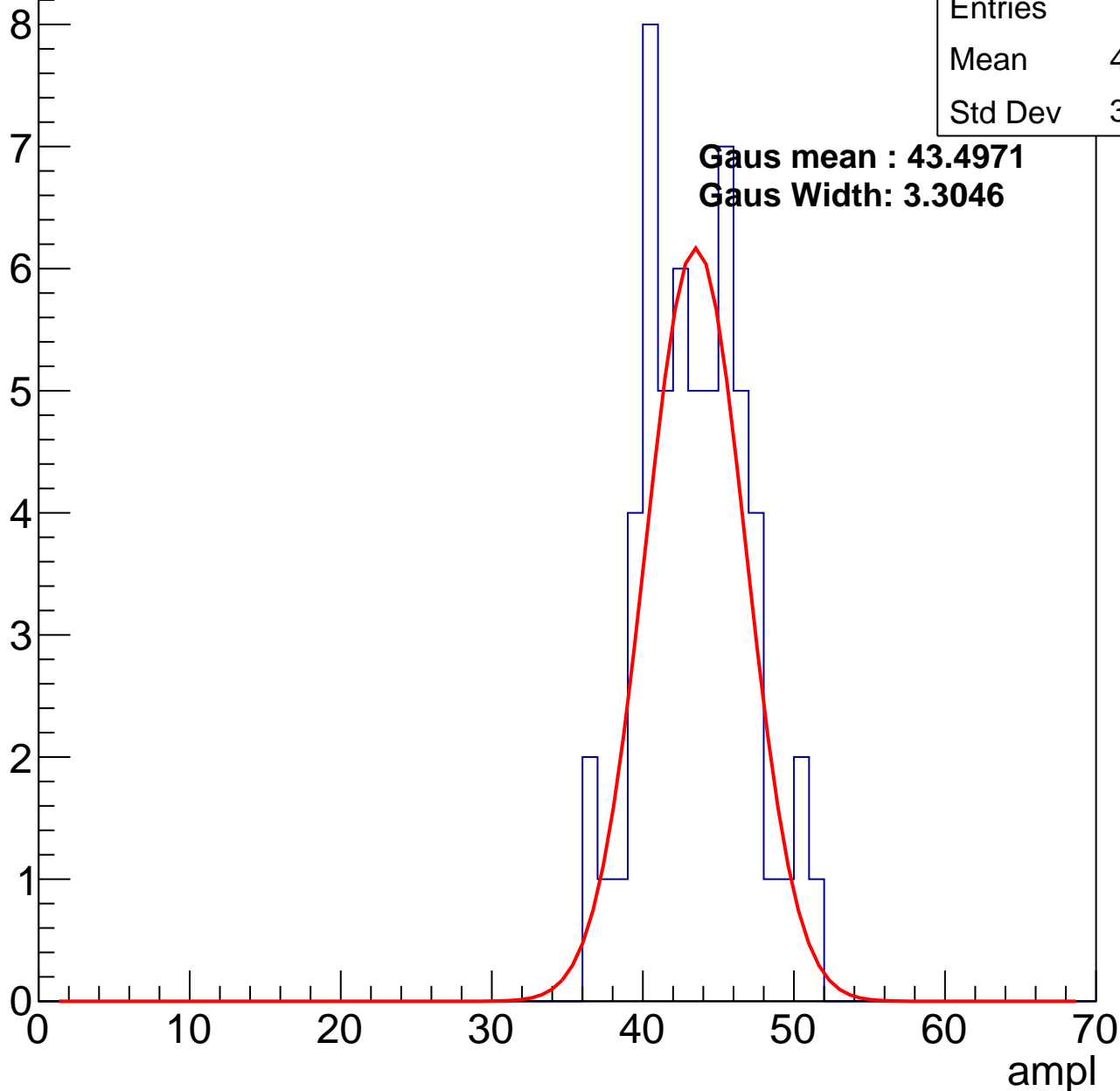
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.03
Std Dev	3.439

Gaus mean : 43.4971

Gaus Width: 3.3046

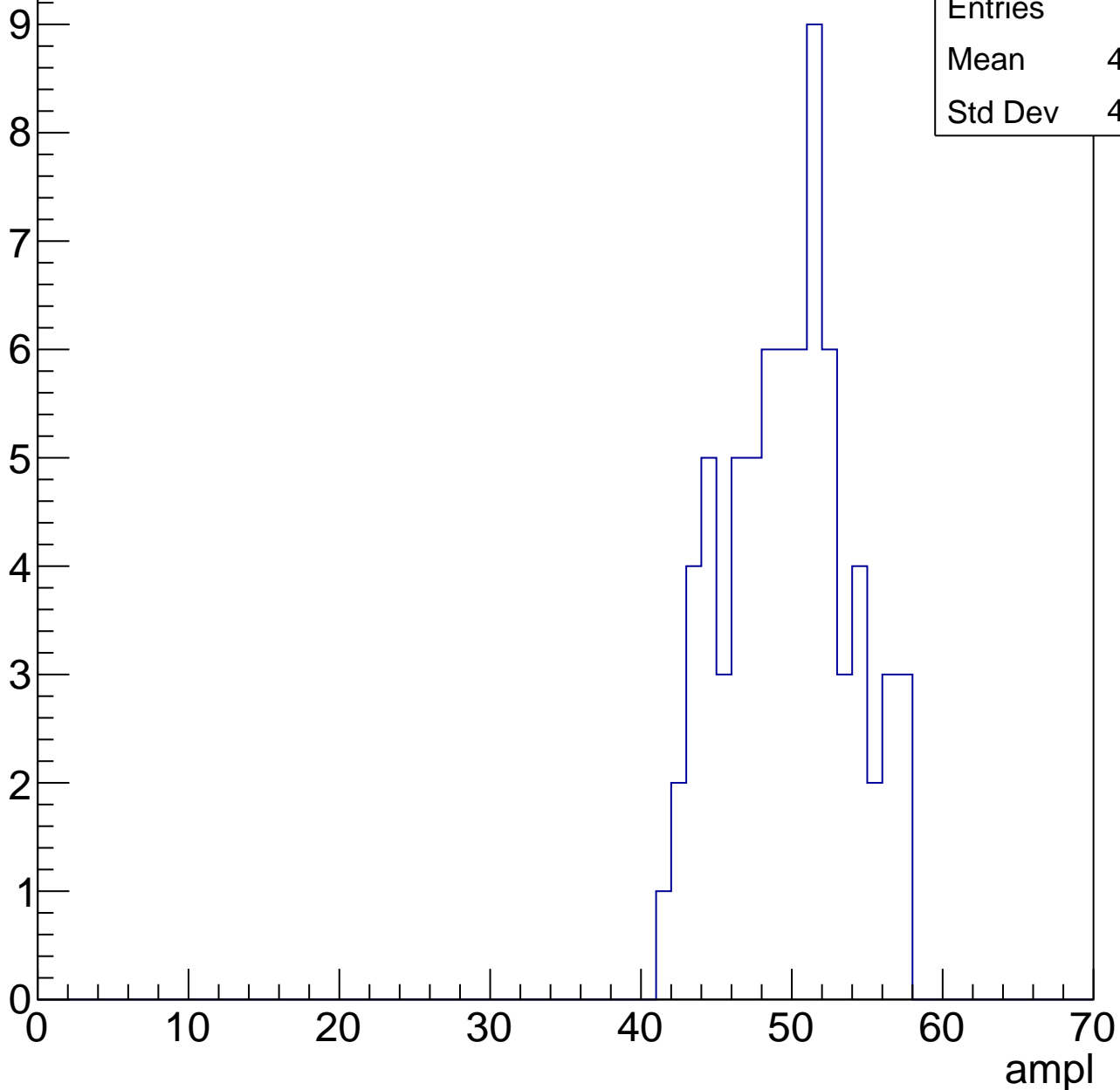


B1L101S, U17-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	49.23
Std Dev	4.056

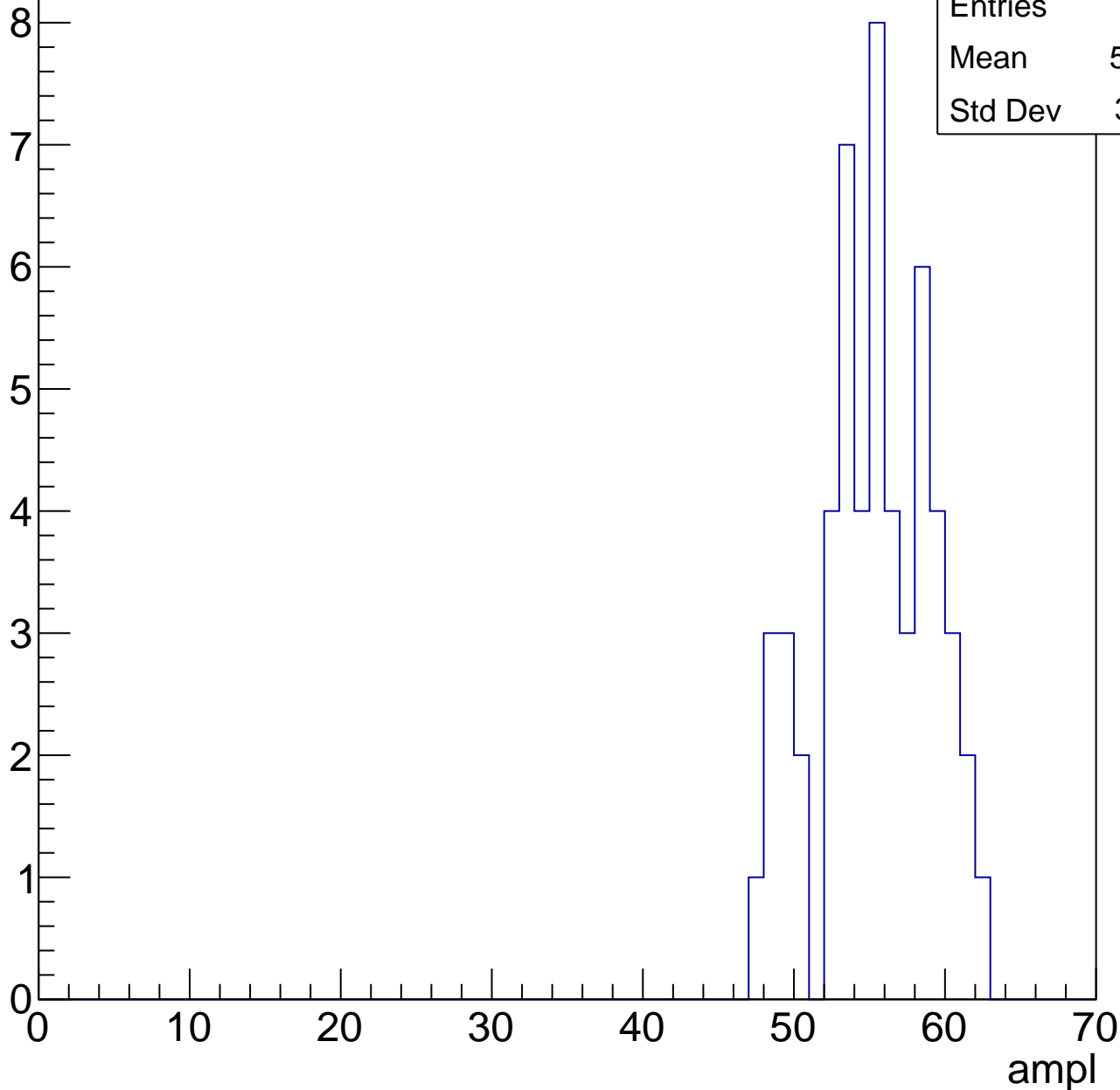


B1L101S, U17-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.84
Std Dev	3.731

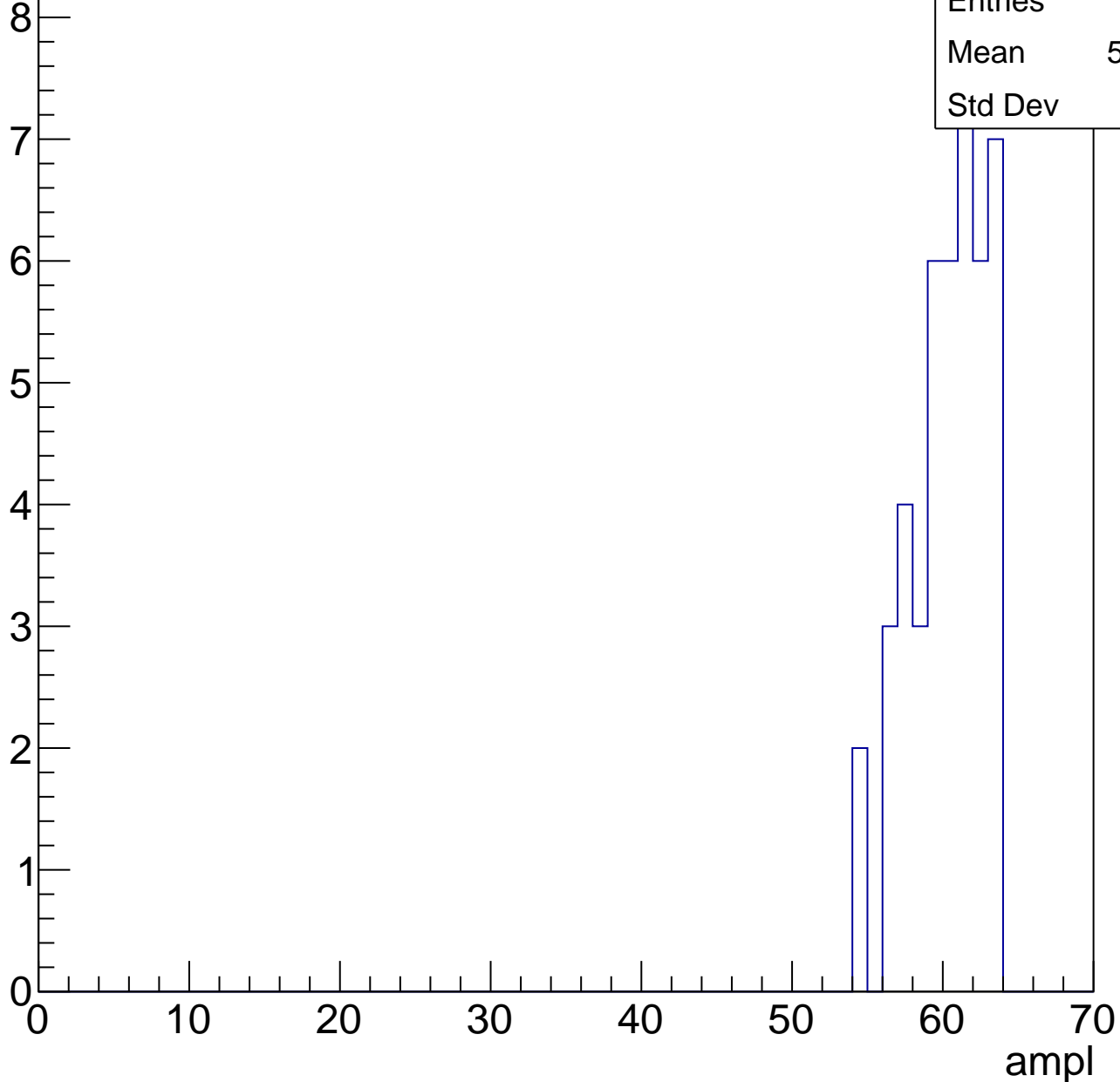


B1L101S, U17-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.84
Std Dev	2.44



B1L101S, U17-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

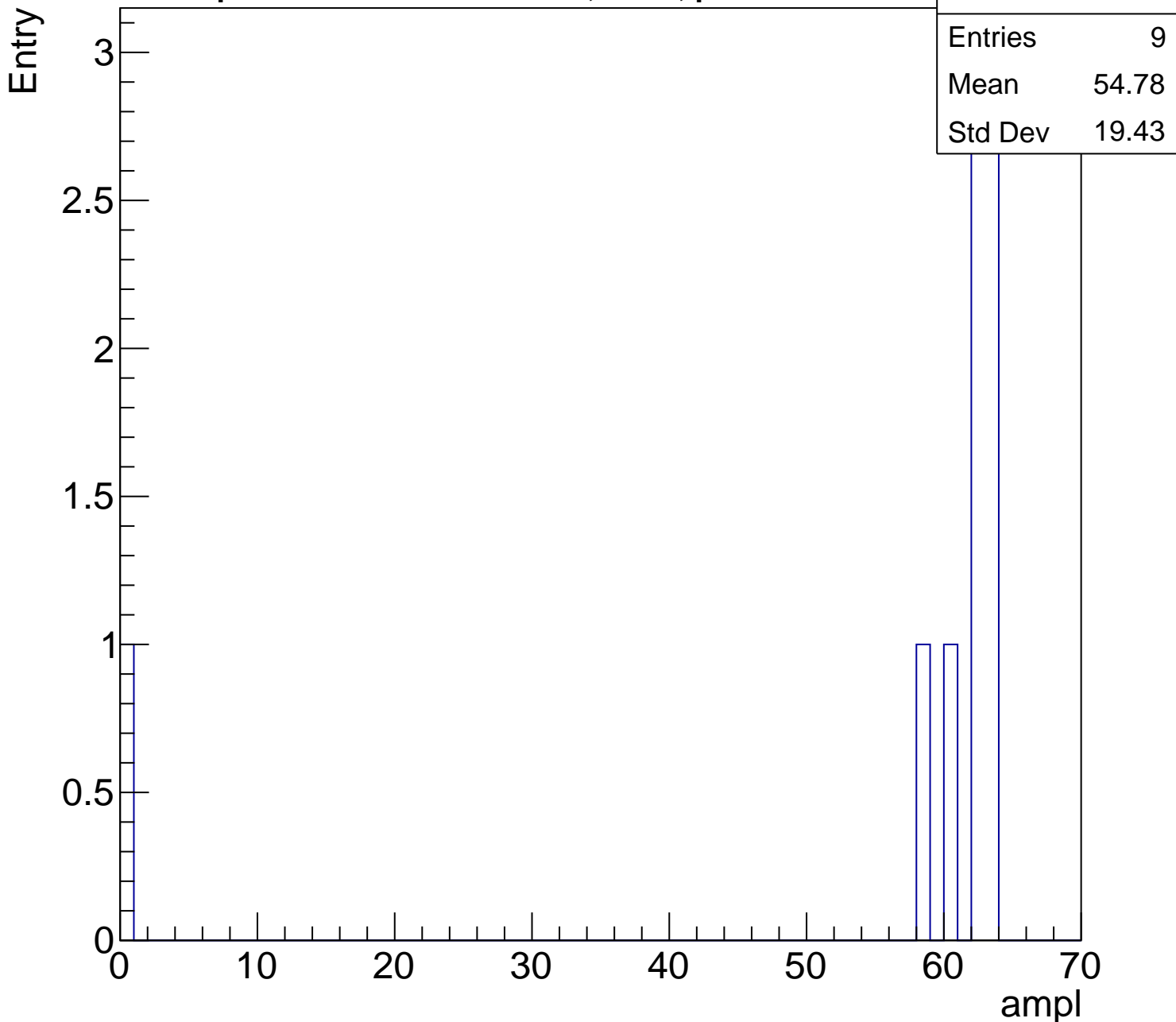
Mean

54.78

Std Dev

19.43

ampl



B1L101S, U17-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U17-ch123, adc0

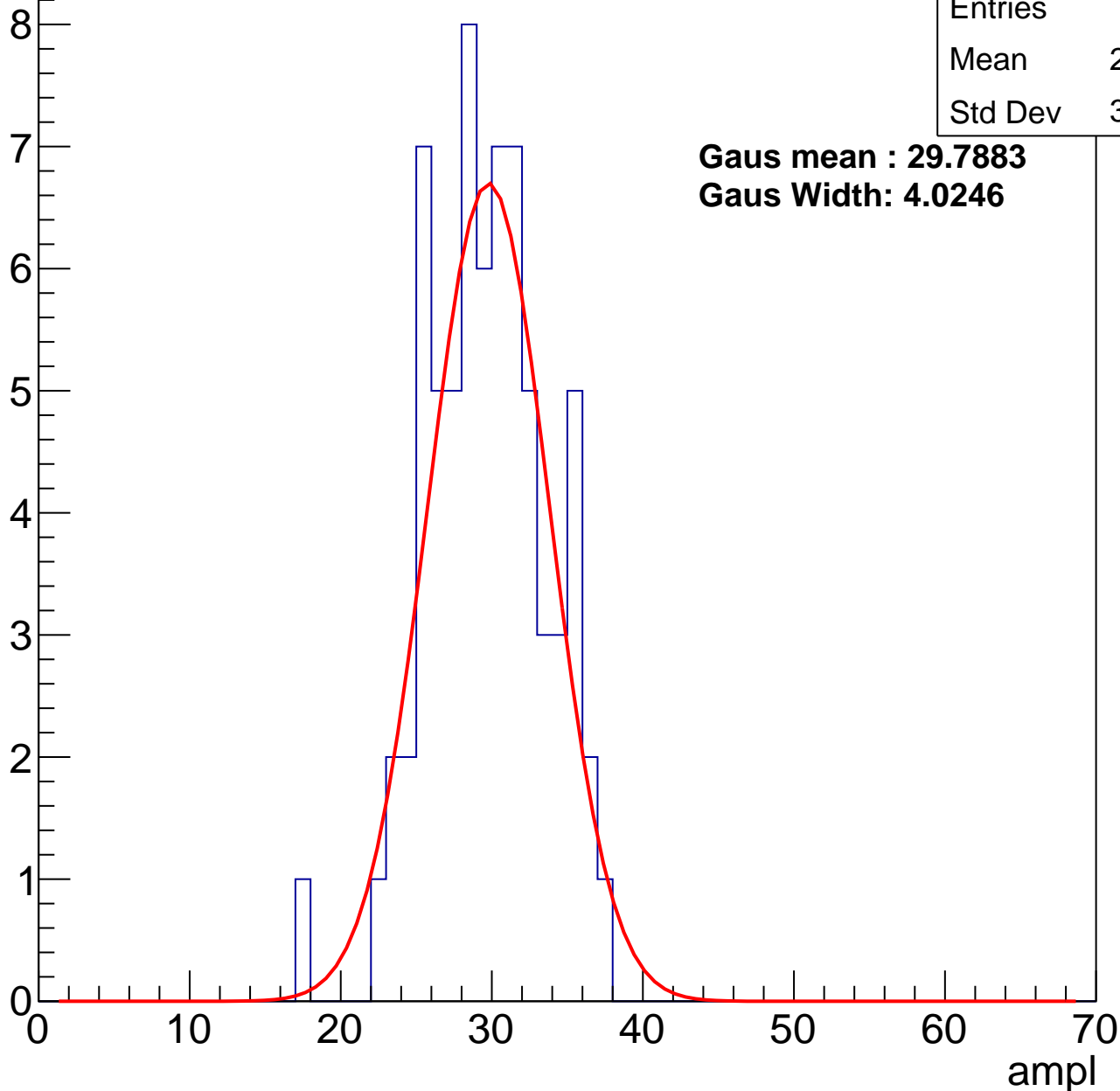
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.19
Std Dev	3.859

Gaus mean : 29.7883

Gaus Width: 4.0246



B1L101S, U17-ch123, adc1

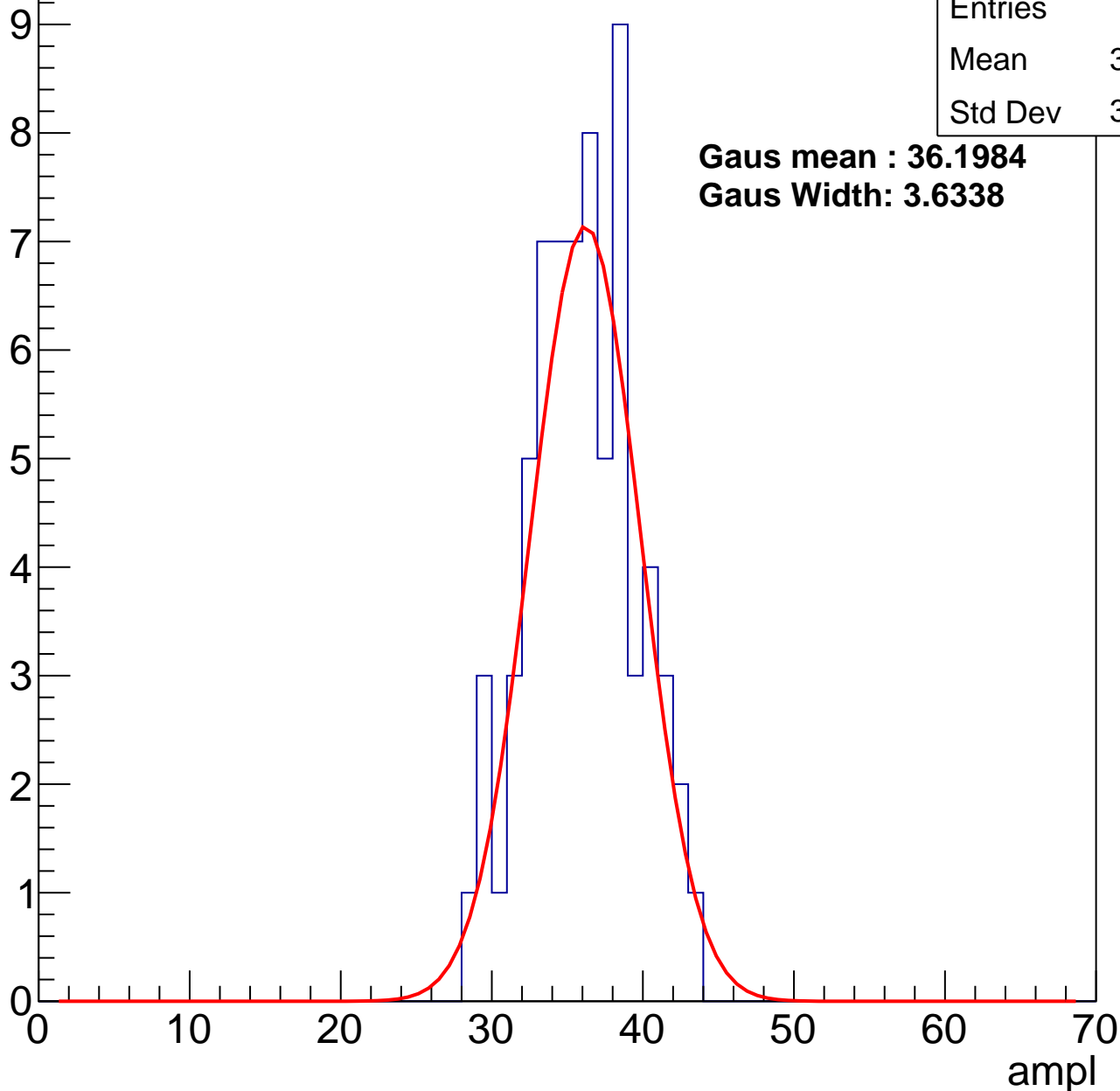
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.57
Std Dev	3.437

Gaus mean : 36.1984

Gaus Width: 3.6338



B1L101S, U17-ch123, adc2

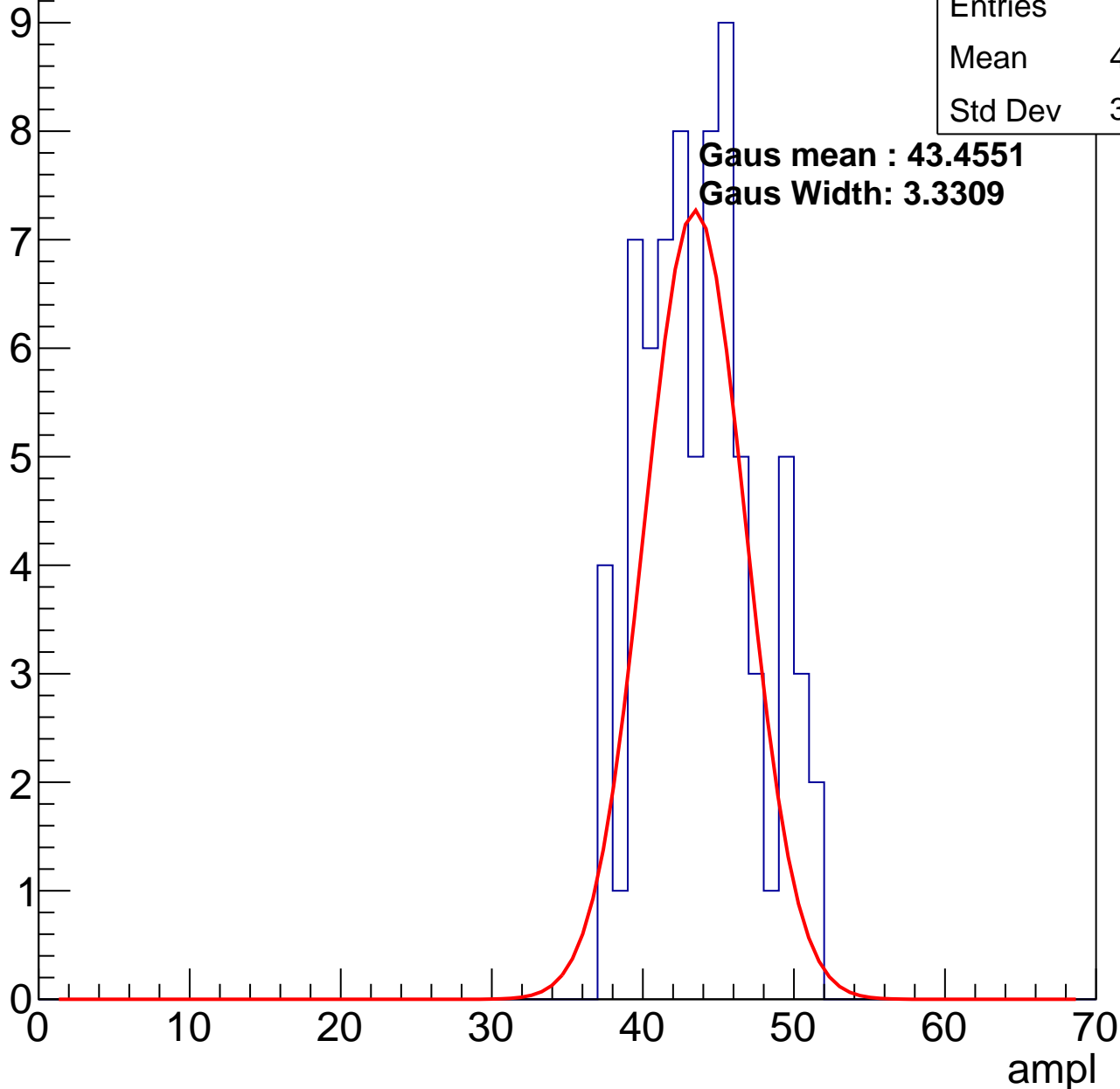
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.38
Std Dev	3.634

Gaus mean : 43.4551

Gaus Width: 3.3309

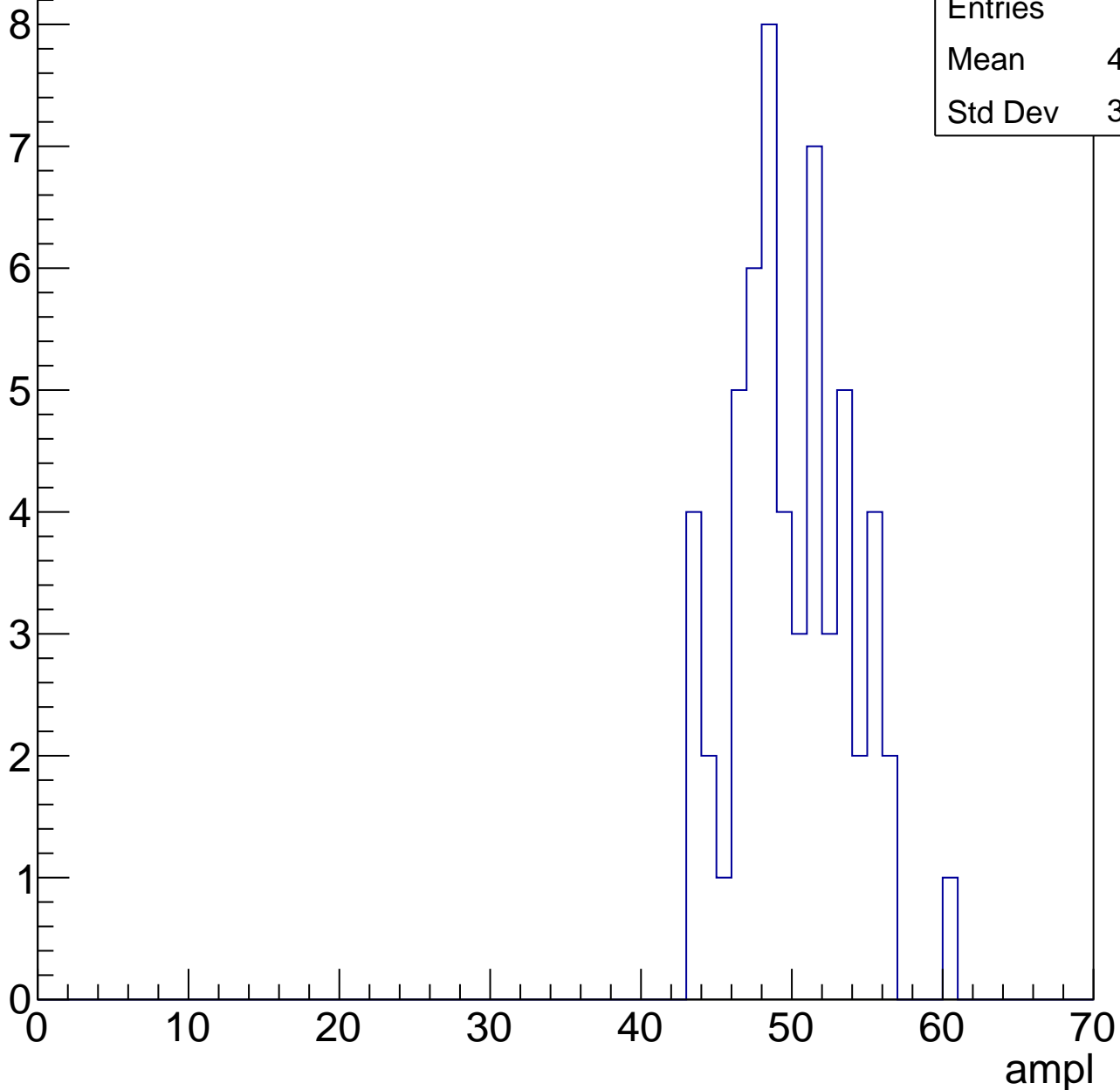


B1L101S, U17-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	49.56
Std Dev	3.793

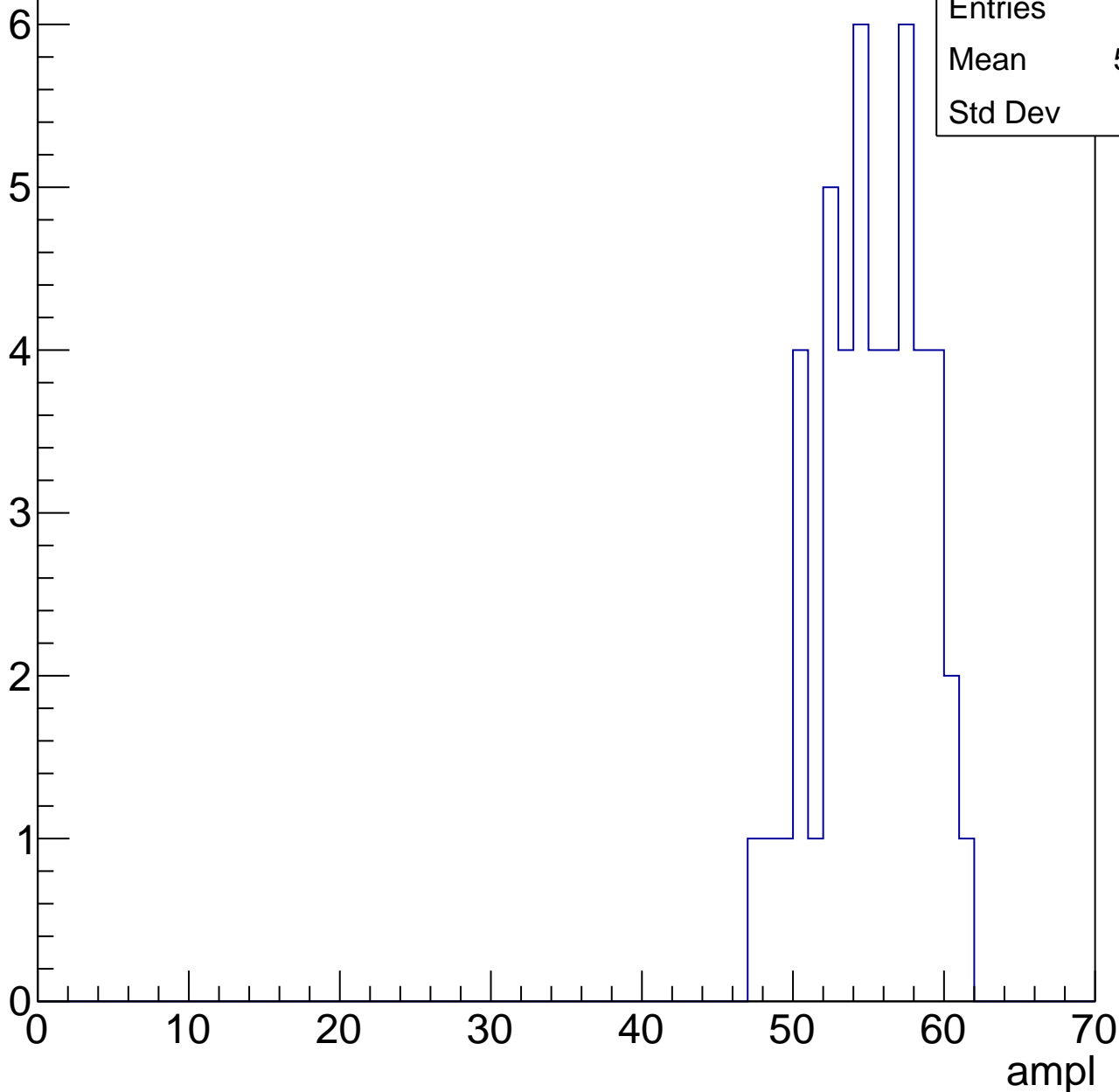


B1L101S, U17-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	54.71
Std Dev	3.36

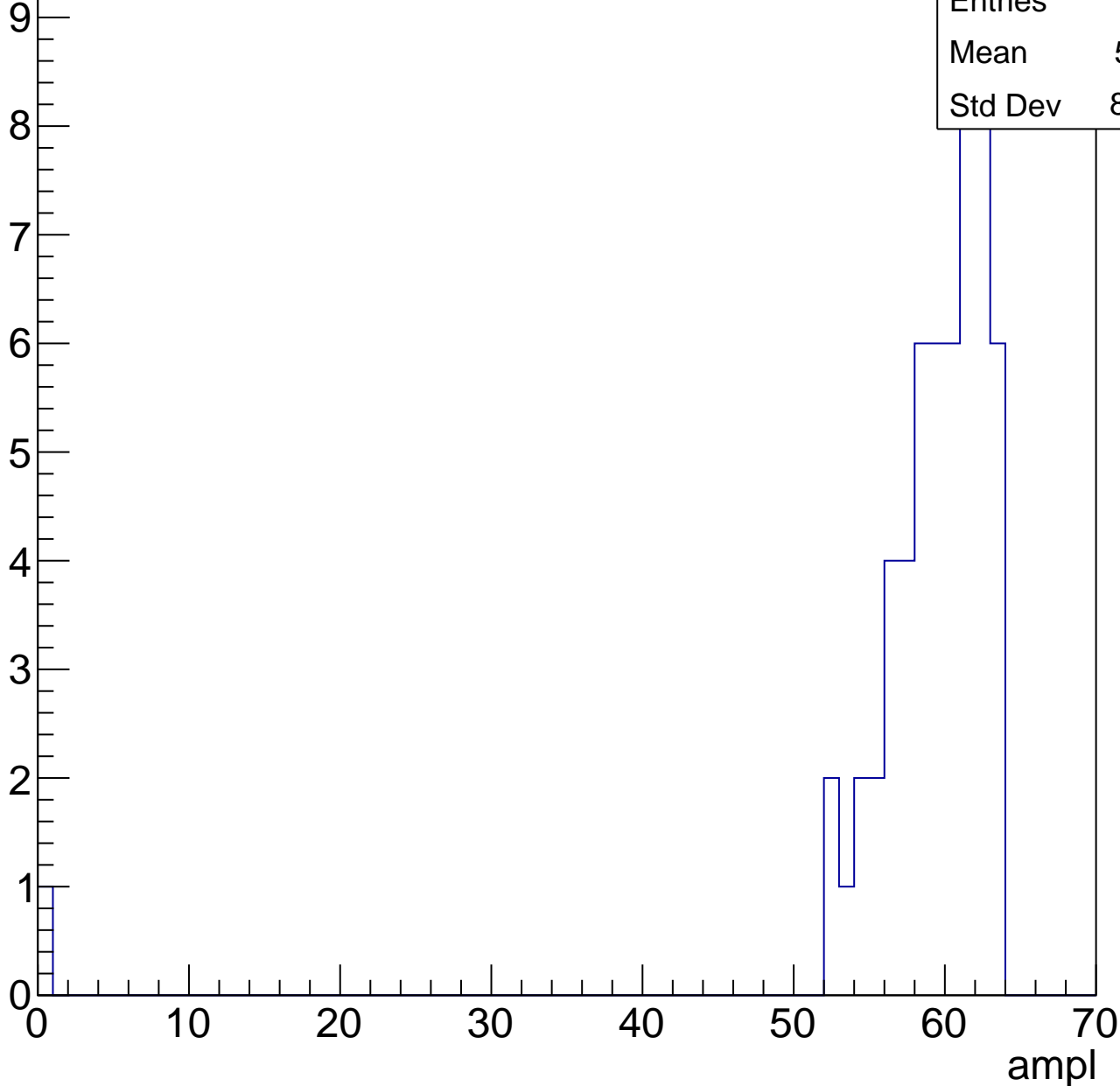


B1L101S, U17-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

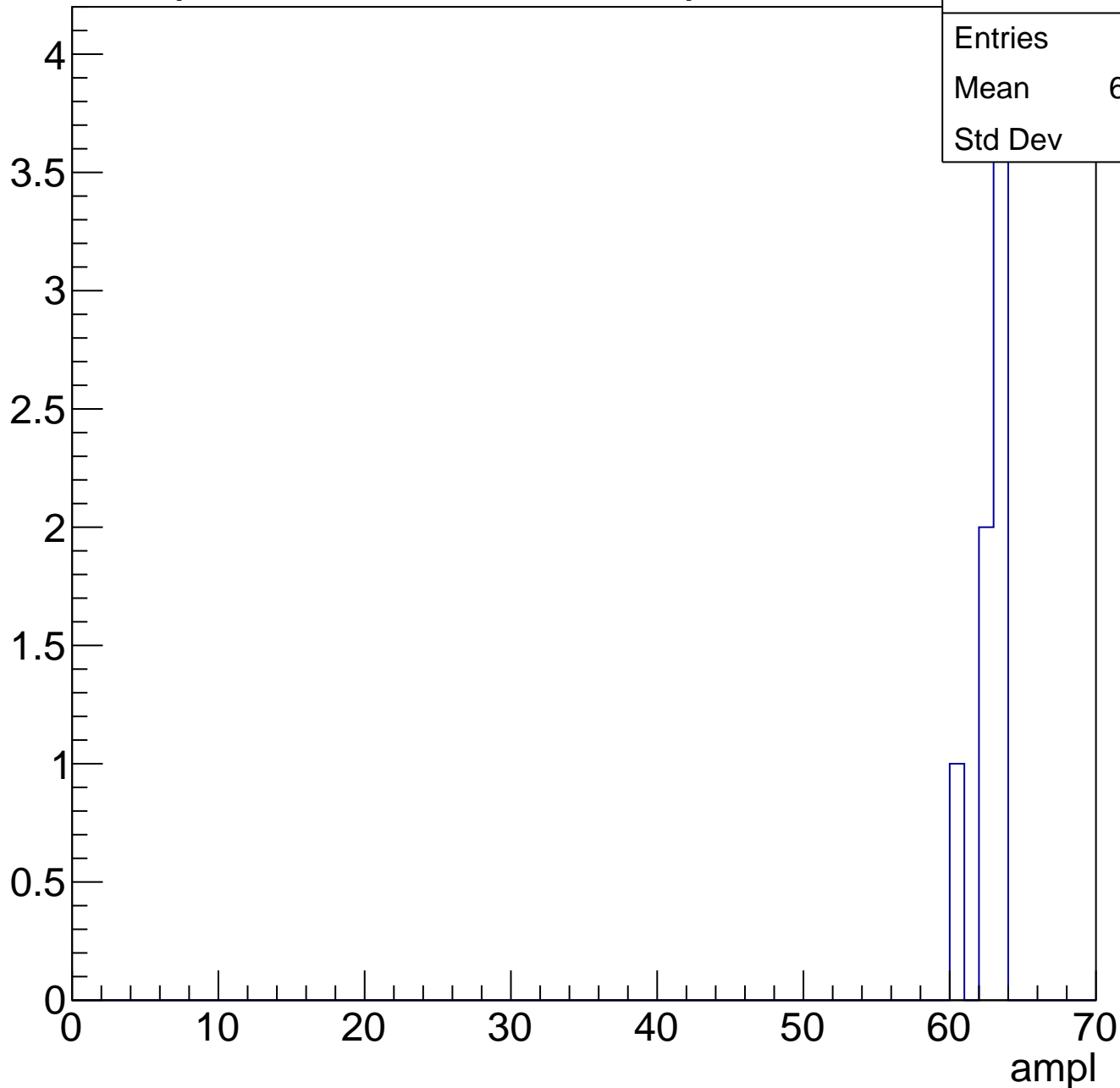
Entries	57
Mean	58.11
Std Dev	8.292



B1L101S, U17-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

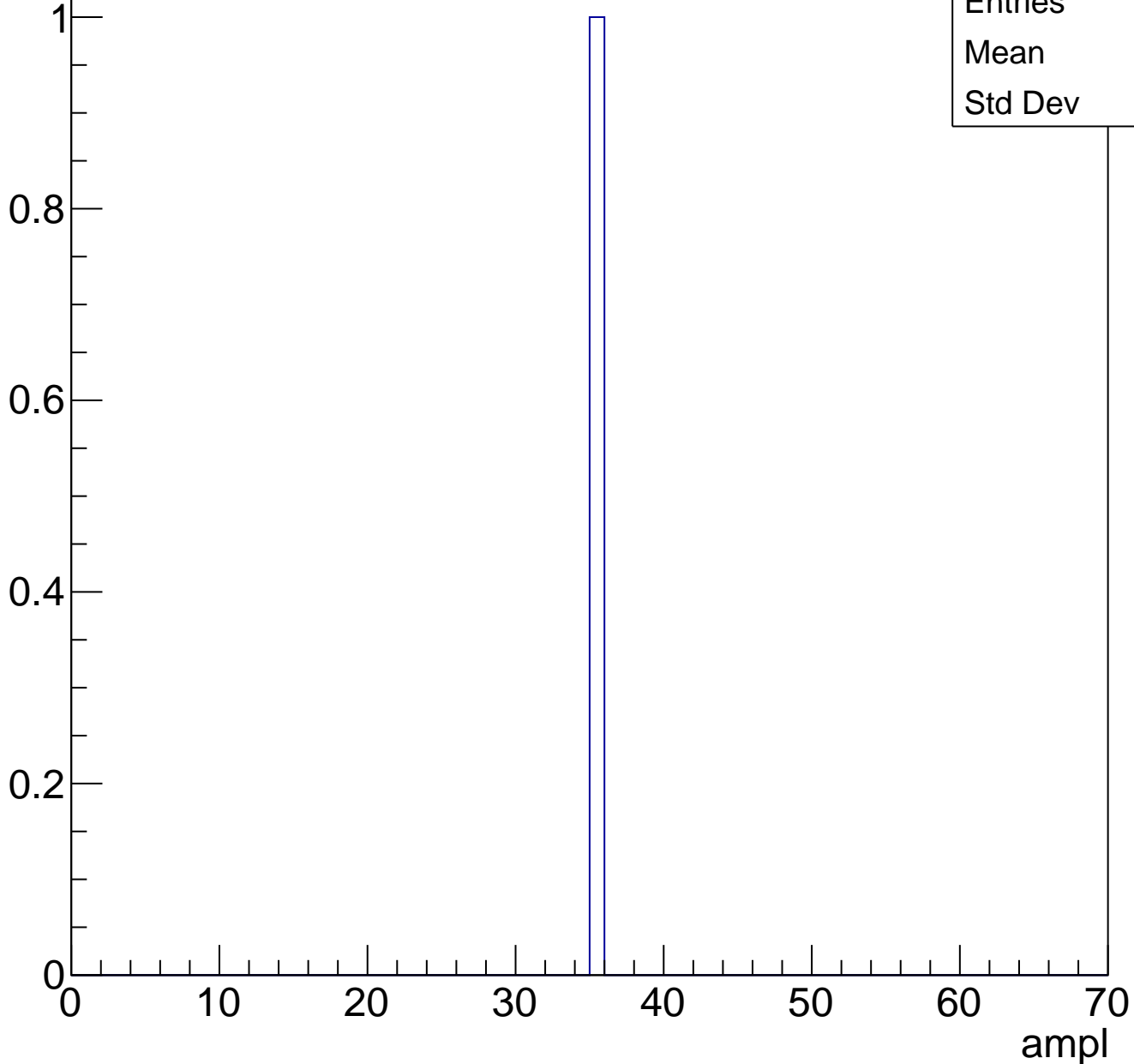
Entry



B1L101S, U17-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch124, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	29.75
Std Dev	5.05

Gaus mean : 30.9493

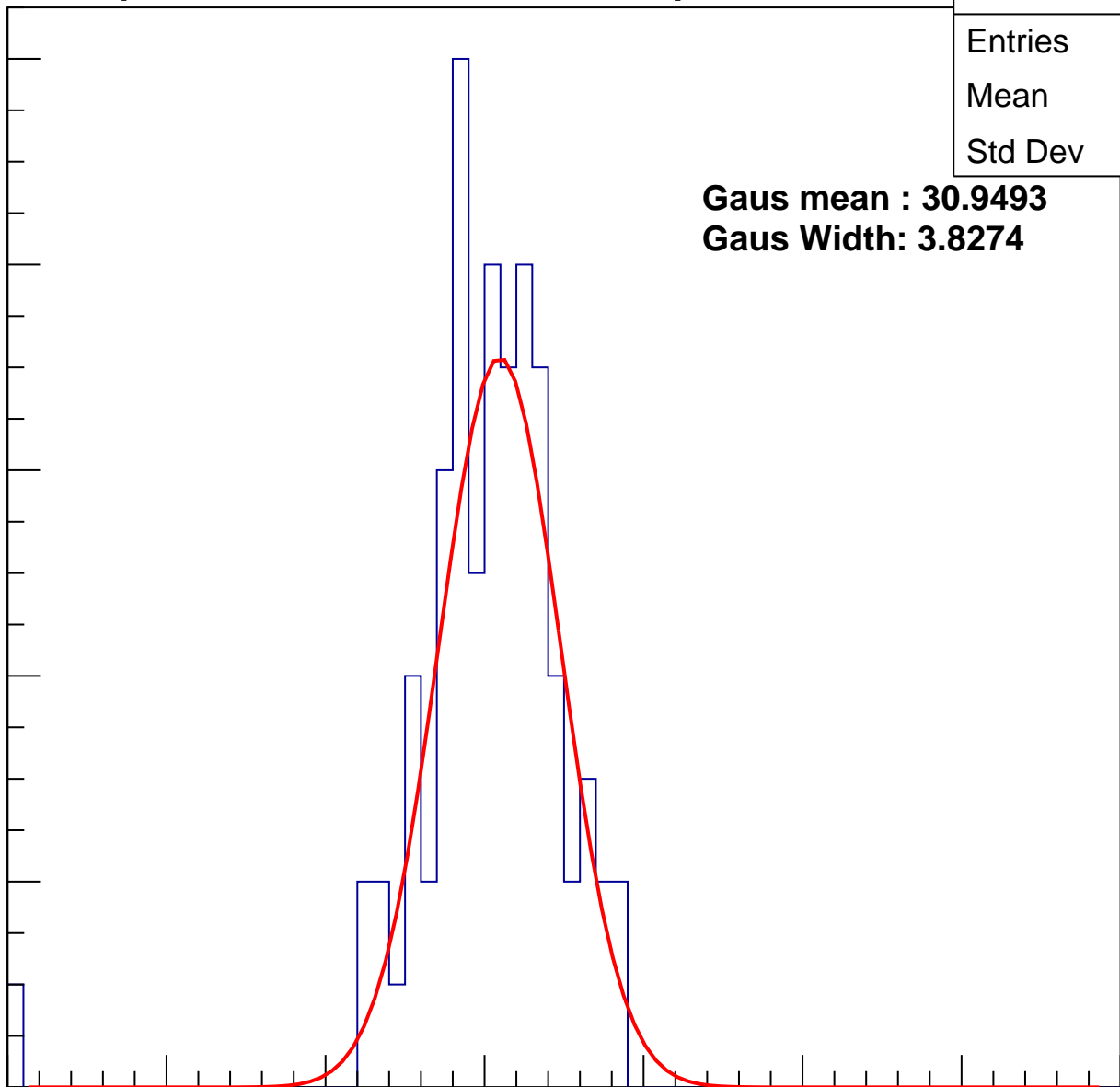
Gaus Width: 3.8274

Entry

10
8
6
4
2
0

ampl

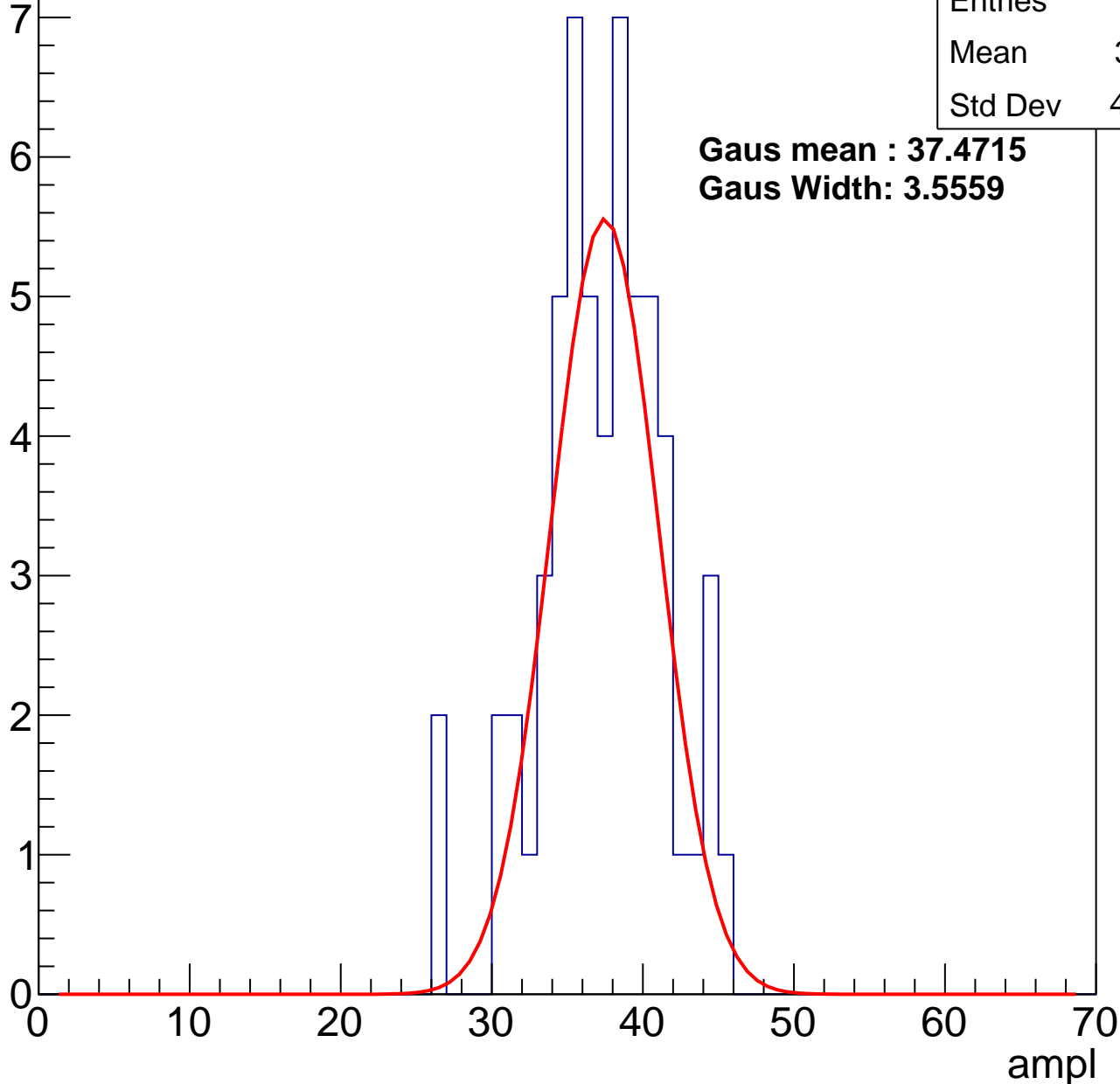
0 10 20 30 40 50 60 70



B1L101S, U17-ch124, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch124, adc2

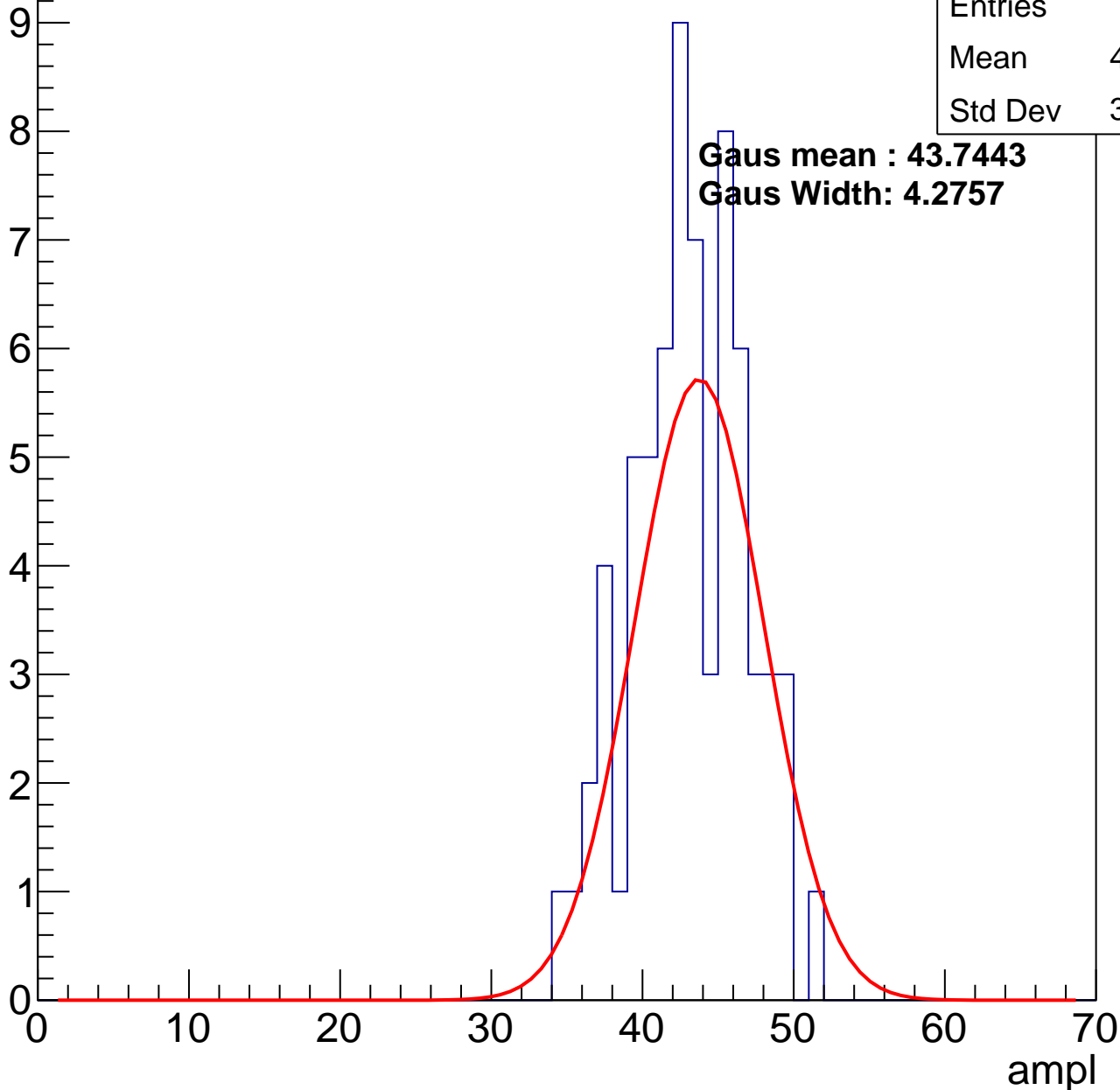
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.62
Std Dev	3.726

Gaus mean : 43.7443

Gaus Width: 4.2757

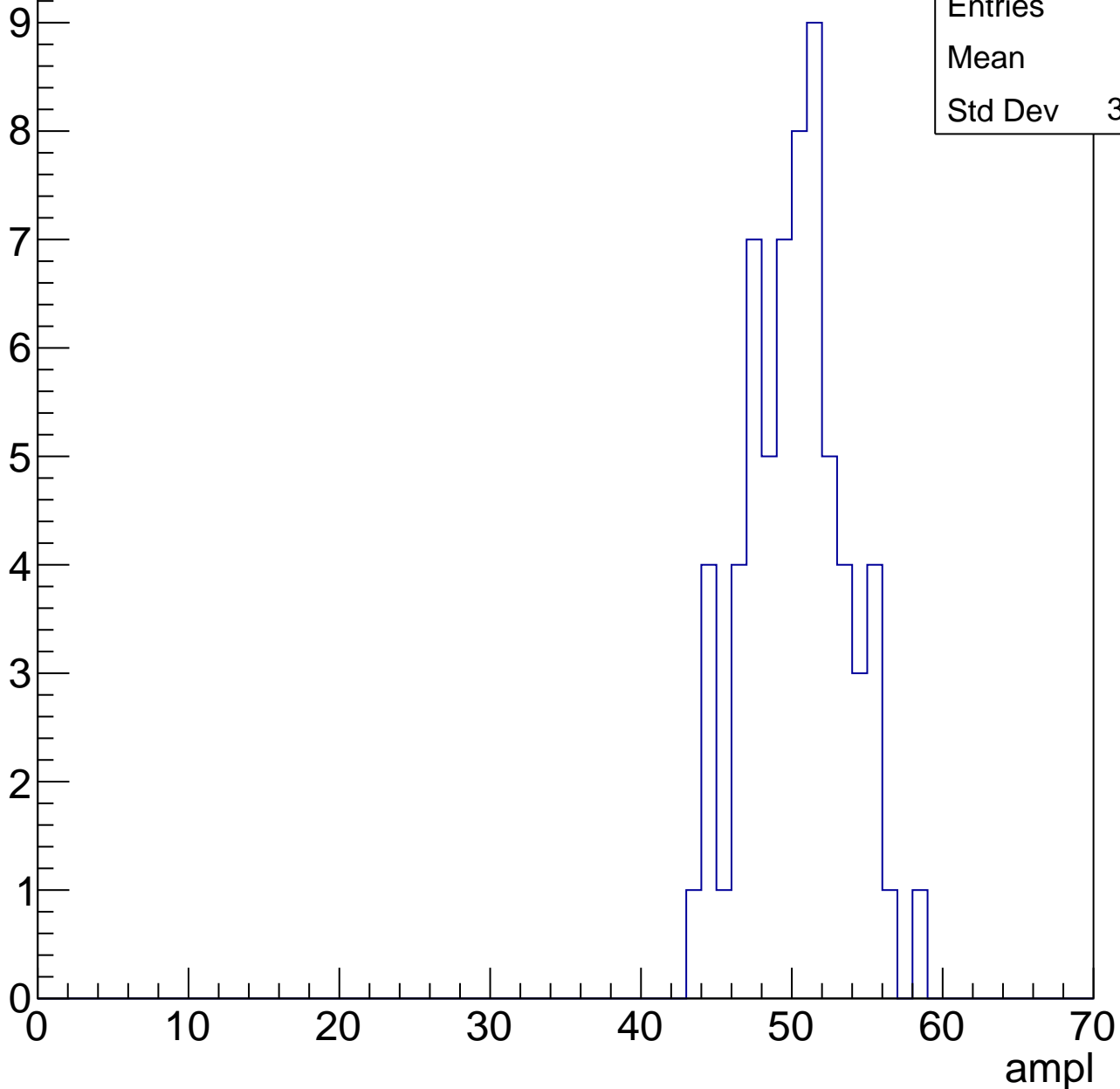


B1L101S, U17-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.8
Std Dev	3.265



B1L101S, U17-ch124, adc4

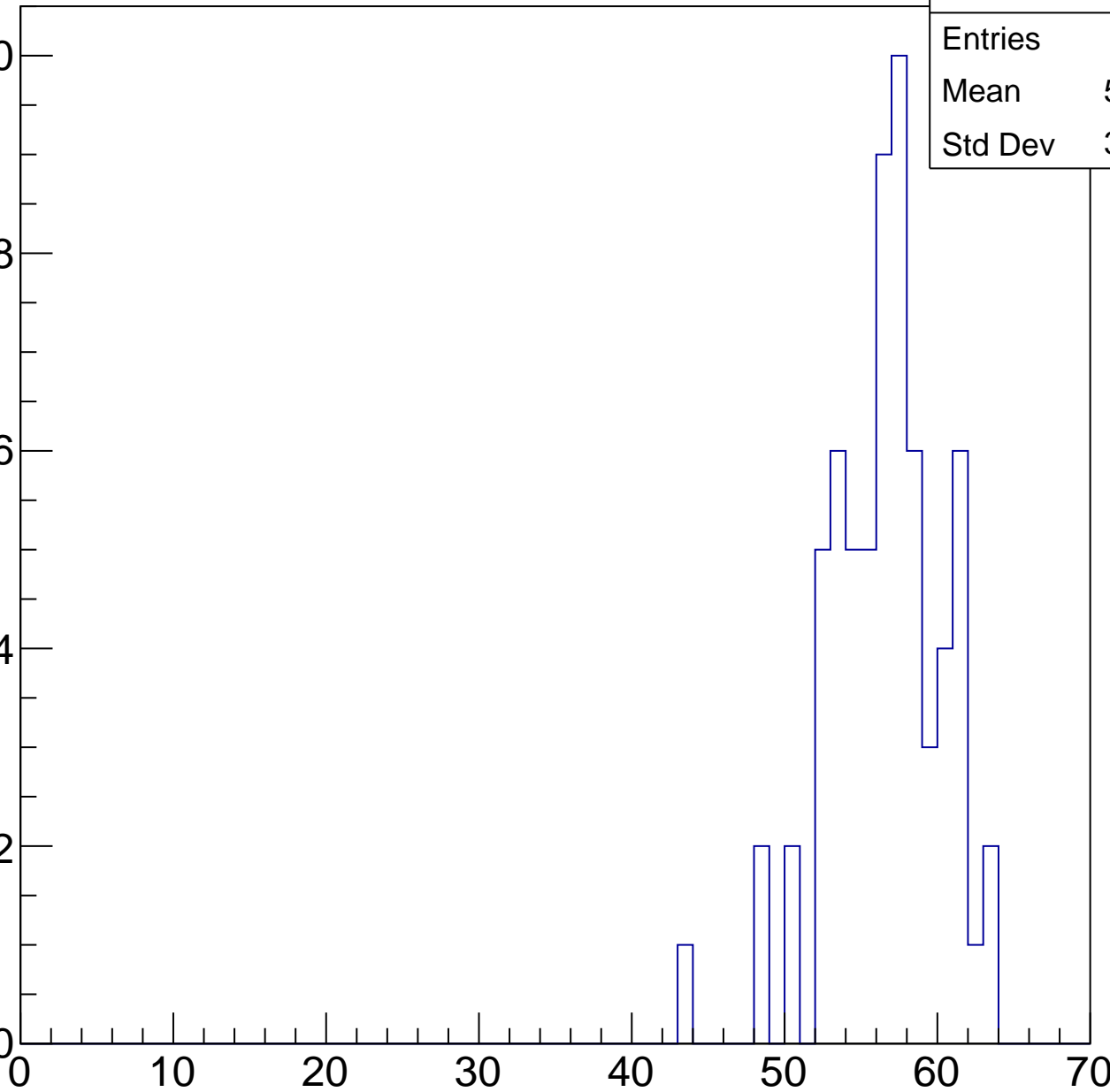
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	56.04
Std Dev	3.739

Entry

10
8
6
4
2
0

ampl

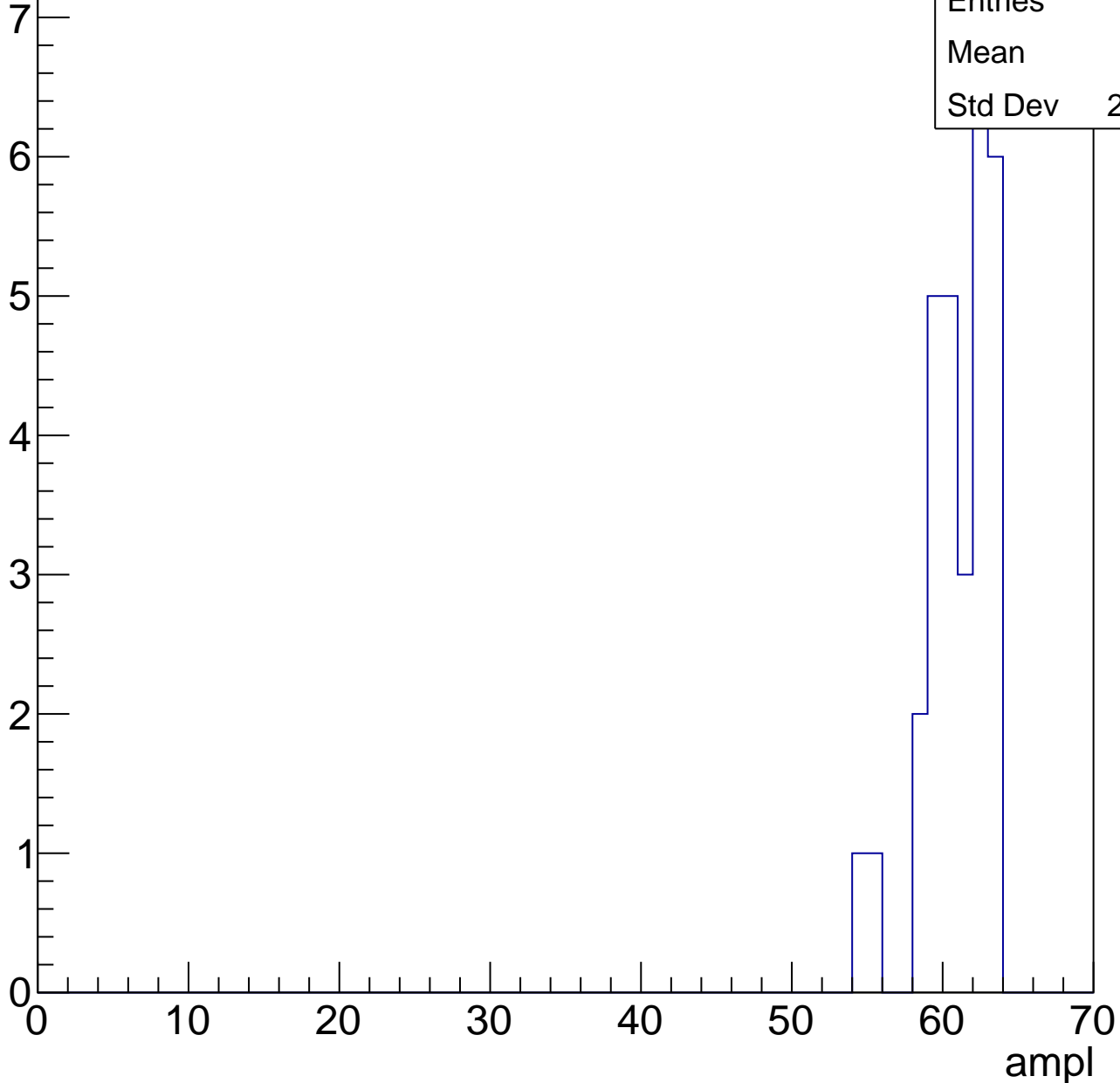


B1L101S, U17-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	60.5
Std Dev	2.247



B1L101S, U17-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

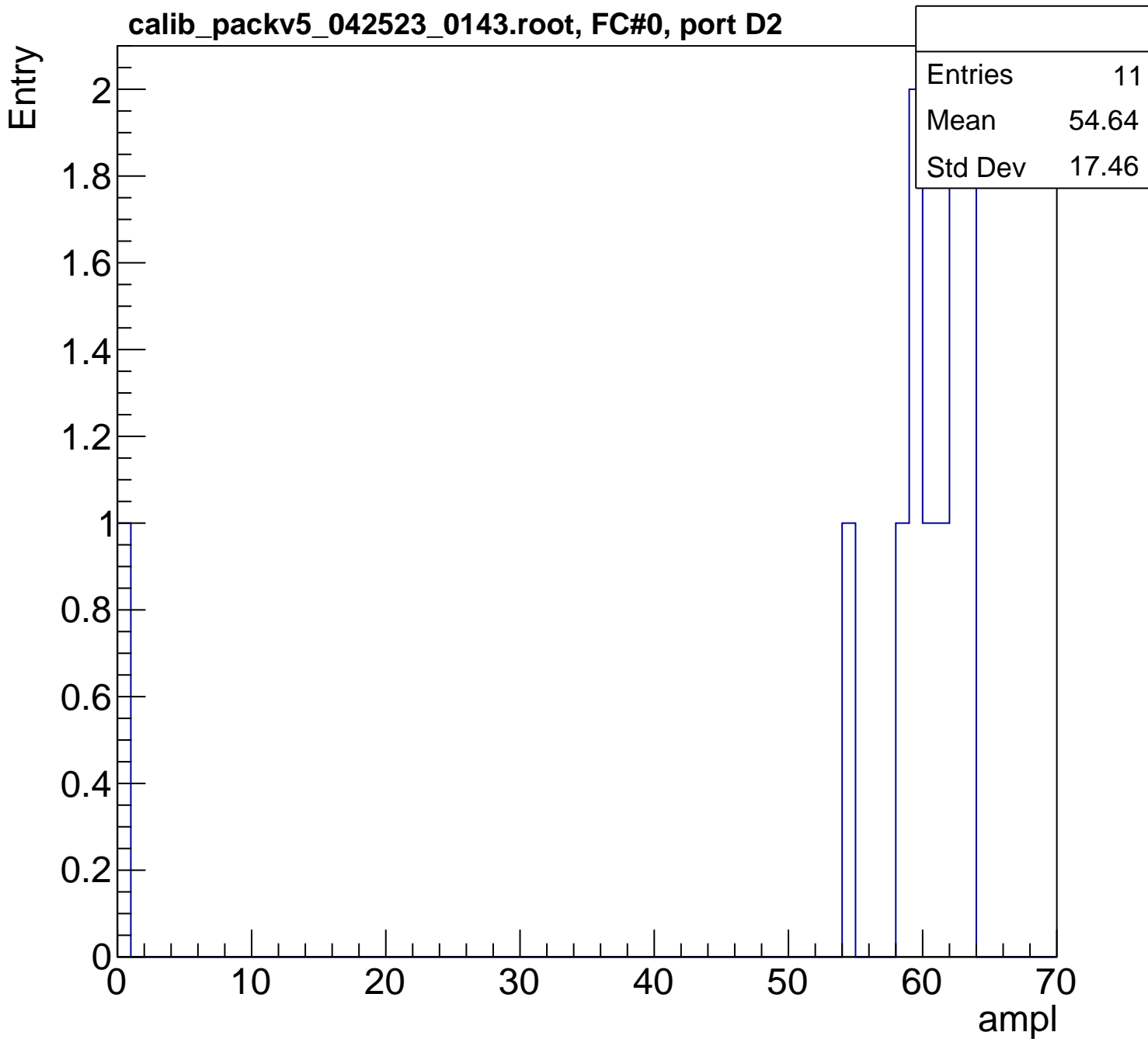
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	11
Mean	54.64
Std Dev	17.46

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U17-ch125, adc0

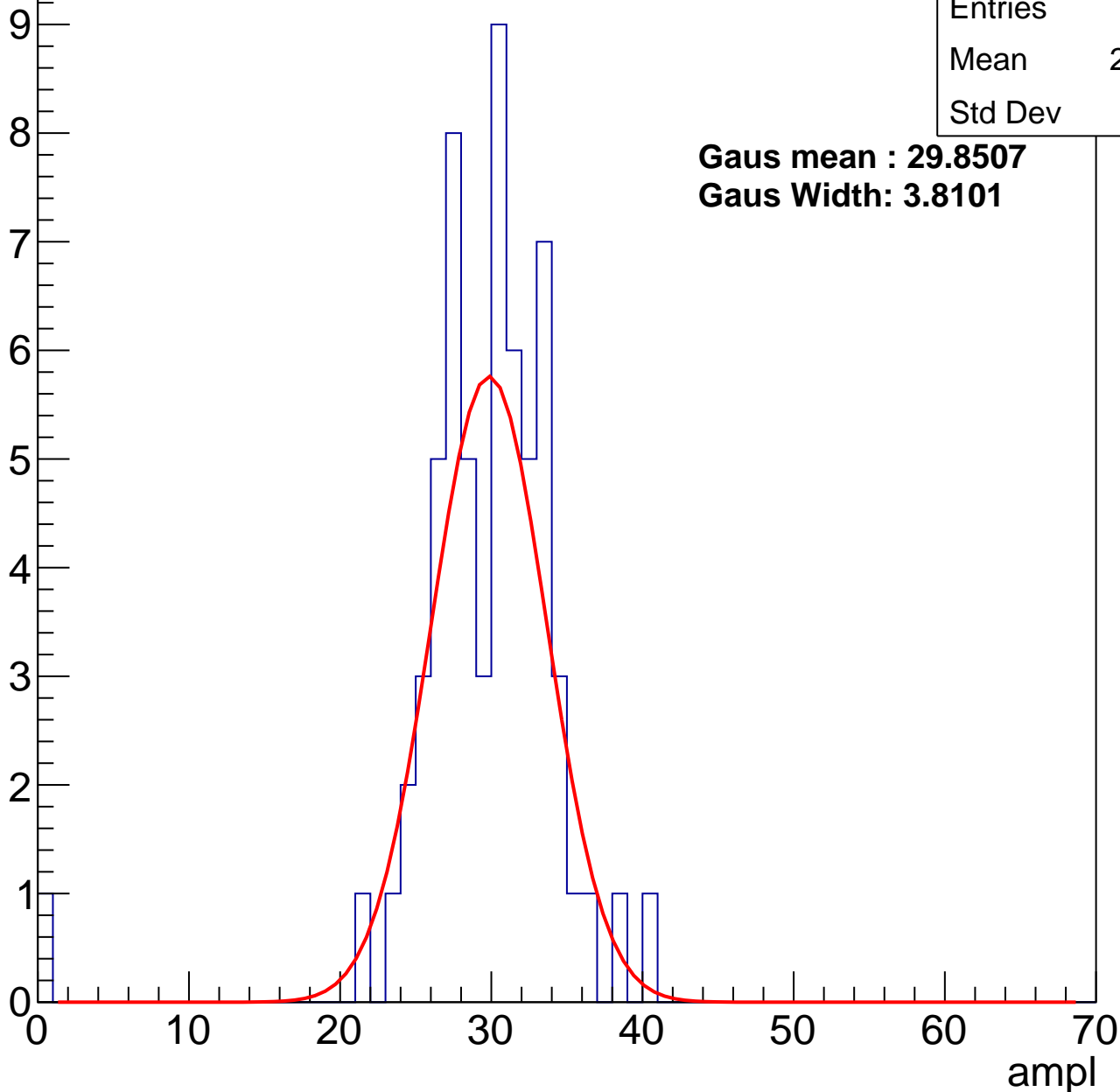
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.17
Std Dev	5.15

Gaus mean : 29.8507

Gaus Width: 3.8101



B1L101S, U17-ch125, adc1

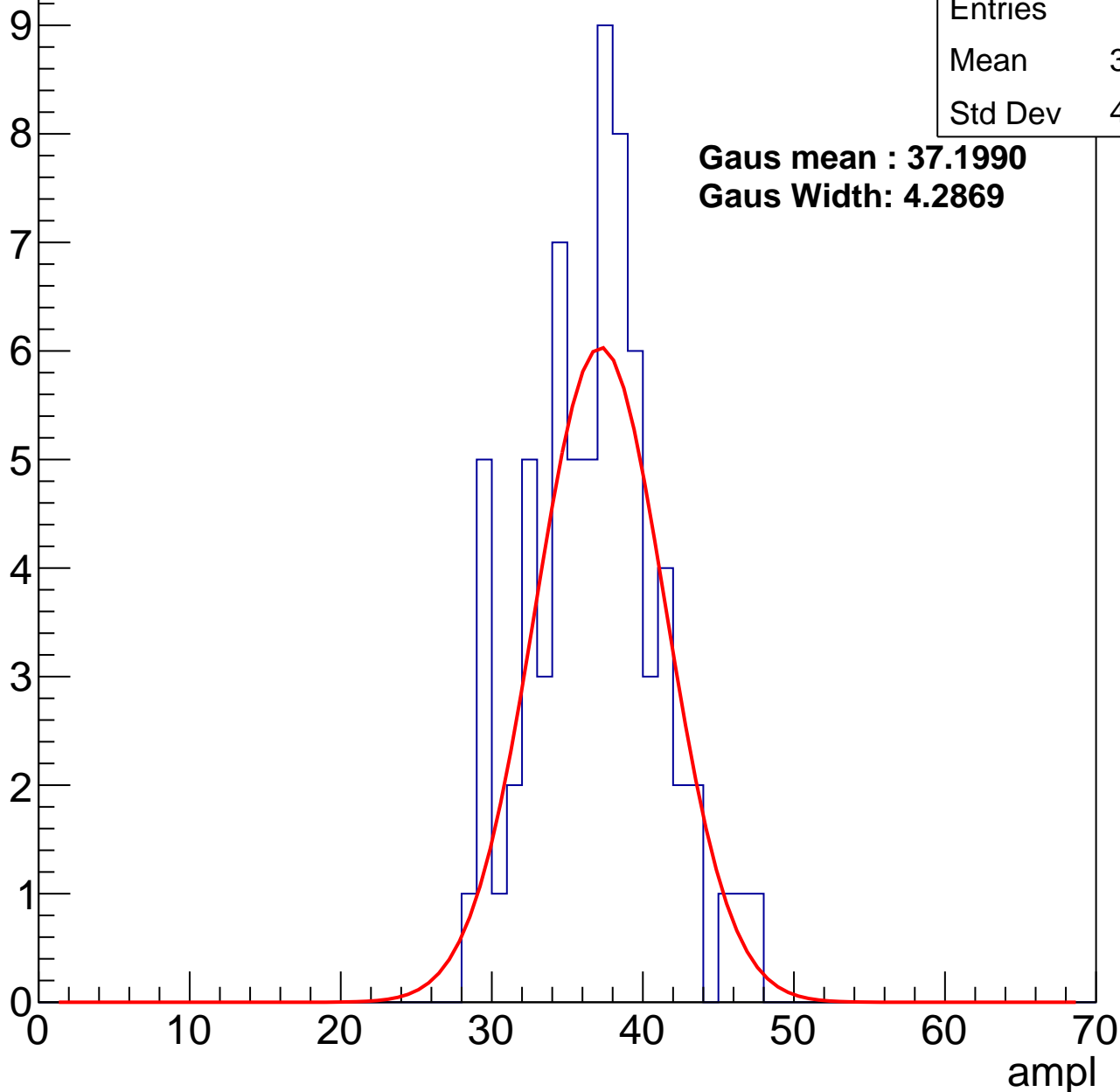
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.34
Std Dev	4.182

Gaus mean : 37.1990

Gaus Width: 4.2869



B1L101S, U17-ch125, adc2

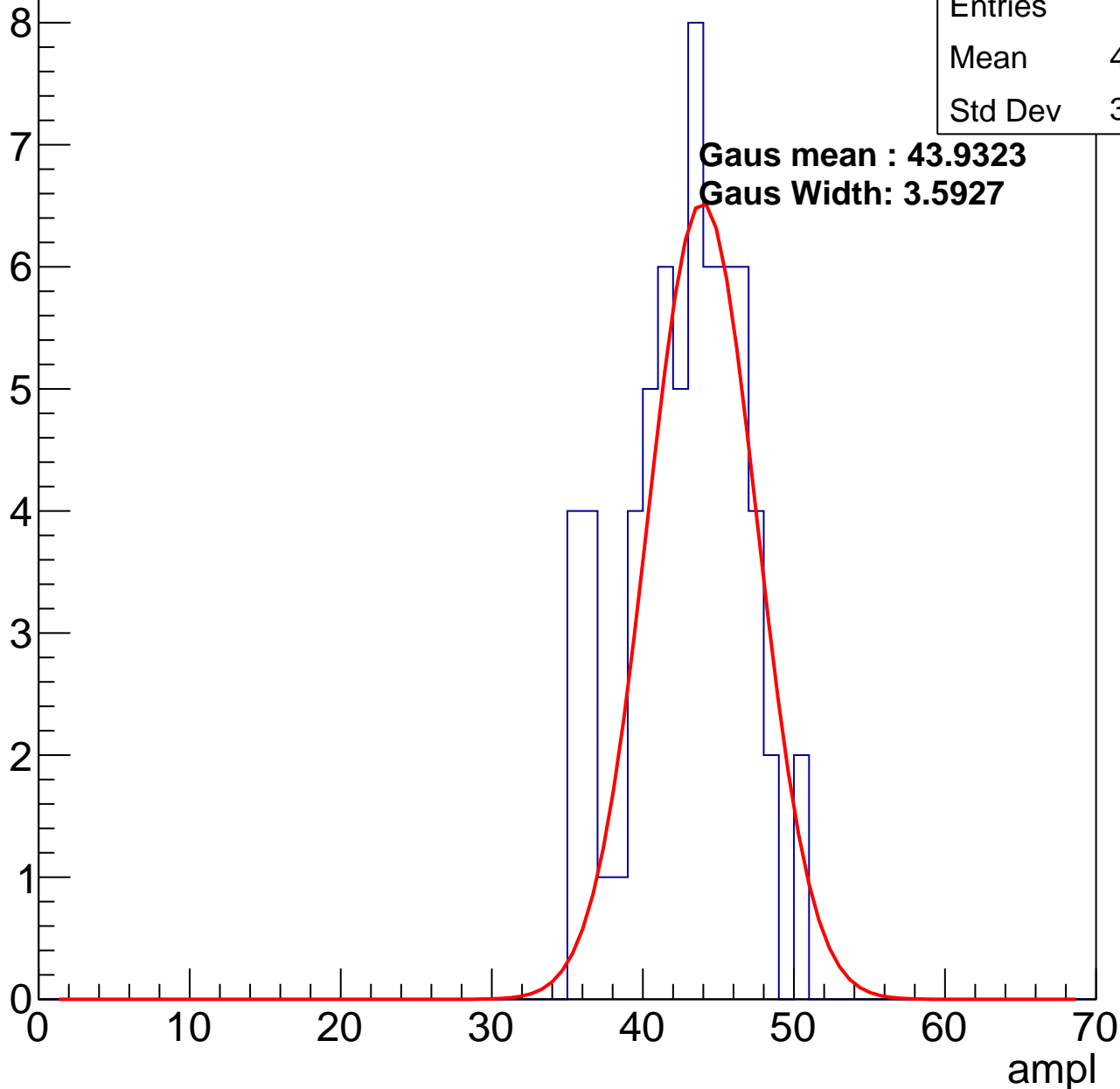
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.33
Std Dev	3.788

Gaus mean : 43.9323

Gaus Width: 3.5927

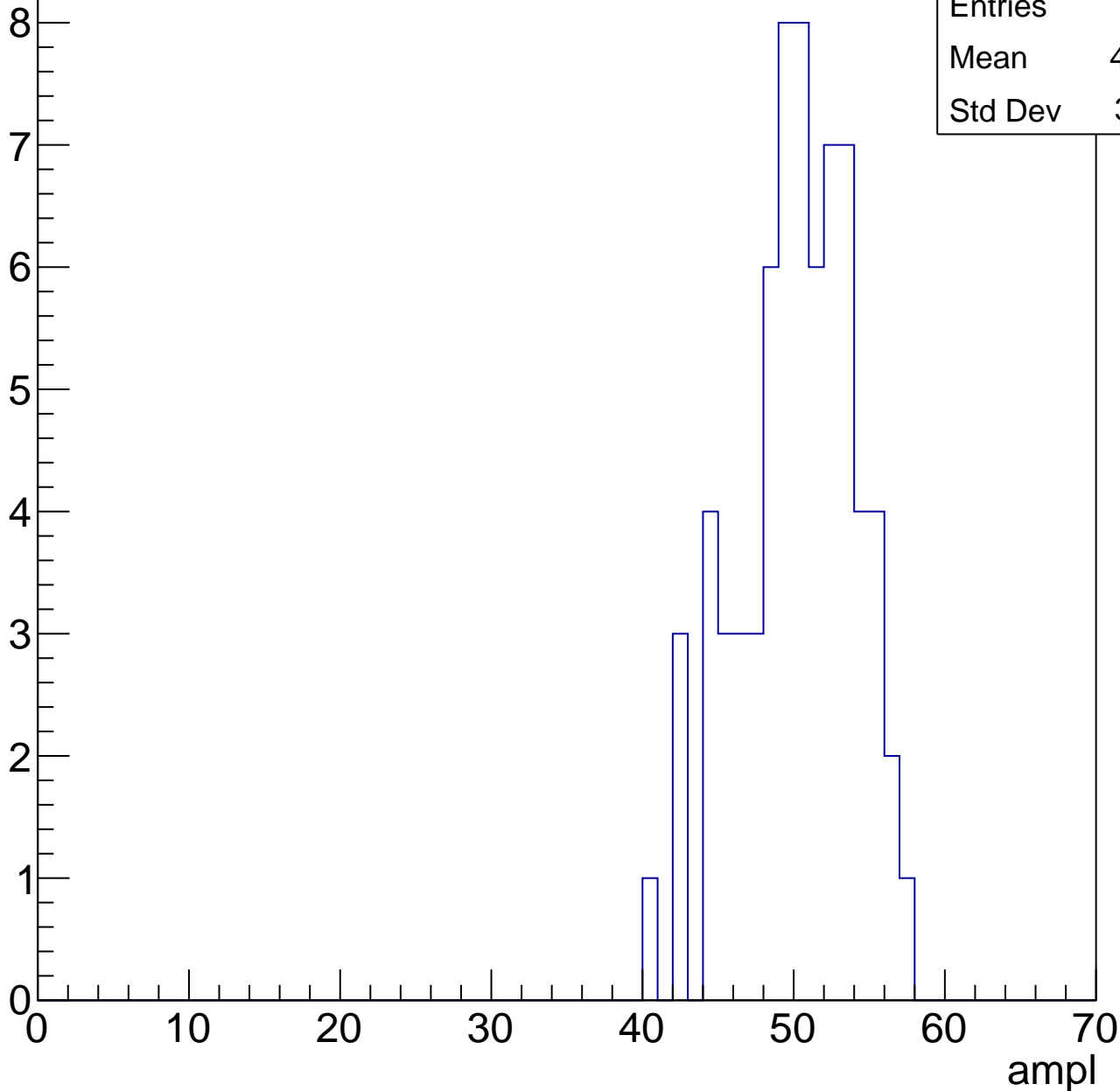


B1L101S, U17-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.74
Std Dev	3.771

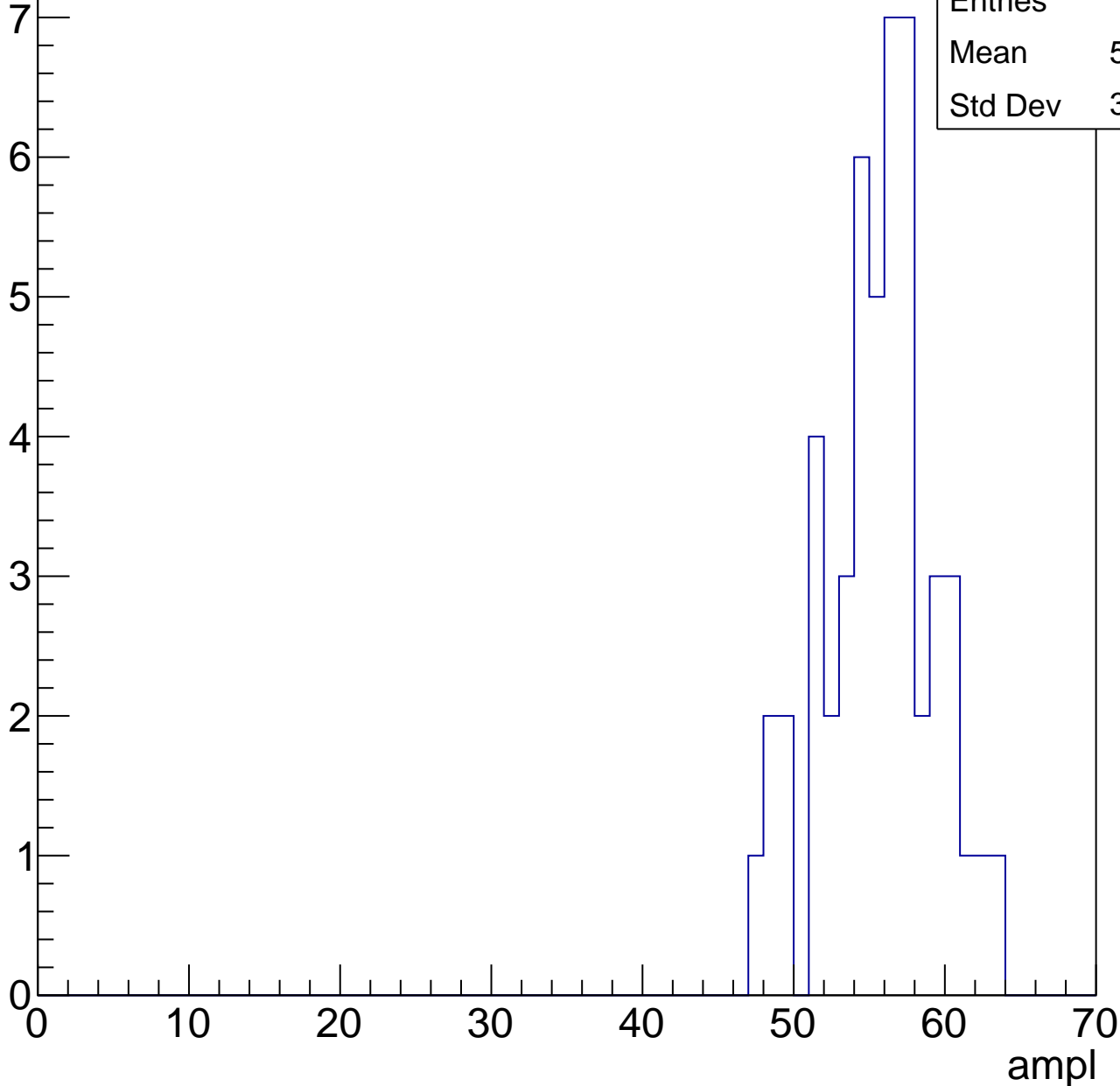


B1L101S, U17-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.14
Std Dev	3.628

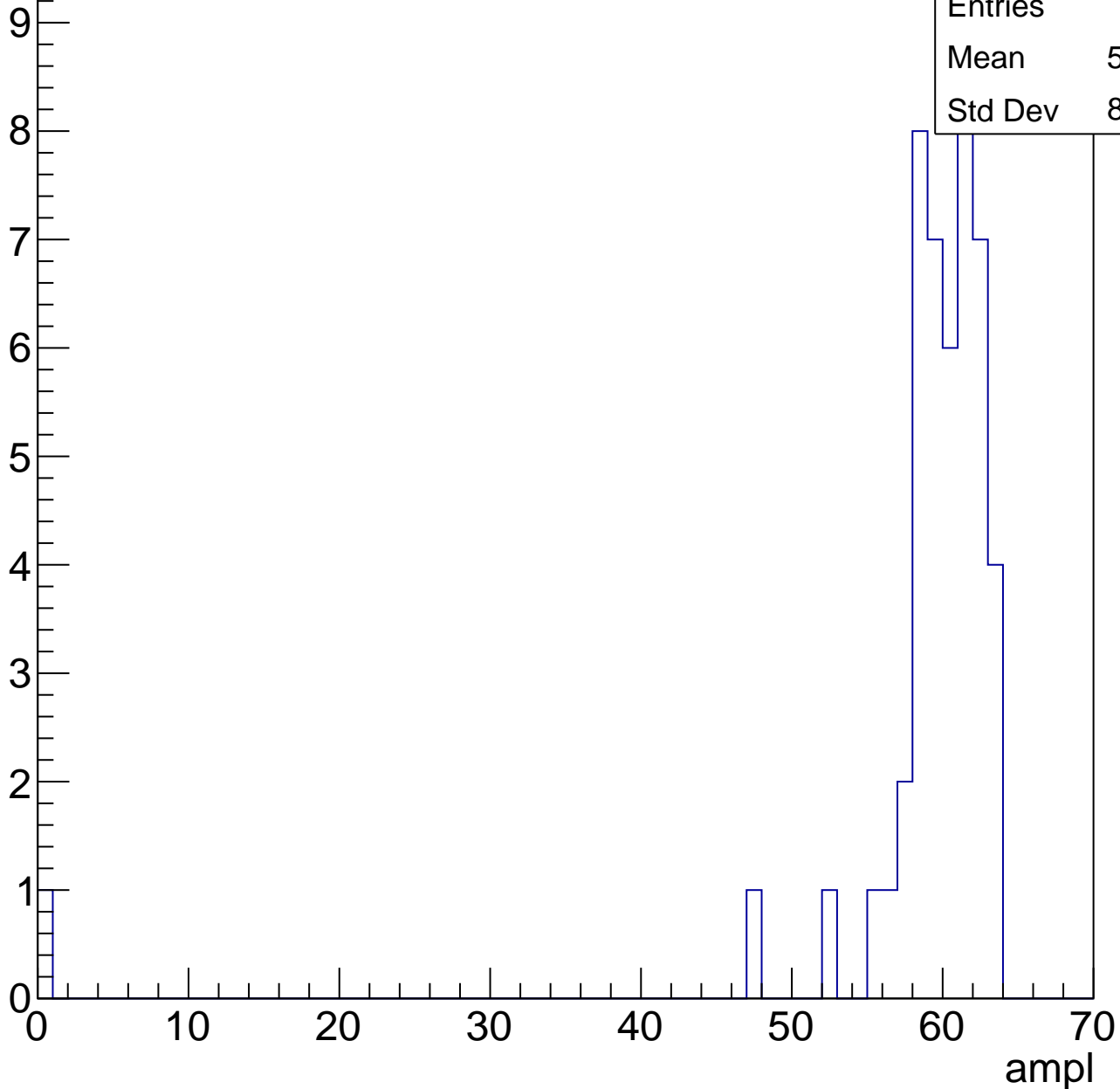


B1L101S, U17-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.25
Std Dev	8.964

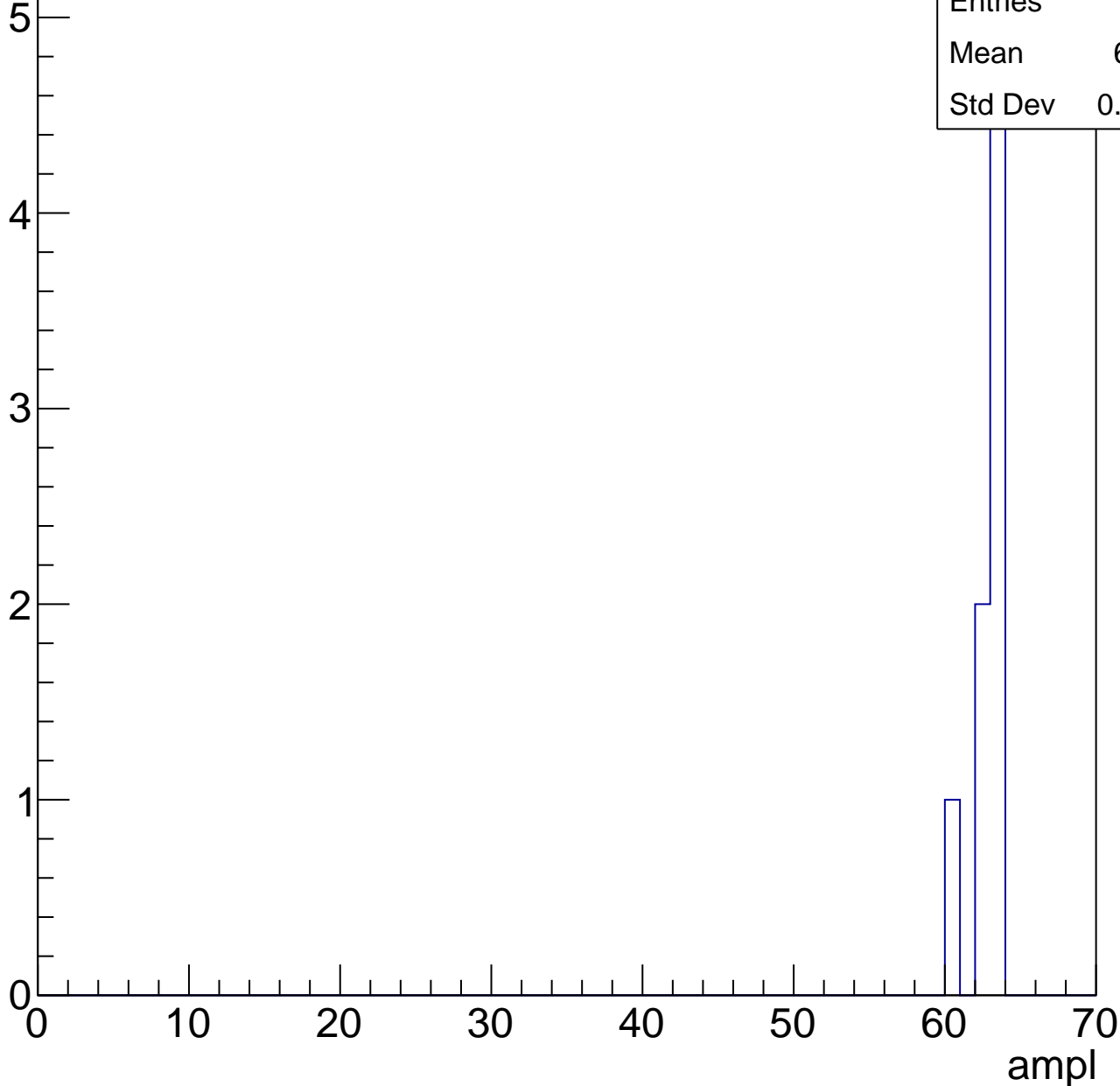


B1L101S, U17-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L101S, U17-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch126, adc0

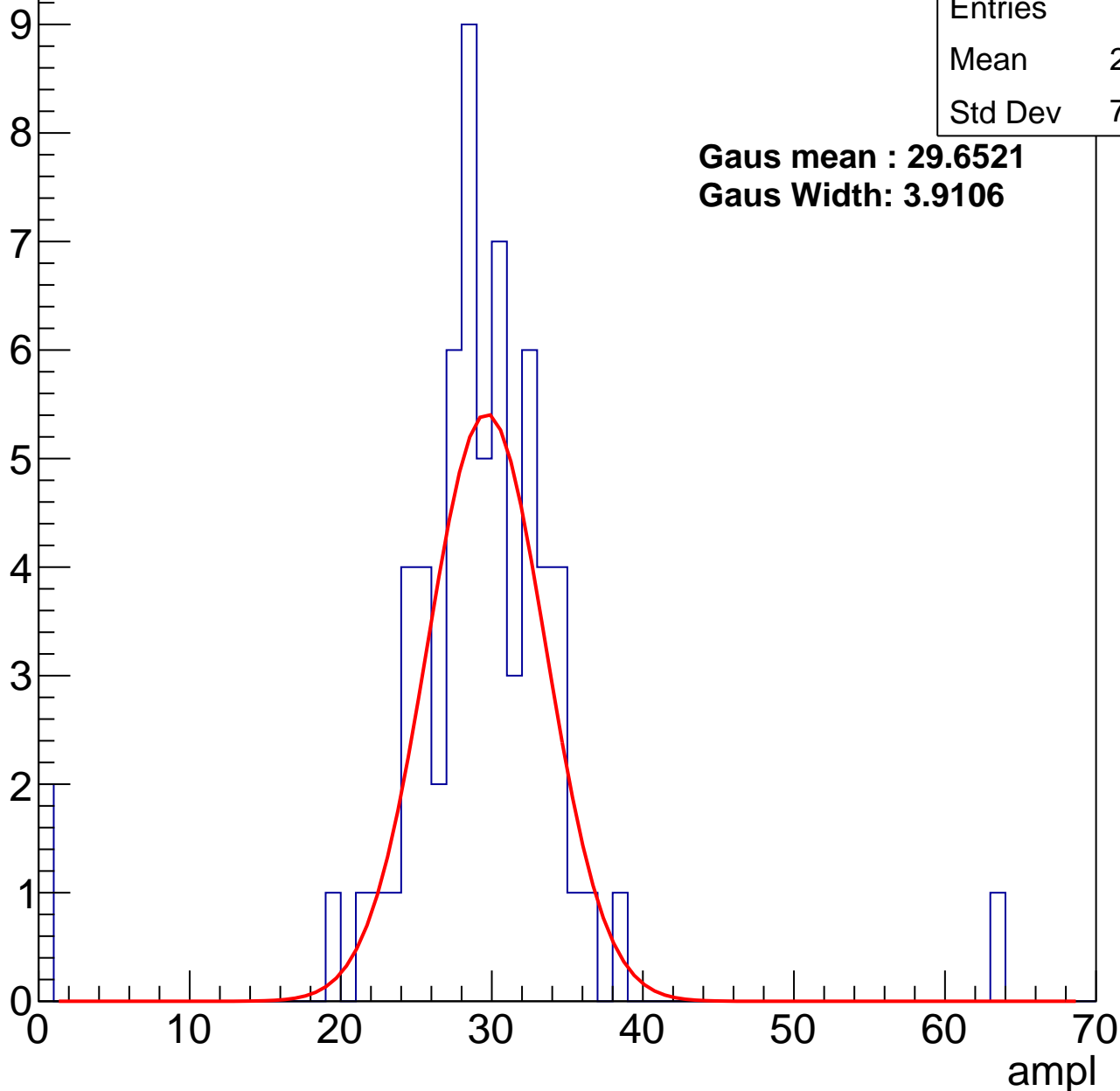
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	28.55
Std Dev	7.595

Gaus mean : 29.6521

Gaus Width: 3.9106



B1L101S, U17-ch126, adc1

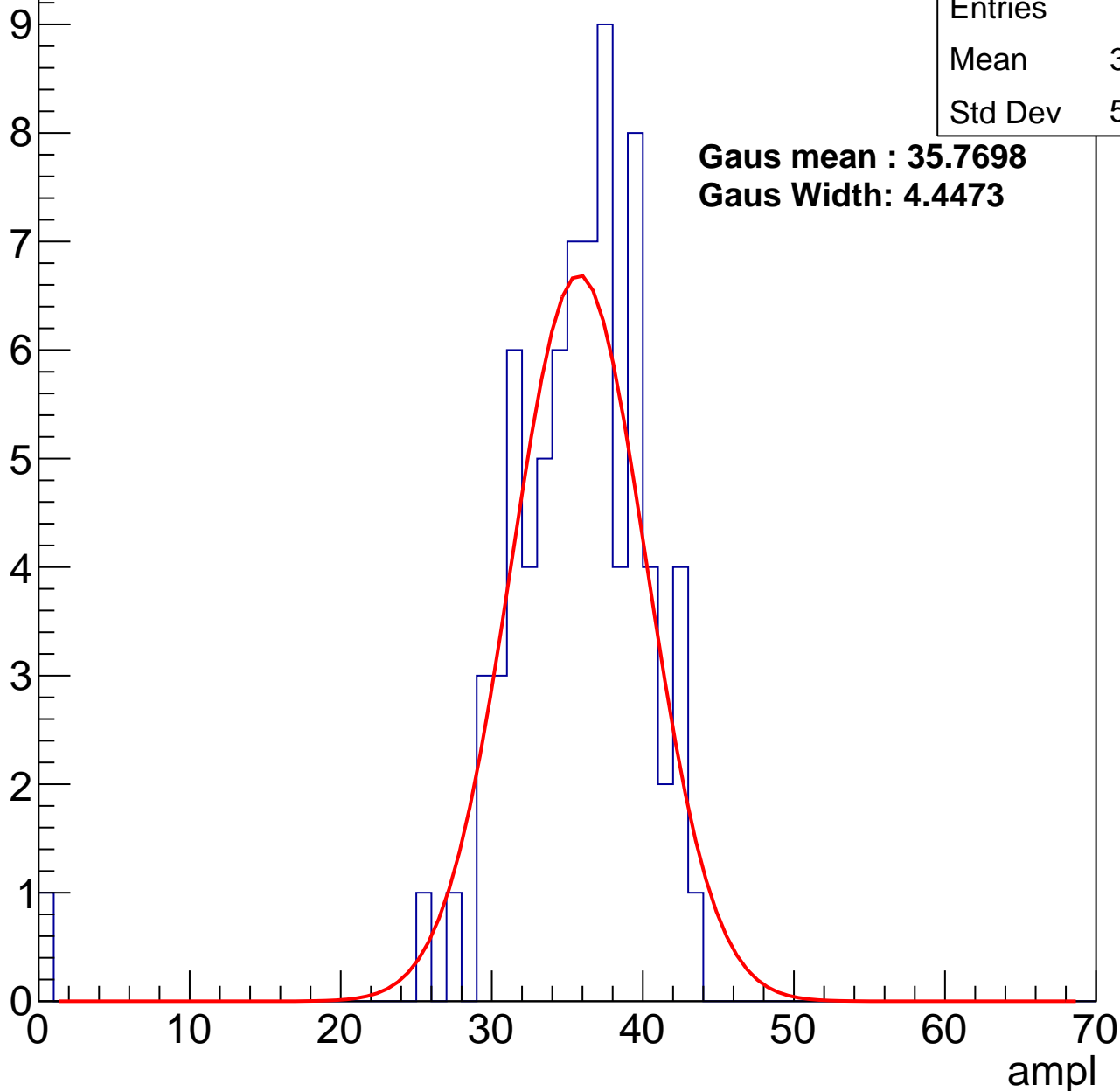
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	34.99
Std Dev	5.583

Gaus mean : 35.7698

Gaus Width: 4.4473



B1L101S, U17-ch126, adc2

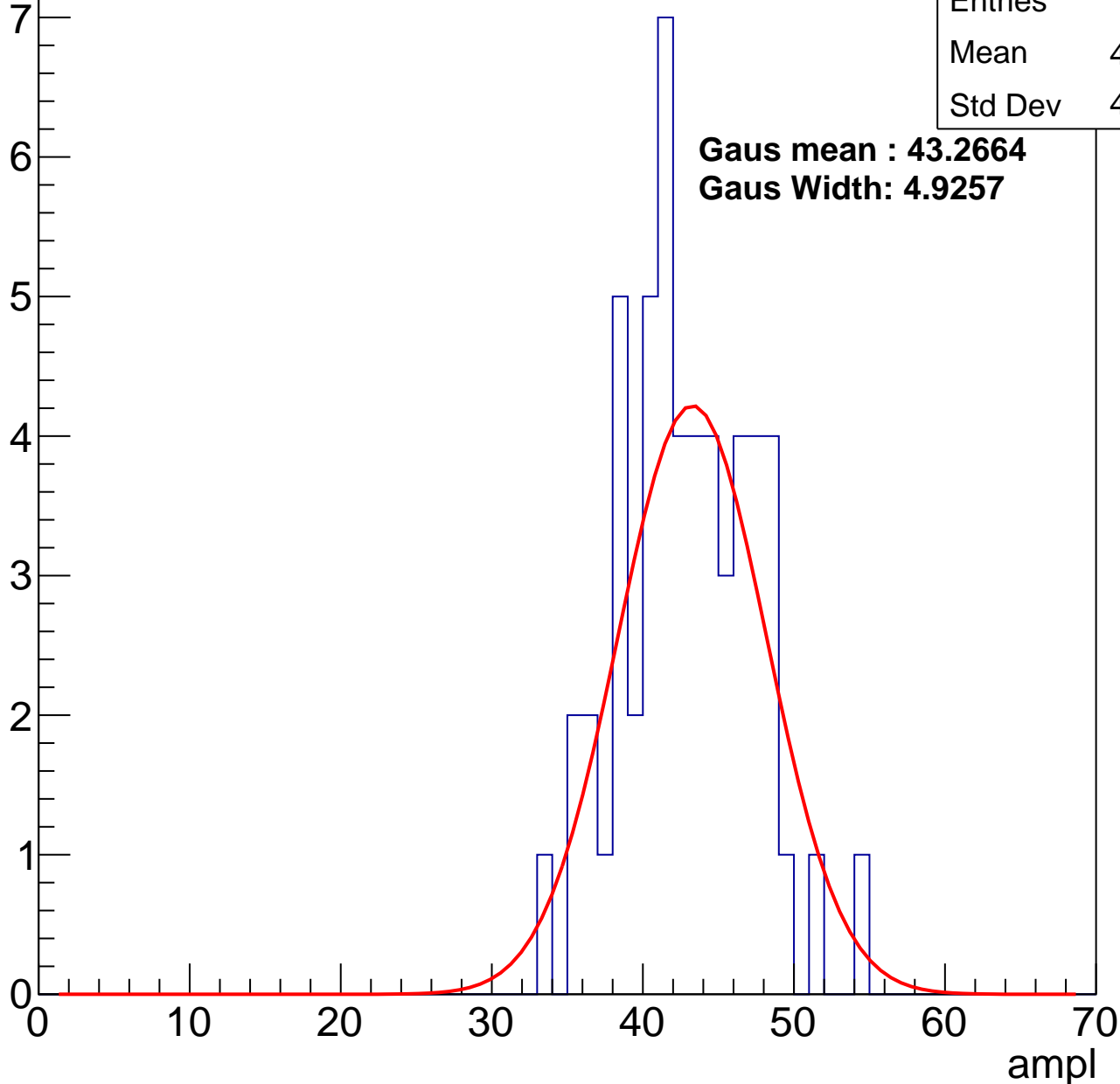
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.47
Std Dev	4.302

Gaus mean : 43.2664

Gaus Width: 4.9257

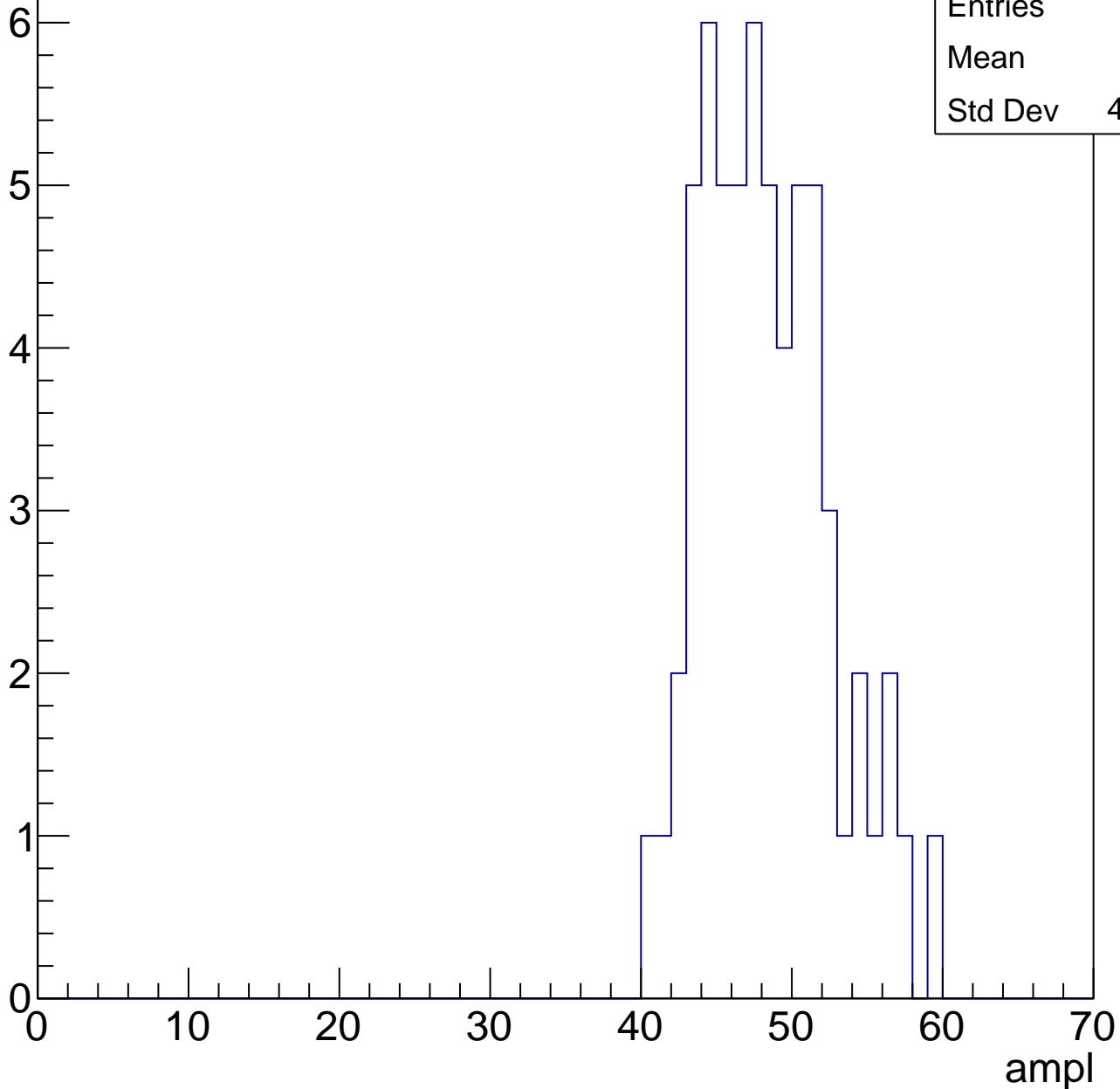


B1L101S, U17-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	47.9
Std Dev	4.187

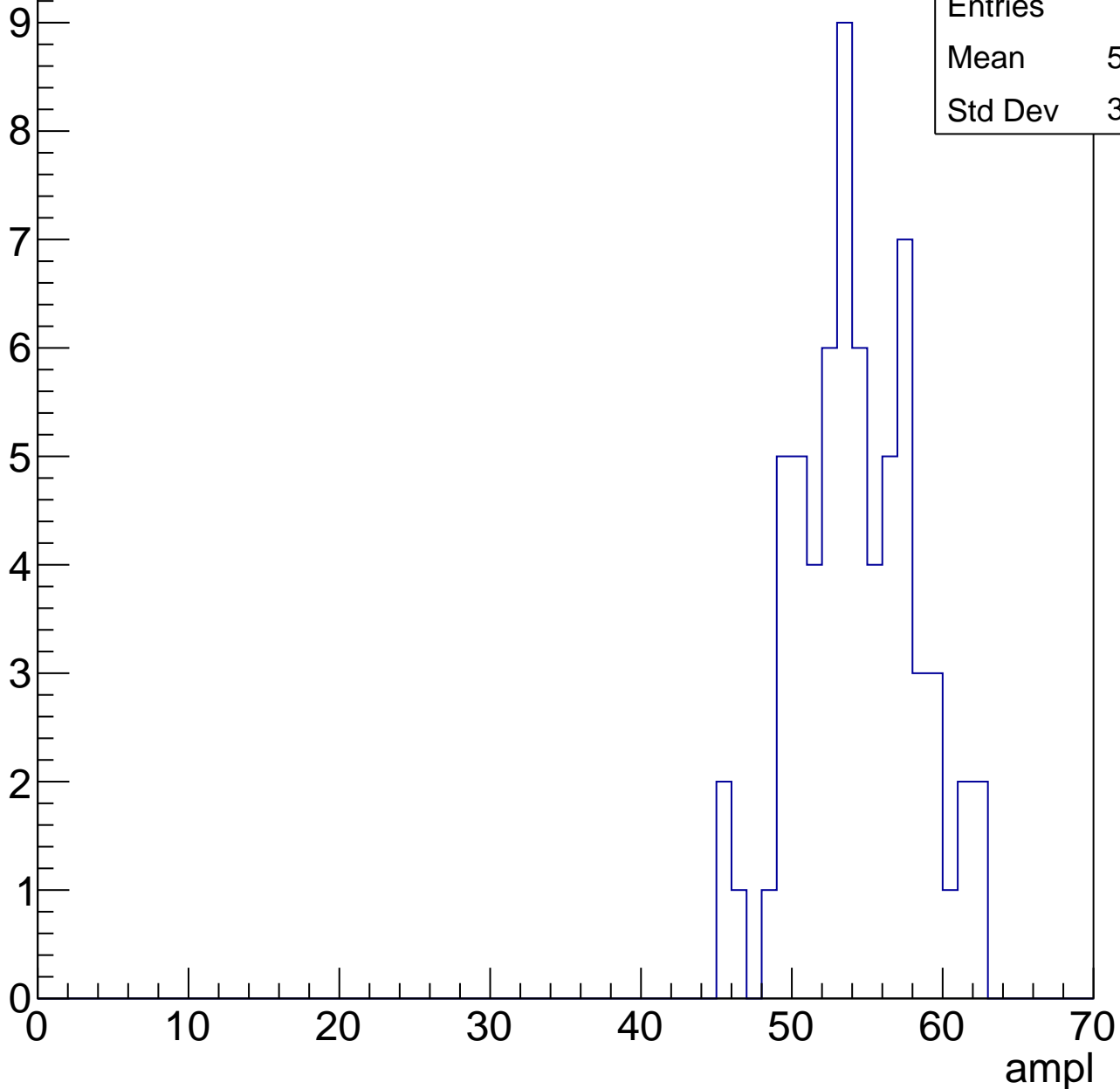


B1L101S, U17-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	53.82
Std Dev	3.904

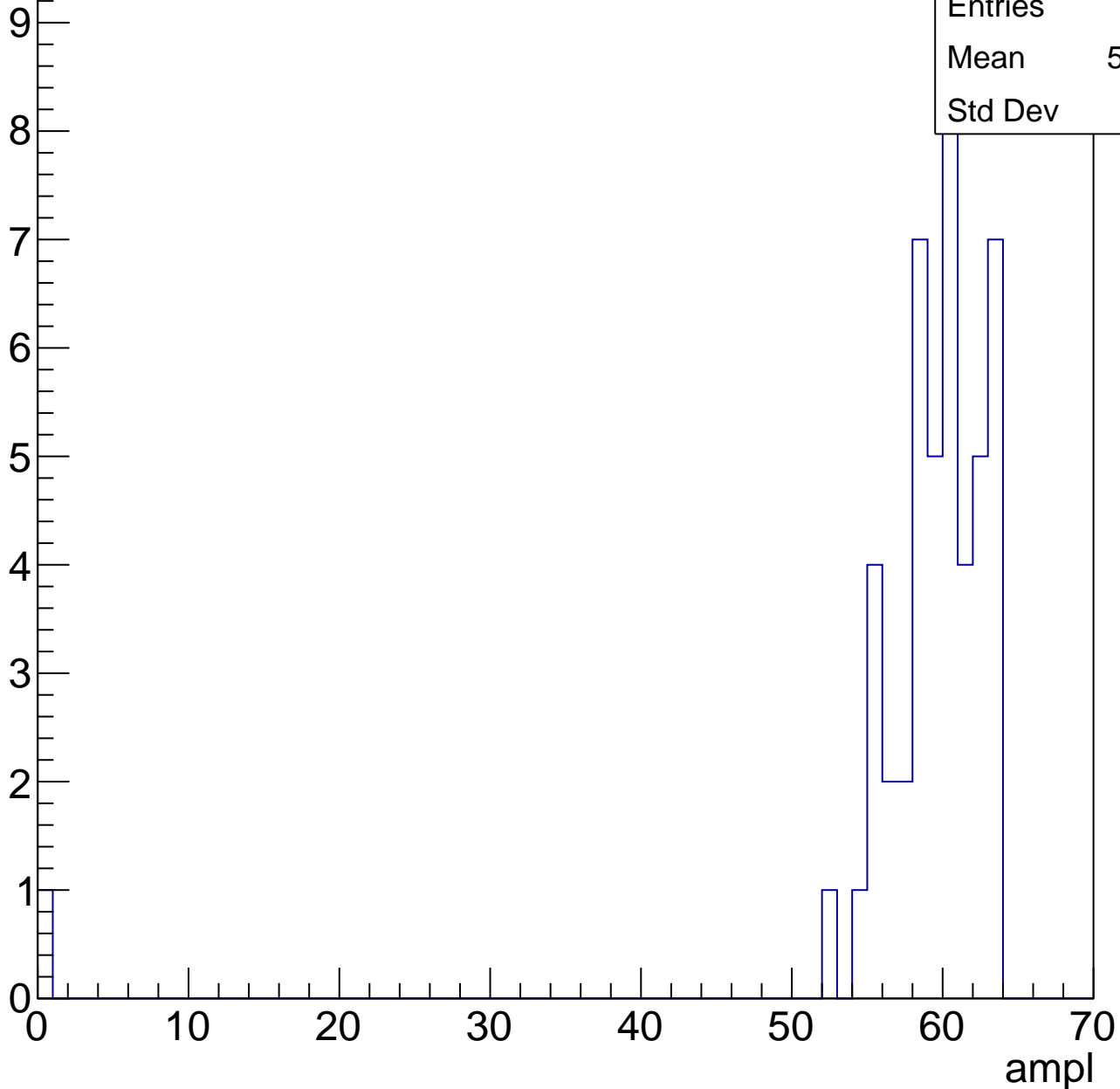


B1L101S, U17-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

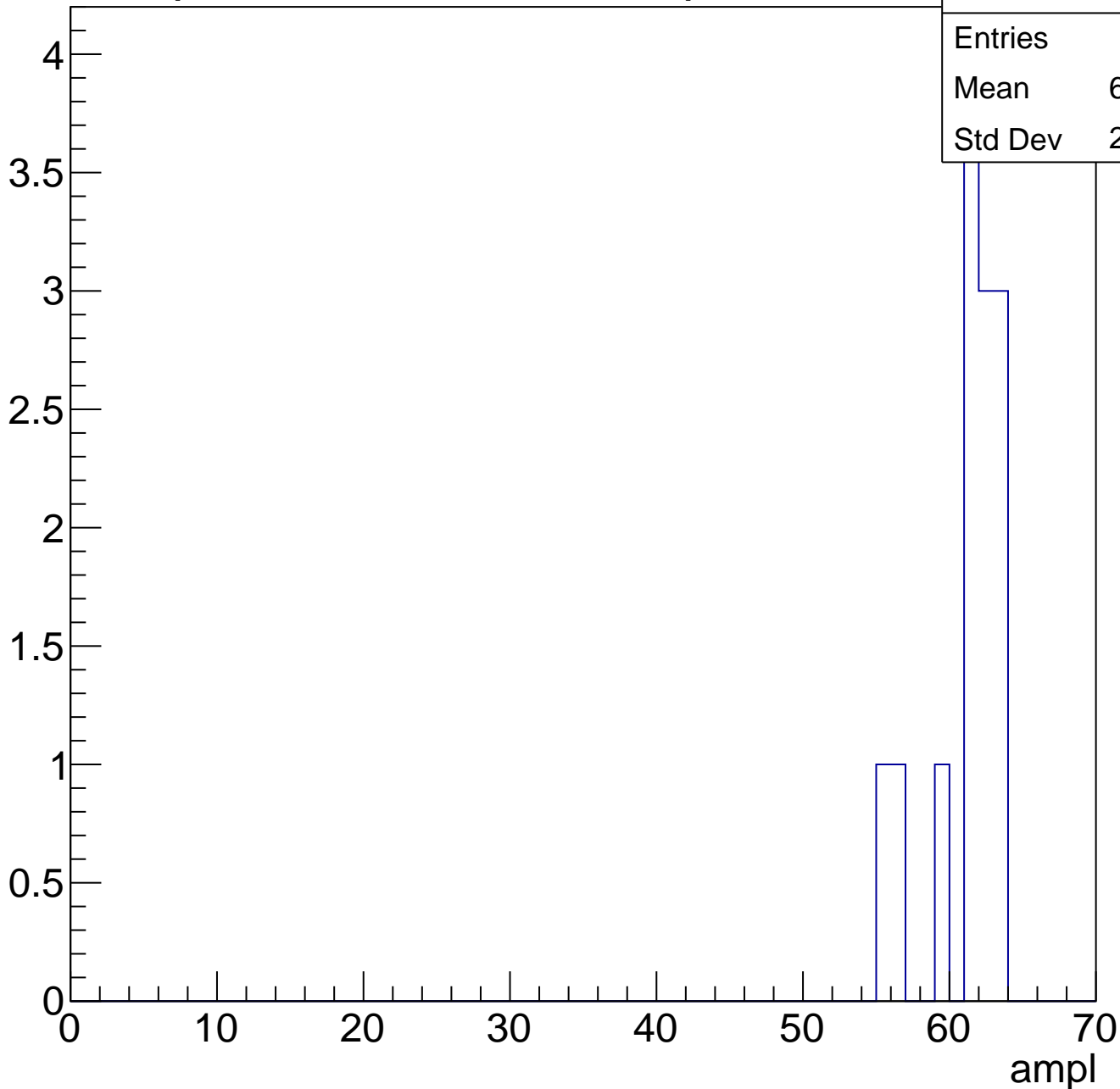
Entries	48
Mean	58.08
Std Dev	8.89



B1L101S, U17-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U17-ch127, adc0

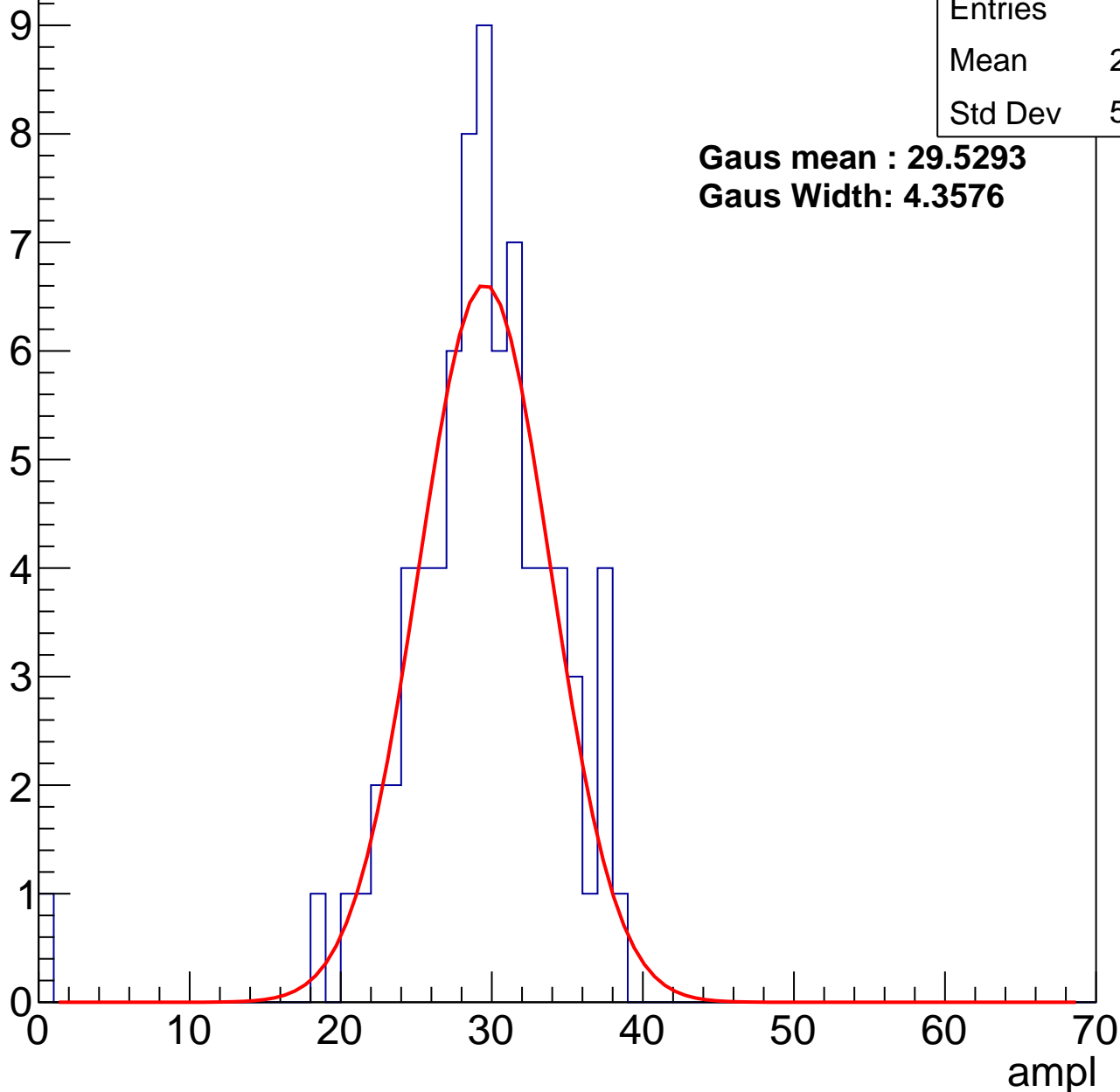
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.78
Std Dev	5.385

Gaus mean : 29.5293

Gaus Width: 4.3576



B1L101S, U17-ch127, adc1

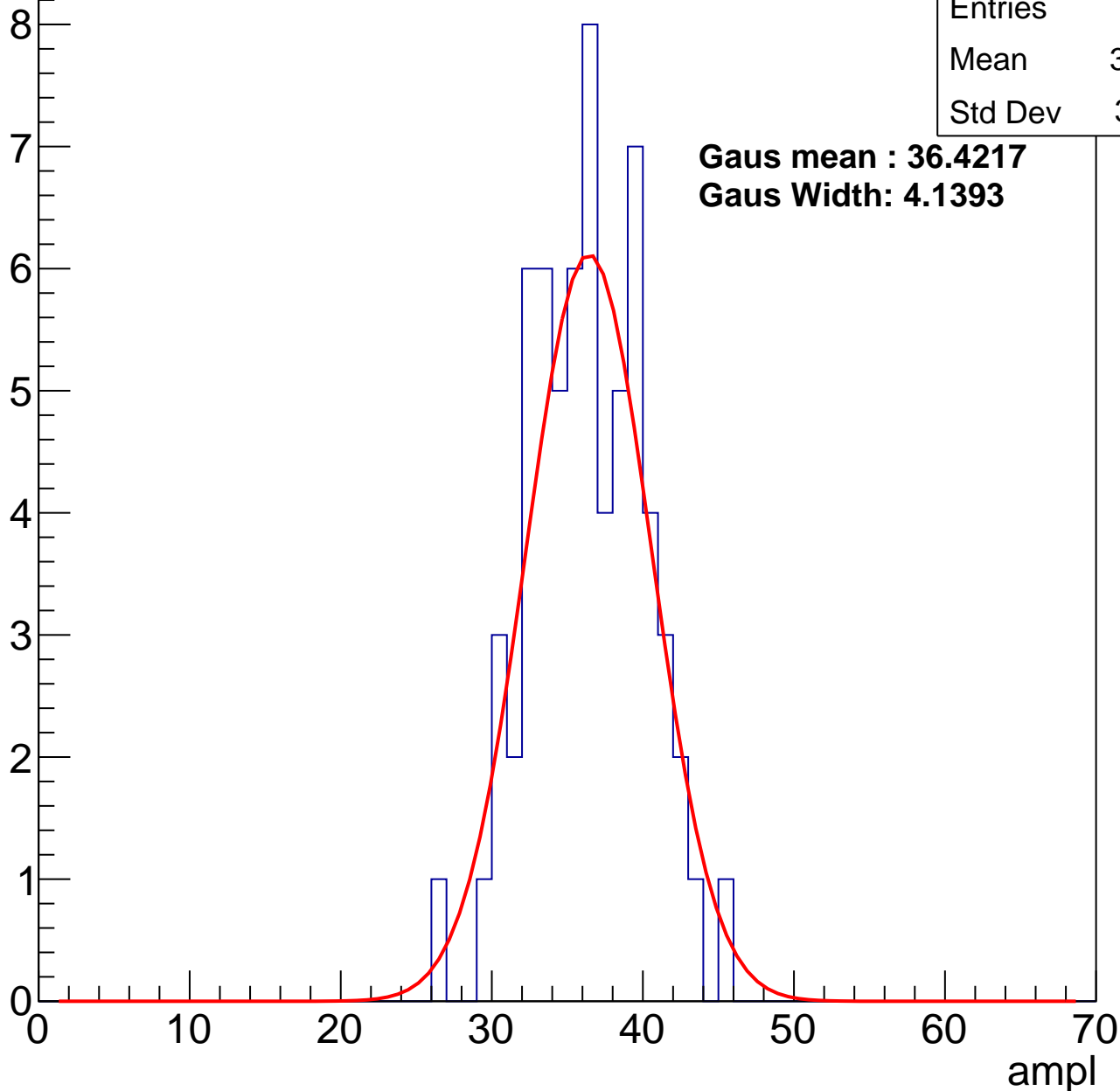
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	35.86
Std Dev	3.741

Gaus mean : 36.4217

Gaus Width: 4.1393



B1L101S, U17-ch127, adc2

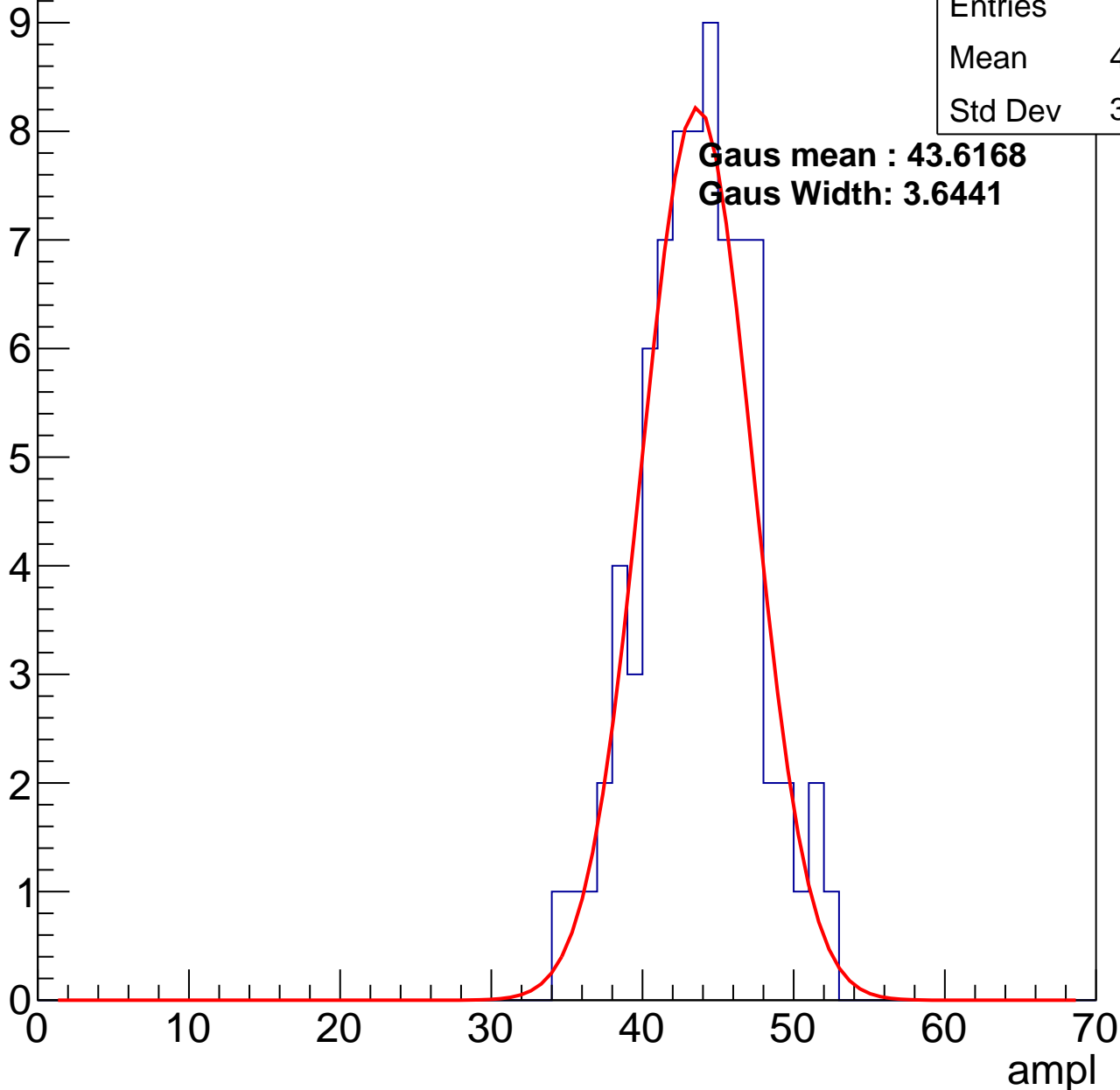
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	43.23
Std Dev	3.718

Gaus mean : 43.6168

Gaus Width: 3.6441

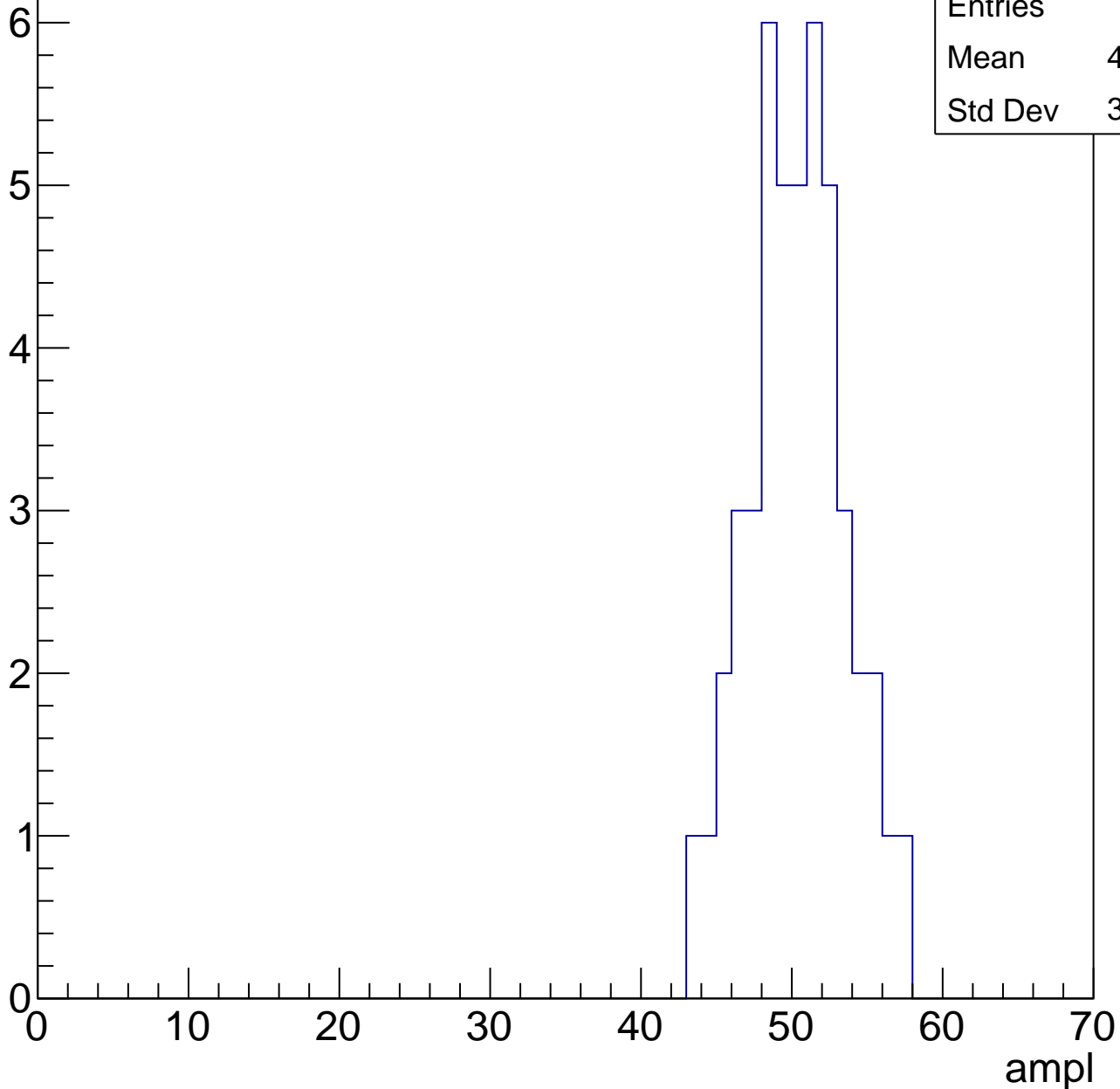


B1L101S, U17-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	49.89
Std Dev	3.157

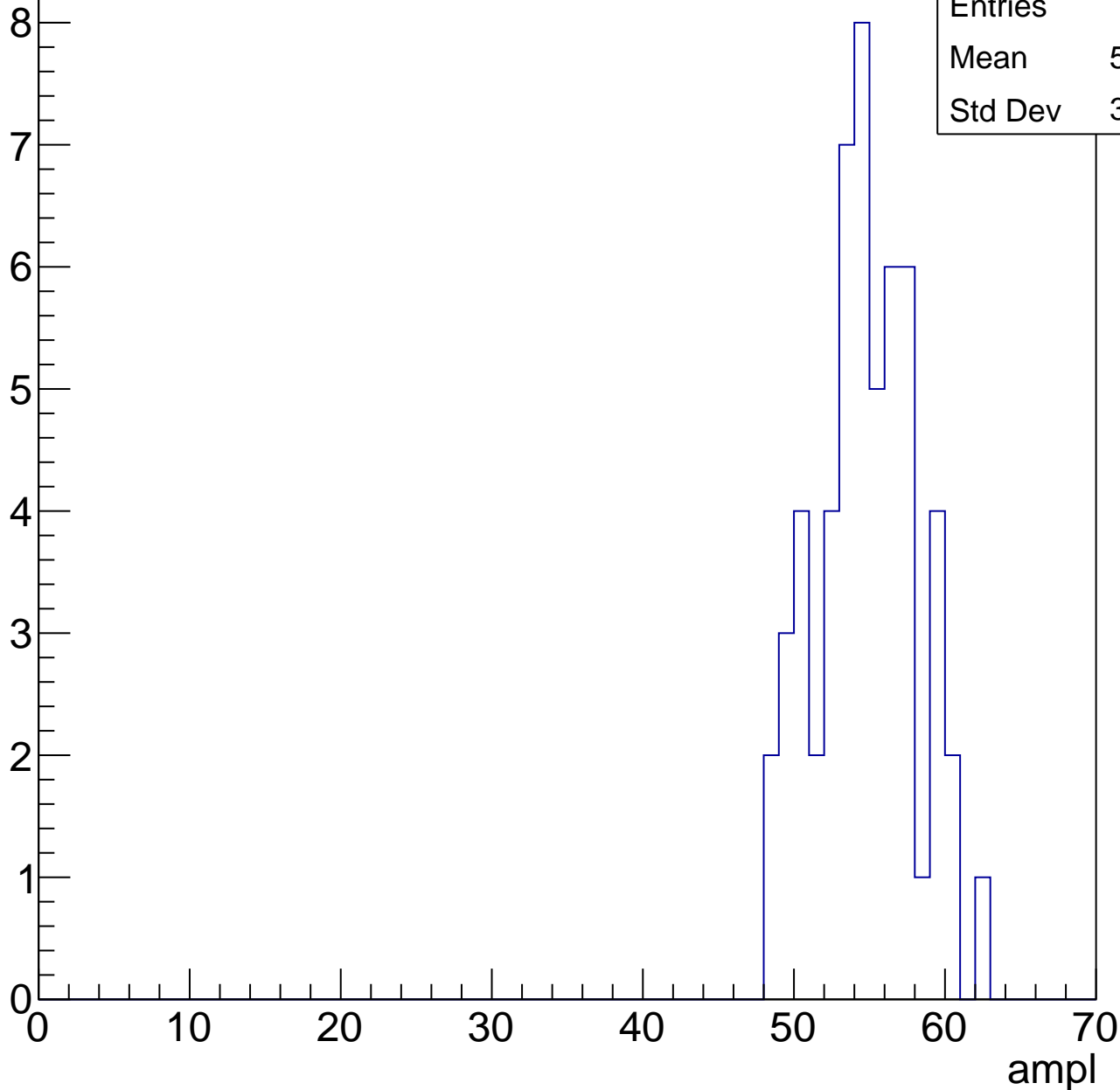


B1L101S, U17-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.27
Std Dev	3.256

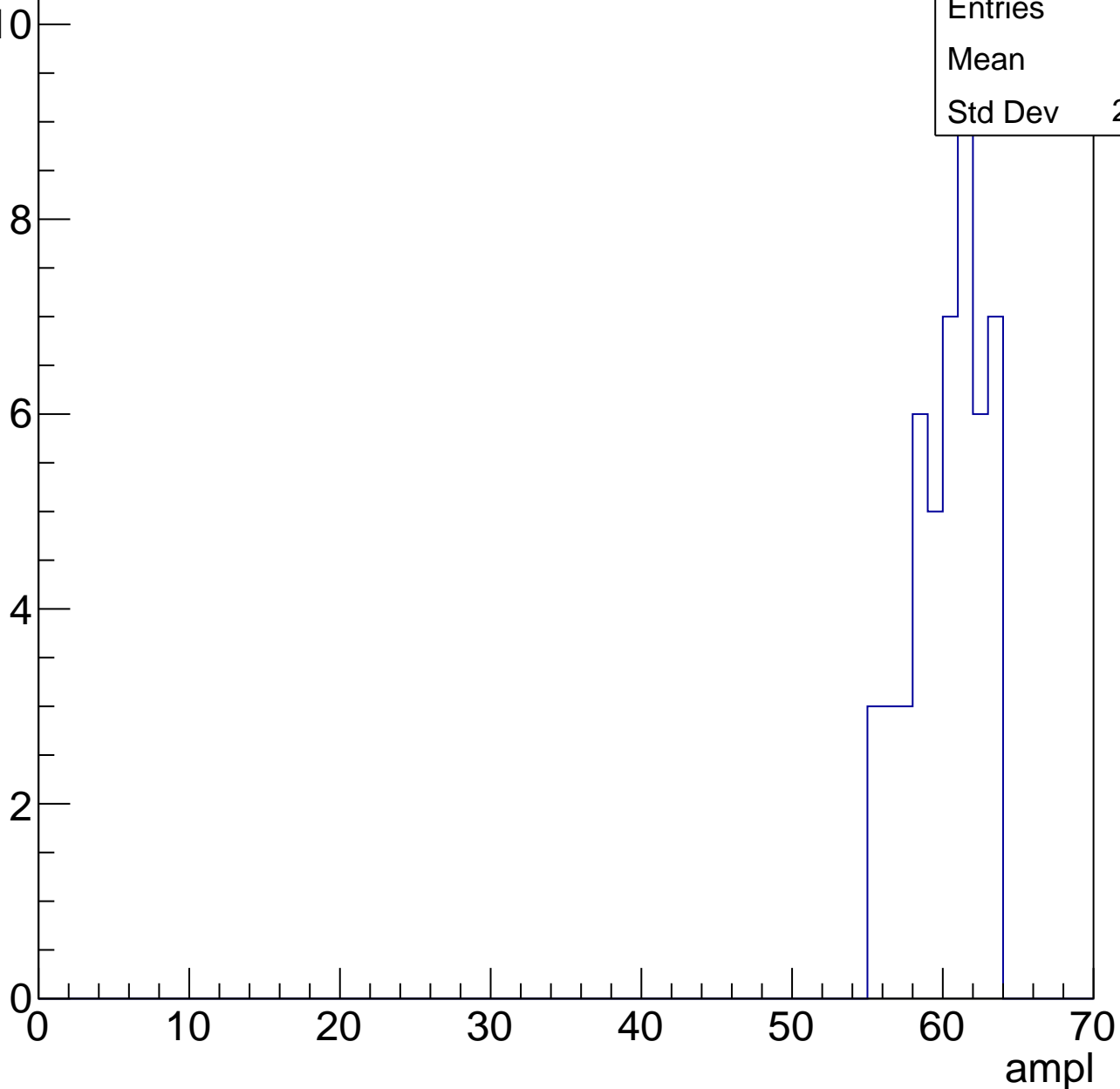


B1L101S, U17-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	59.8
Std Dev	2.341



B1L101S, U17-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

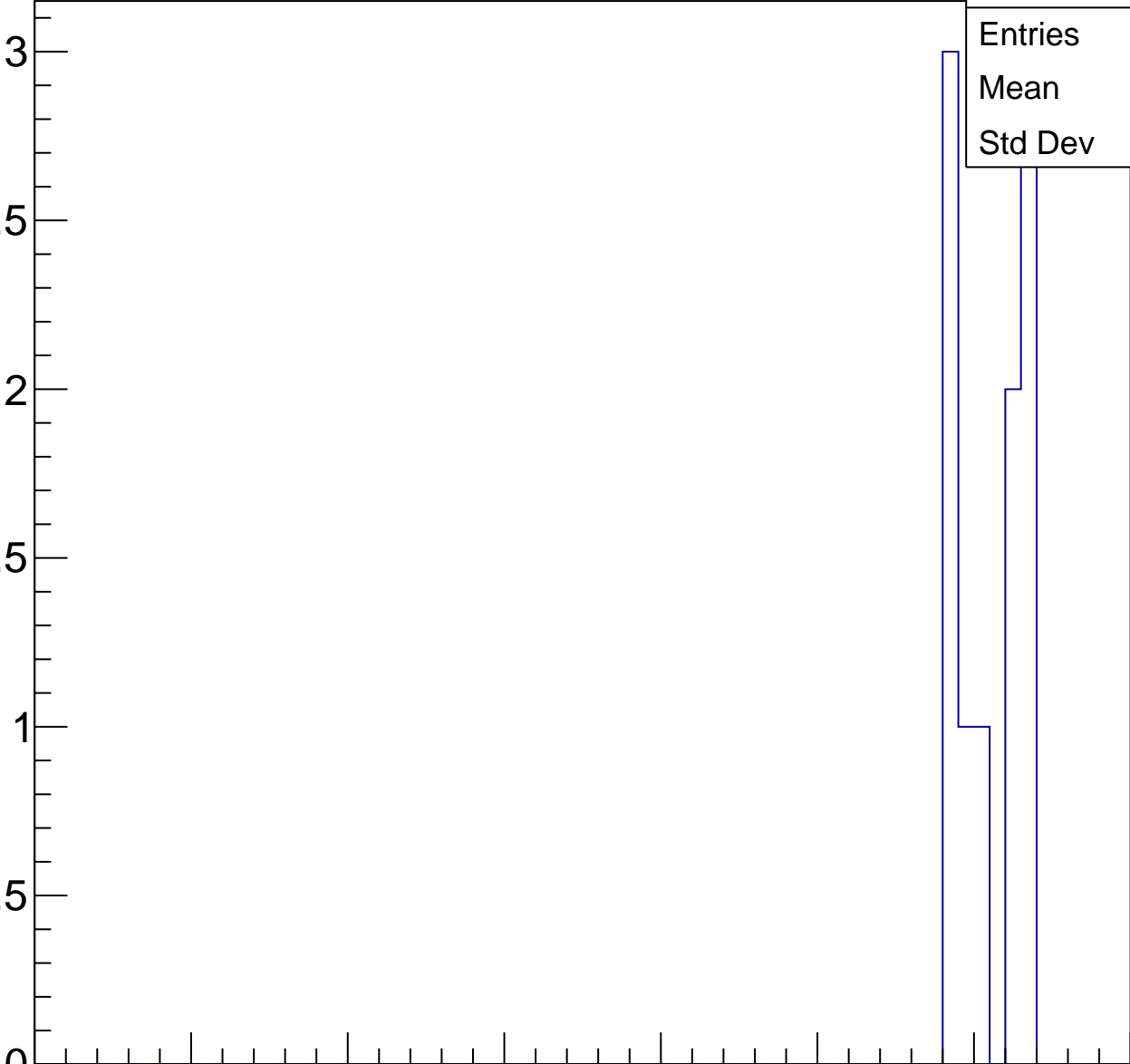
Entry

3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	60.6
Std Dev	2.107

ampl

0 10 20 30 40 50 60 70



B1L101S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

