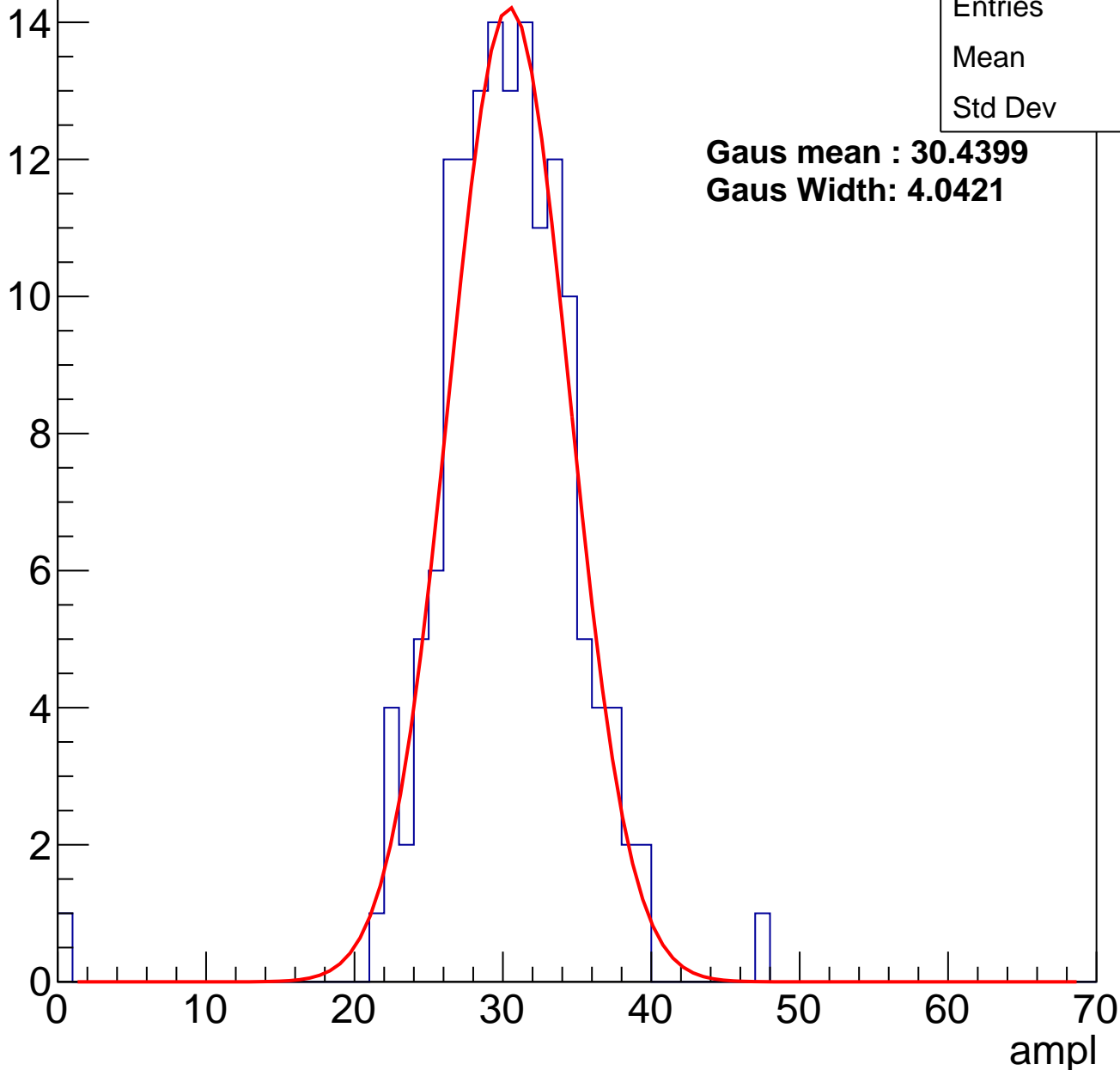


B0L101S, U22-ch0, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch0, adc1

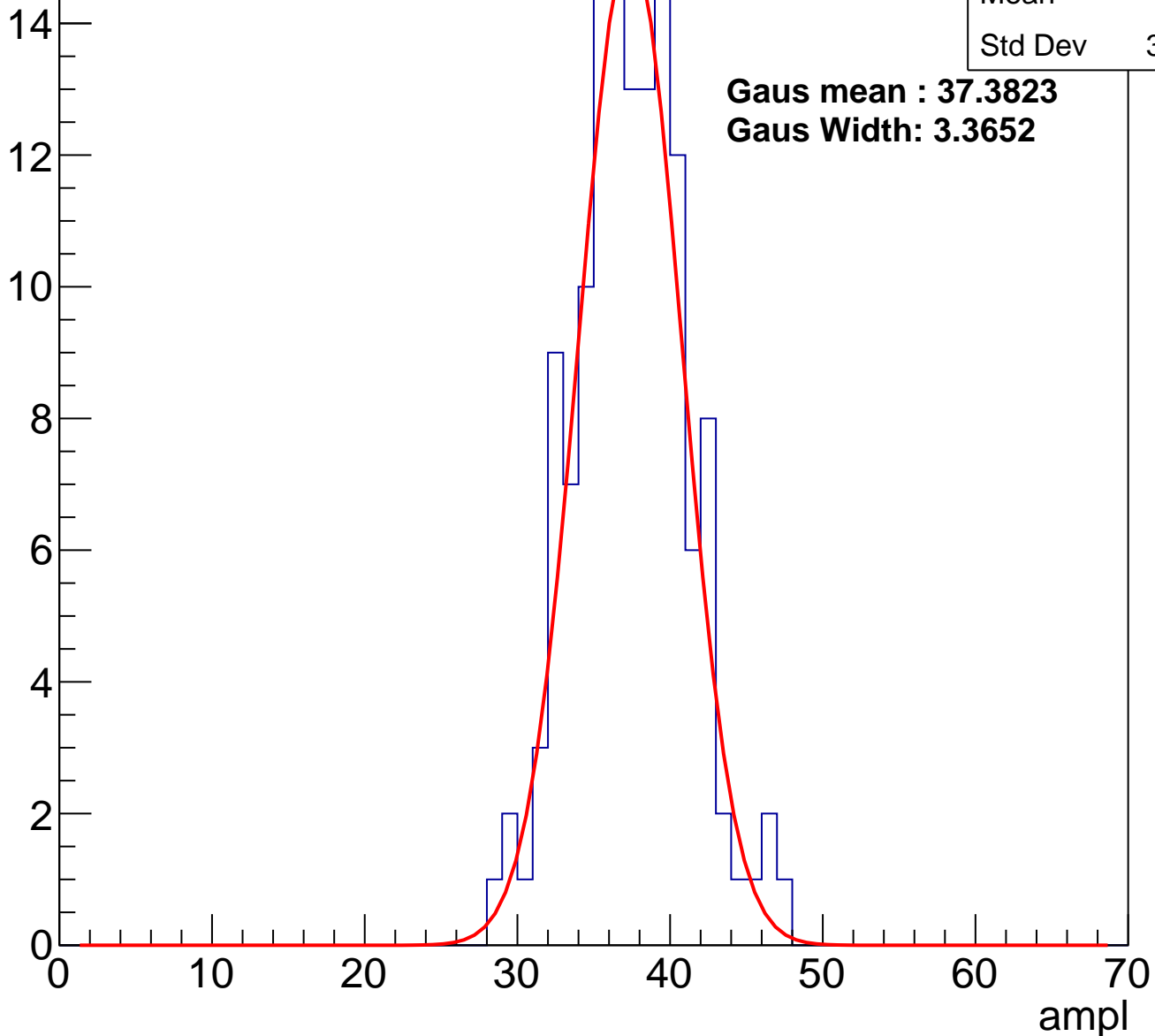
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 37 |
| Std Dev | 3.617 |

Gaus mean : 37.3823

Gaus Width: 3.3652

Entry



B0L101S, U22-ch0, adc2

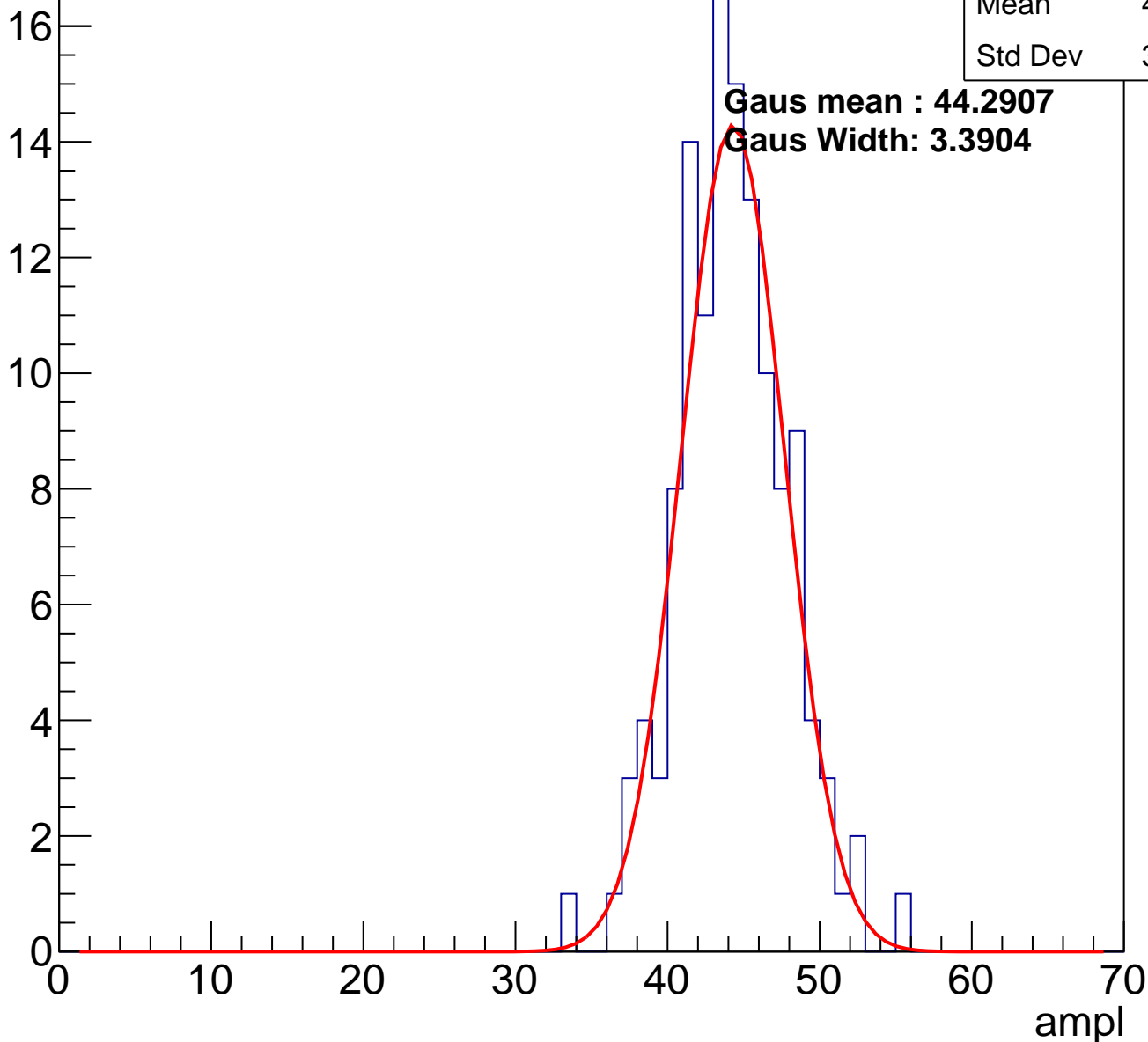
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 128 |
| Mean | 43.79 |
| Std Dev | 3.574 |

Gaus mean : 44.2907

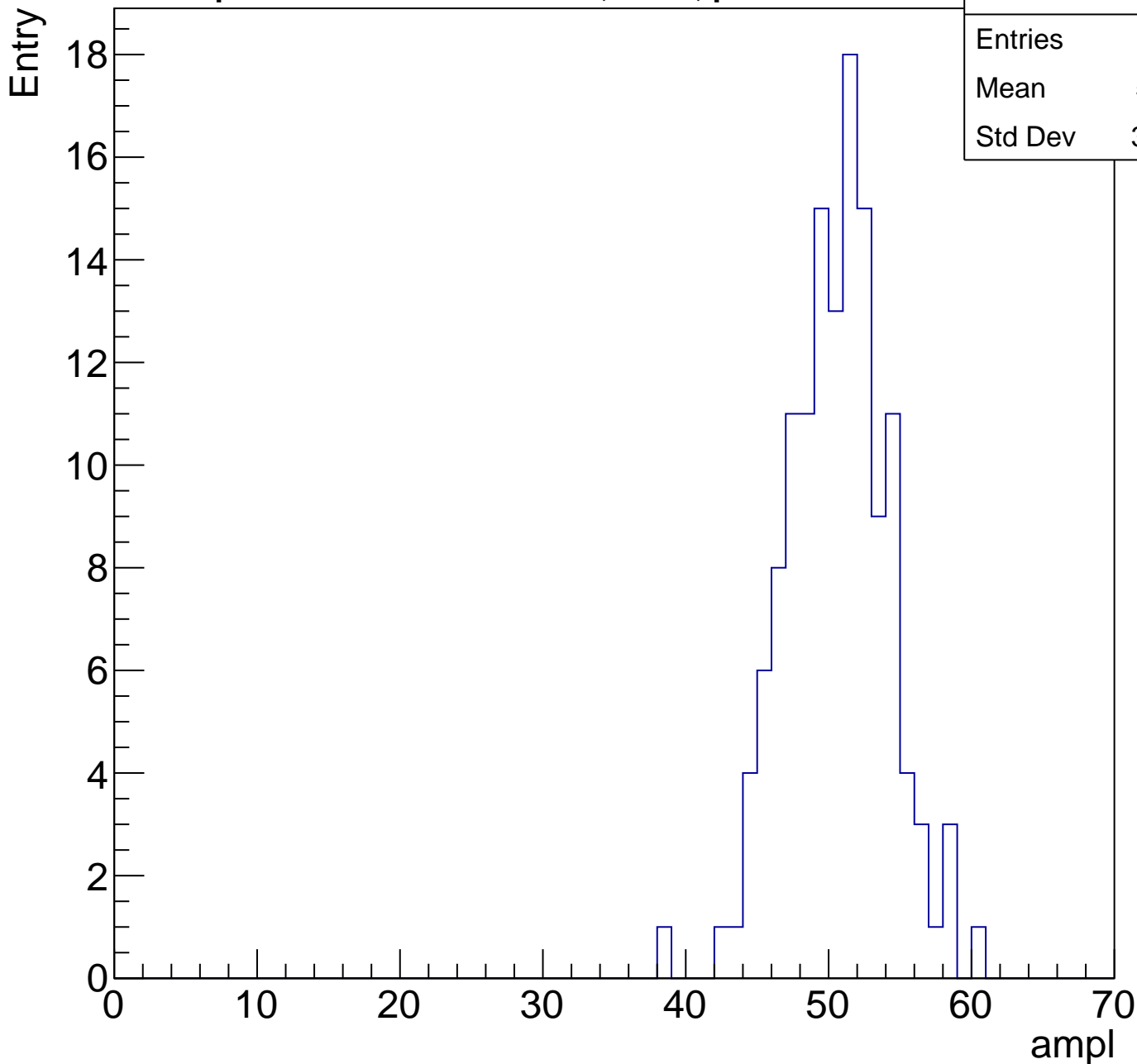
Gaus Width: 3.3904



B0L101S, U22-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 50.11 |
| Std Dev | 3.556 |



B0L101S, U22-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

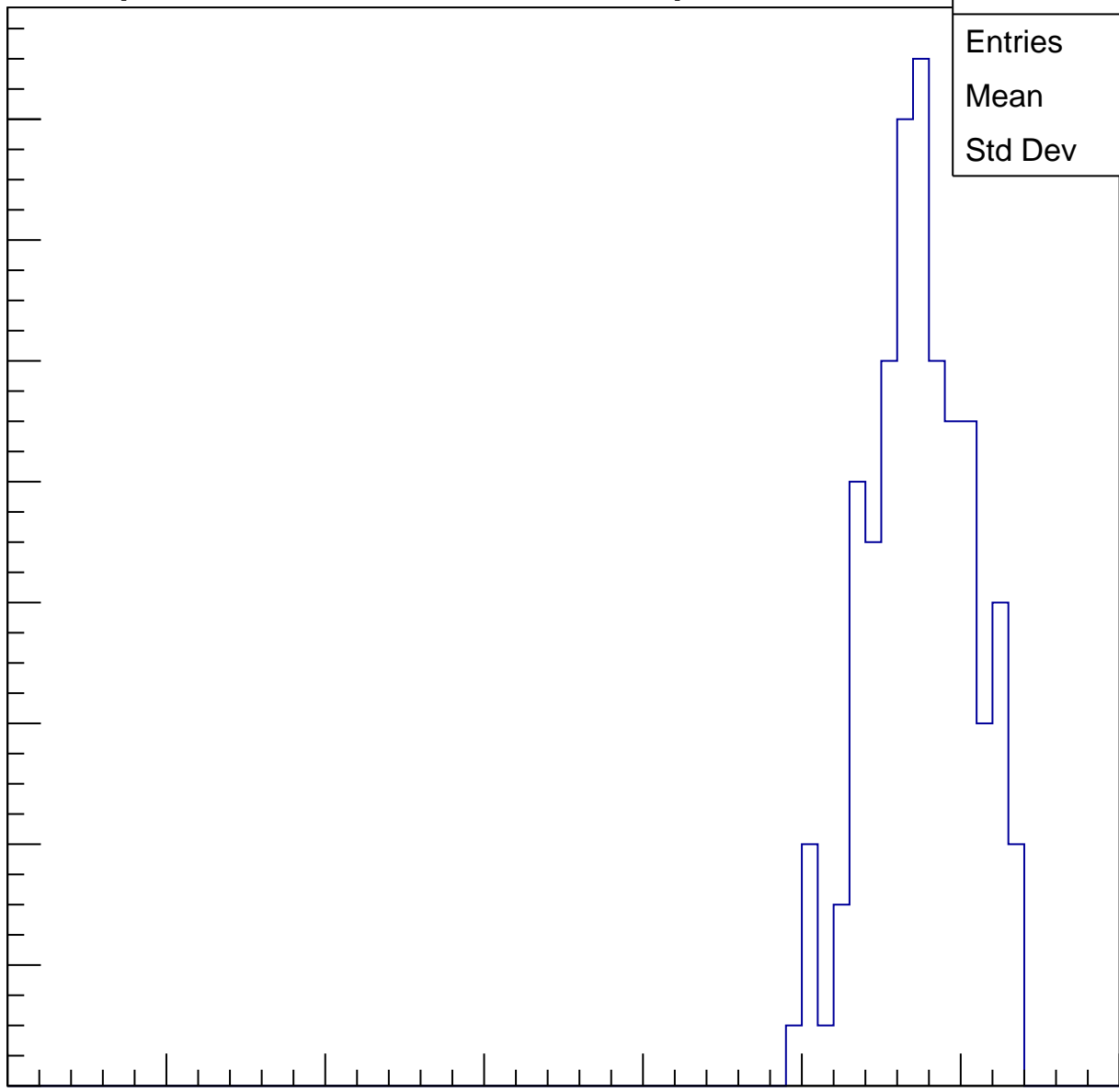
| | |
|---------|-------|
| Entries | 125 |
| Mean | 56.93 |
| Std Dev | 3.188 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

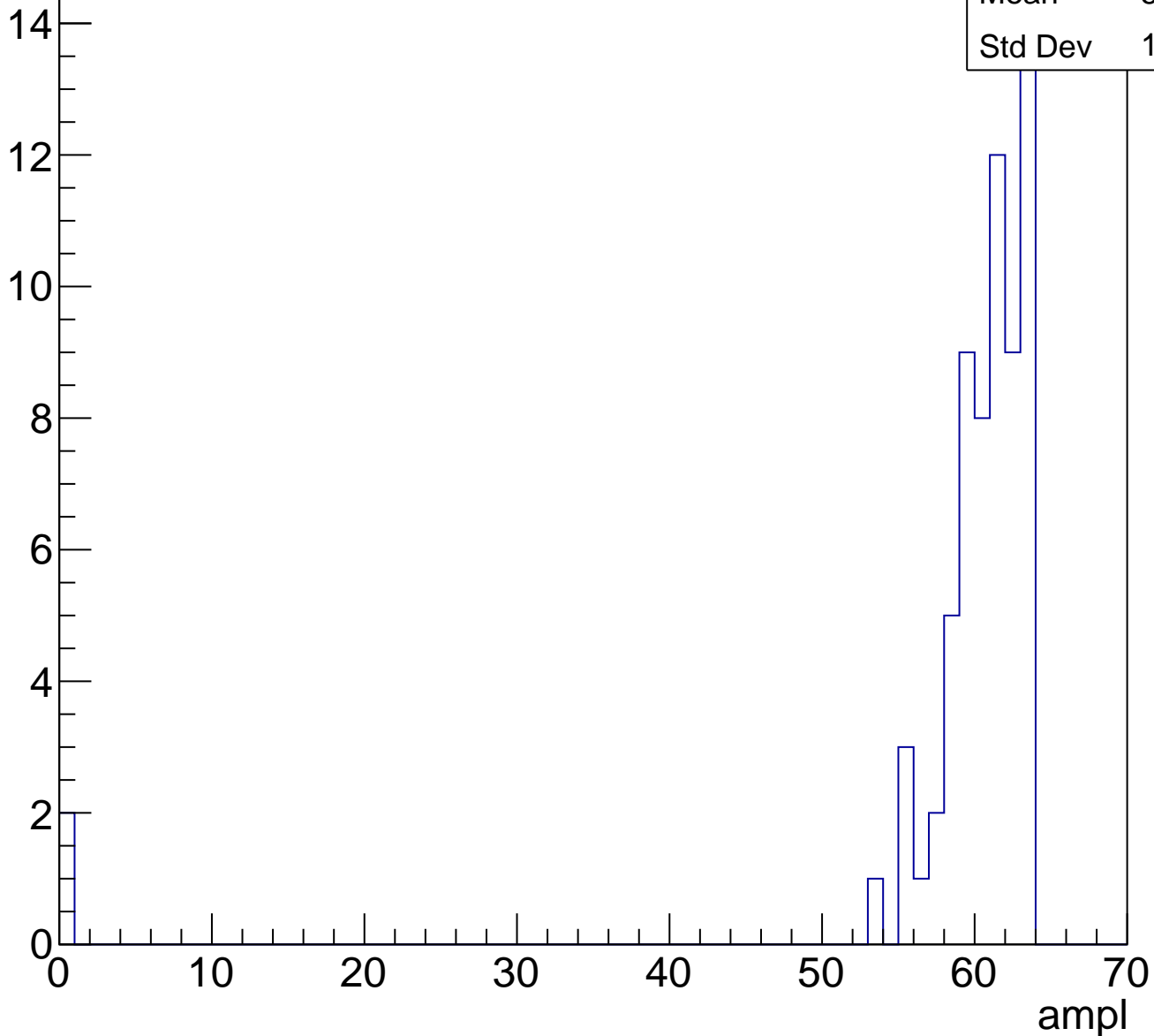
ampl



B0L101S, U22-ch0, adc5

calib_packv5_042523_0143.root, FC#1, port C1

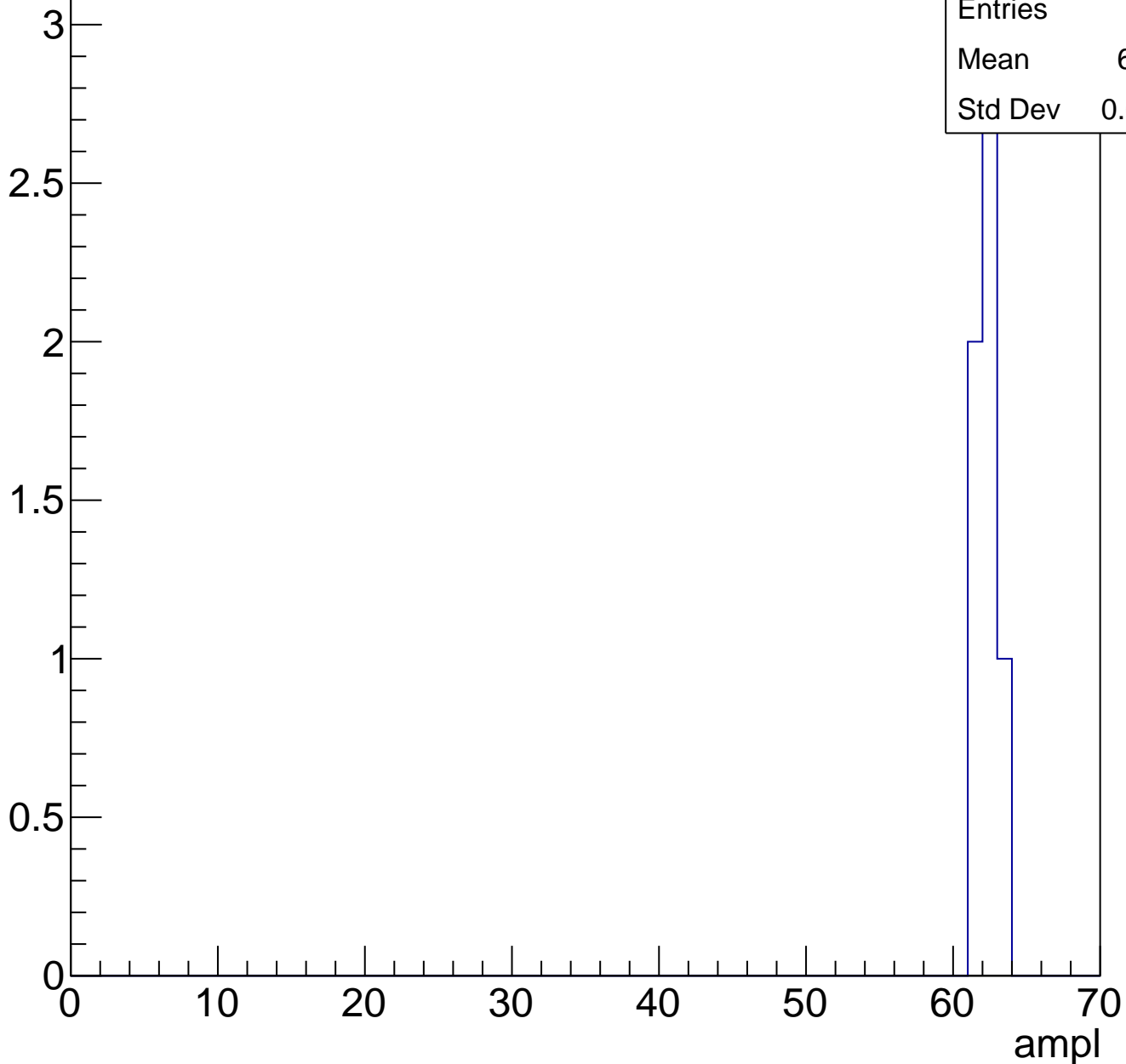
Entry



B0L101S, U22-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

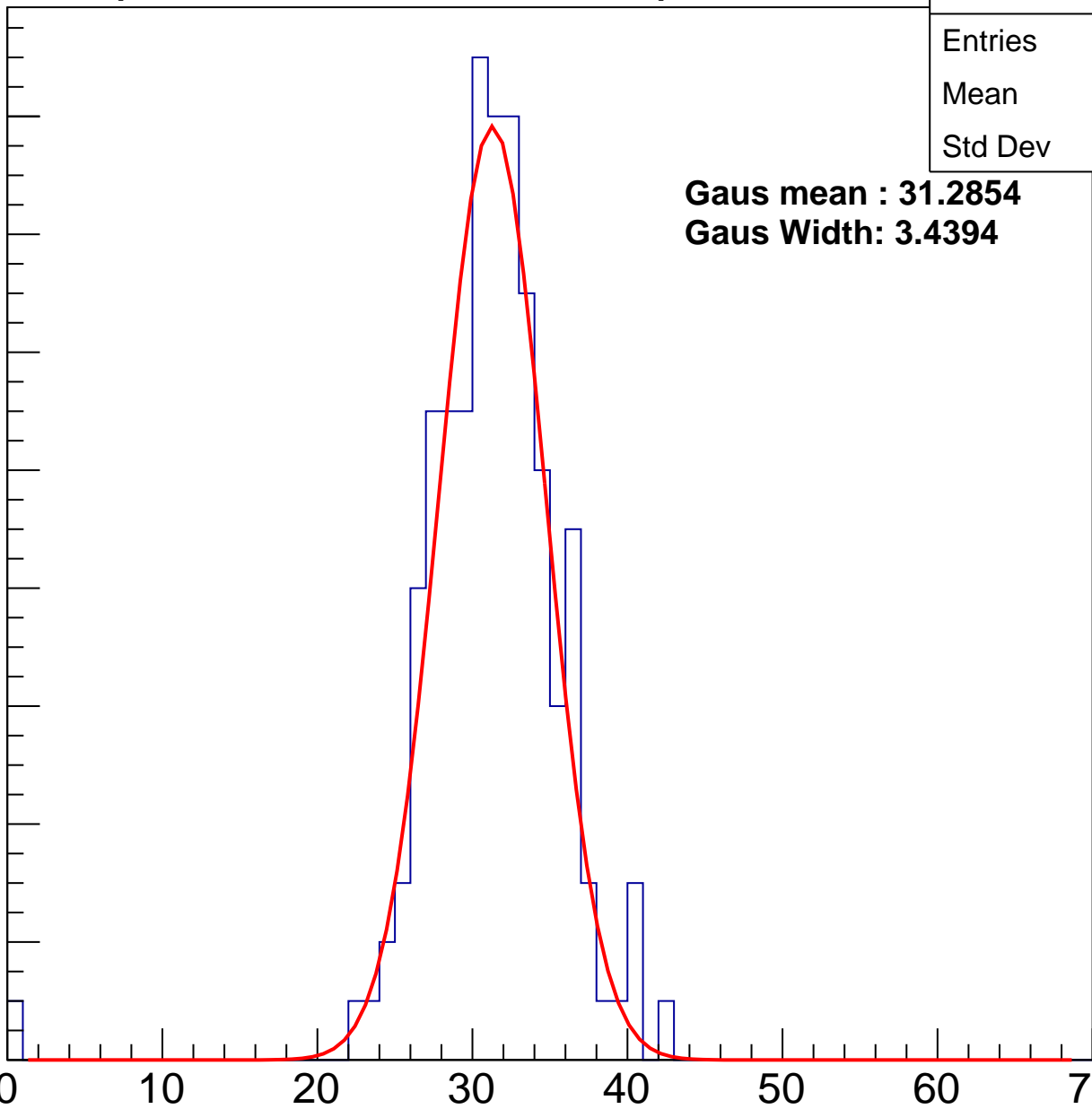
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 145 |
| Mean | 30.83 |
| Std Dev | 4.478 |

Gaus mean : 31.2854

Gaus Width: 3.4394

ampl



B0L101S, U22-ch1, adc1

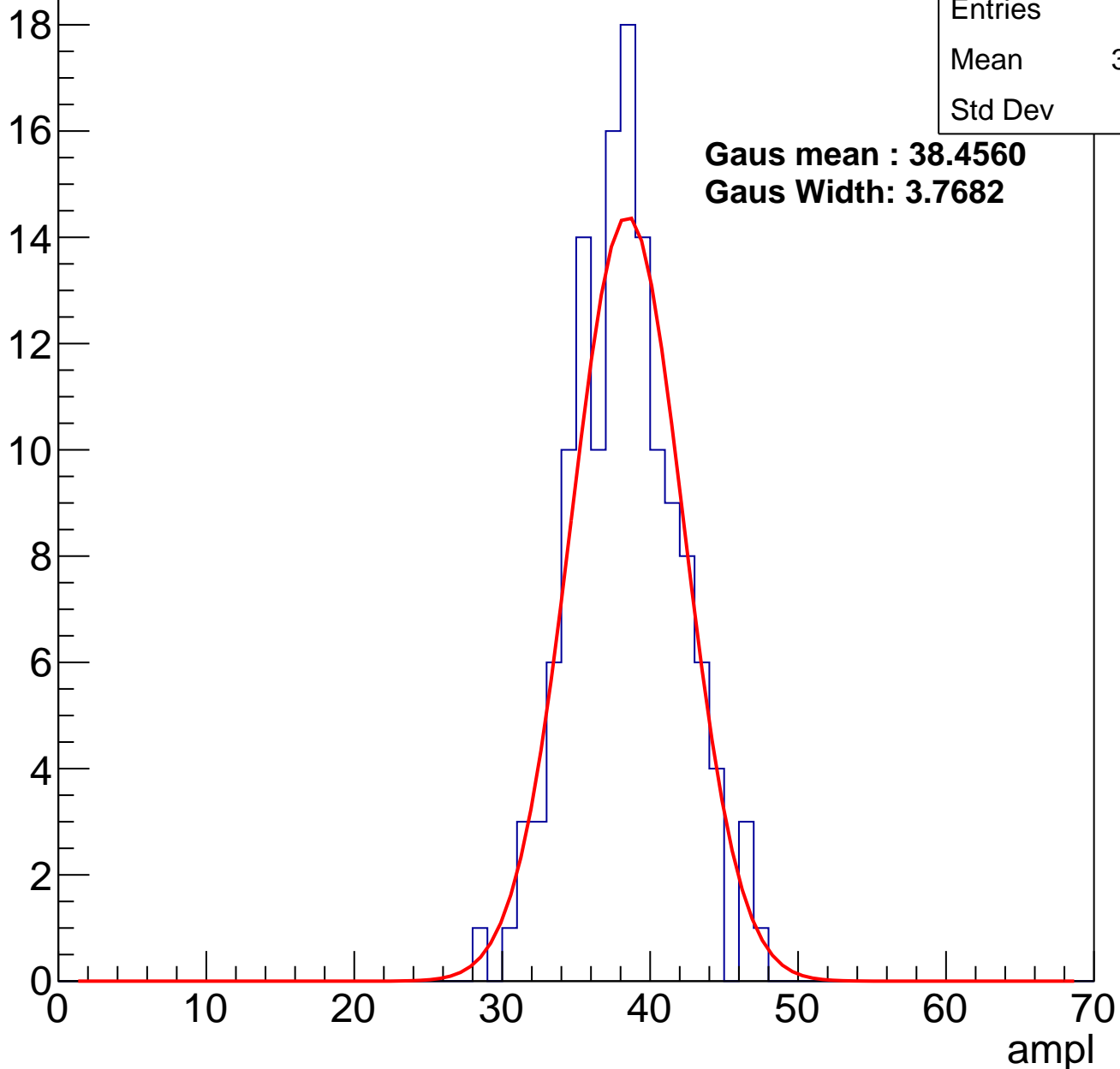
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 37.82 |
| Std Dev | 3.56 |

Gaus mean : 38.4560

Gaus Width: 3.7682

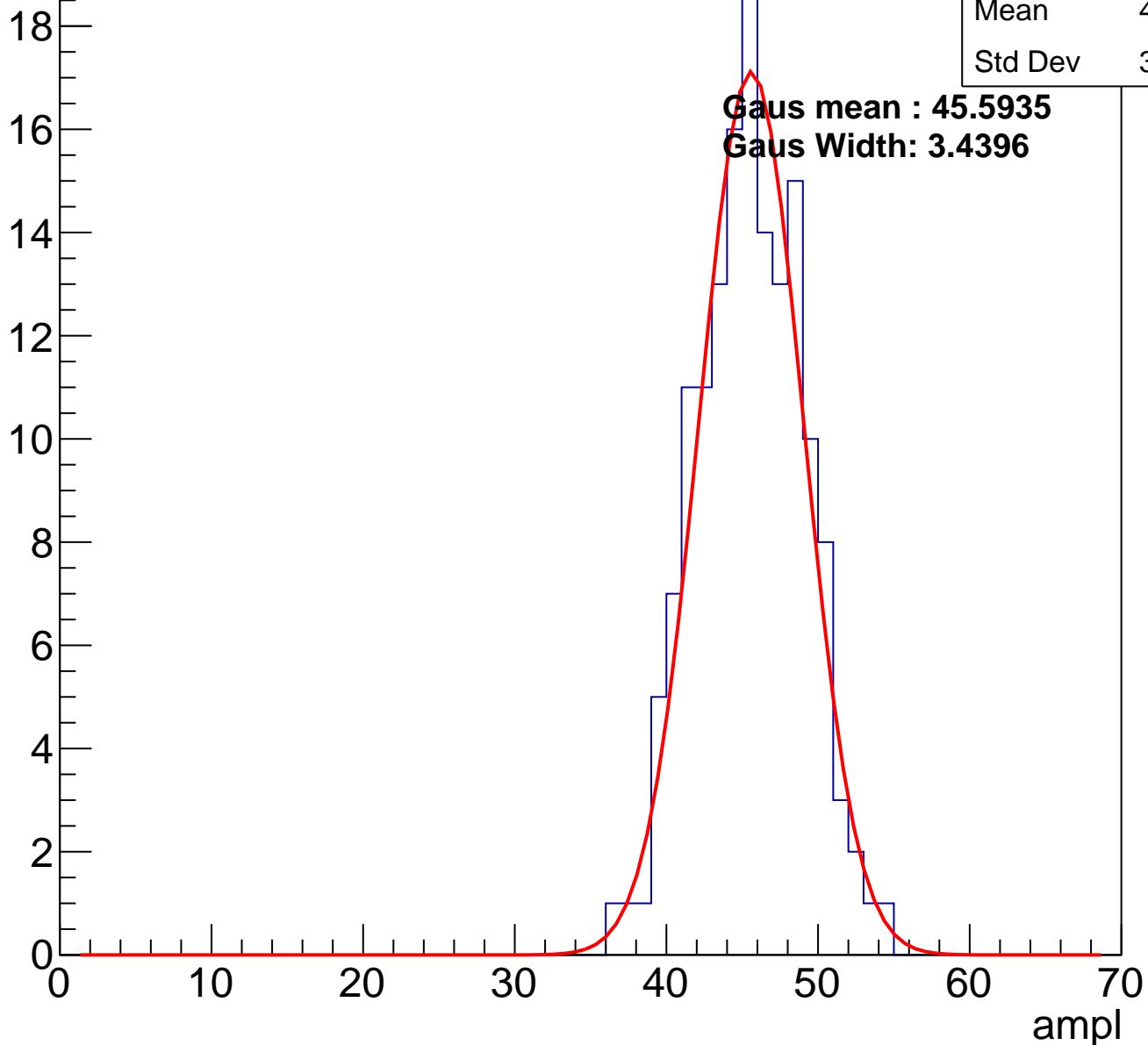
Entry



B0L101S, U22-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 152 |
| Mean | 45.04 |
| Std Dev | 3.445 |

B0L101S, U22-ch1, adc3

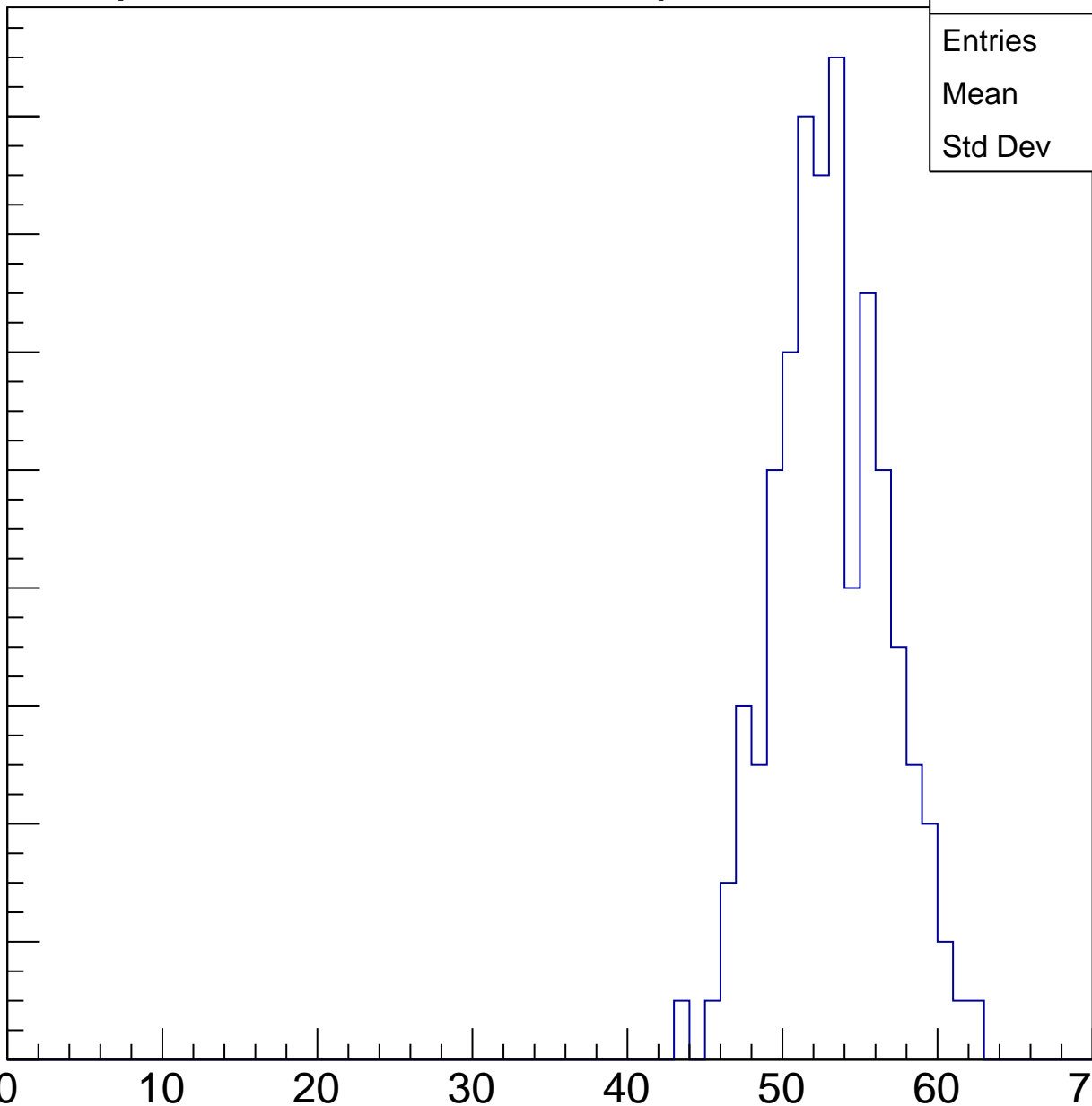
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 137 |
| Mean | 52.63 |
| Std Dev | 3.578 |

ampl

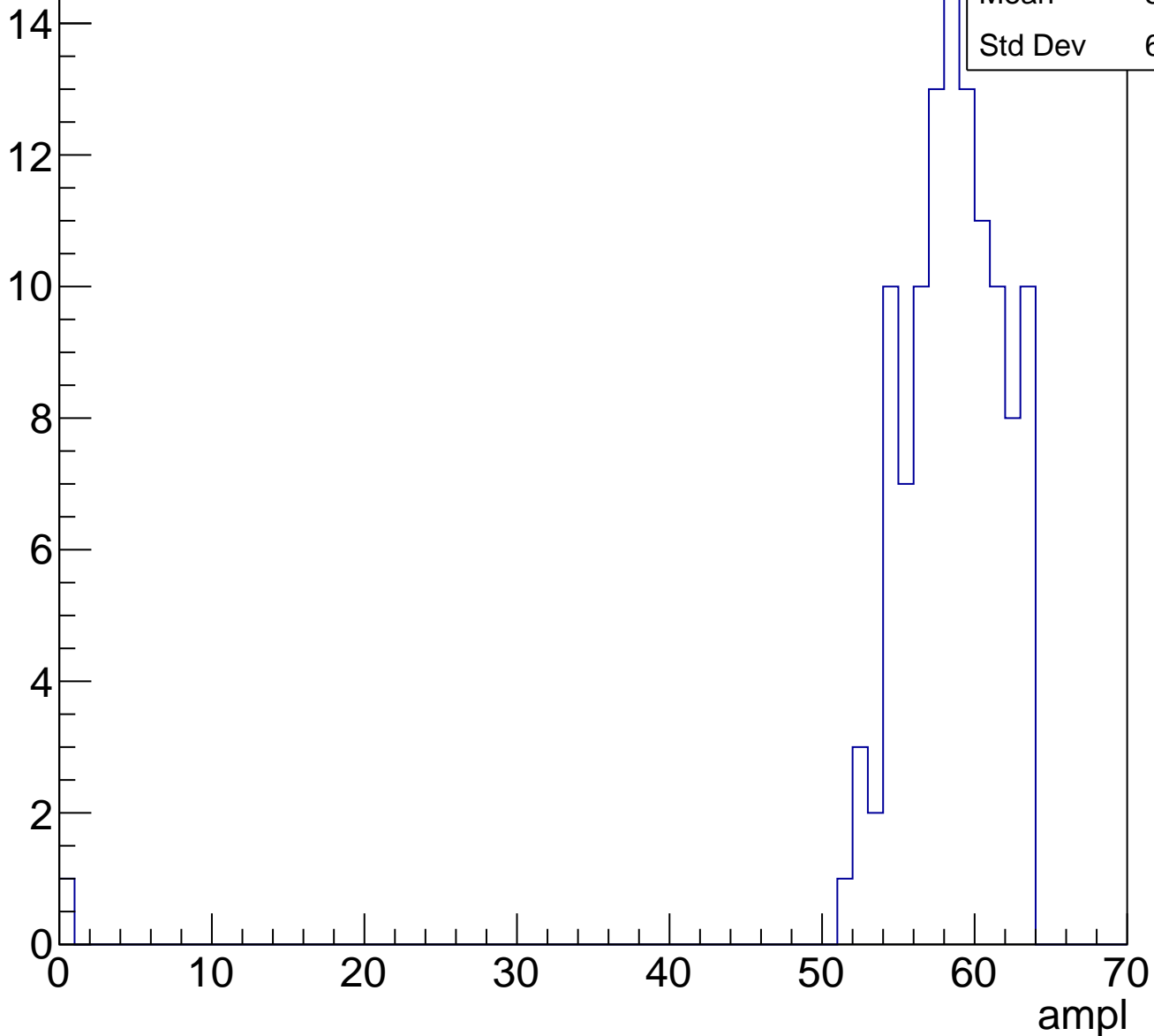


B0L101S, U22-ch1, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 114 |
| Mean | 57.65 |
| Std Dev | 6.183 |

Entry



B0L101S, U22-ch1, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.65 |
| Std Dev | 9.694 |

0

B0L101S, U22-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 30.01 |
| Std Dev | 5.165 |

Gaus mean : 30.8559

Gaus Width: 3.8157

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

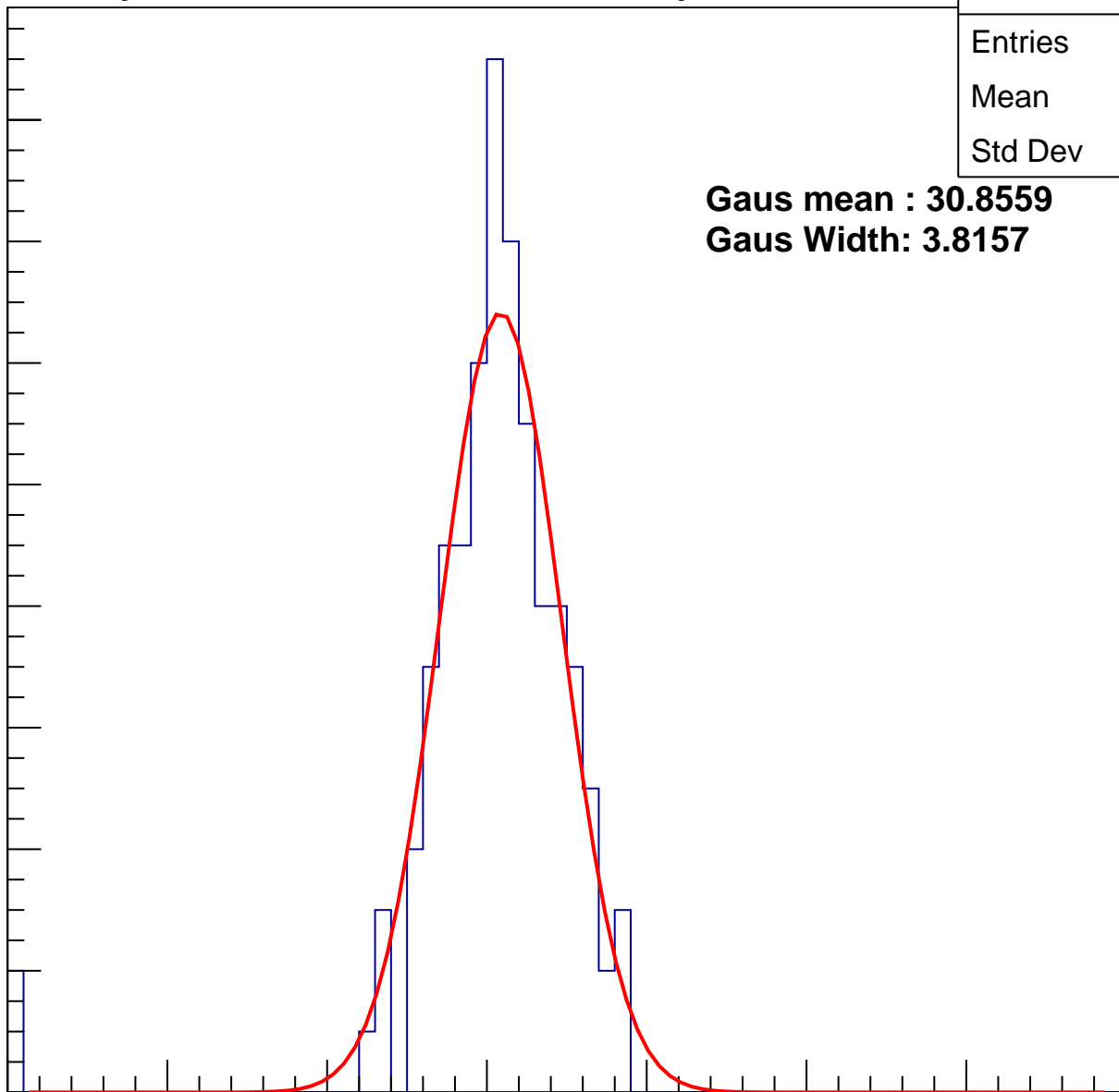
40

50

60

70

ampl



B0L101S, U22-ch2, adc1

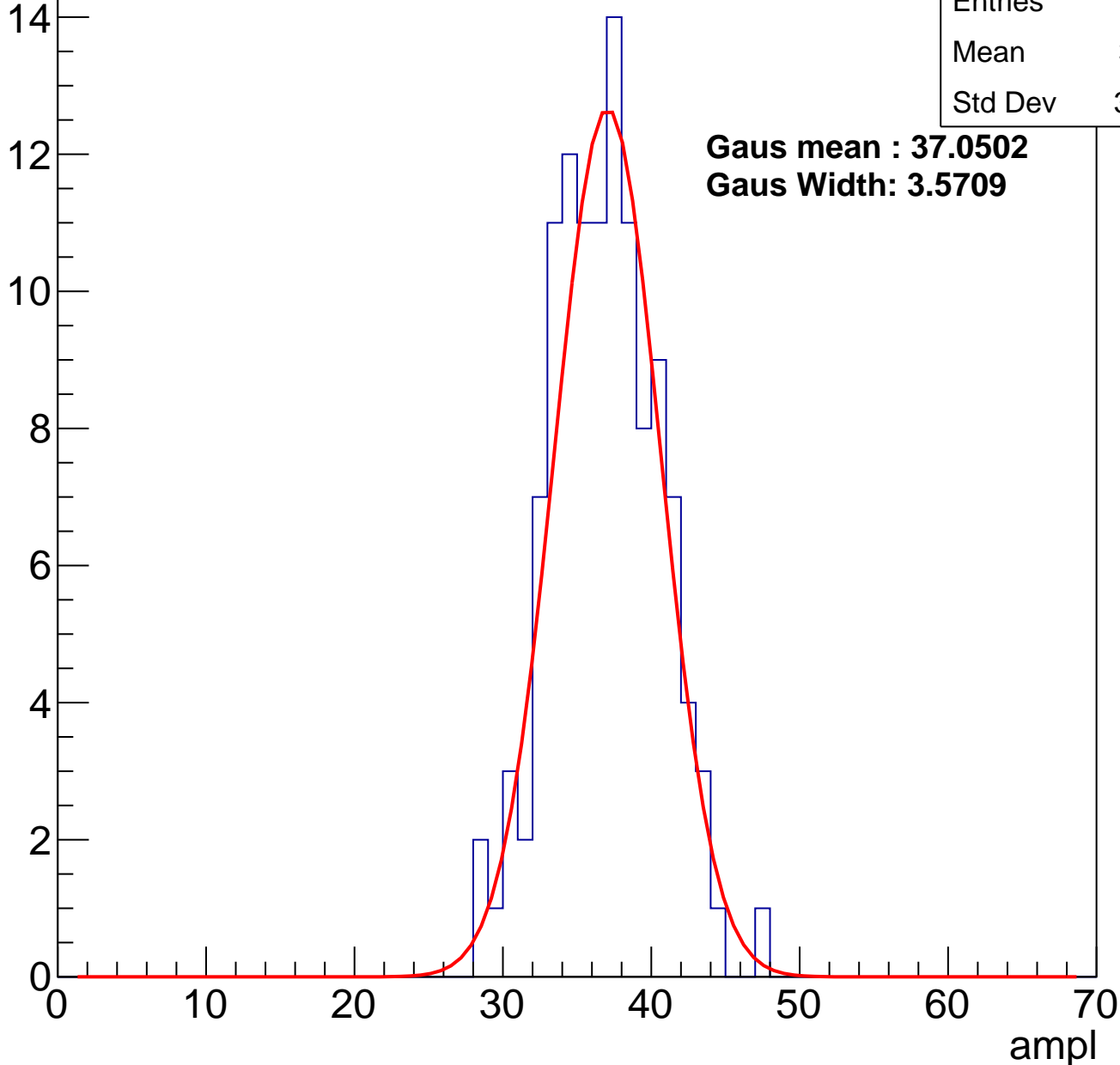
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 118 |
| Mean | 36.41 |
| Std Dev | 3.578 |

Gaus mean : 37.0502

Gaus Width: 3.5709

Entry



B0L101S, U22-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

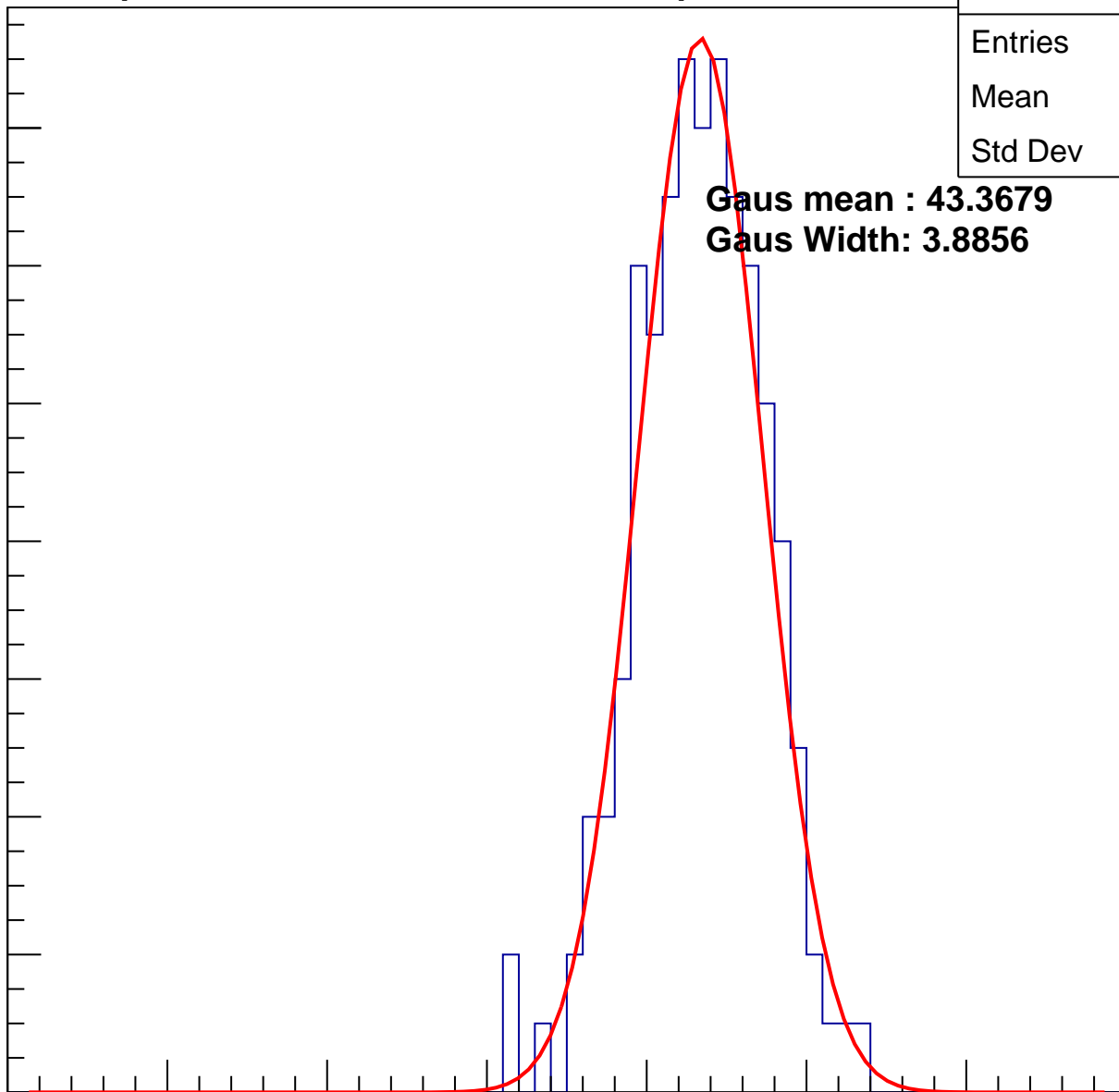
60

ampl

| | |
|---------|-------|
| Entries | 152 |
| Mean | 42.83 |
| Std Dev | 3.988 |

Gaus mean : 43.3679

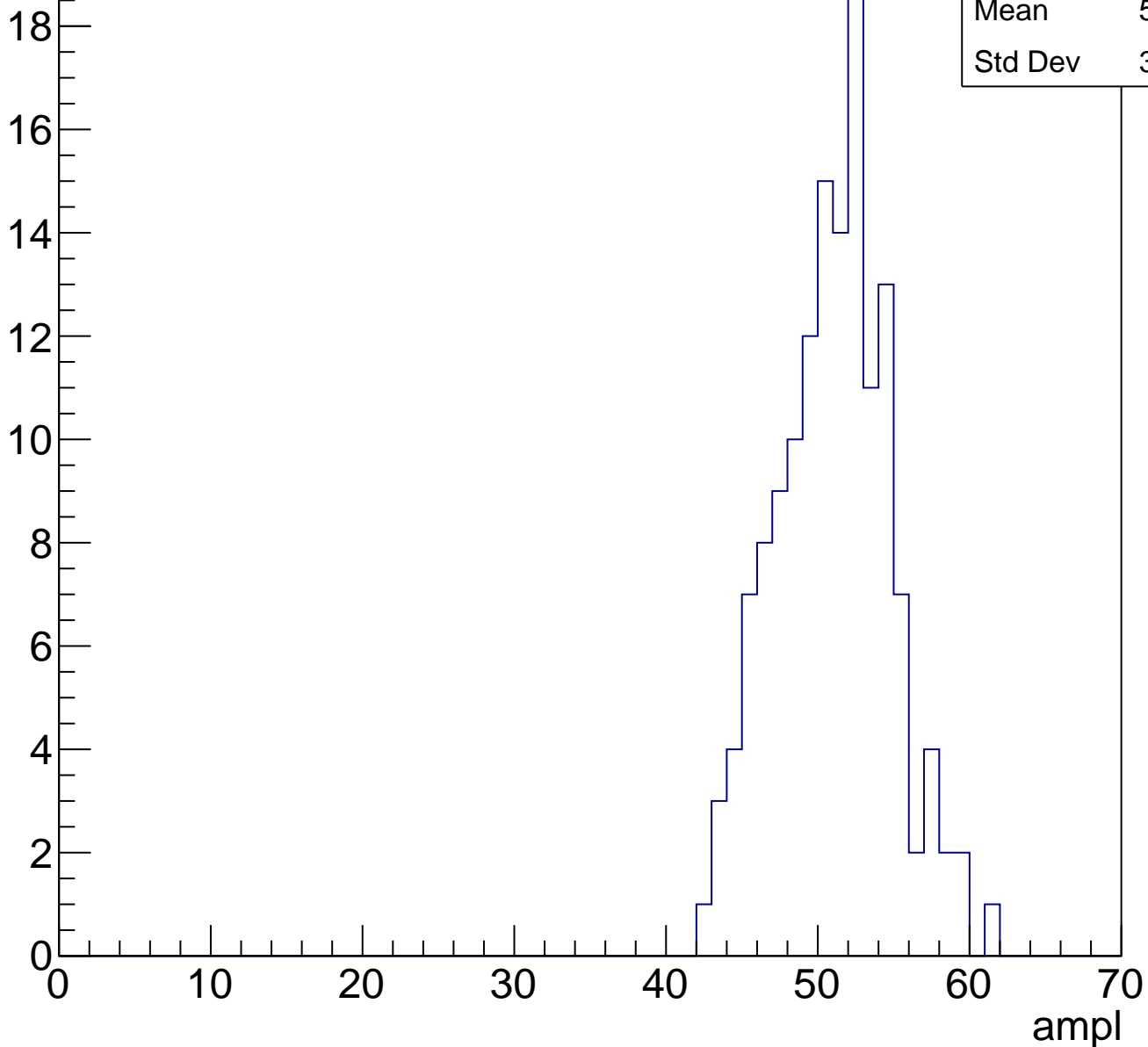
Gaus Width: 3.8856



B0L101S, U22-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 144 |
| Mean | 50.54 |
| Std Dev | 3.727 |

B0L101S, U22-ch2, adc4

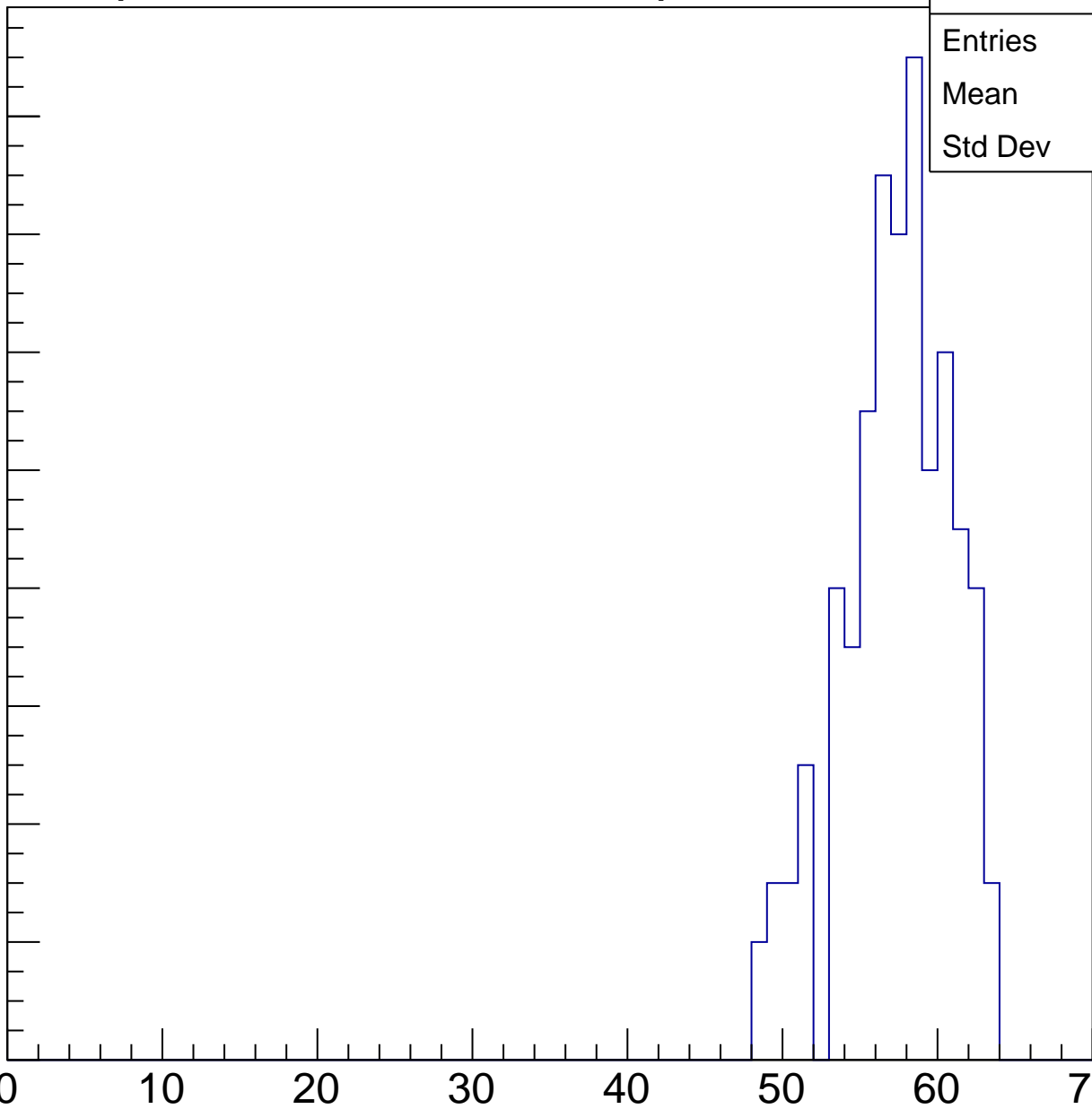
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 127 |
| Mean | 56.87 |
| Std Dev | 3.489 |

ampl



B0L101S, U22-ch2, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.49 |
| Std Dev | 11.17 |

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B0L101S, U22-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch3, adc0

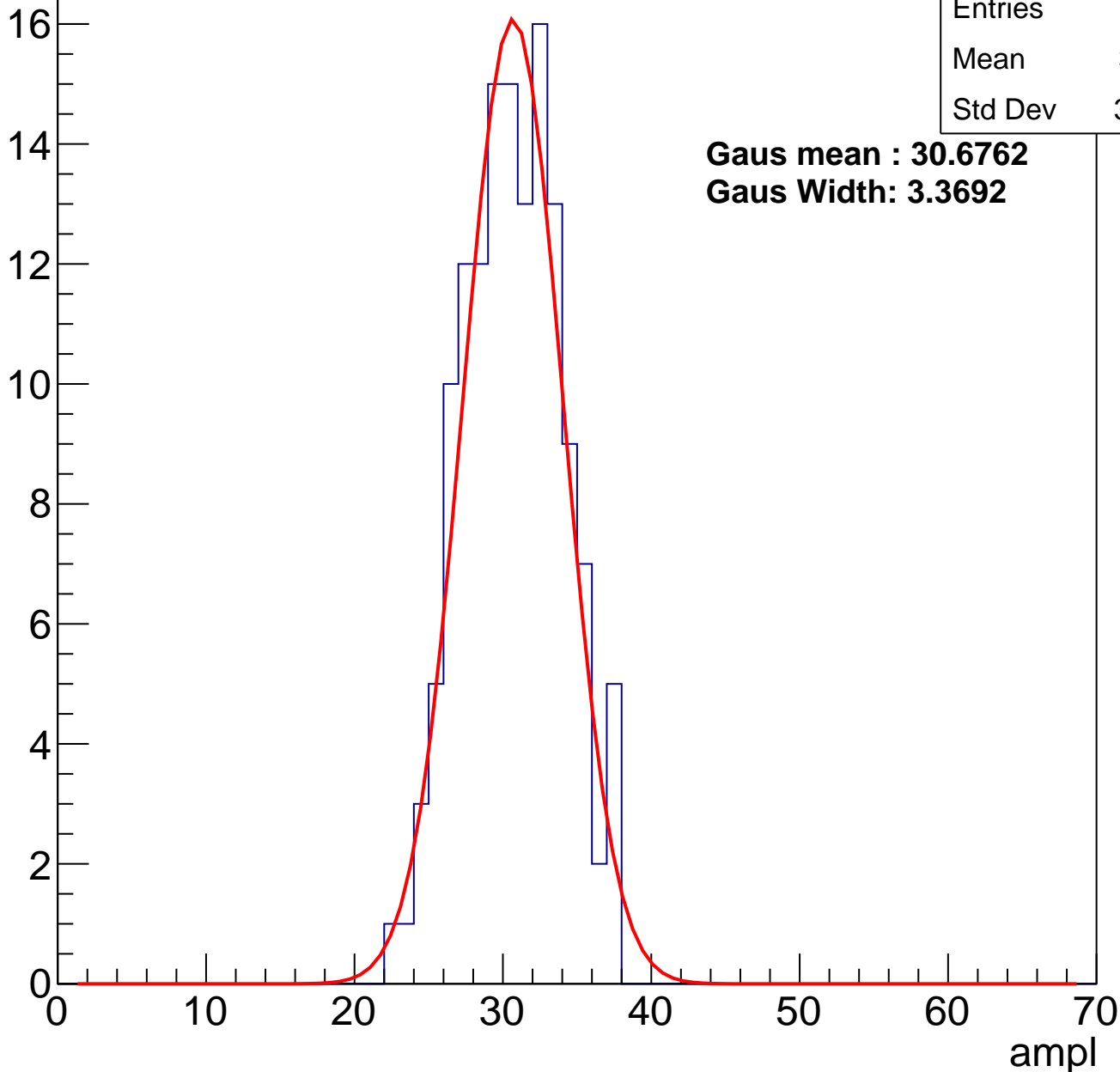
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 30.21 |
| Std Dev | 3.286 |

Gaus mean : 30.6762

Gaus Width: 3.3692

Entry



B0L101S, U22-ch3, adc1

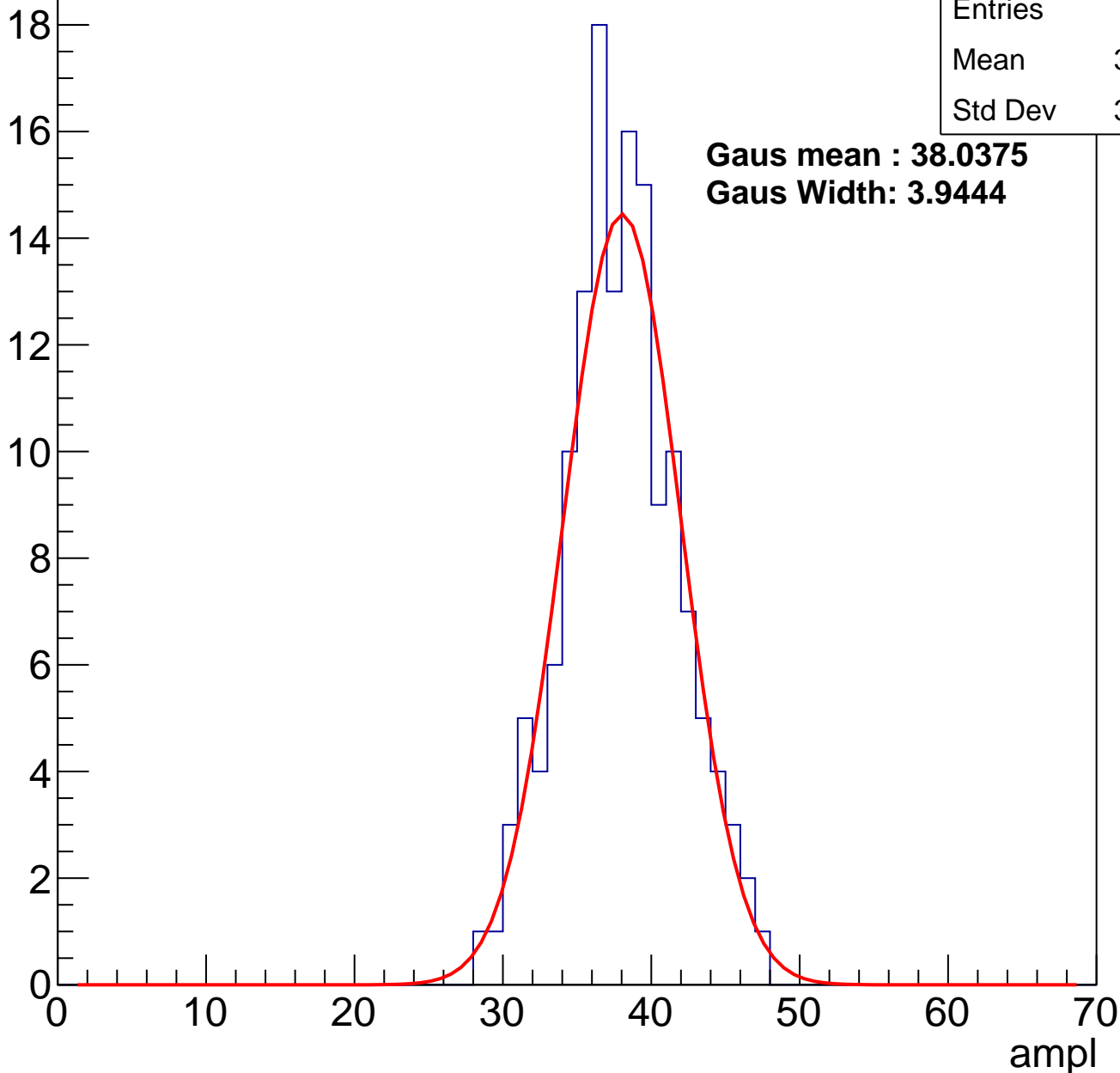
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 37.49 |
| Std Dev | 3.806 |

Gaus mean : 38.0375

Gaus Width: 3.9444

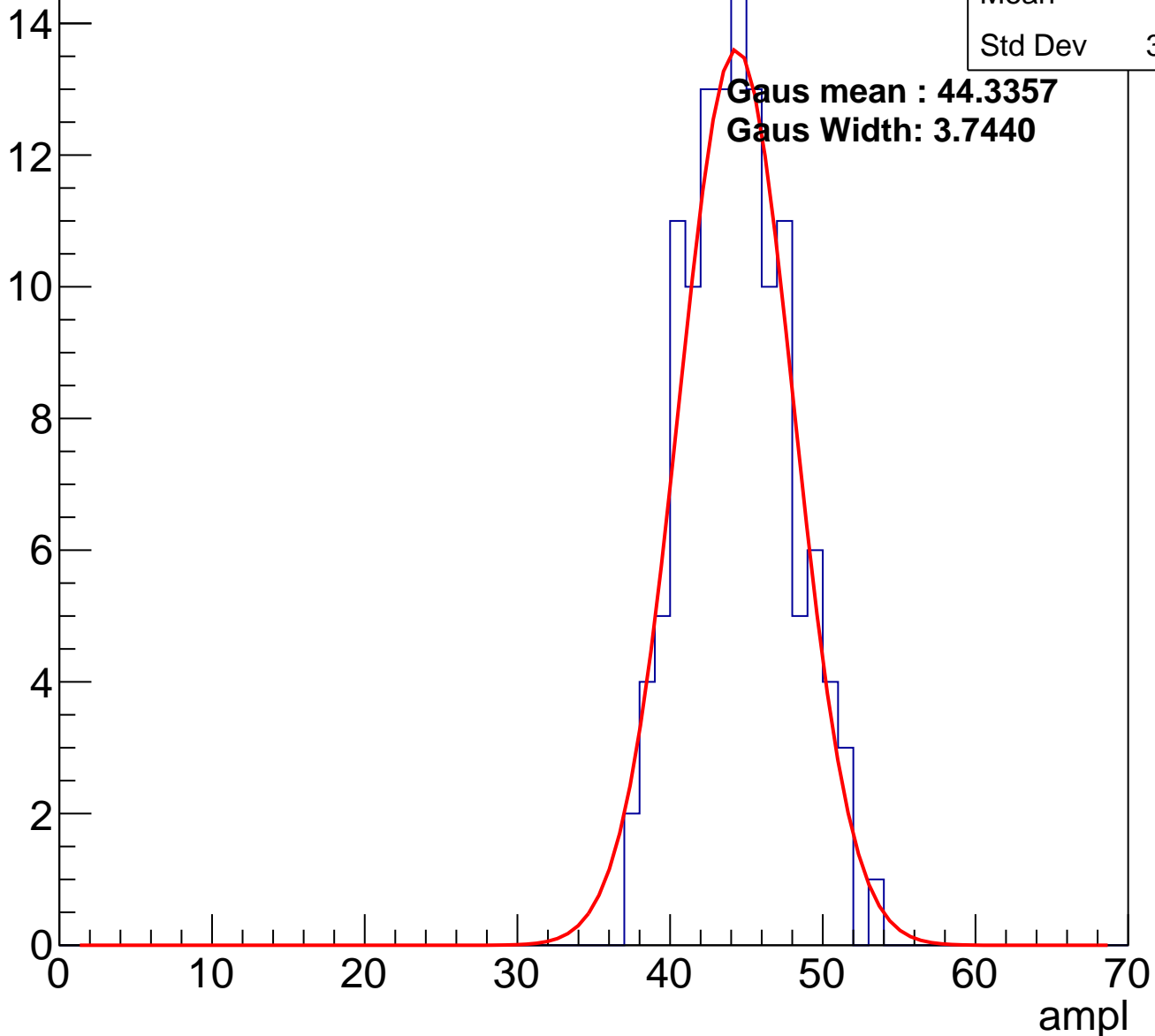
Entry



B0L101S, U22-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

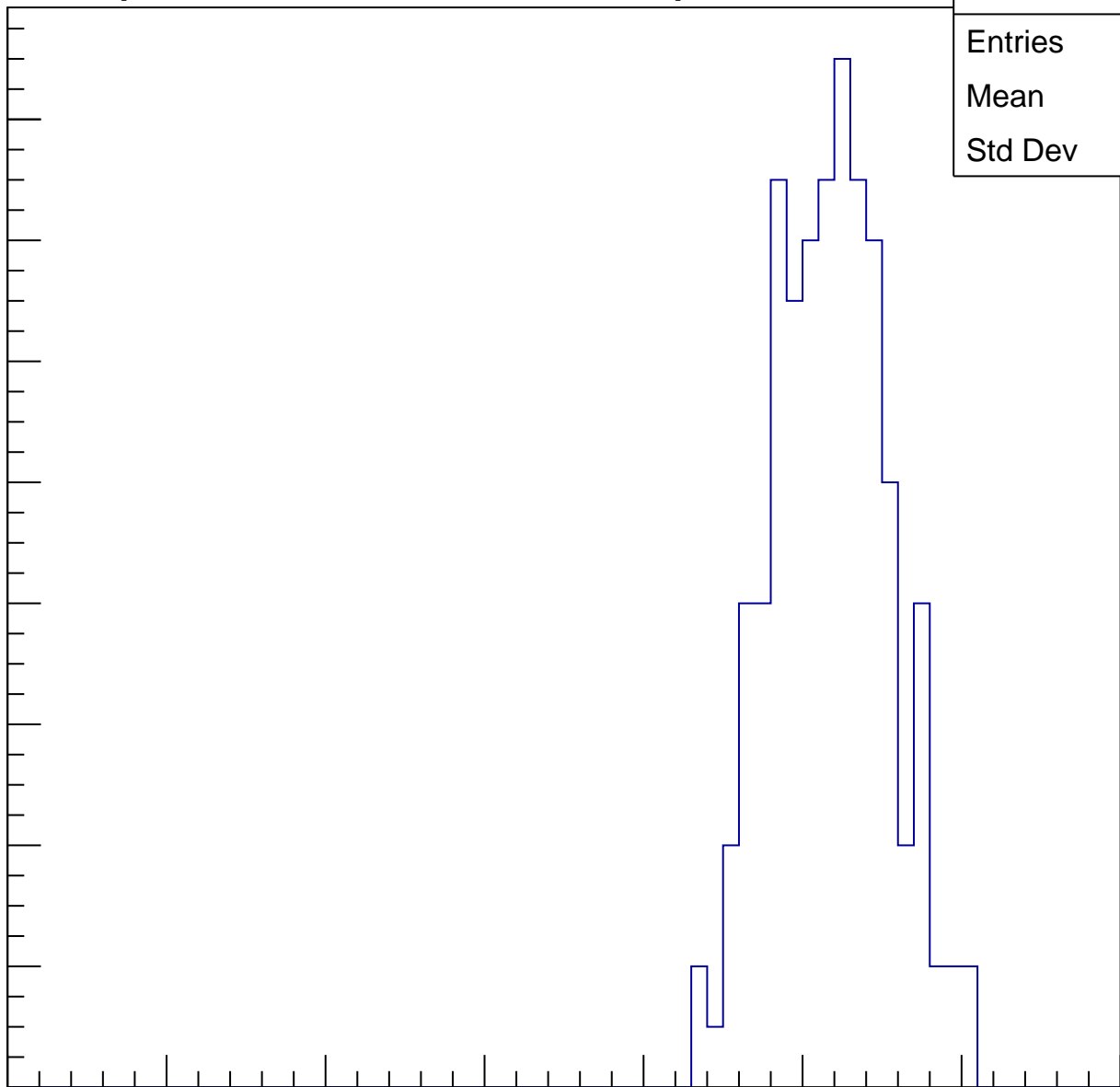
| | |
|---------|-------|
| Entries | 154 |
| Mean | 51.27 |
| Std Dev | 3.584 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch3, adc4

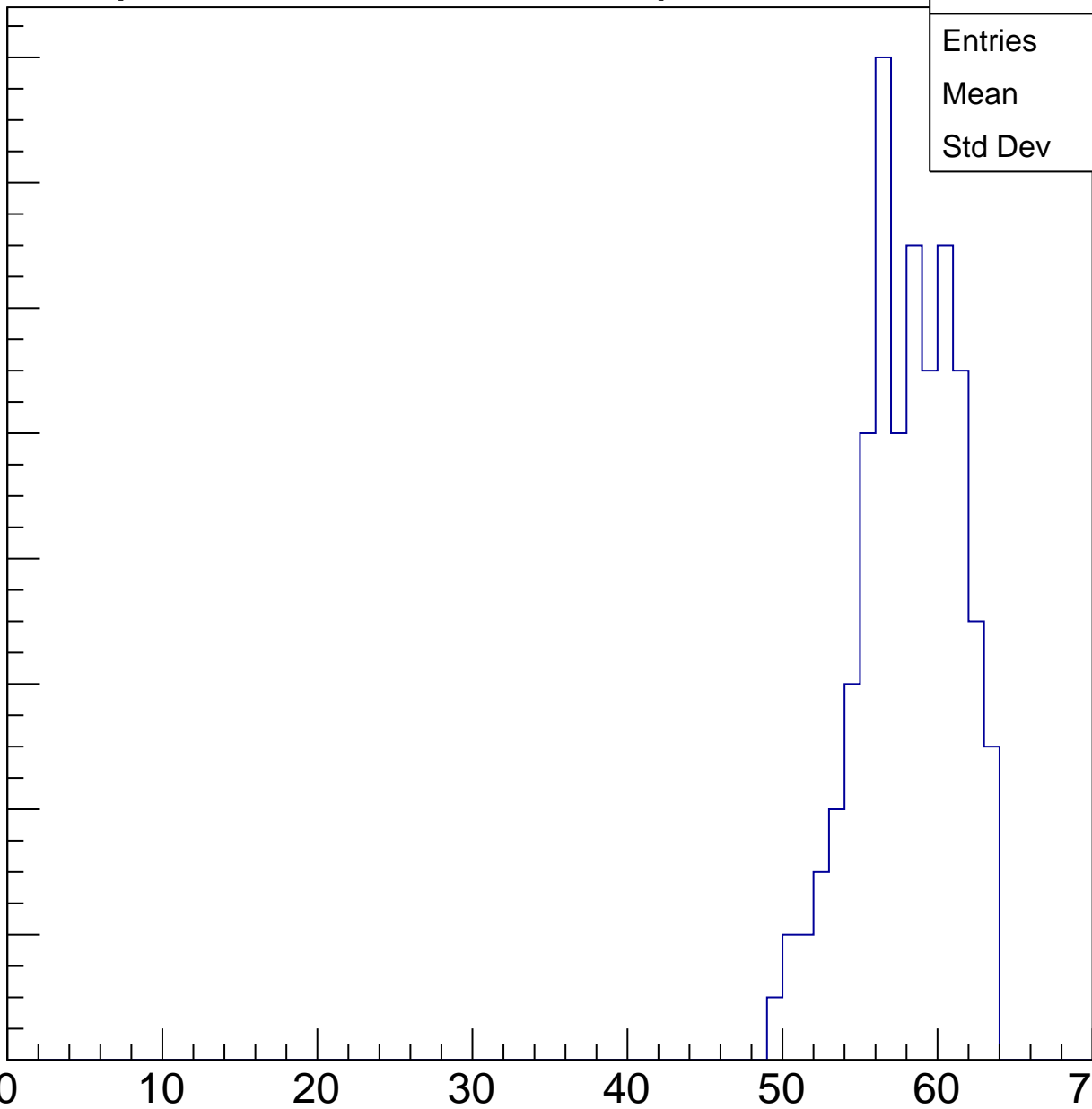
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 114 |
| Mean | 57.56 |
| Std Dev | 3.179 |

ampl



B0L101S, U22-ch3, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.64 |
| Std Dev | 11.75 |

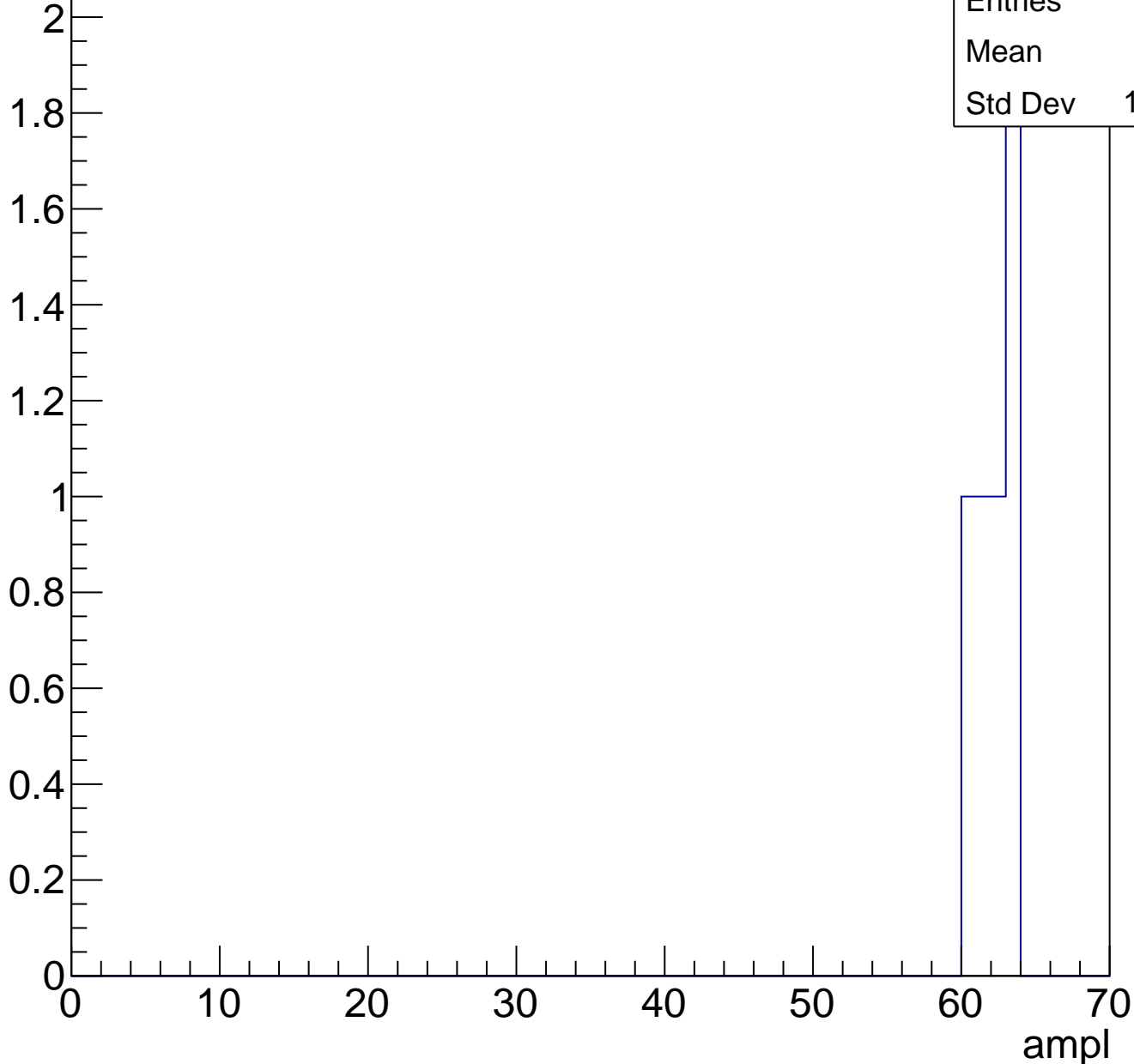
ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch4, adc0

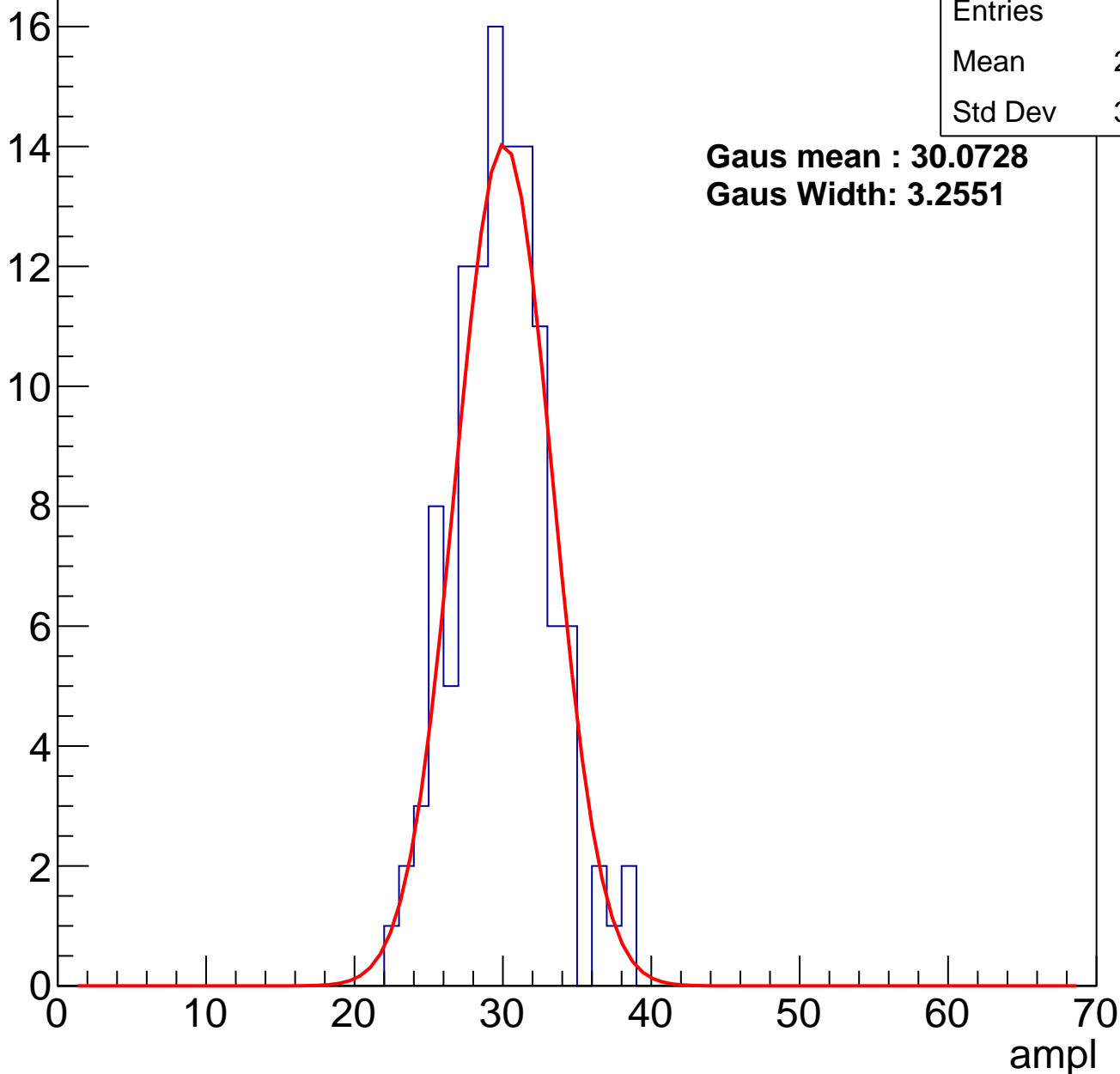
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 115 |
| Mean | 29.45 |
| Std Dev | 3.155 |

Gaus mean : 30.0728

Gaus Width: 3.2551

Entry



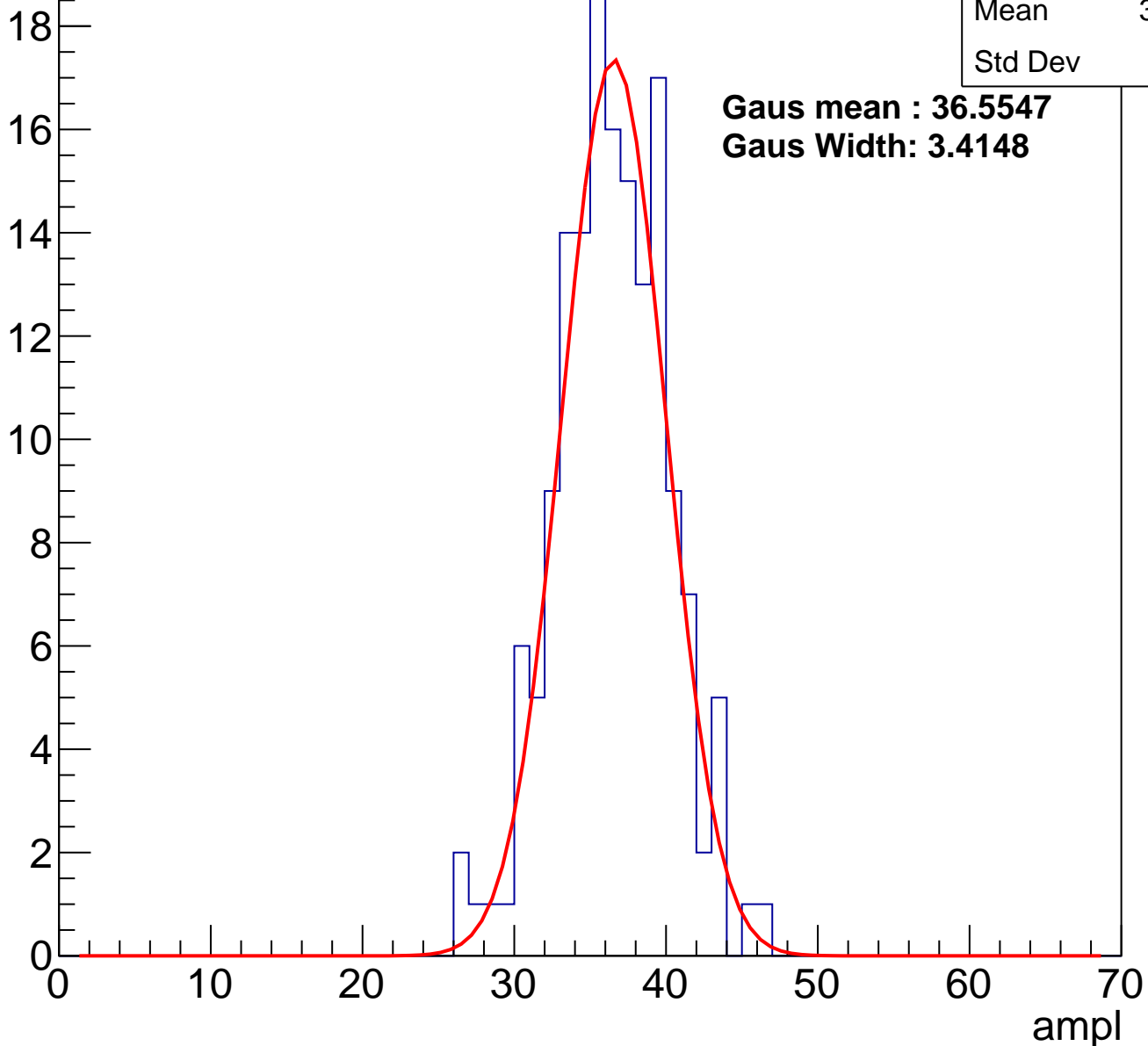
B0L101S, U22-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 35.99 |
| Std Dev | 3.66 |

Gaus mean : 36.5547
Gaus Width: 3.4148

Entry



B0L101S, U22-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

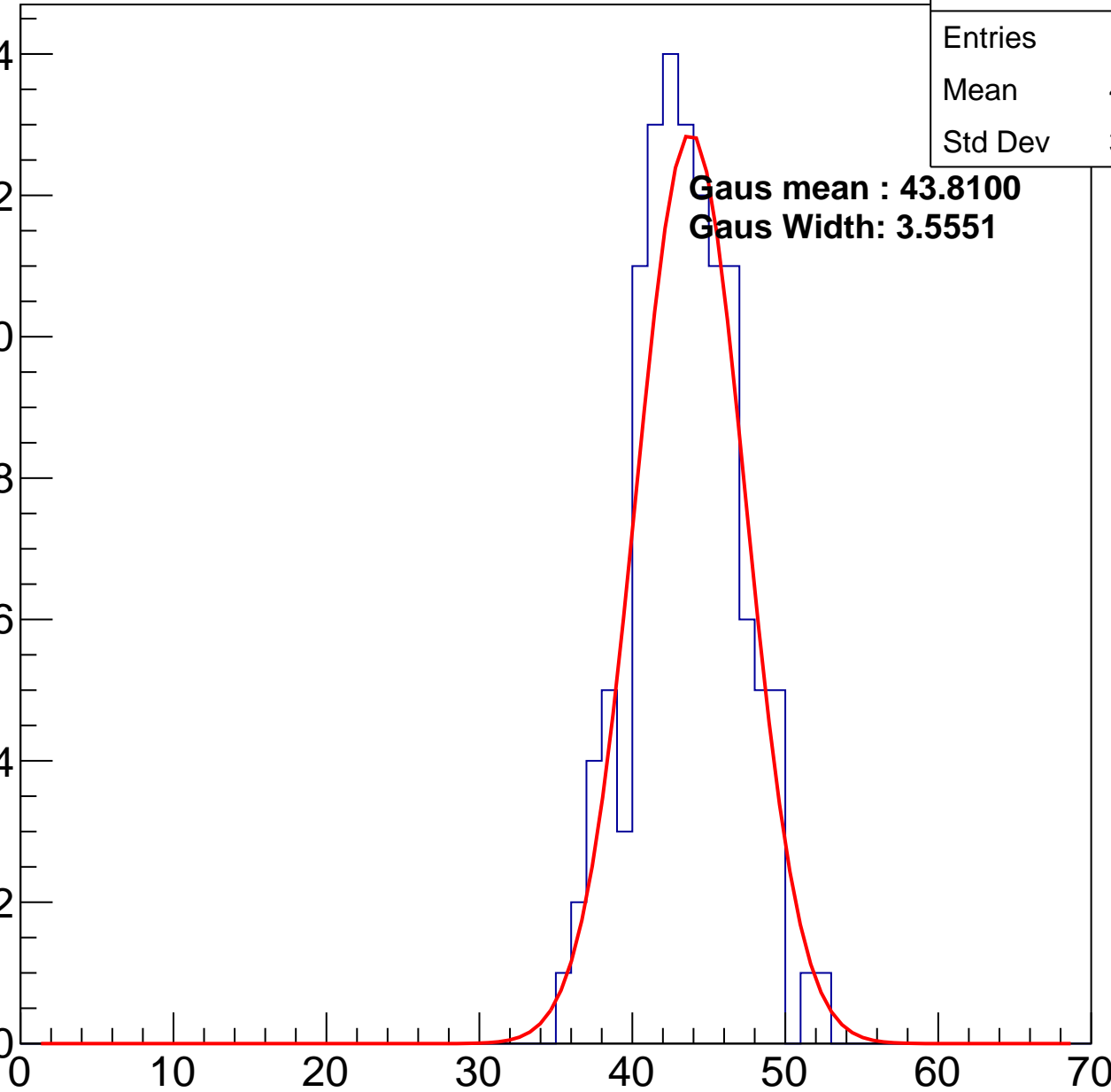
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 118 |
| Mean | 43.06 |
| Std Dev | 3.376 |

Gaus mean : 43.8100

Gaus Width: 3.5551

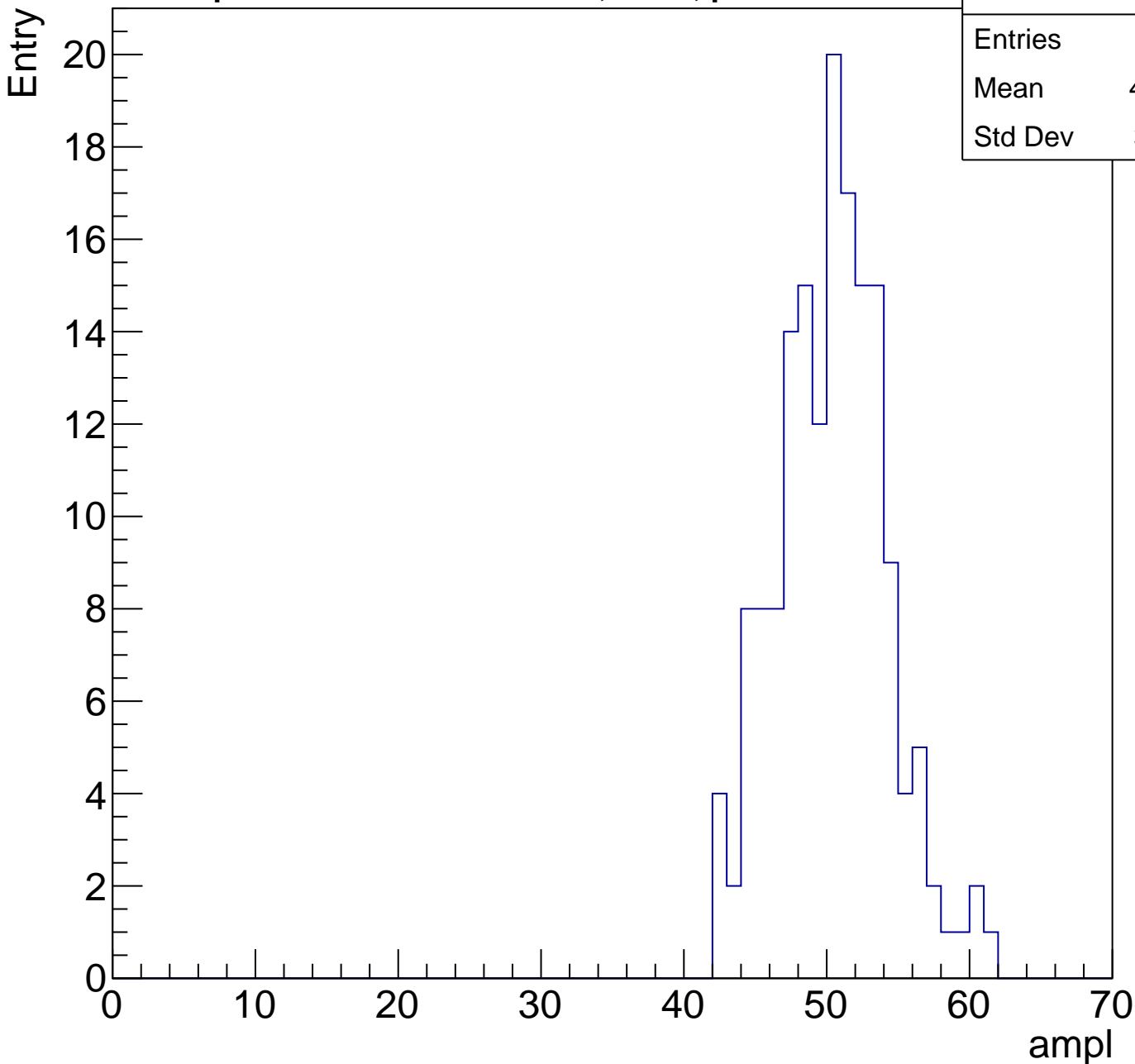
ampl



B0L101S, U22-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 163 |
| Mean | 49.94 |
| Std Dev | 3.811 |



B0L101S, U22-ch4, adc4

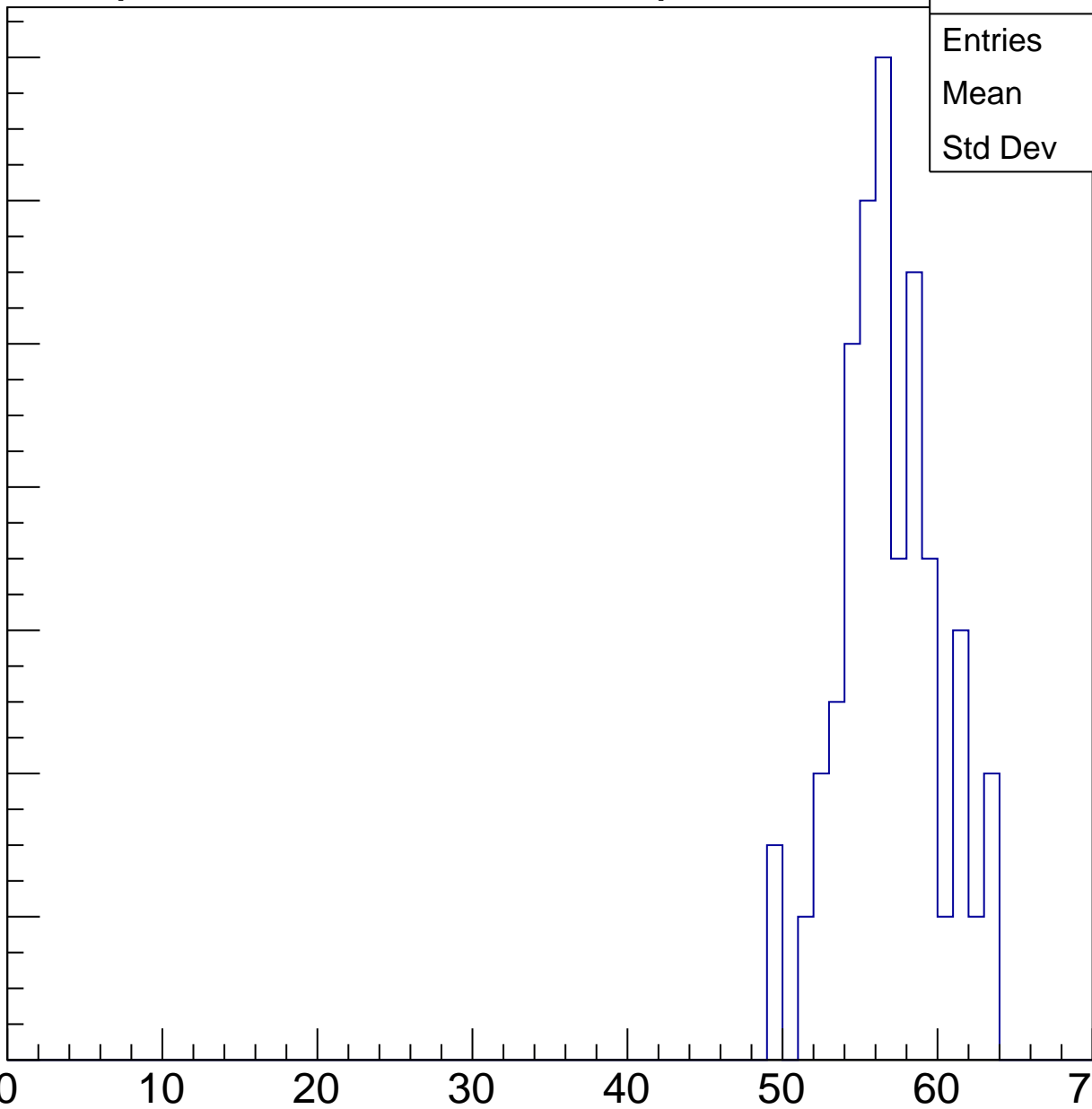
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 56.38 |
| Std Dev | 3.196 |

ampl



B0L101S, U22-ch4, adc5

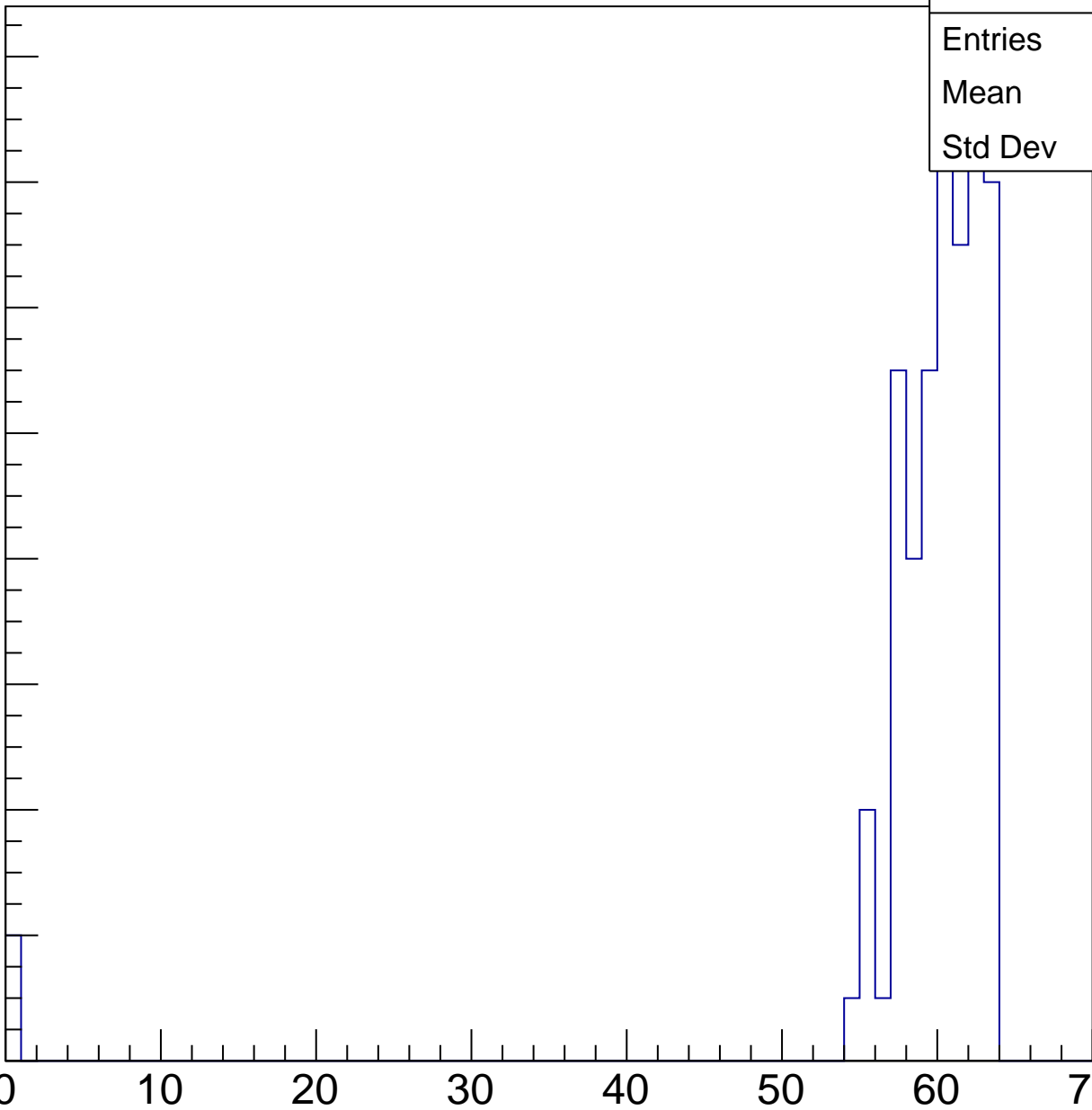
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 96 |
| Mean | 58.7 |
| Std Dev | 8.857 |

ampl



B0L101S, U22-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

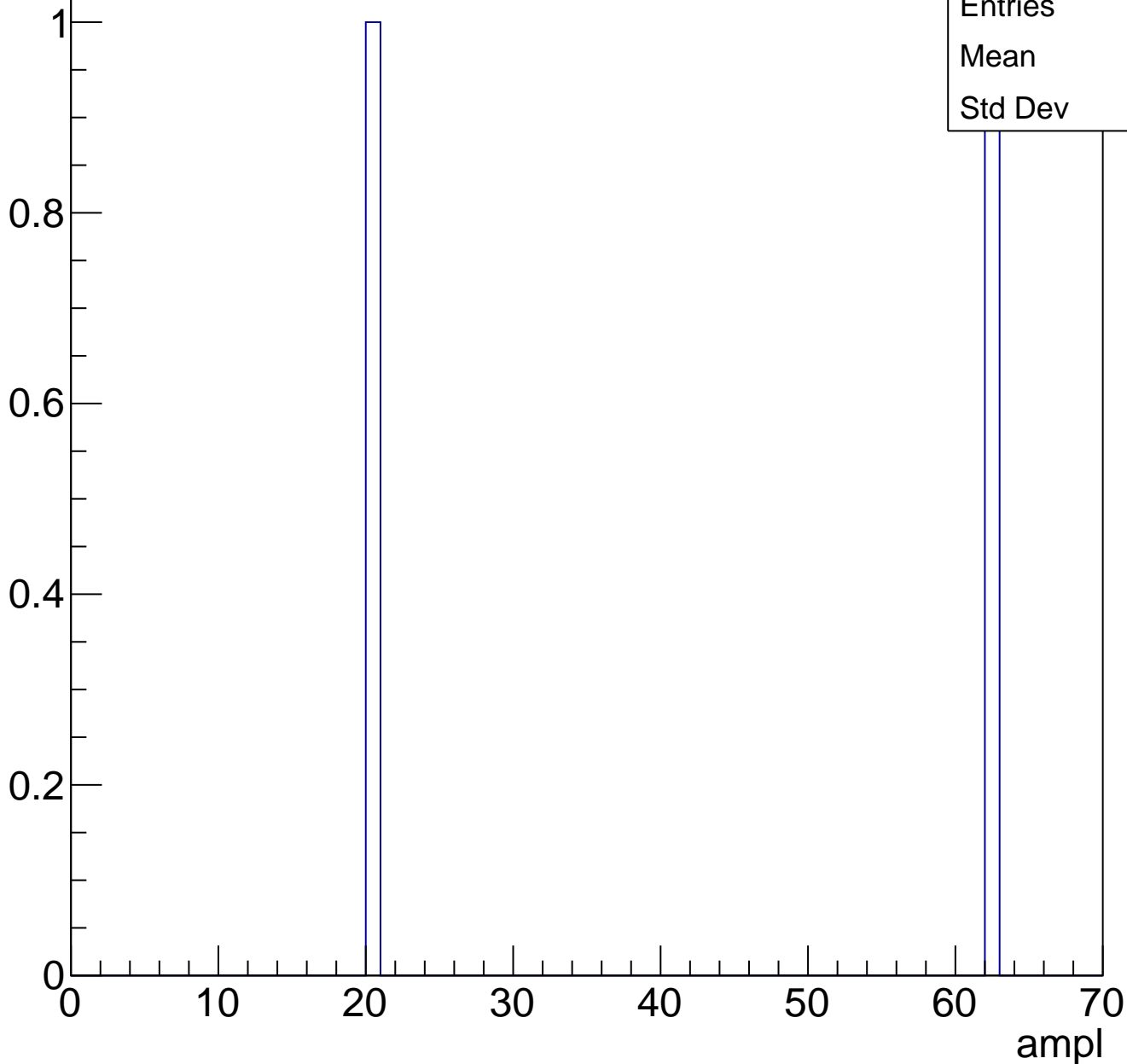
Entry



B0L101S, U22-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

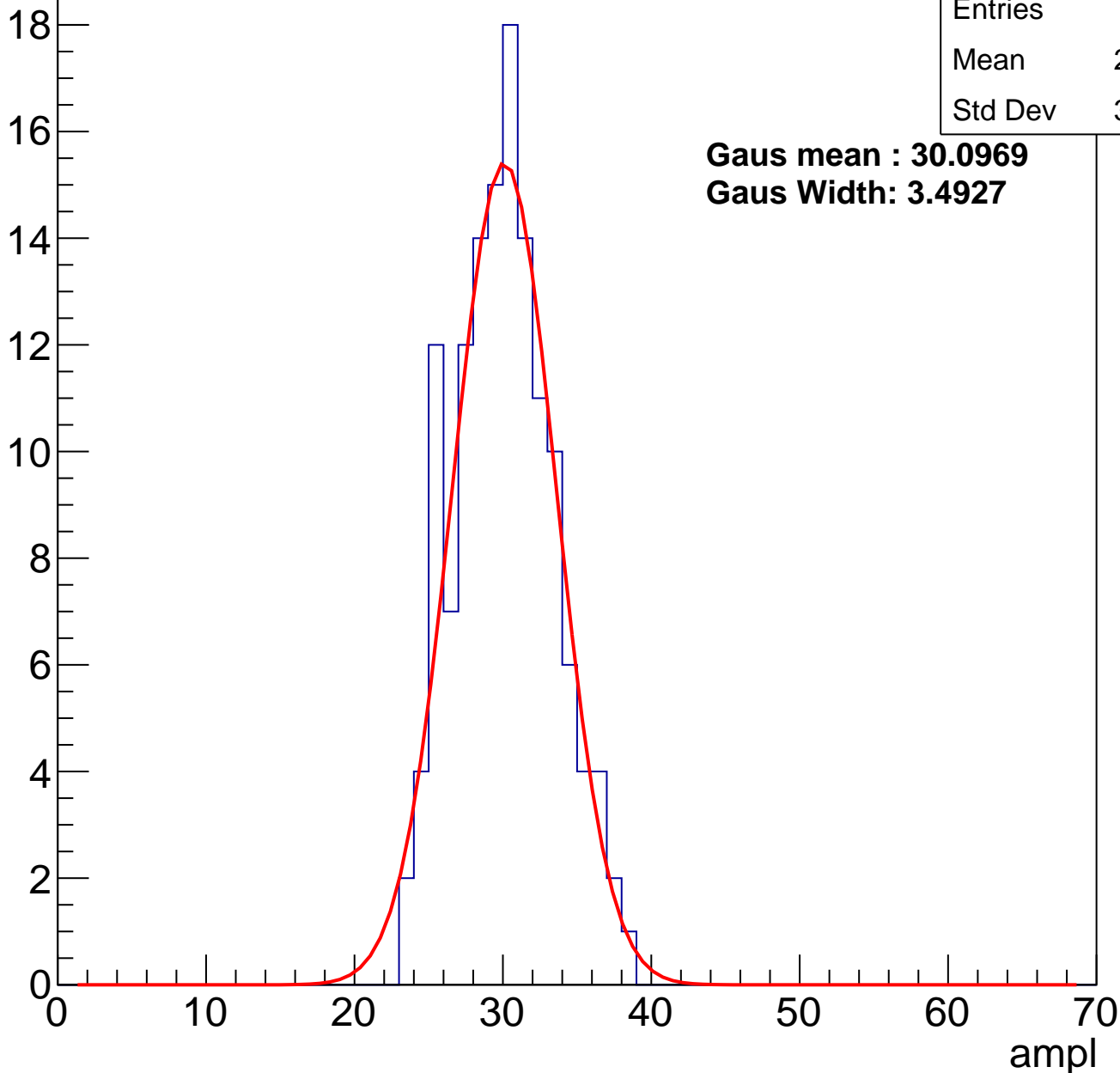
Entry



B0L101S, U22-ch5, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch5, adc1

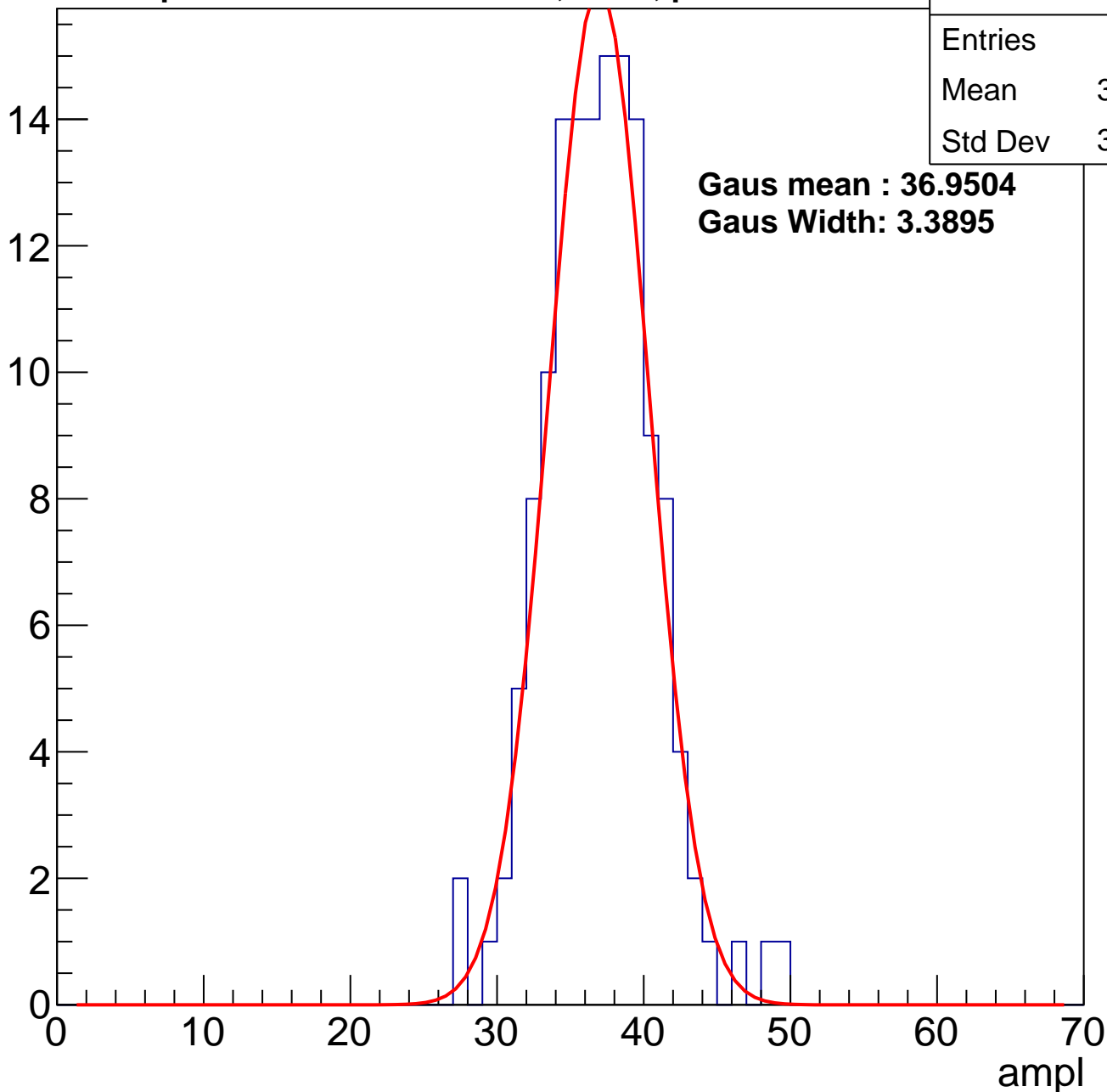
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 36.55 |
| Std Dev | 3.687 |

Gaus mean : 36.9504

Gaus Width: 3.3895

Entry



B0L101S, U22-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

160

Mean

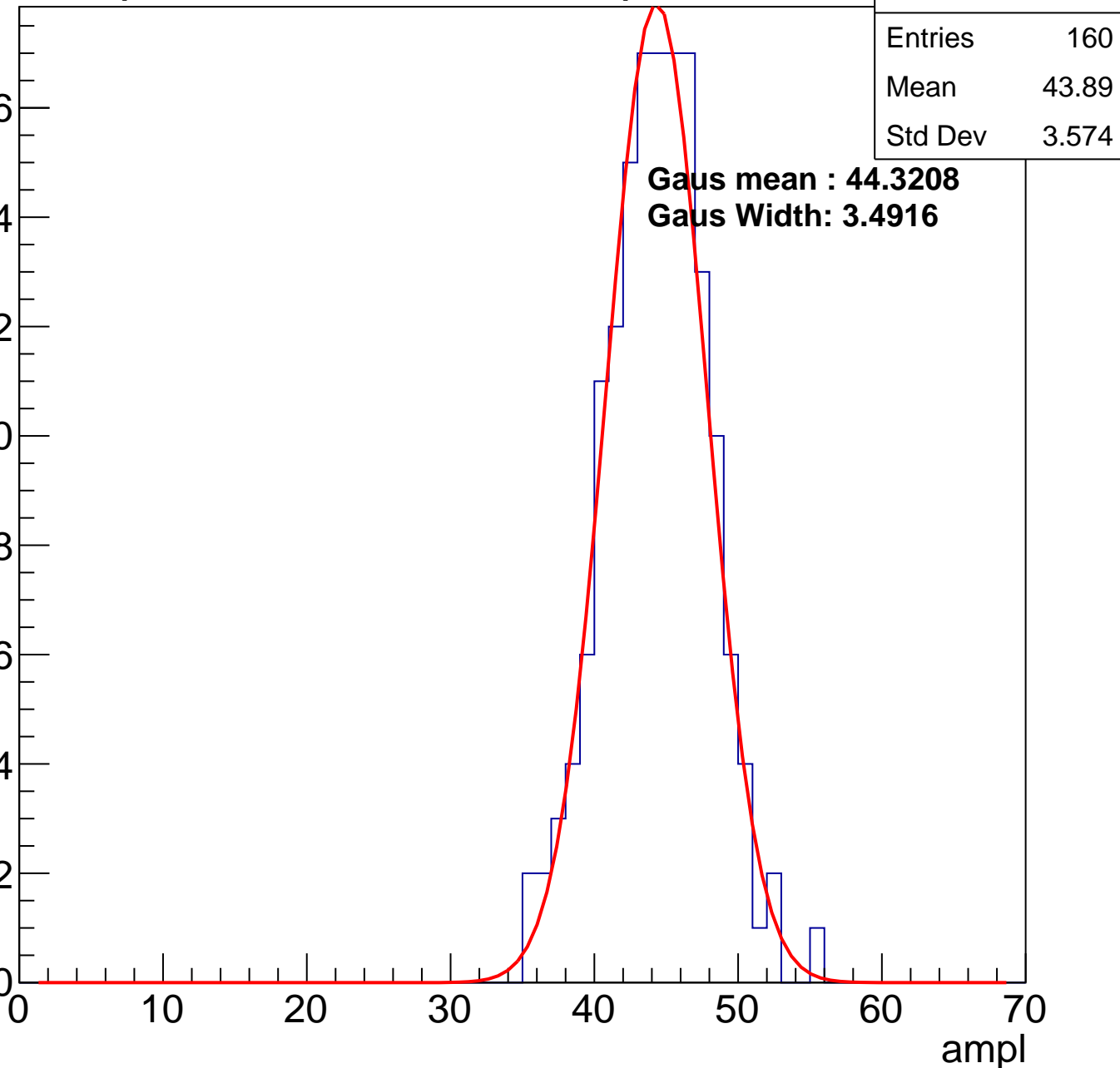
43.89

Std Dev

3.574

Gaus mean : 44.3208

Gaus Width: 3.4916

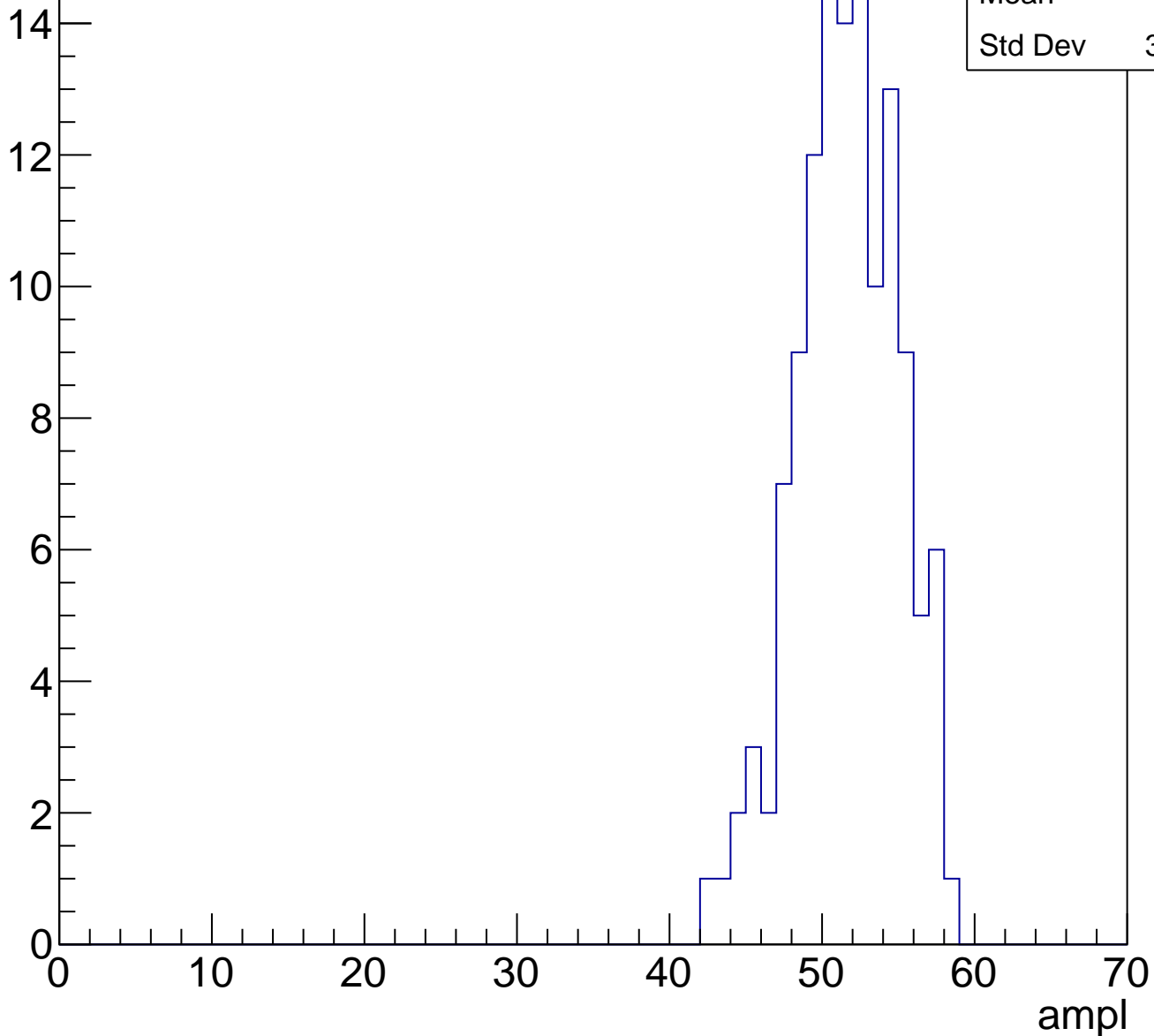


B0L101S, U22-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 125 |
| Mean | 51.2 |
| Std Dev | 3.296 |

Entry



B0L101S, U22-ch5, adc4

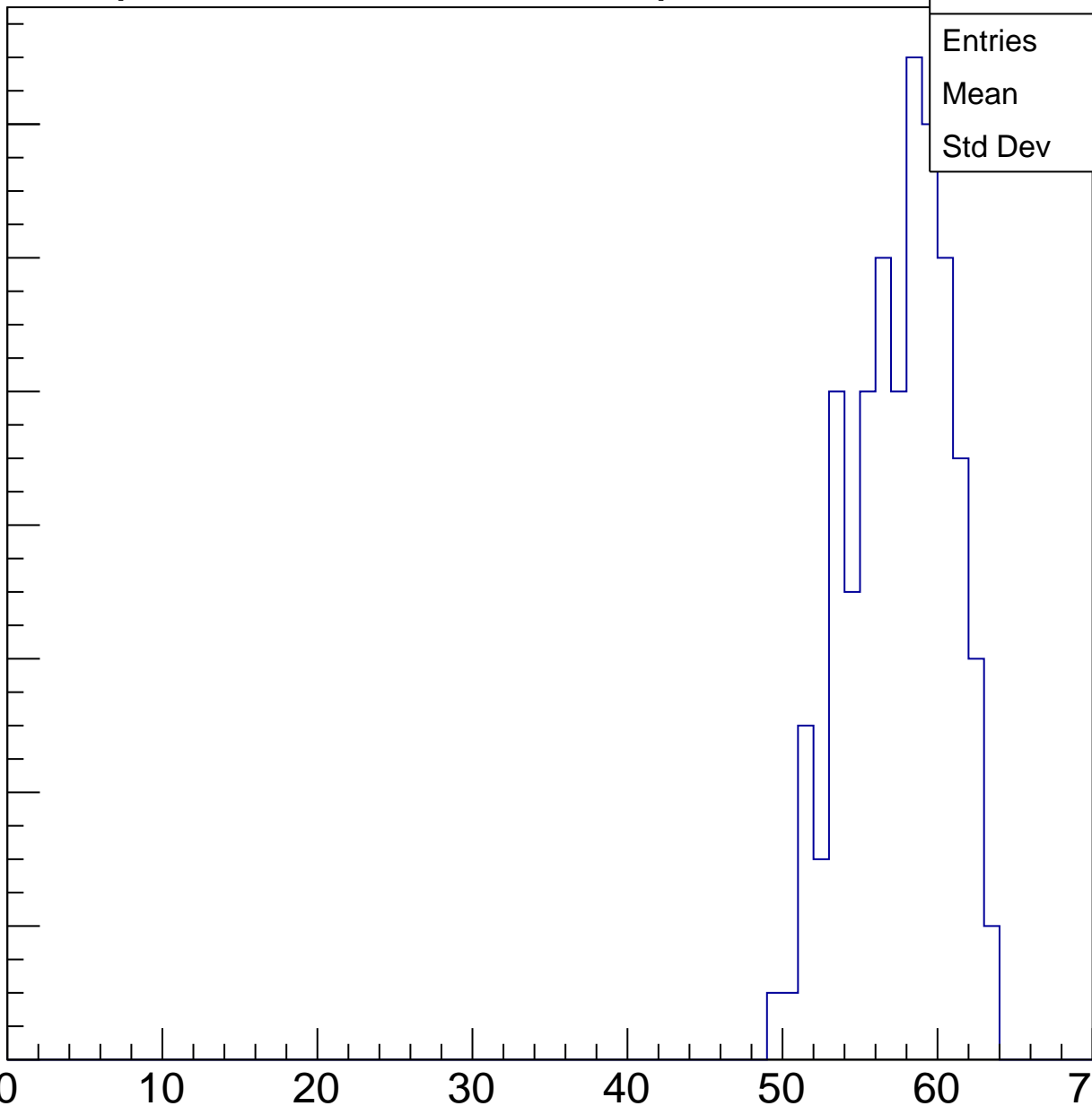
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 117 |
| Mean | 57.03 |
| Std Dev | 3.186 |

ampl



B0L101S, U22-ch5, adc5

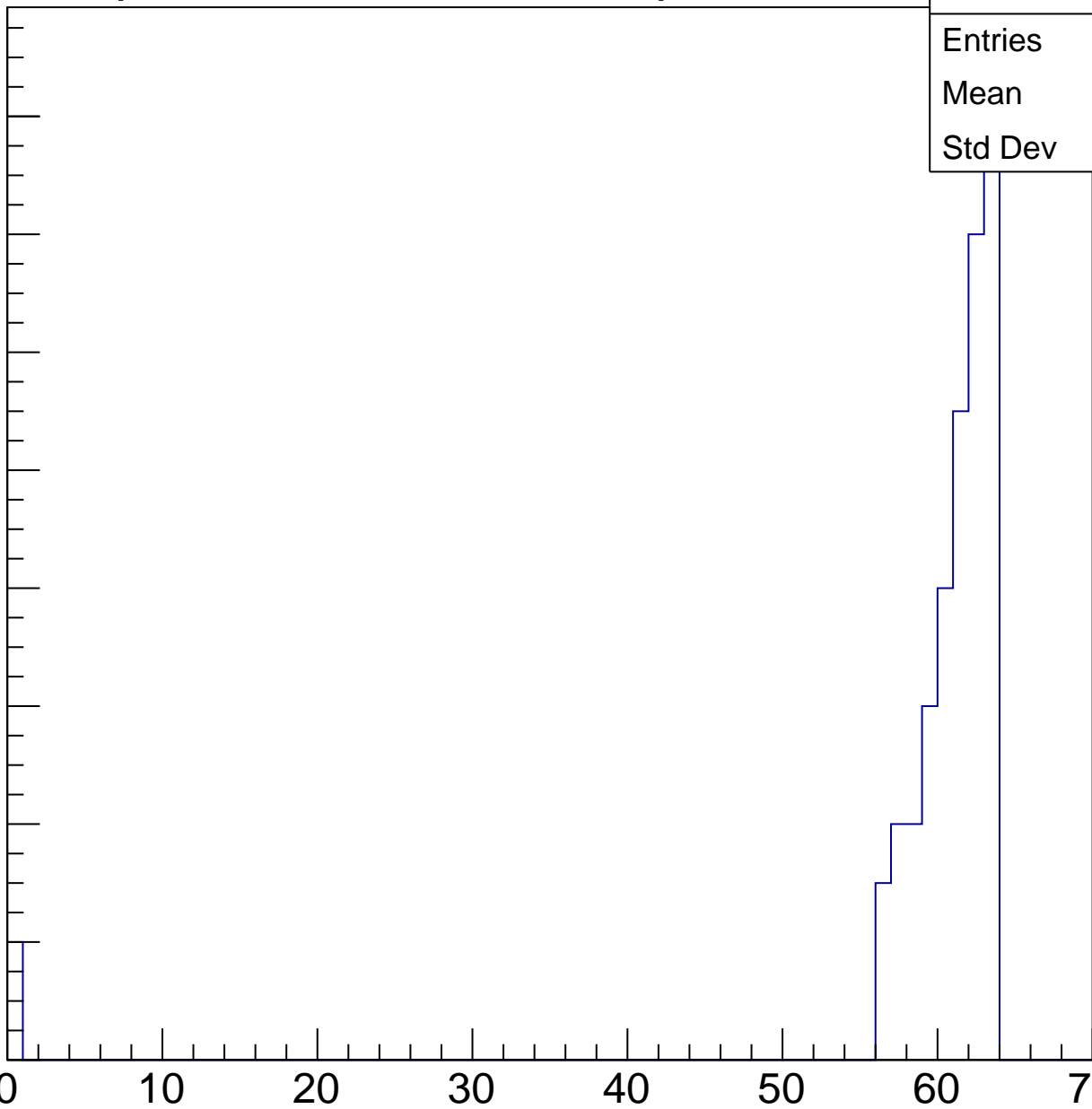
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 69 |
| Mean | 59.01 |
| Std Dev | 10.4 |

ampl



B0L101S, U22-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 30.48 |
| Std Dev | 3.344 |

Gaus mean : 30.6417

Gaus Width: 3.5955

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

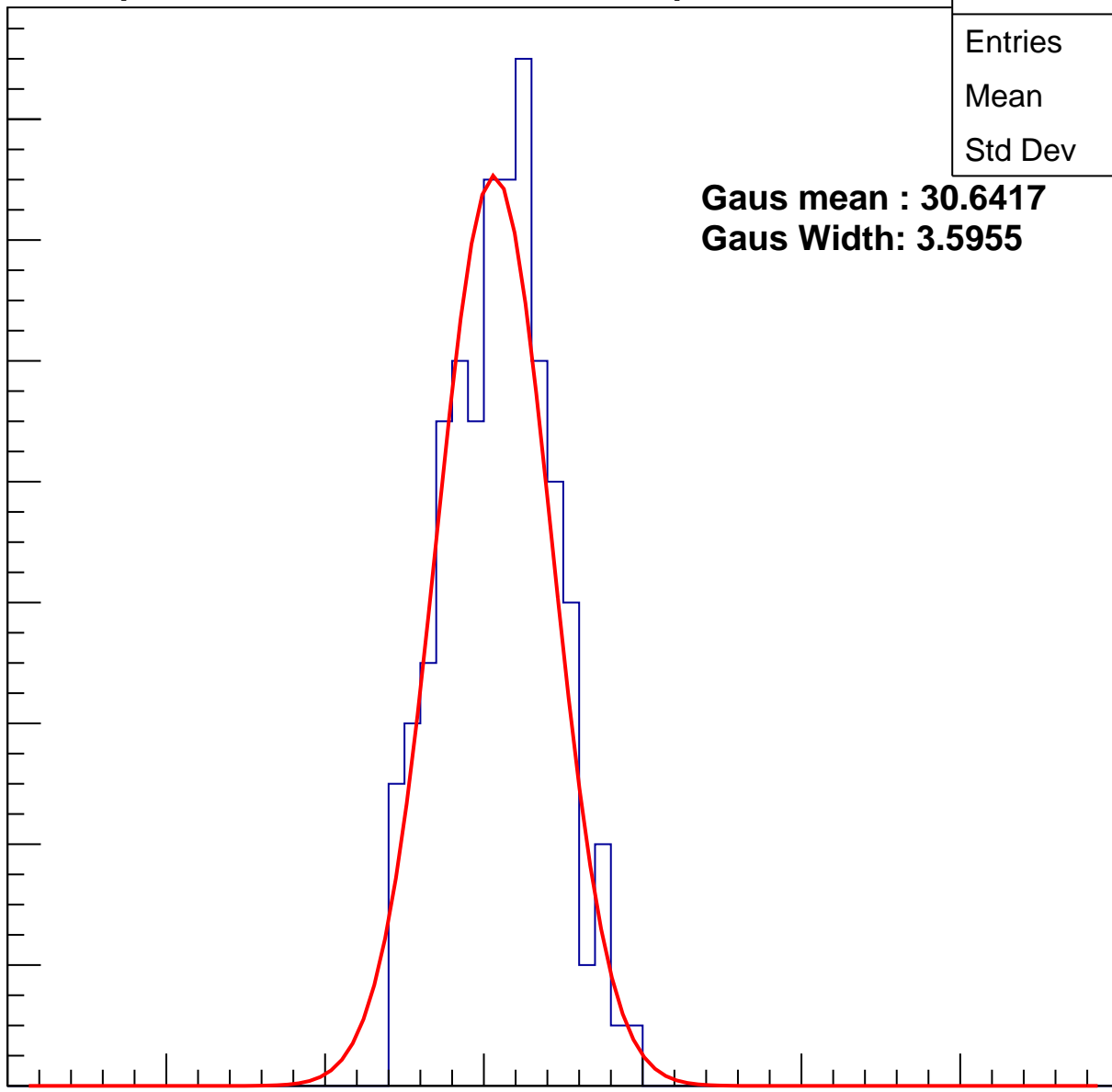
40

50

60

70

ampl

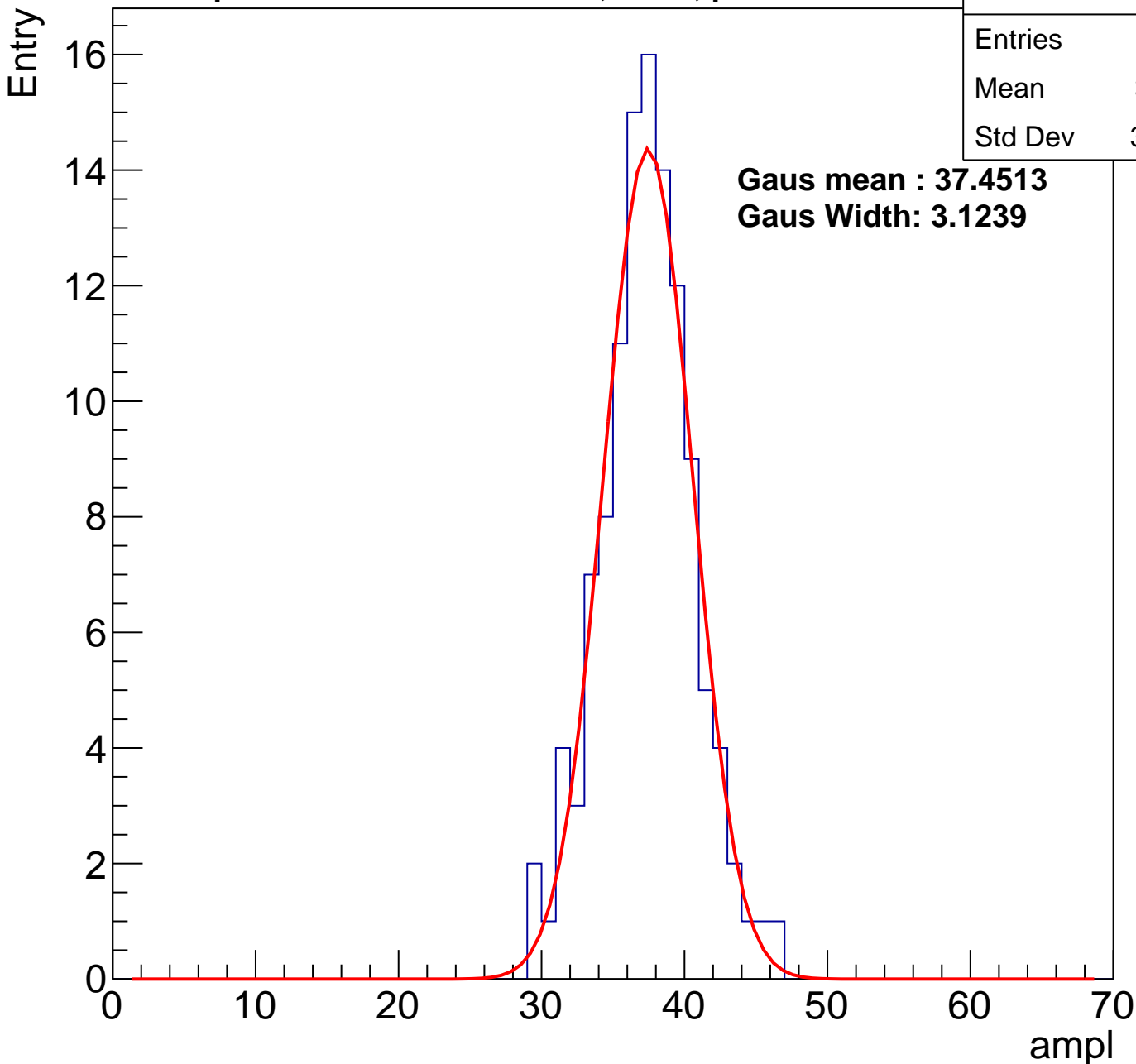


B0L101S, U22-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 116 |
| Mean | 36.91 |
| Std Dev | 3.253 |

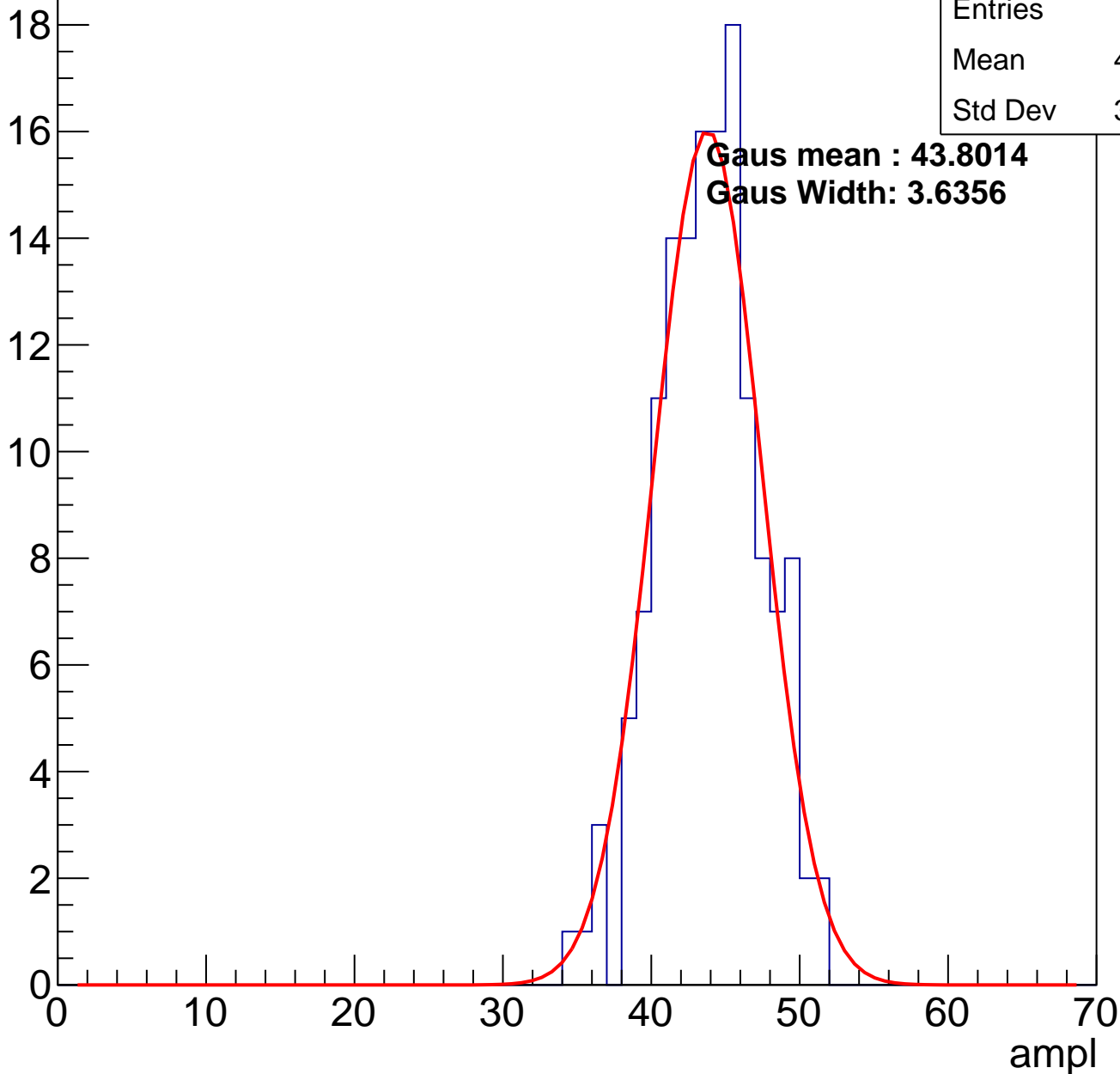
Gaus mean : 37.4513
Gaus Width: 3.1239



B0L101S, U22-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

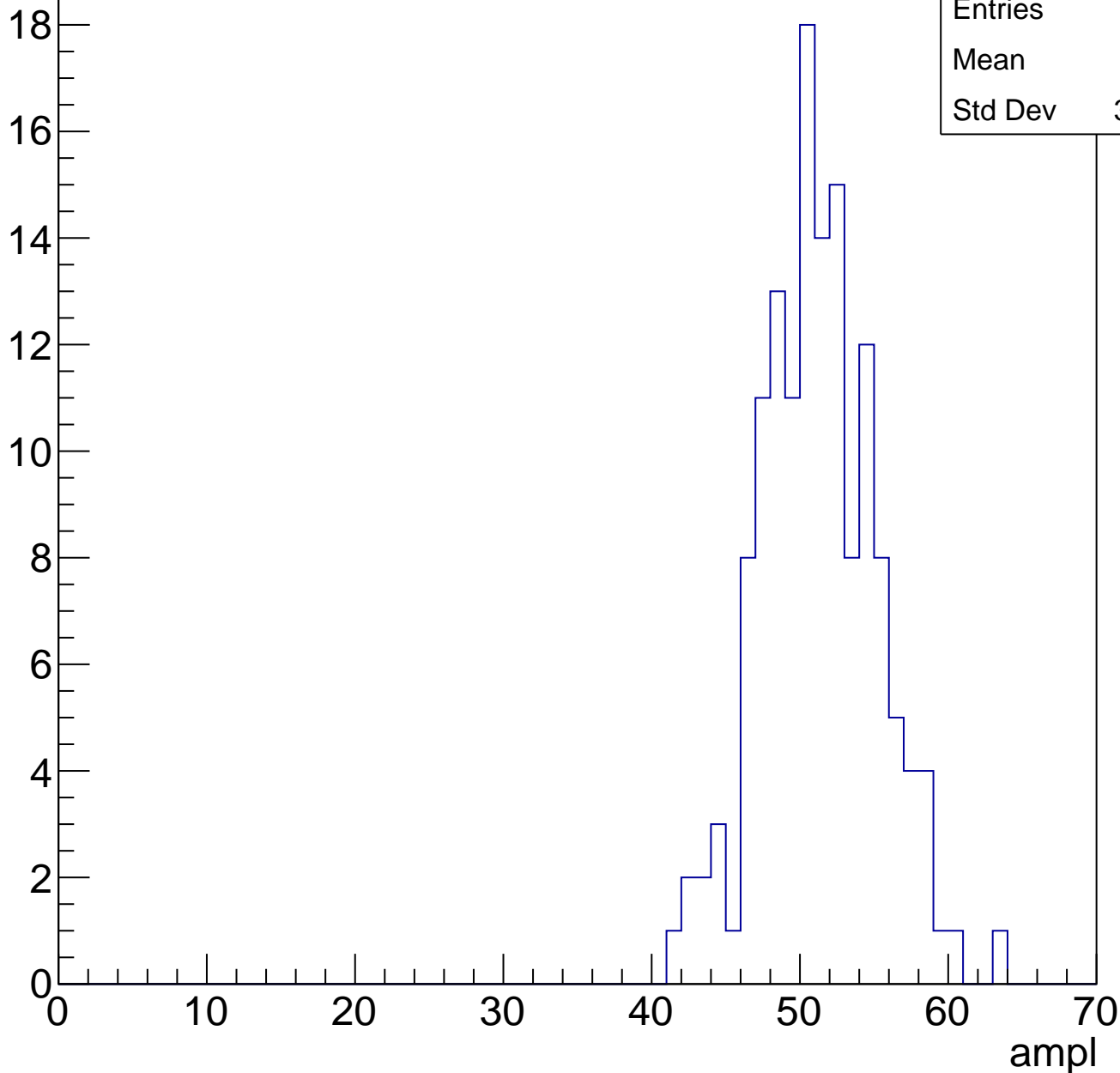
Entry



B0L101S, U22-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch6, adc4

calib_packv5_042523_0143.root, FC#1, port C1

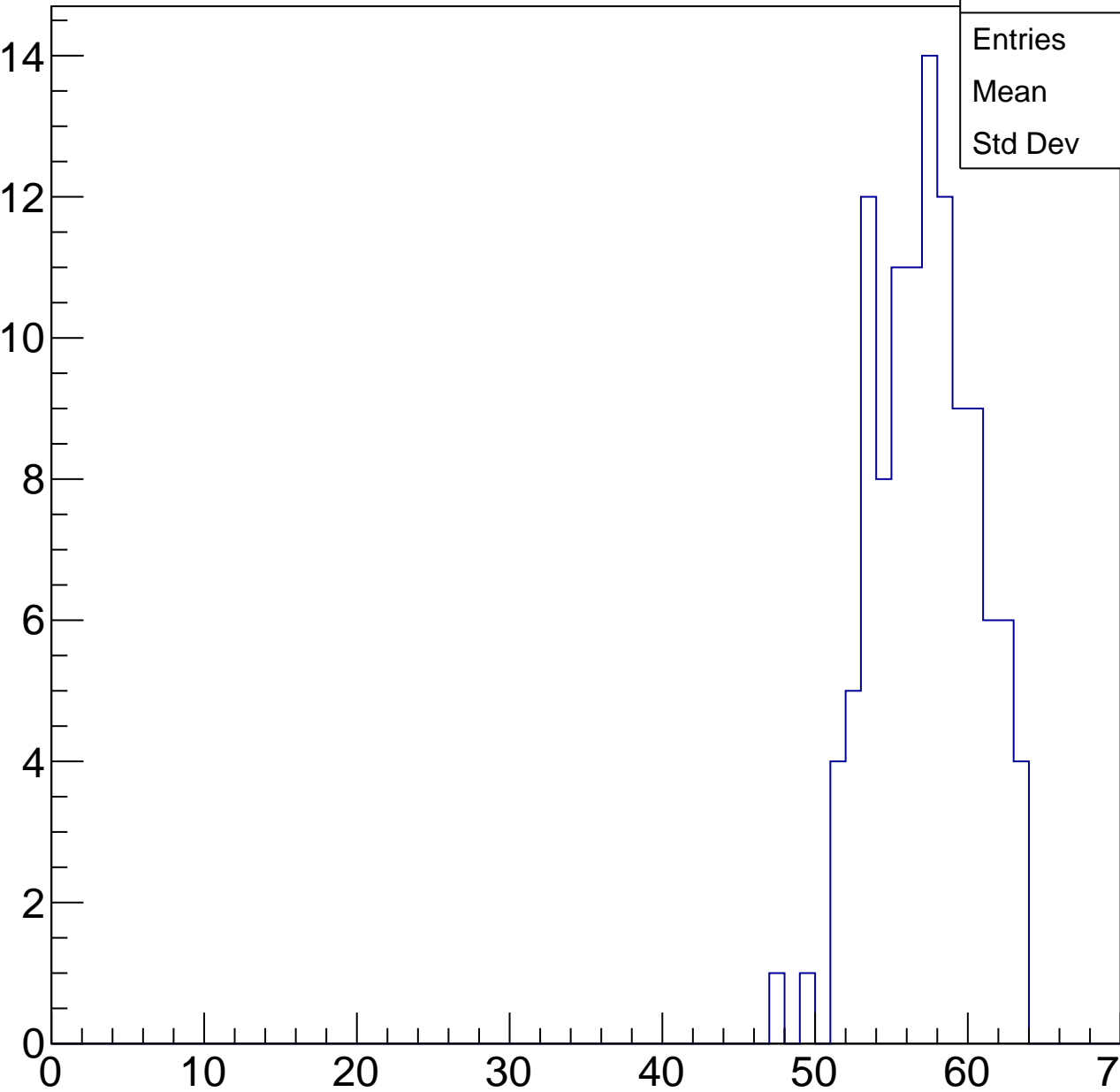
| | |
|---------|-------|
| Entries | 113 |
| Mean | 56.67 |
| Std Dev | 3.338 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch6, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

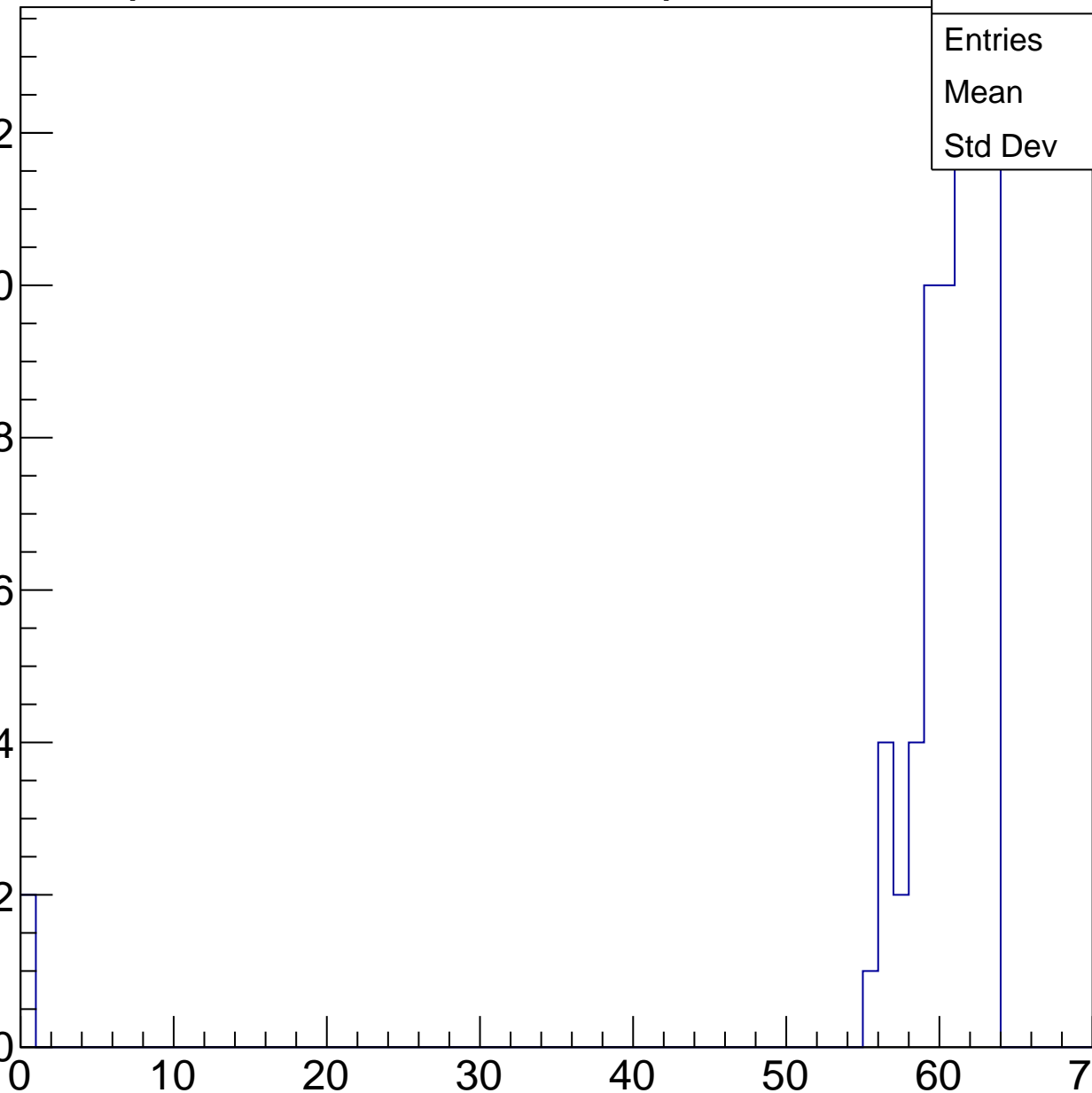
4

2

0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.7 |
| Std Dev | 10.27 |

ampl



B0L101S, U22-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

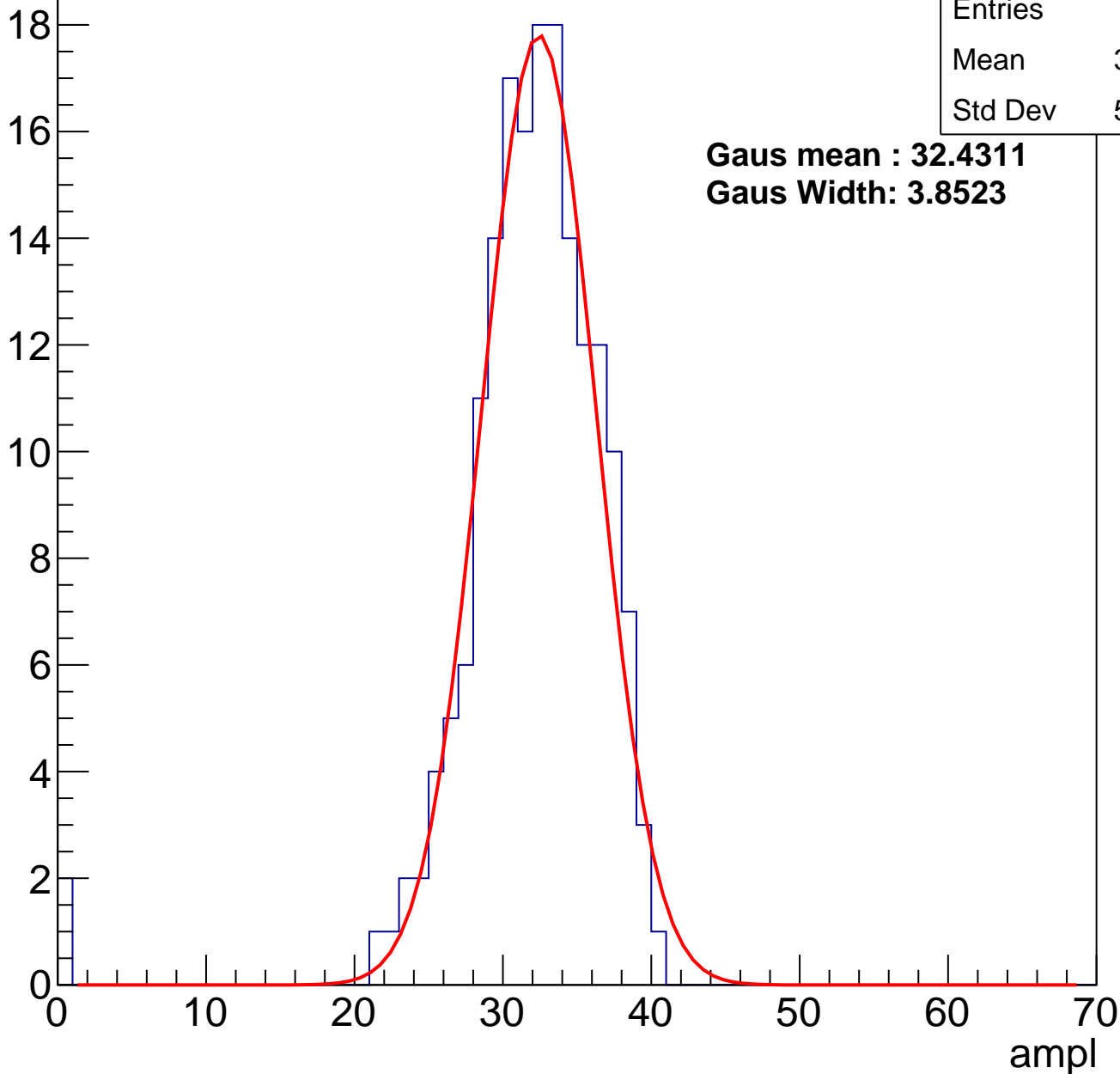


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

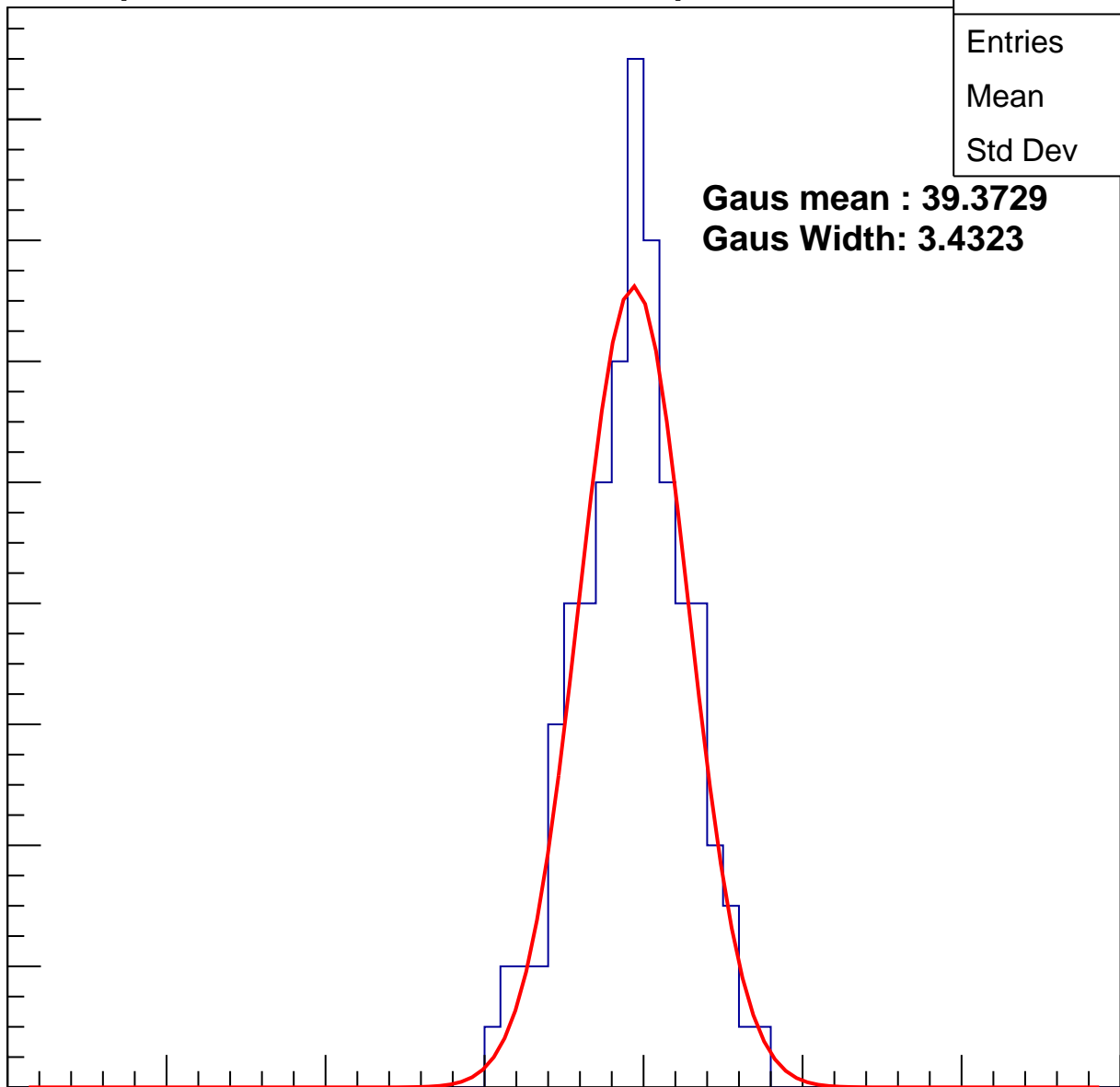
38.78

Std Dev

3.397

Gaus mean : 39.3729

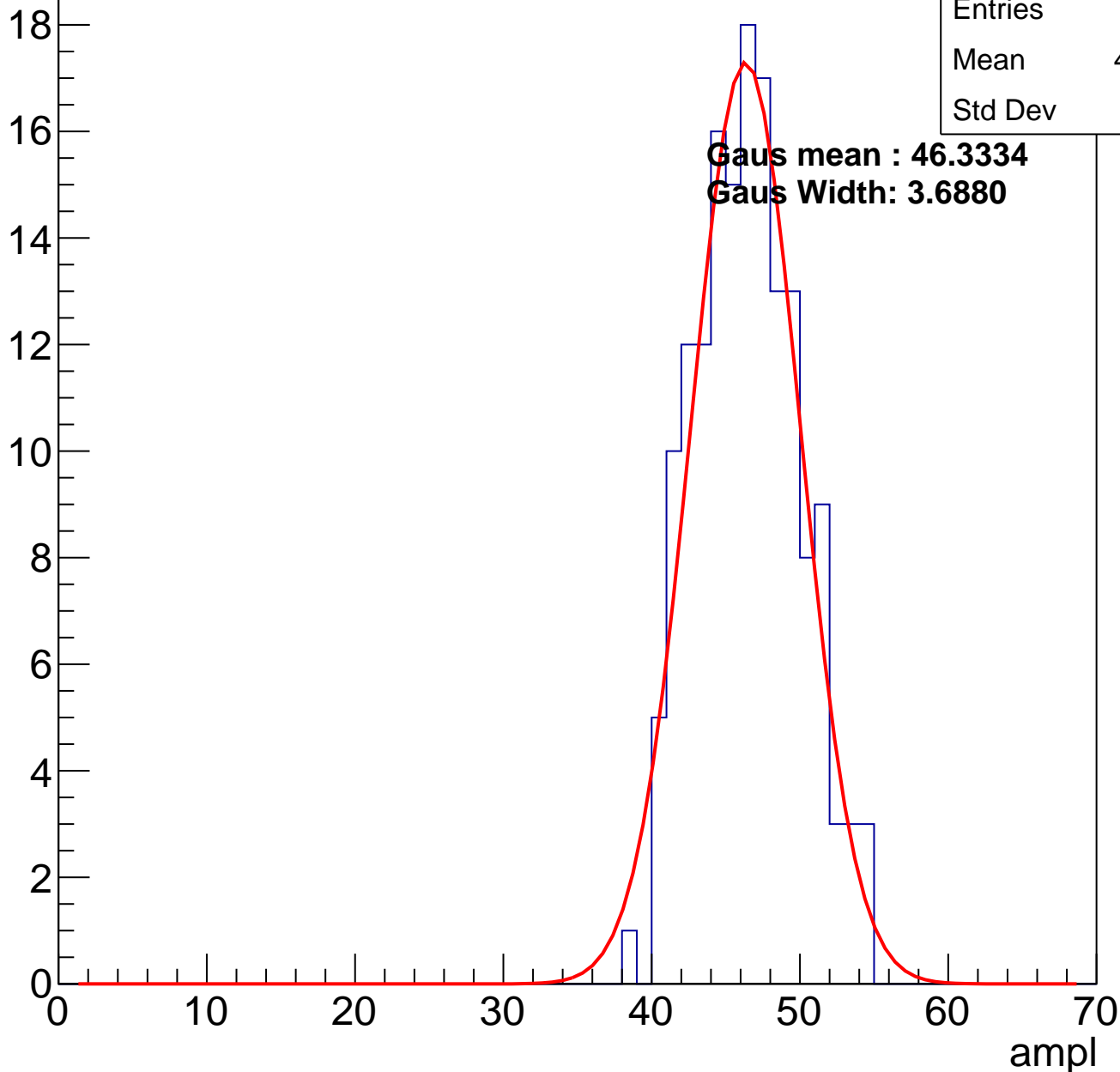
Gaus Width: 3.4323



B0L101S, U22-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

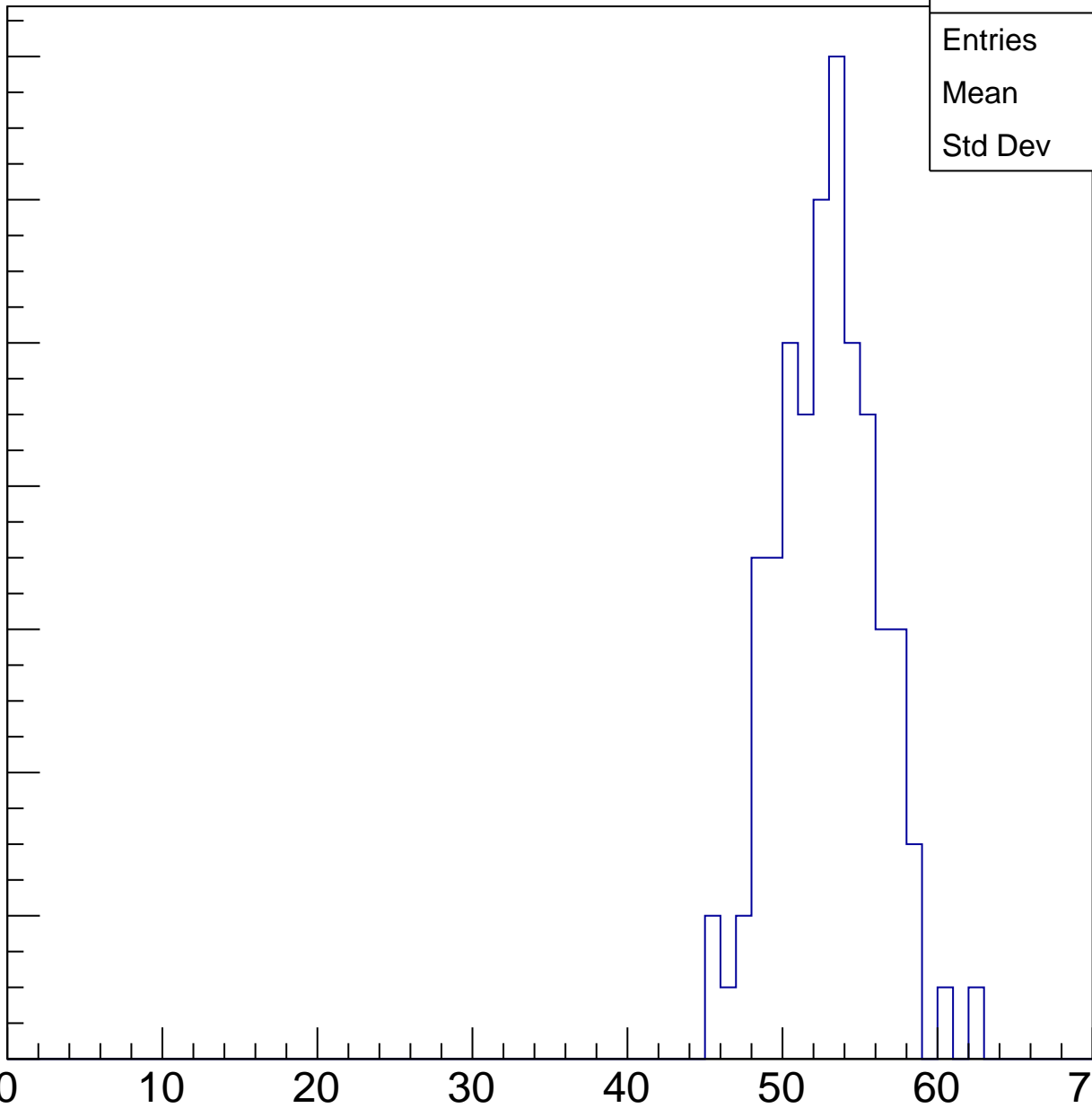
| | |
|---------|-------|
| Entries | 100 |
| Mean | 52.43 |
| Std Dev | 3.229 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch7, adc4

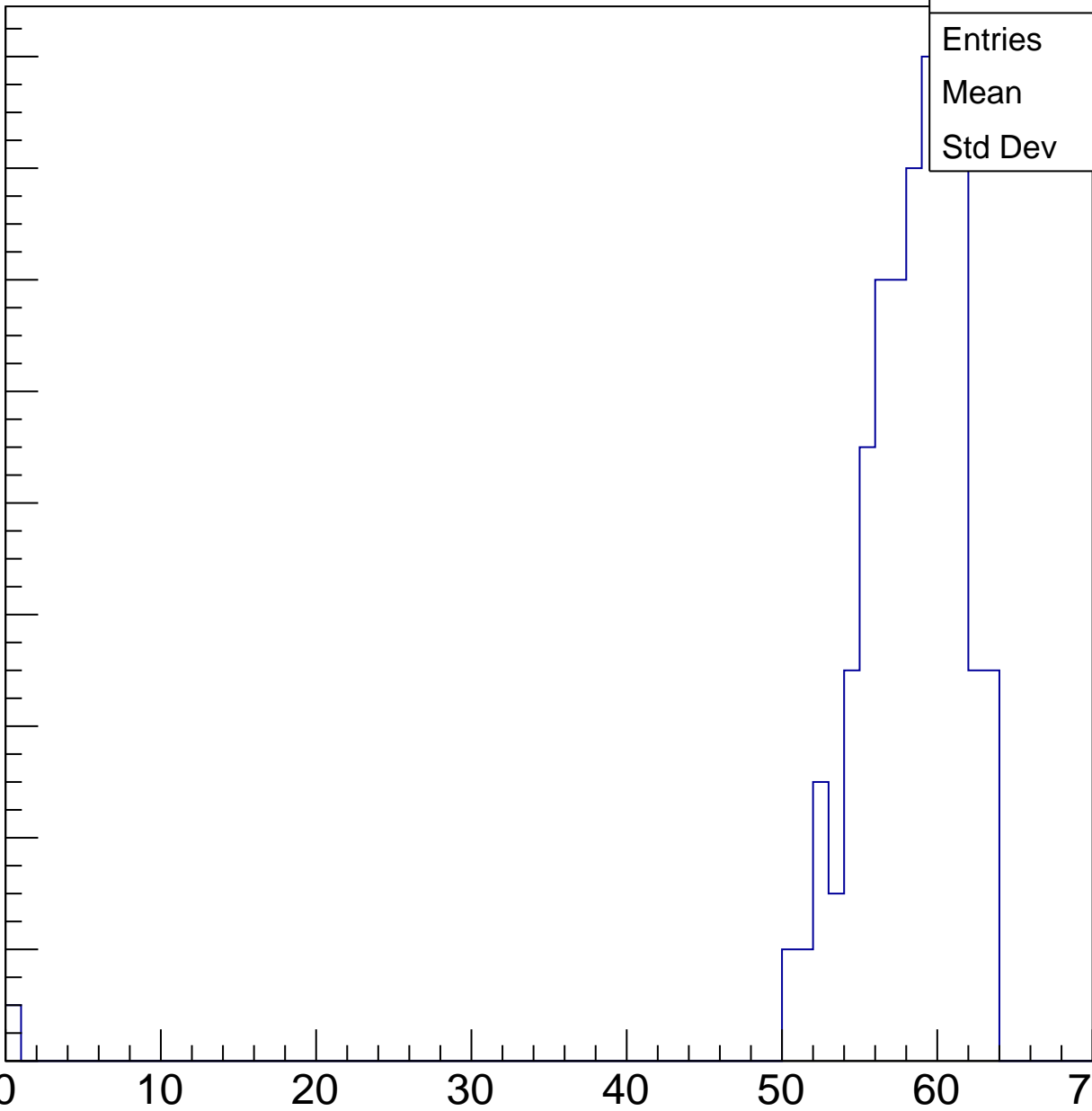
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 141 |
| Mean | 57.5 |
| Std Dev | 5.722 |

ampl



B0L101S, U22-ch7, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

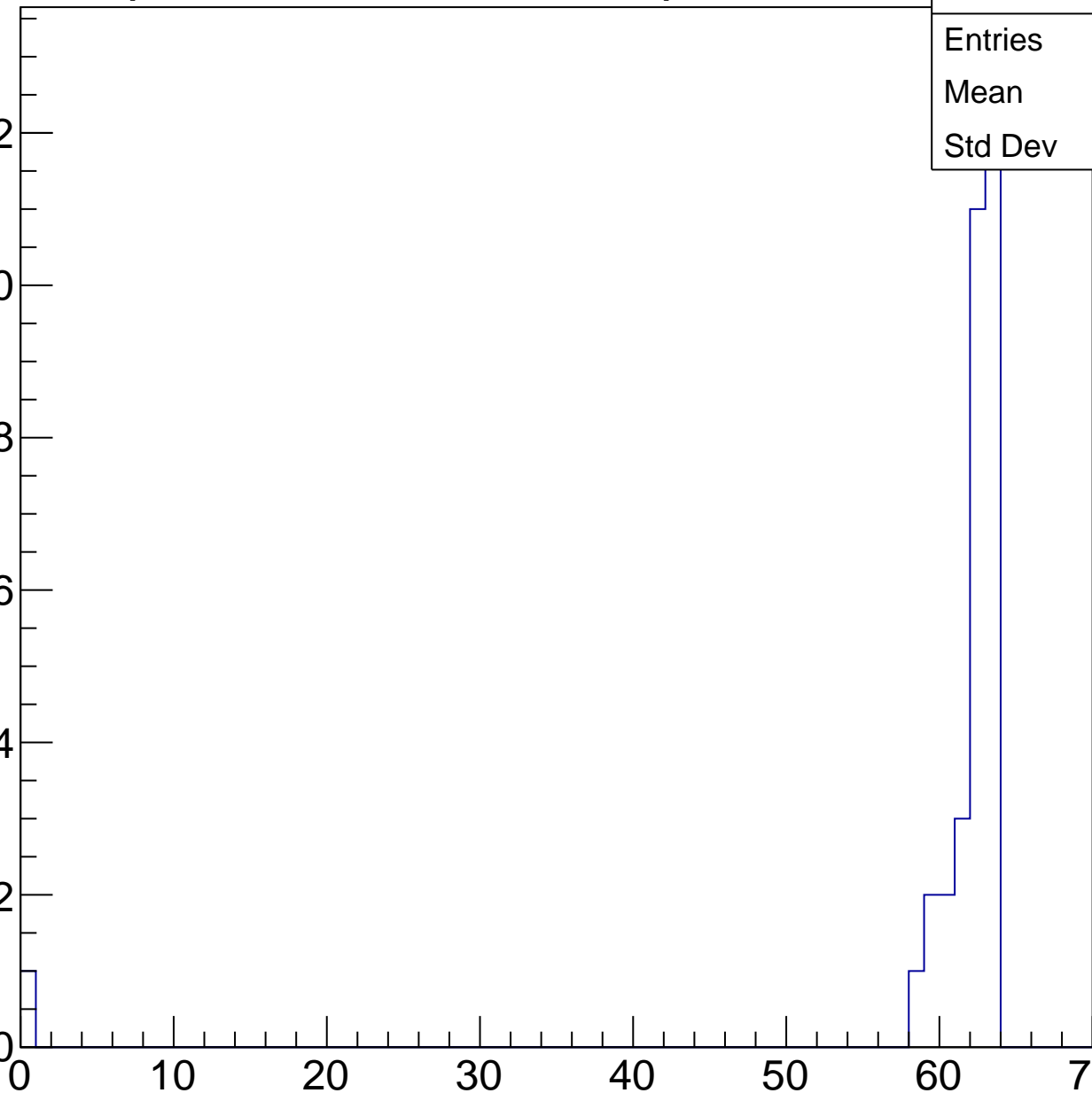
4

2

0

| | |
|---------|-------|
| Entries | 33 |
| Mean | 60 |
| Std Dev | 10.69 |

ampl



B0L101S, U22-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 28.01 |
| Std Dev | 5.304 |

Gaus mean : 28.9346

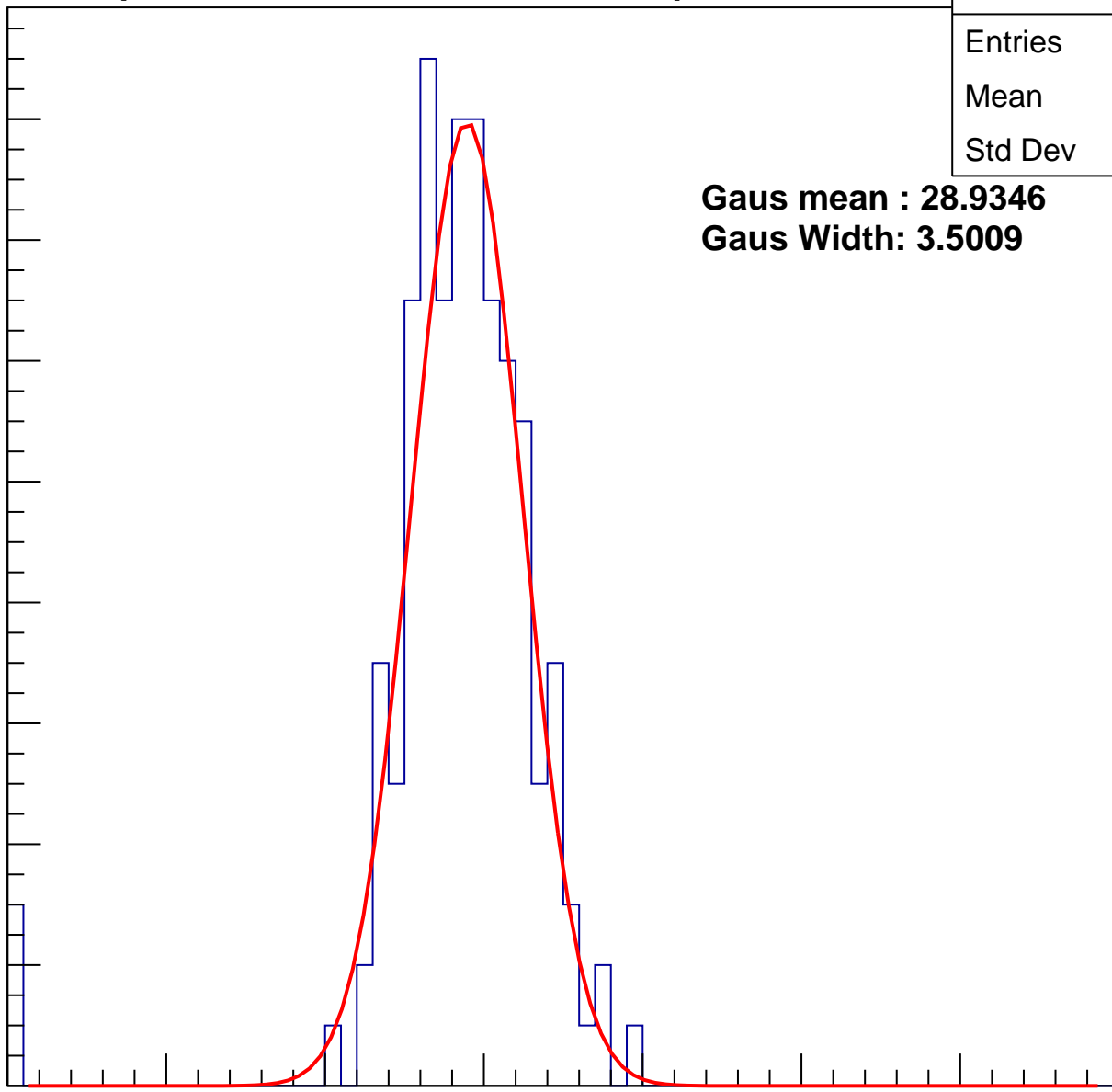
Gaus Width: 3.5009

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch8, adc1

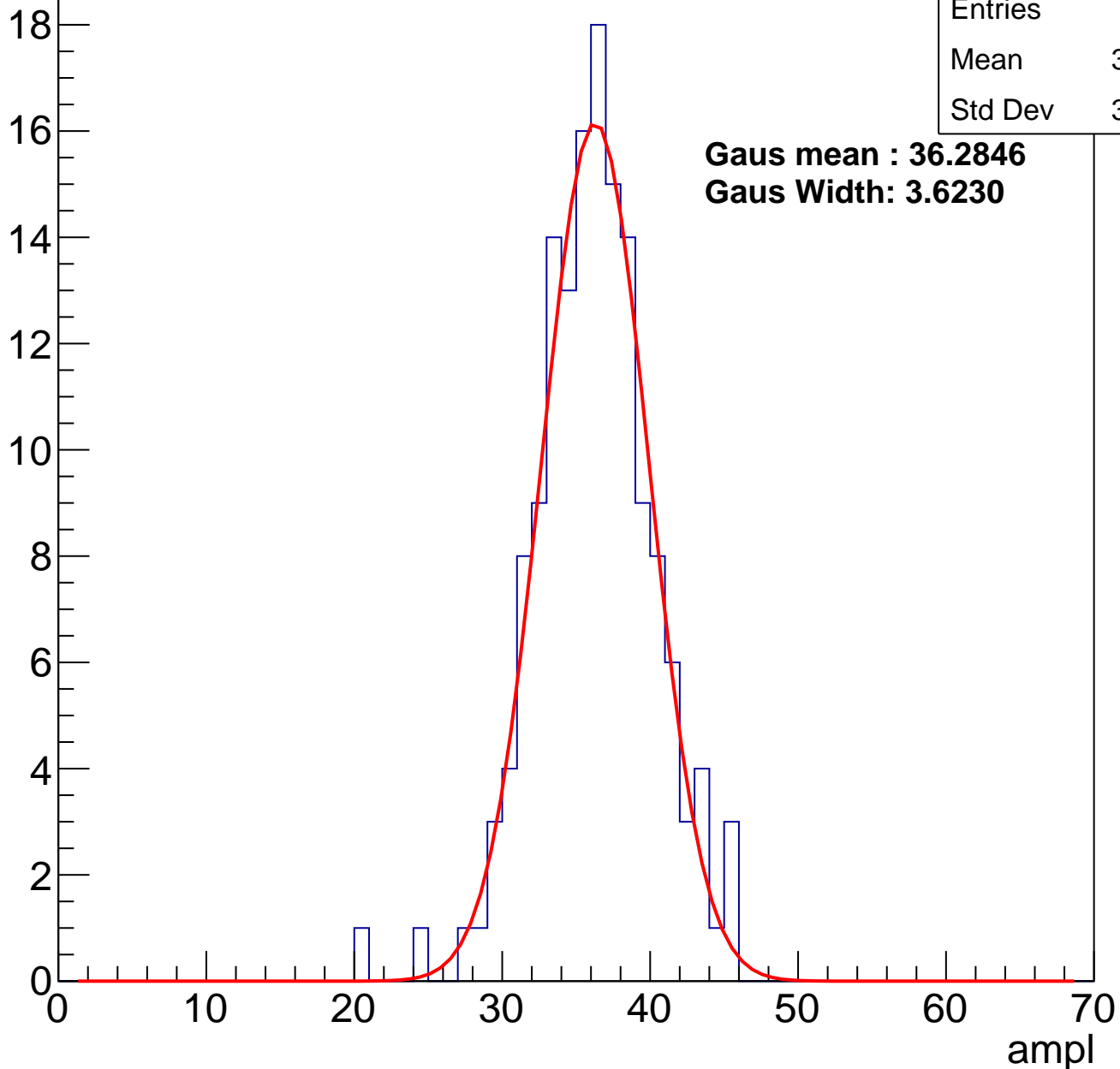
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 152 |
| Mean | 35.76 |
| Std Dev | 3.979 |

Gaus mean : 36.2846

Gaus Width: 3.6230

Entry



B0L101S, U22-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

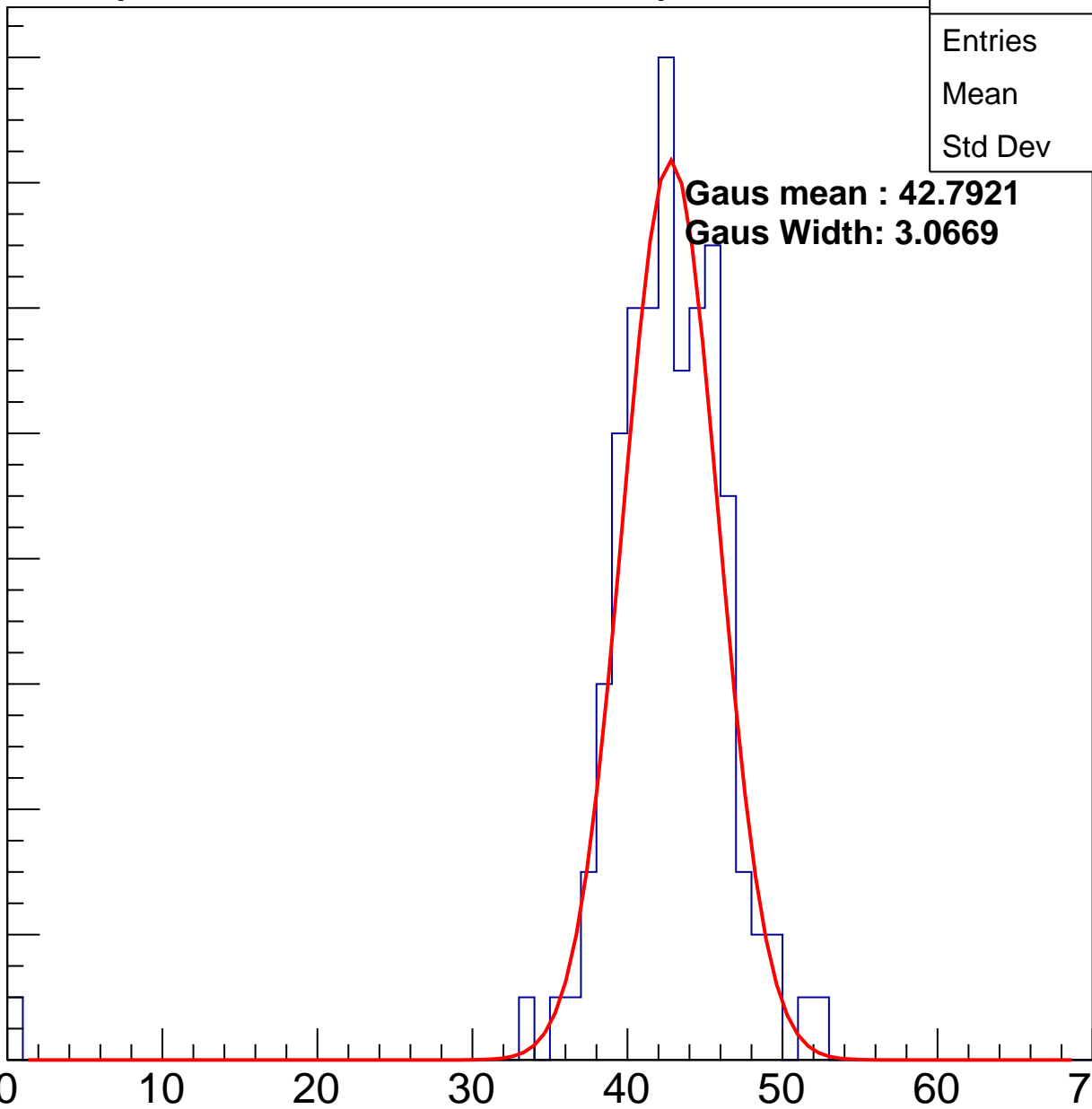
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 117 |
| Mean | 42.01 |
| Std Dev | 5.061 |

Gaus mean : 42.7921

Gaus Width: 3.0669

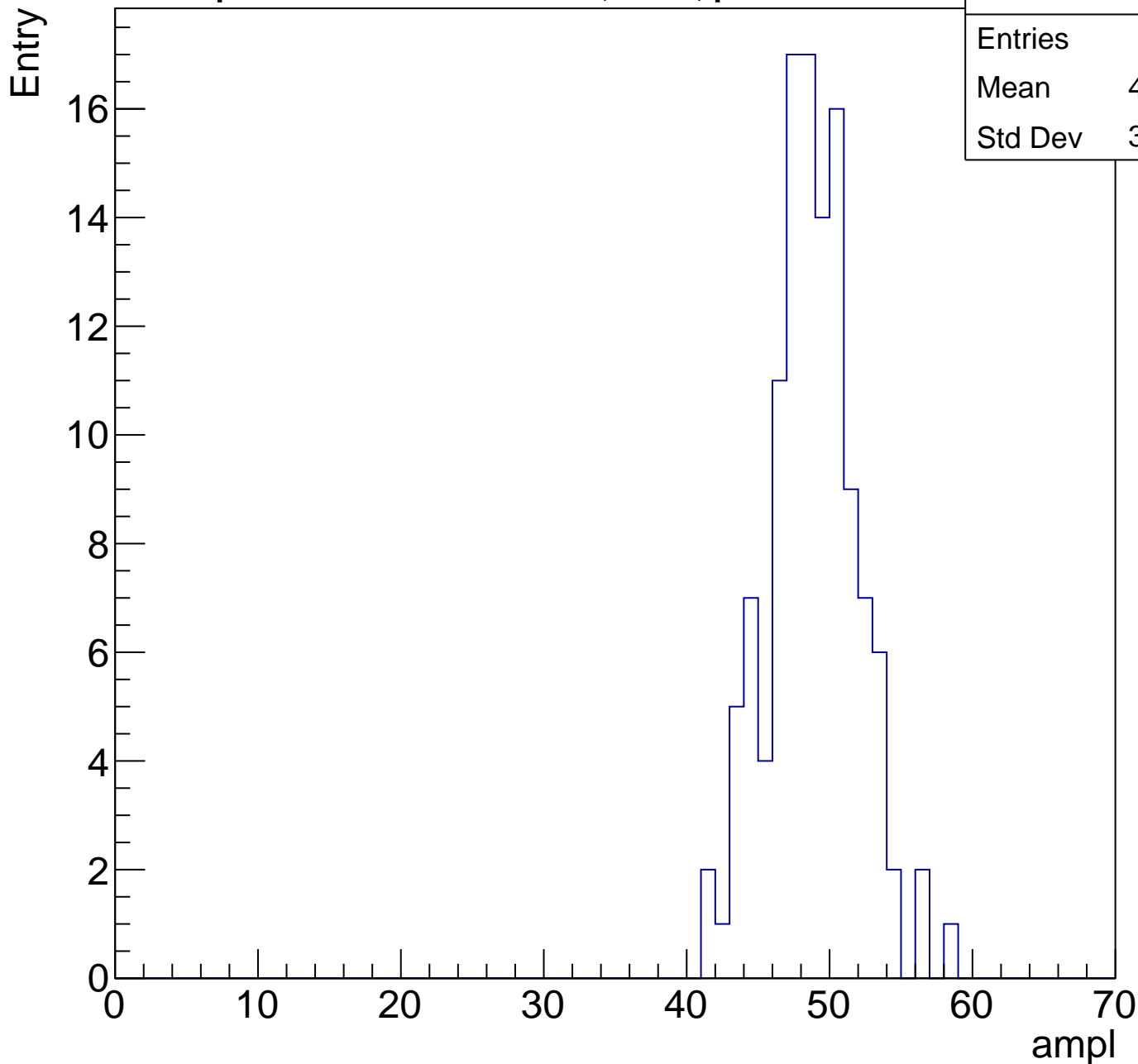
ampl



B0L101S, U22-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 121 |
| Mean | 48.37 |
| Std Dev | 3.113 |

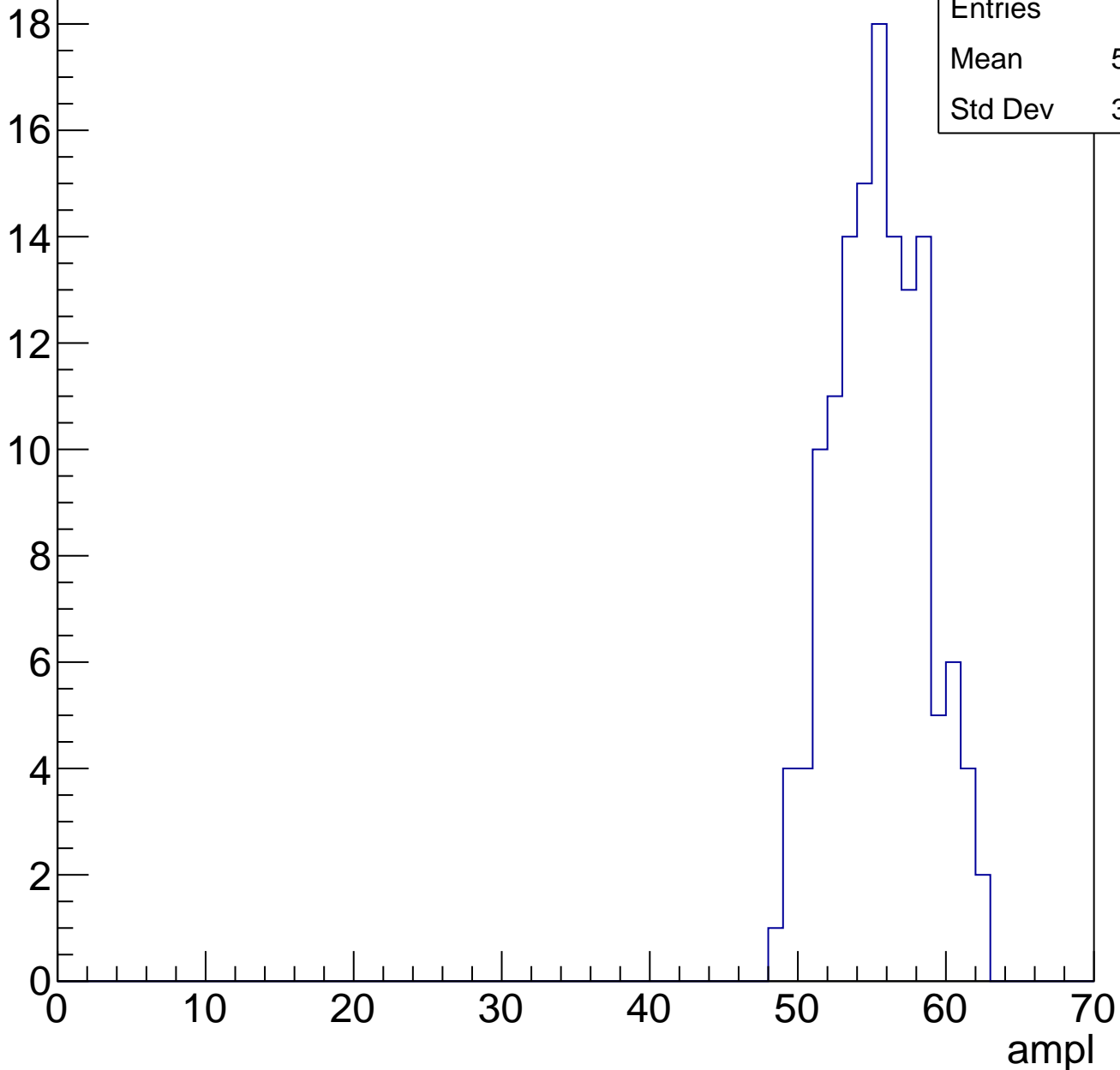


B0L101S, U22-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 55.02 |
| Std Dev | 3.088 |

Entry



B0L101S, U22-ch8, adc5

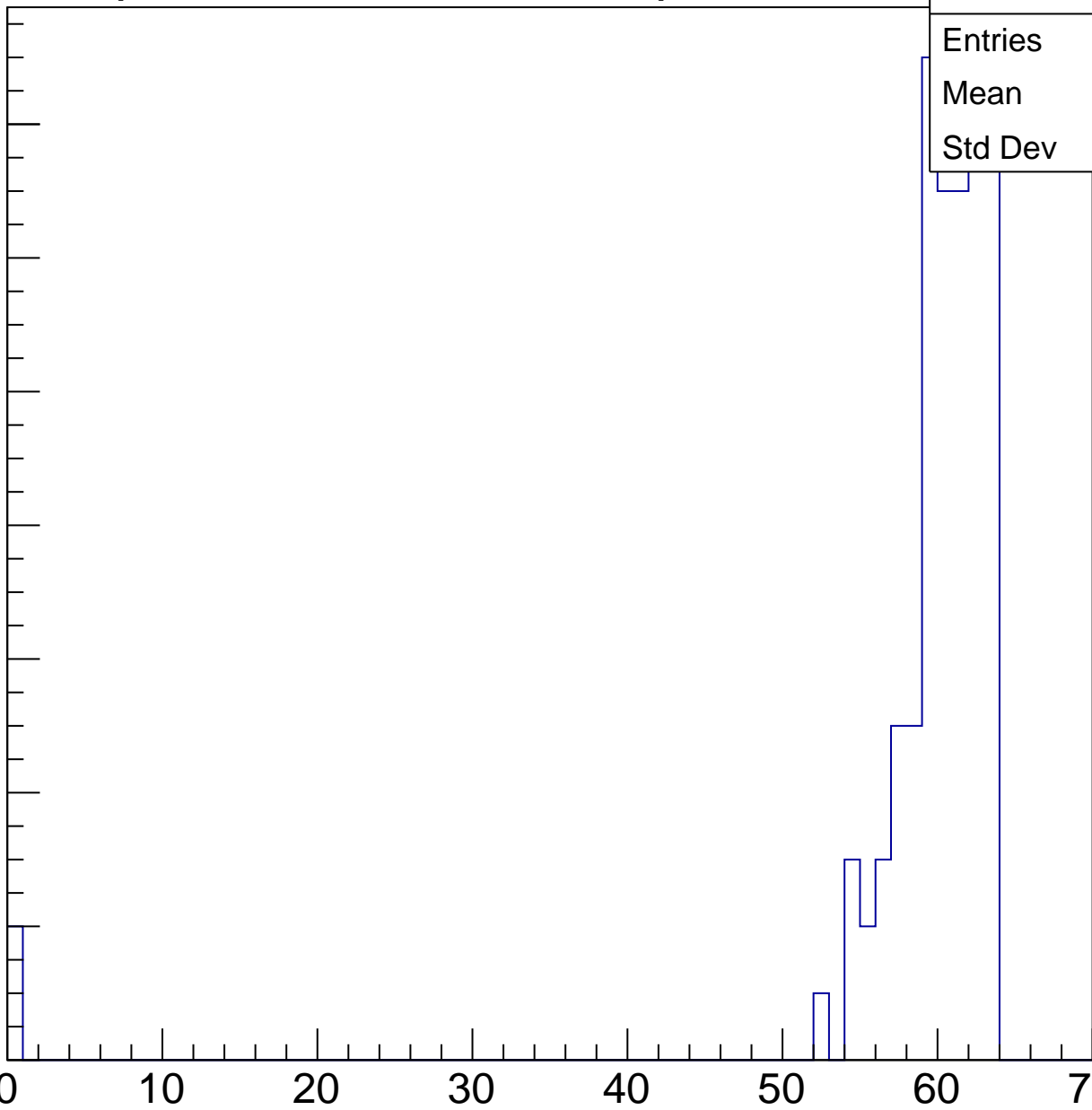
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 58.61 |
| Std Dev | 9.172 |

ampl

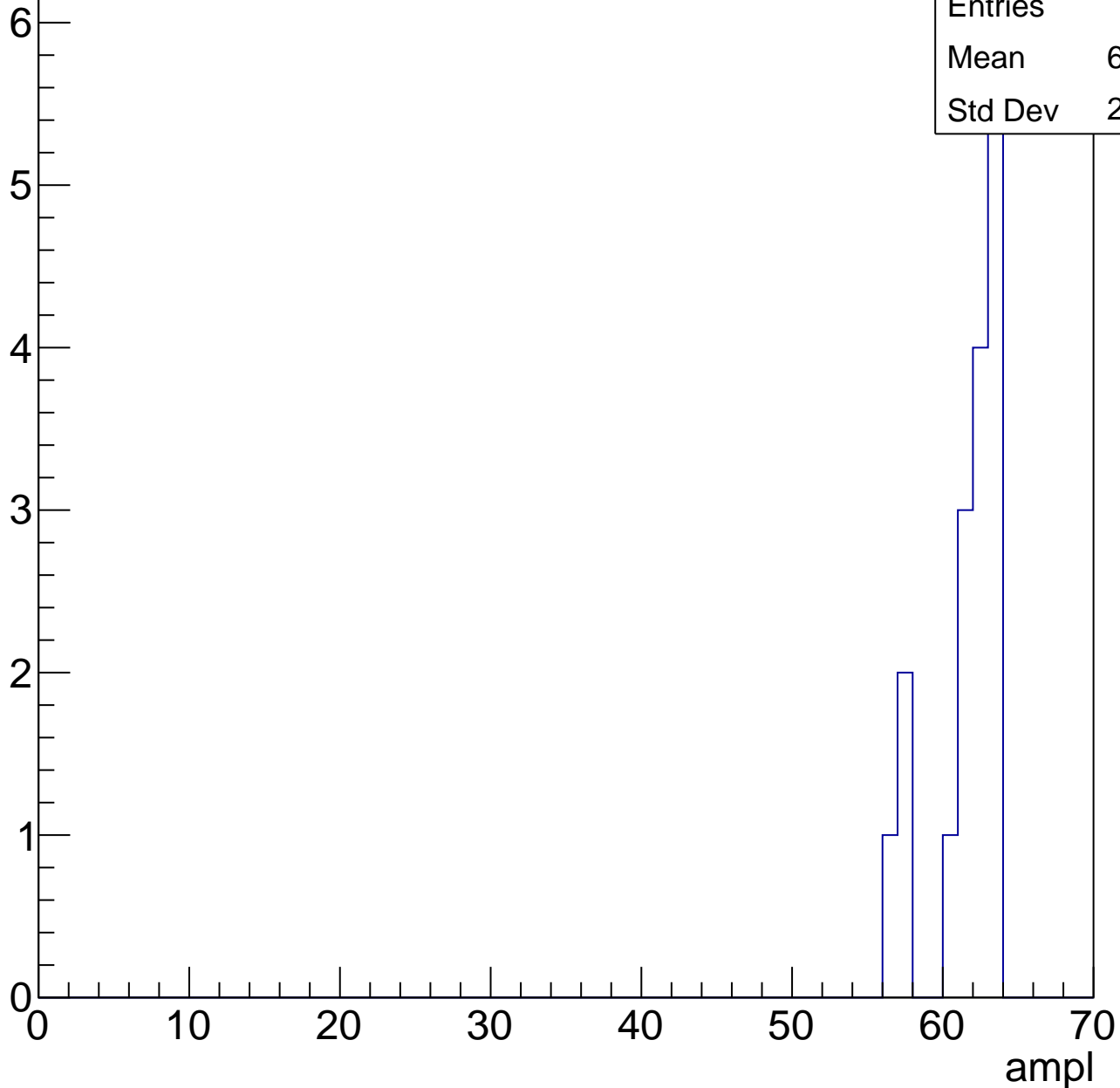


B0L101S, U22-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.12 |
| Std Dev | 2.246 |



B0L101S, U22-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

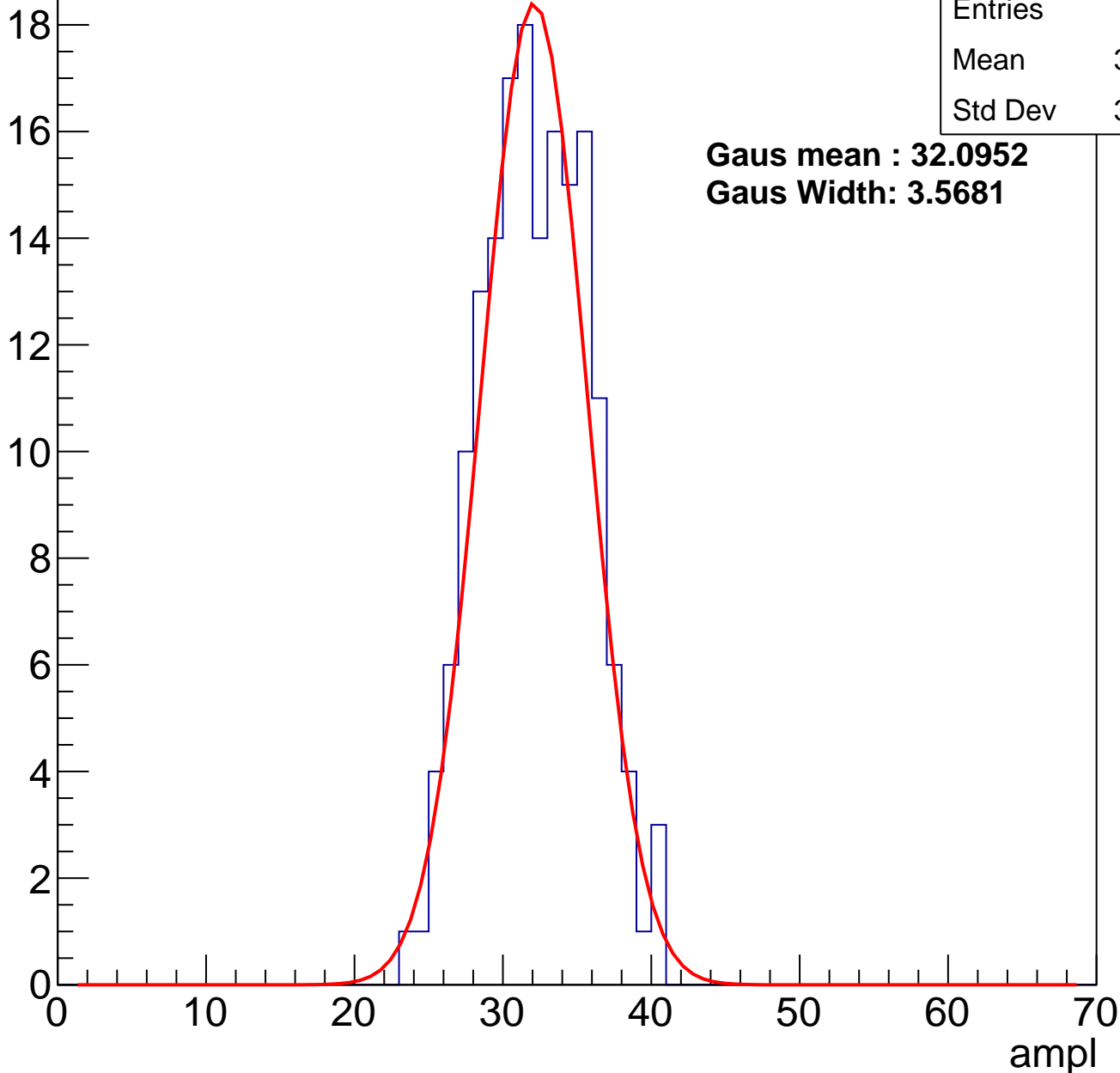


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch9, adc1

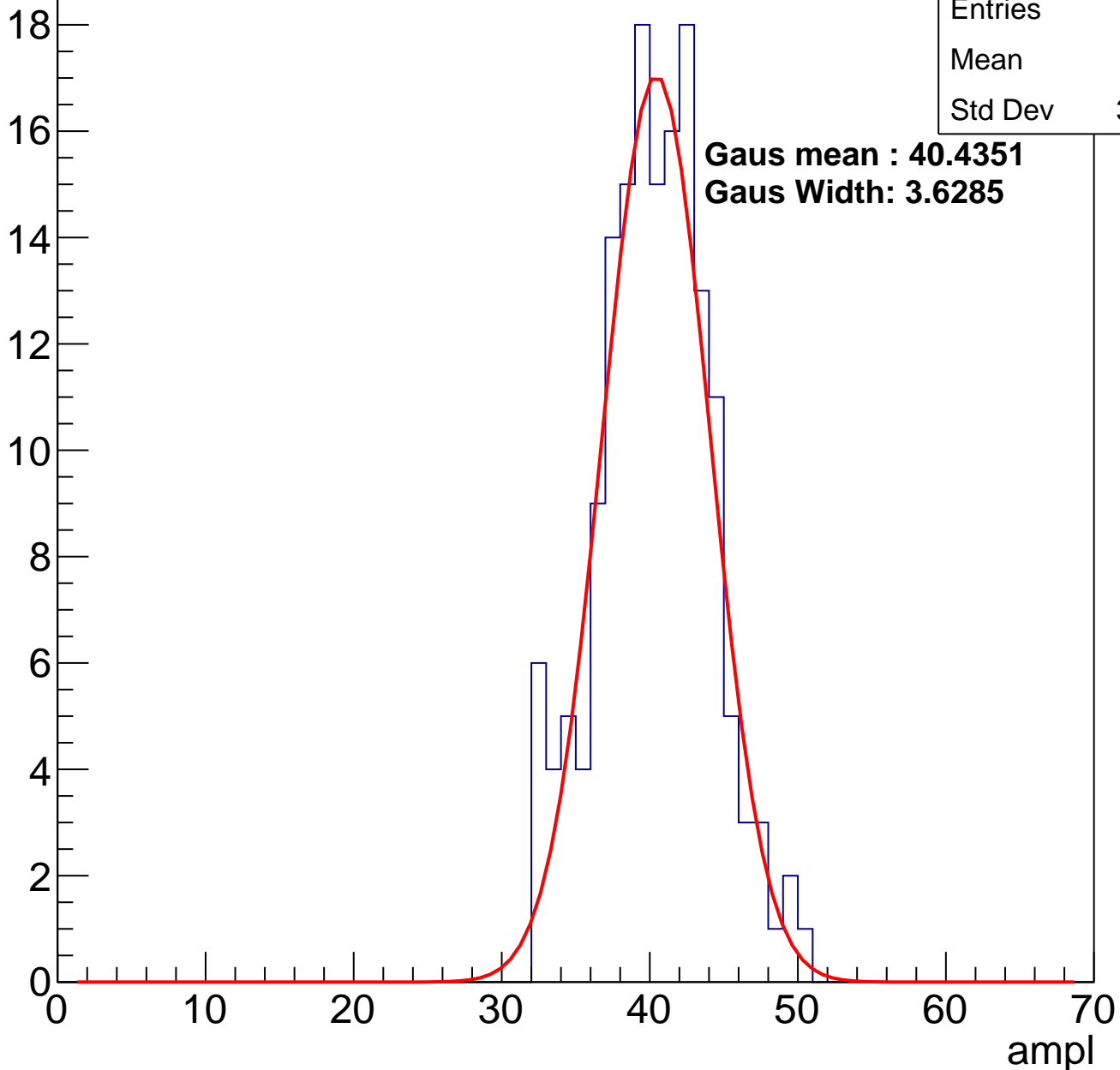
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 163 |
| Mean | 39.9 |
| Std Dev | 3.751 |

Gaus mean : 40.4351

Gaus Width: 3.6285

Entry



B0L101S, U22-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

127

Mean

47.16

Std Dev

3.361

Gaus mean : 47.7852

Gaus Width: 3.2120

0

10

20

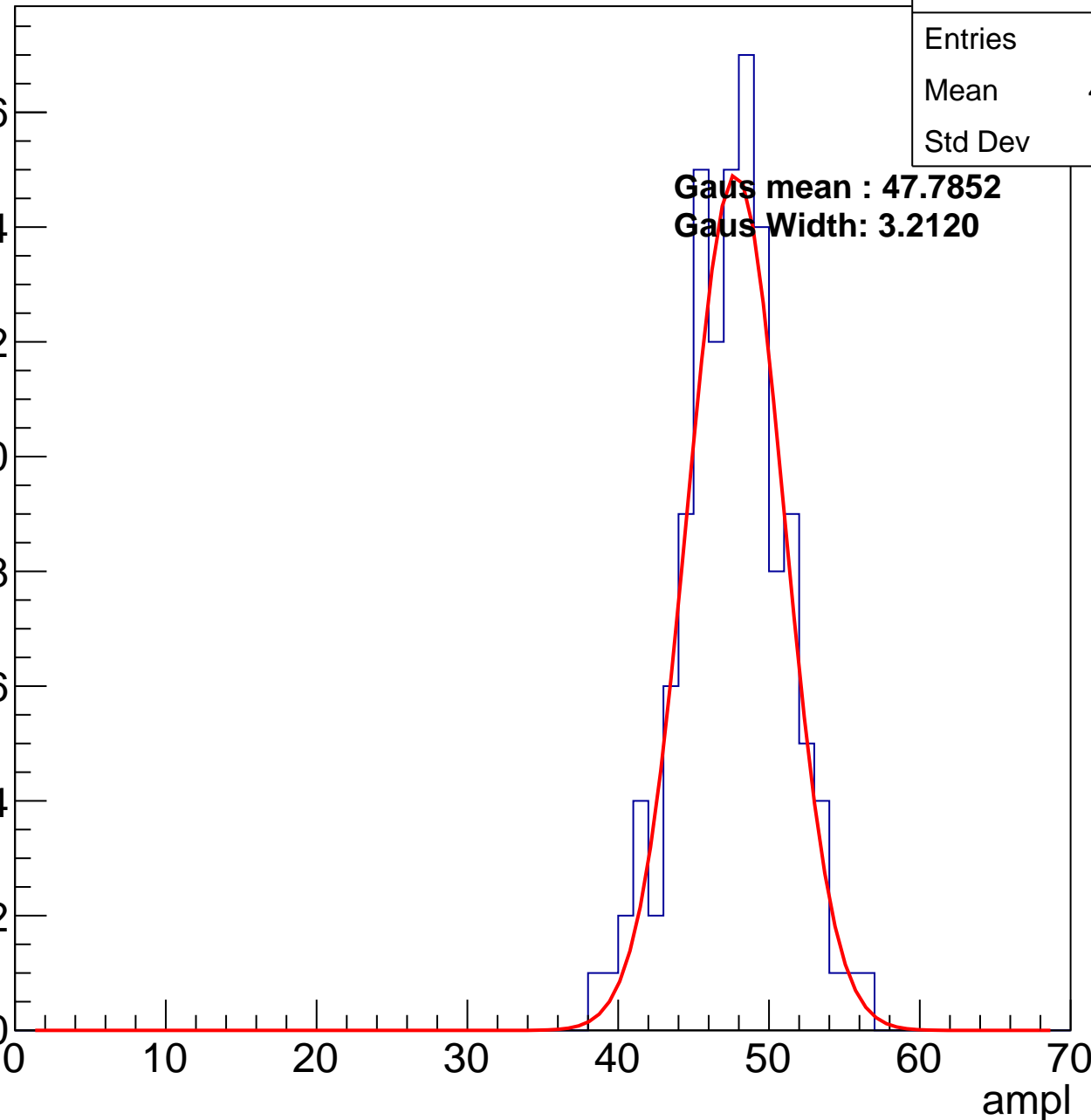
30

40

50

60

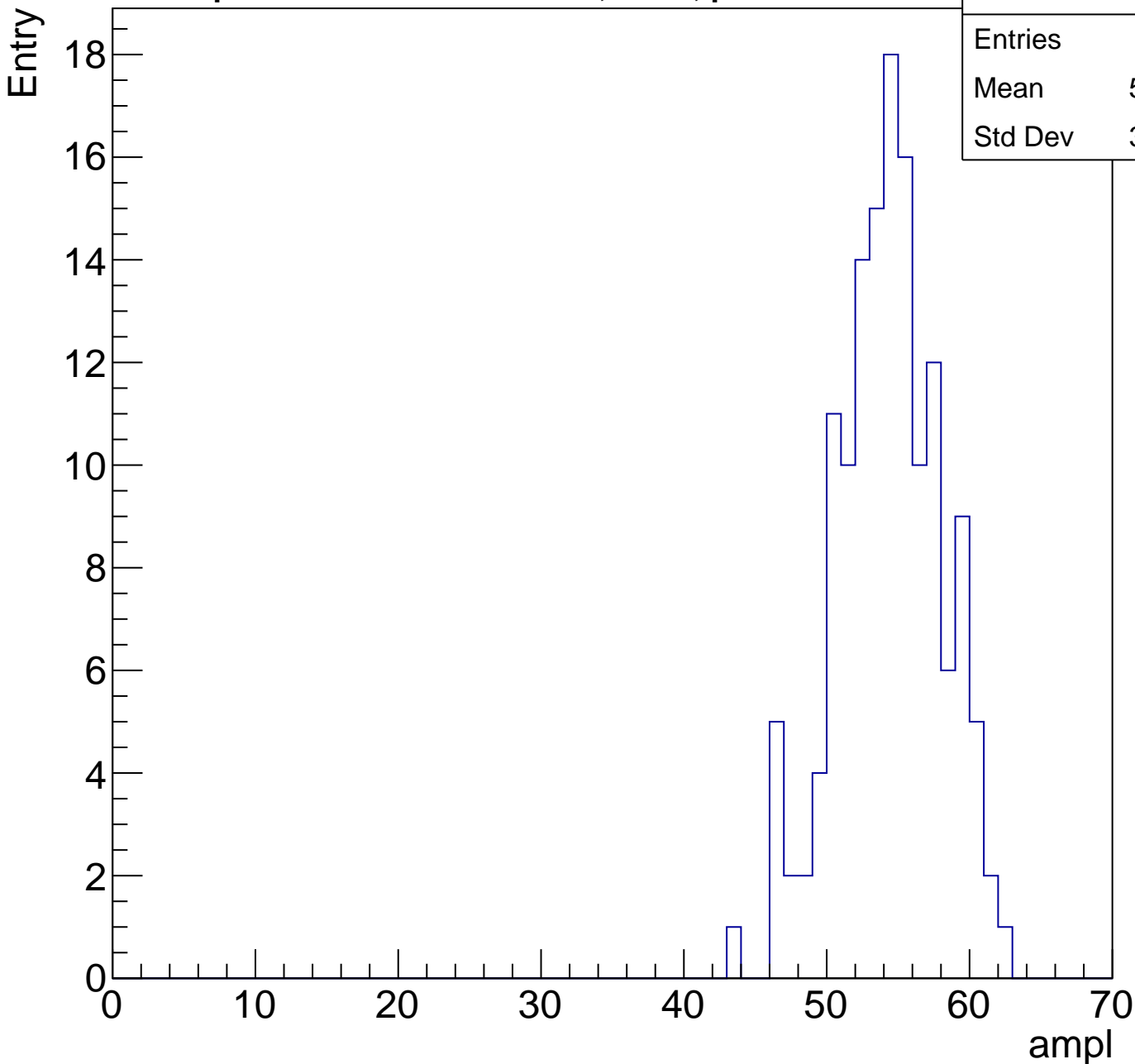
ampl



B0L101S, U22-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 53.85 |
| Std Dev | 3.618 |



B0L101S, U22-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

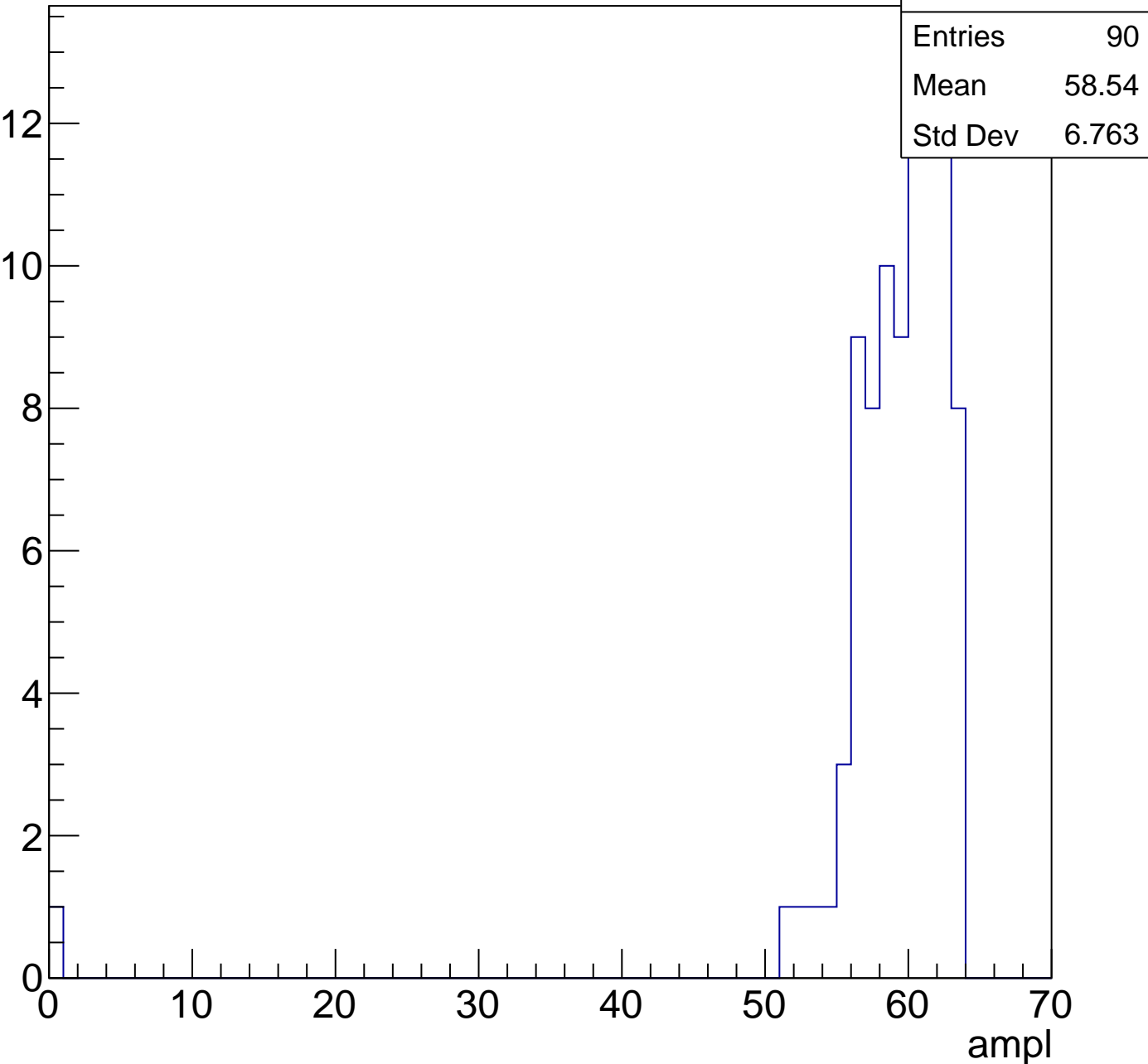
50

60

70

ampl

| | |
|---------|-------|
| Entries | 90 |
| Mean | 58.54 |
| Std Dev | 6.763 |



B0L101S, U22-ch9, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

32

Mean

59.41

Std Dev

10.81

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch10, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 29.79 |
| Std Dev | 3.748 |

Gaus mean : 30.4102

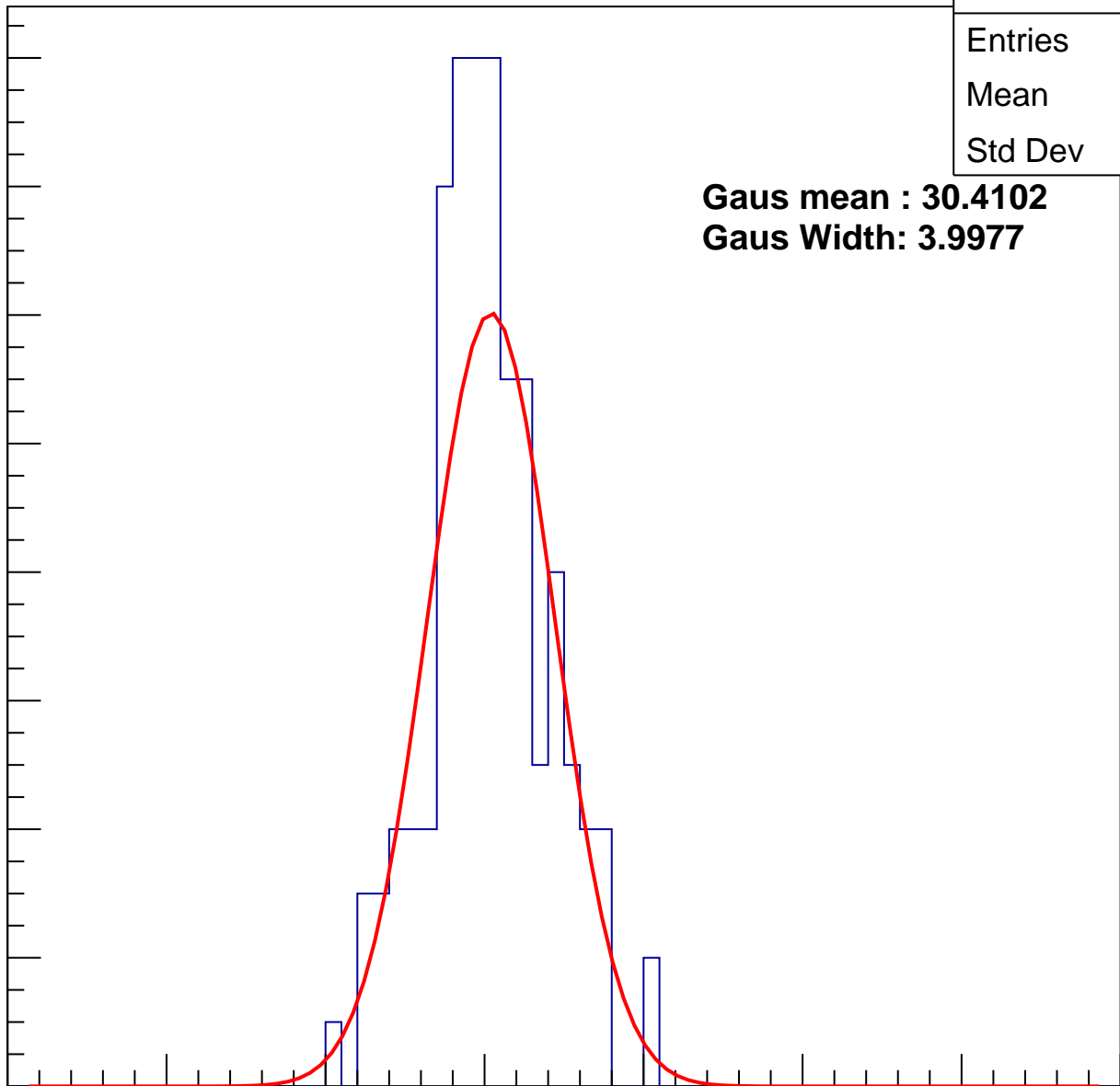
Gaus Width: 3.9977

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch10, adc1

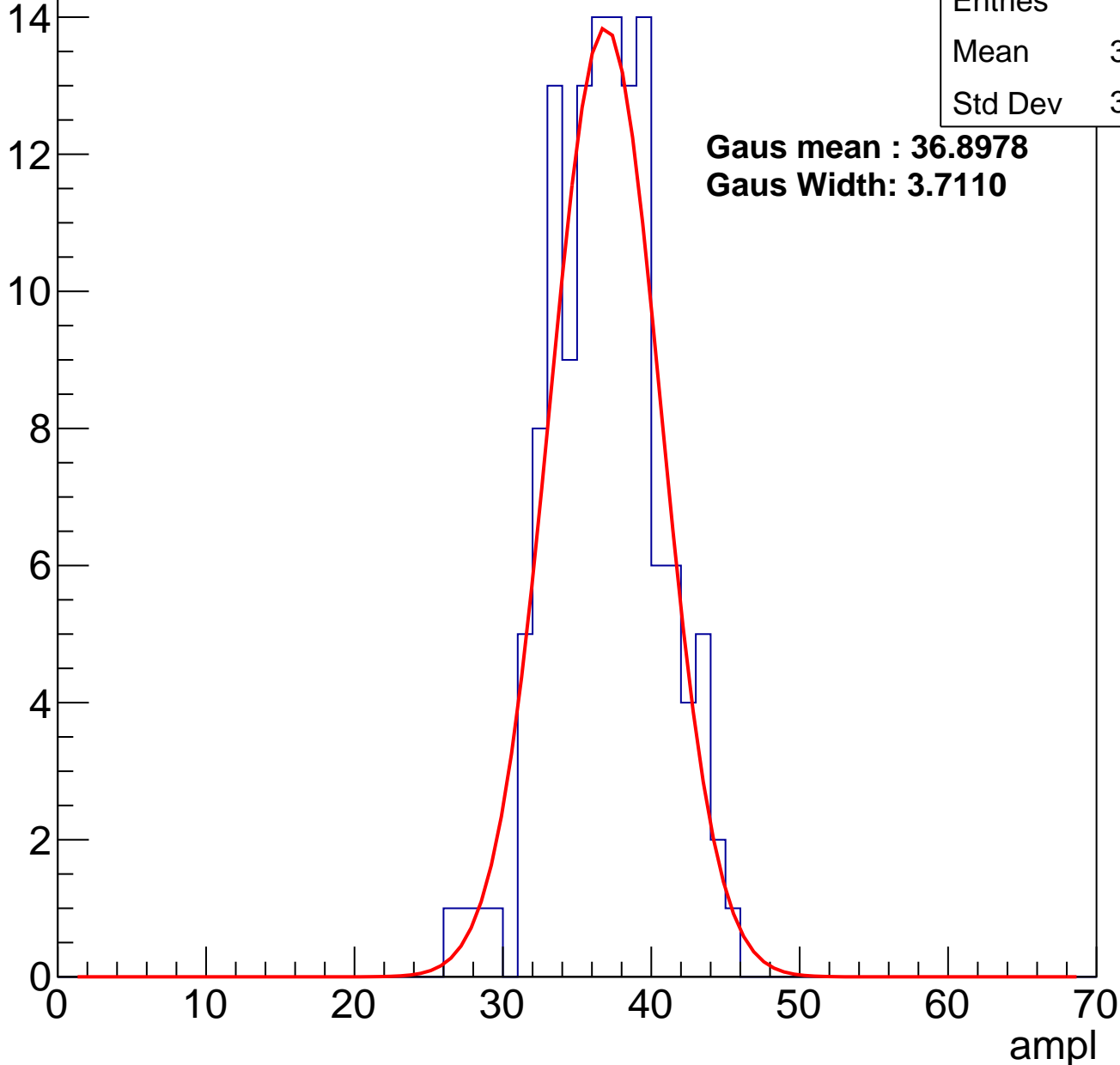
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 36.45 |
| Std Dev | 3.626 |

Gaus mean : 36.8978

Gaus Width: 3.7110

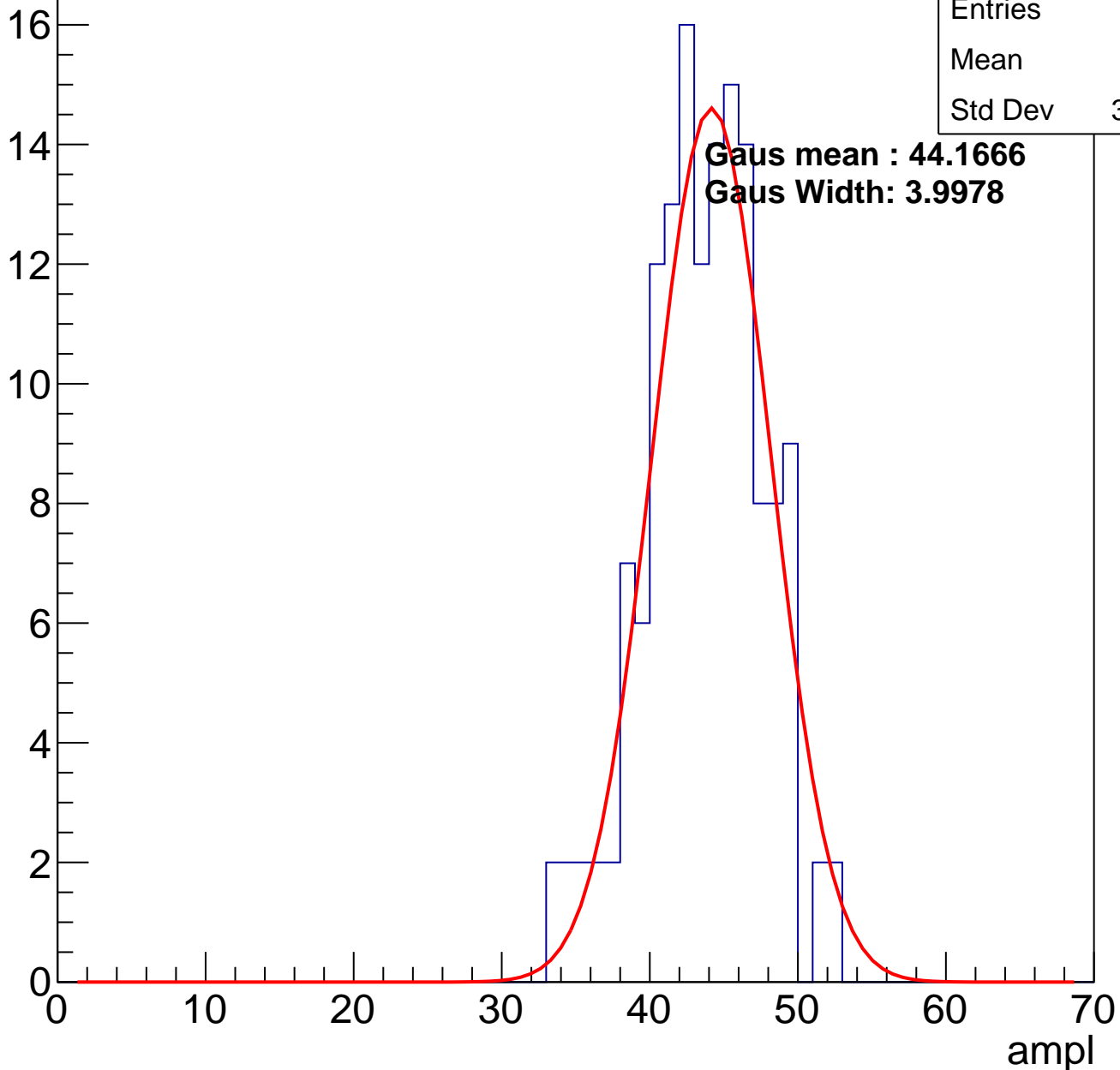
Entry



B0L101S, U22-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

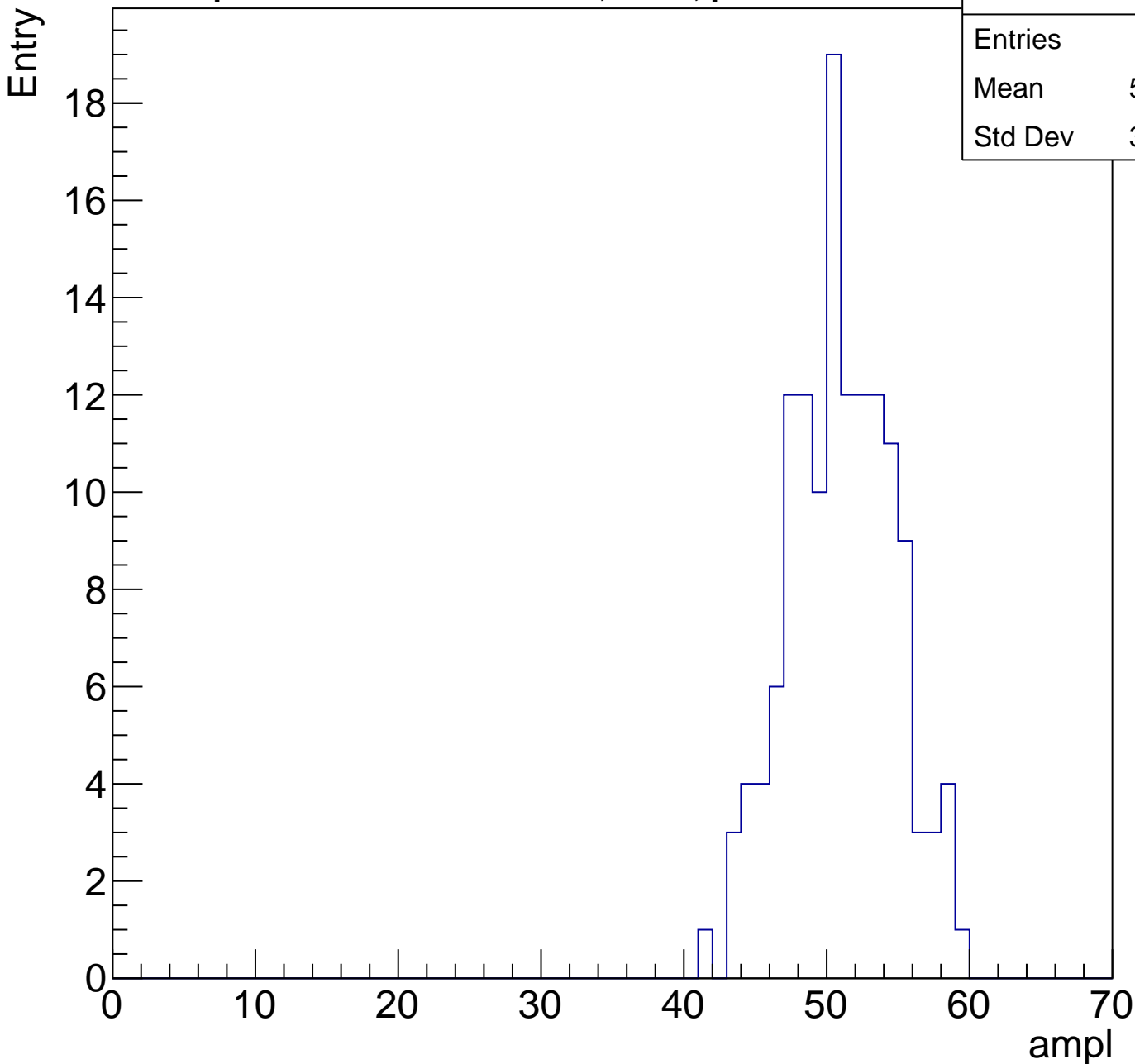
Entry



B0L101S, U22-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 50.53 |
| Std Dev | 3.668 |



B0L101S, U22-ch10, adc4

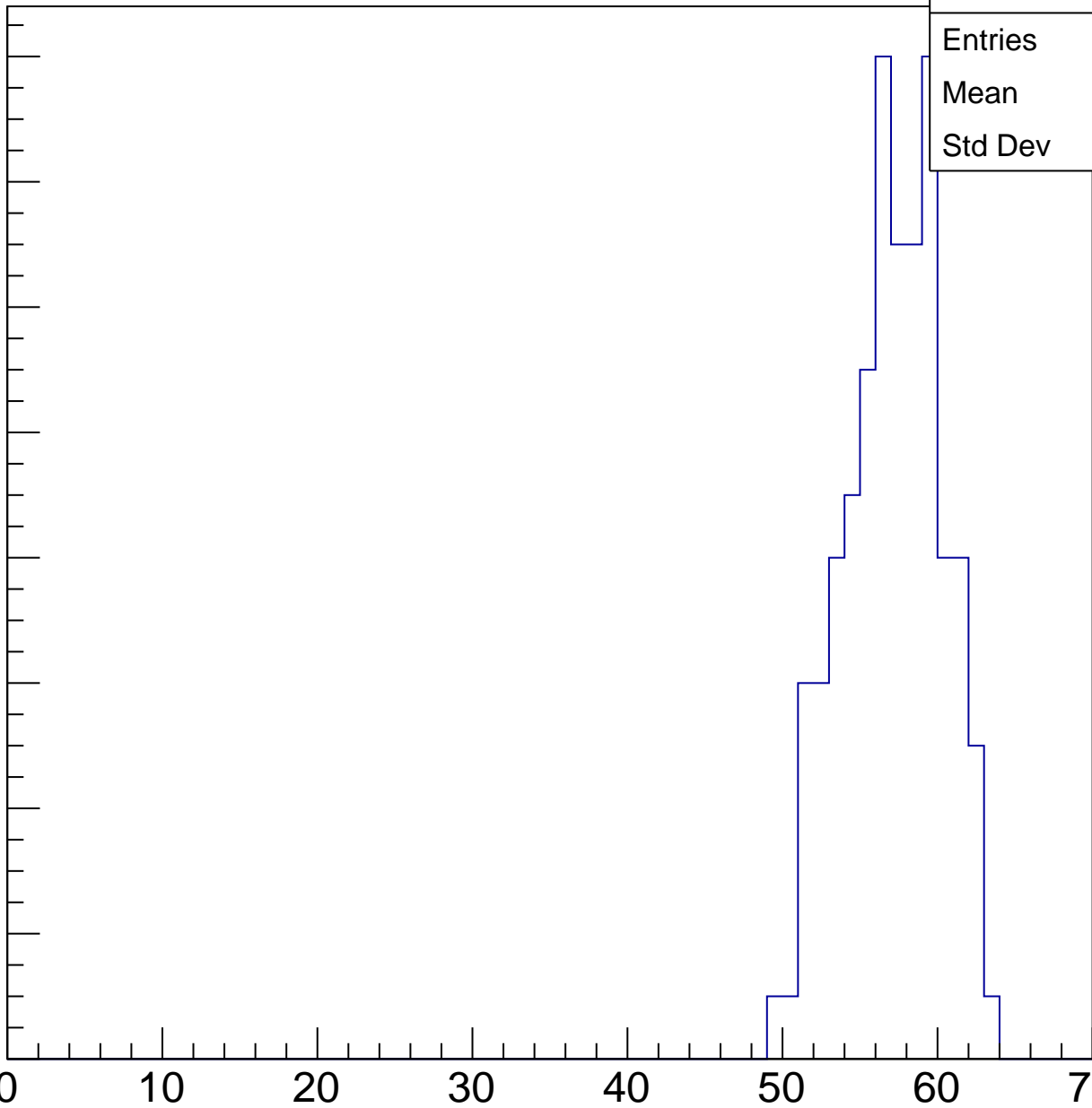
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 122 |
| Mean | 56.62 |
| Std Dev | 3.095 |

ampl



B0L101S, U22-ch10, adc5

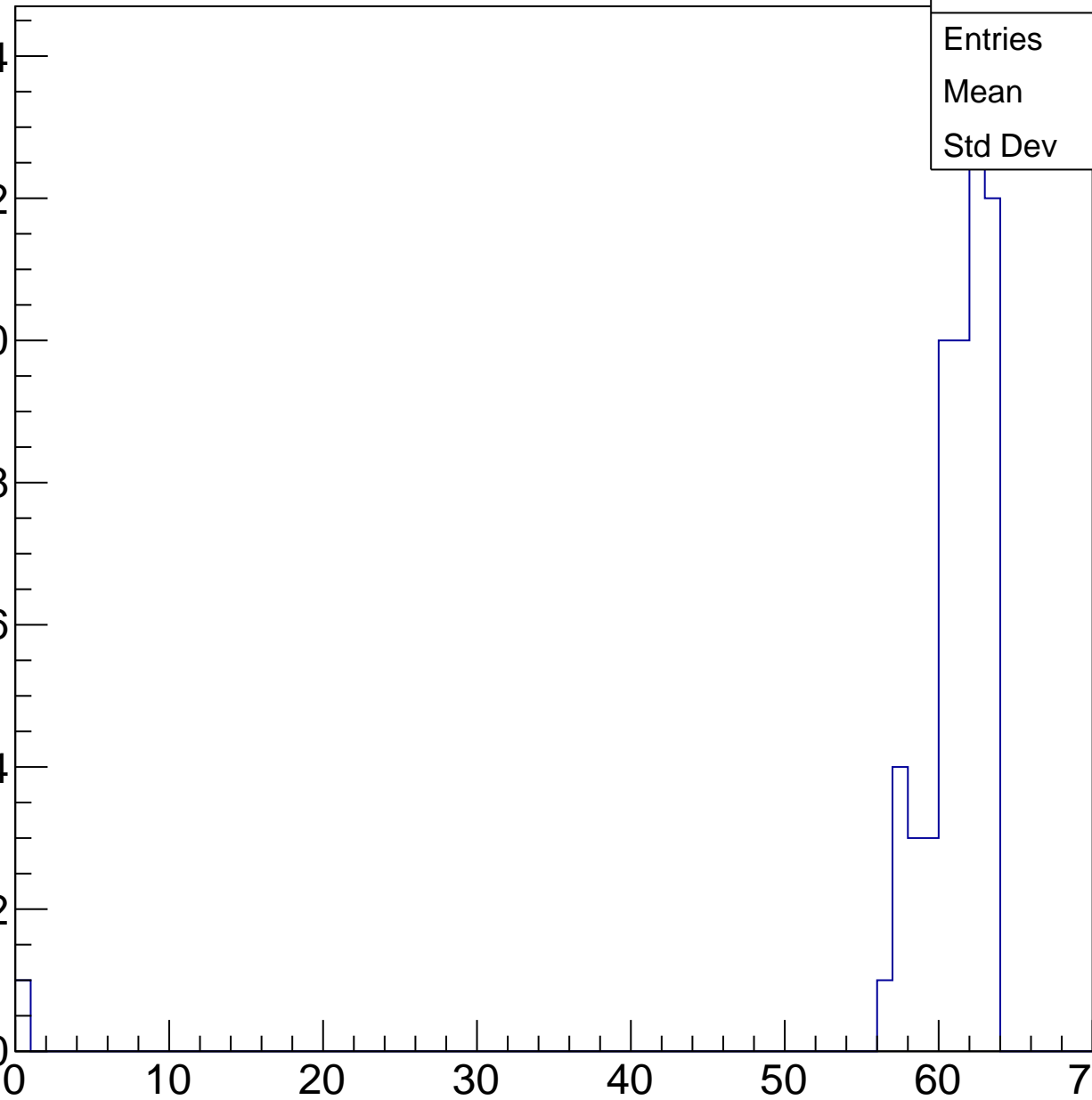
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 59.81 |
| Std Dev | 8.136 |

ampl

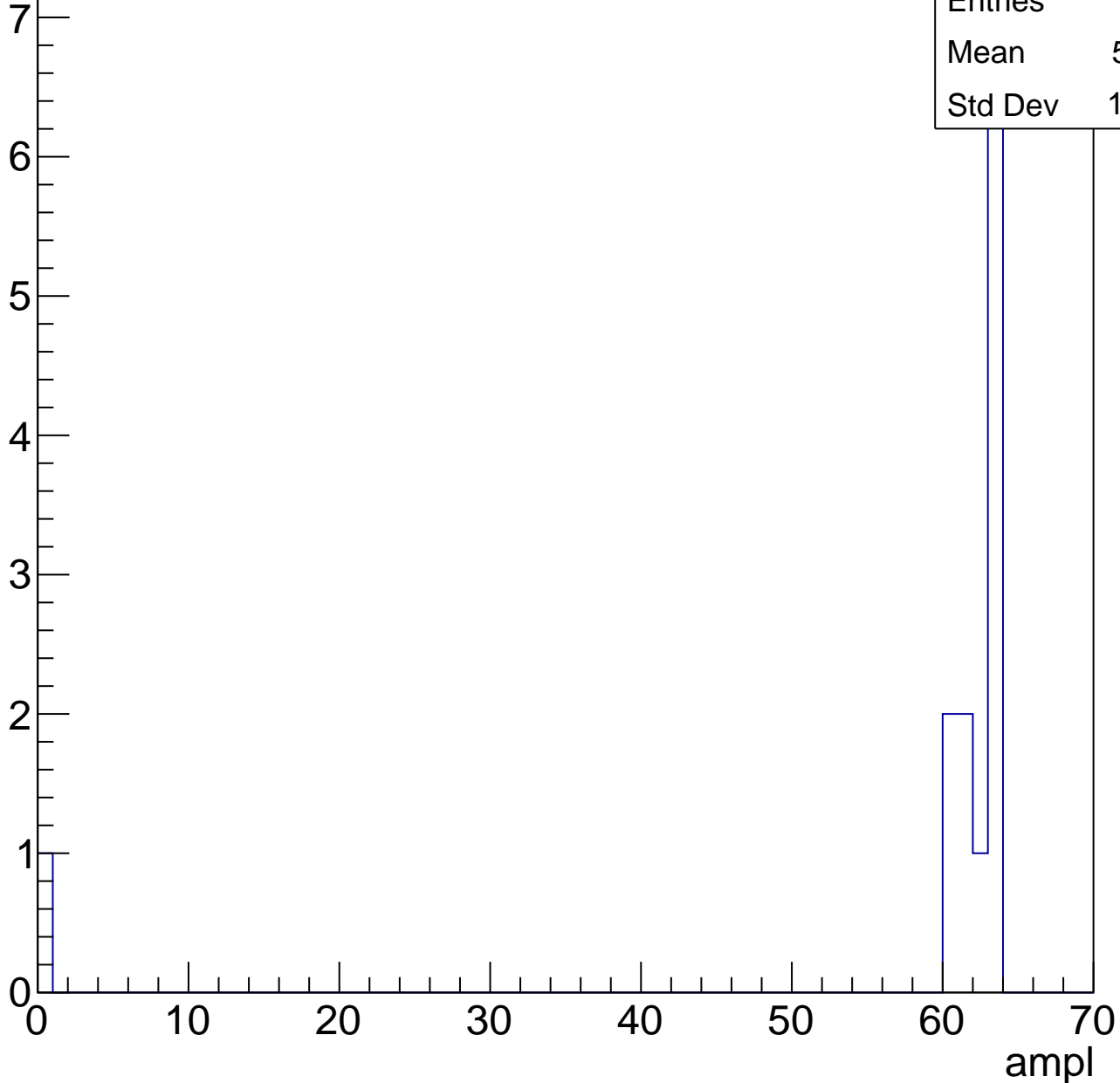


B0L101S, U22-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 57.31 |
| Std Dev | 16.58 |



B0L101S, U22-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

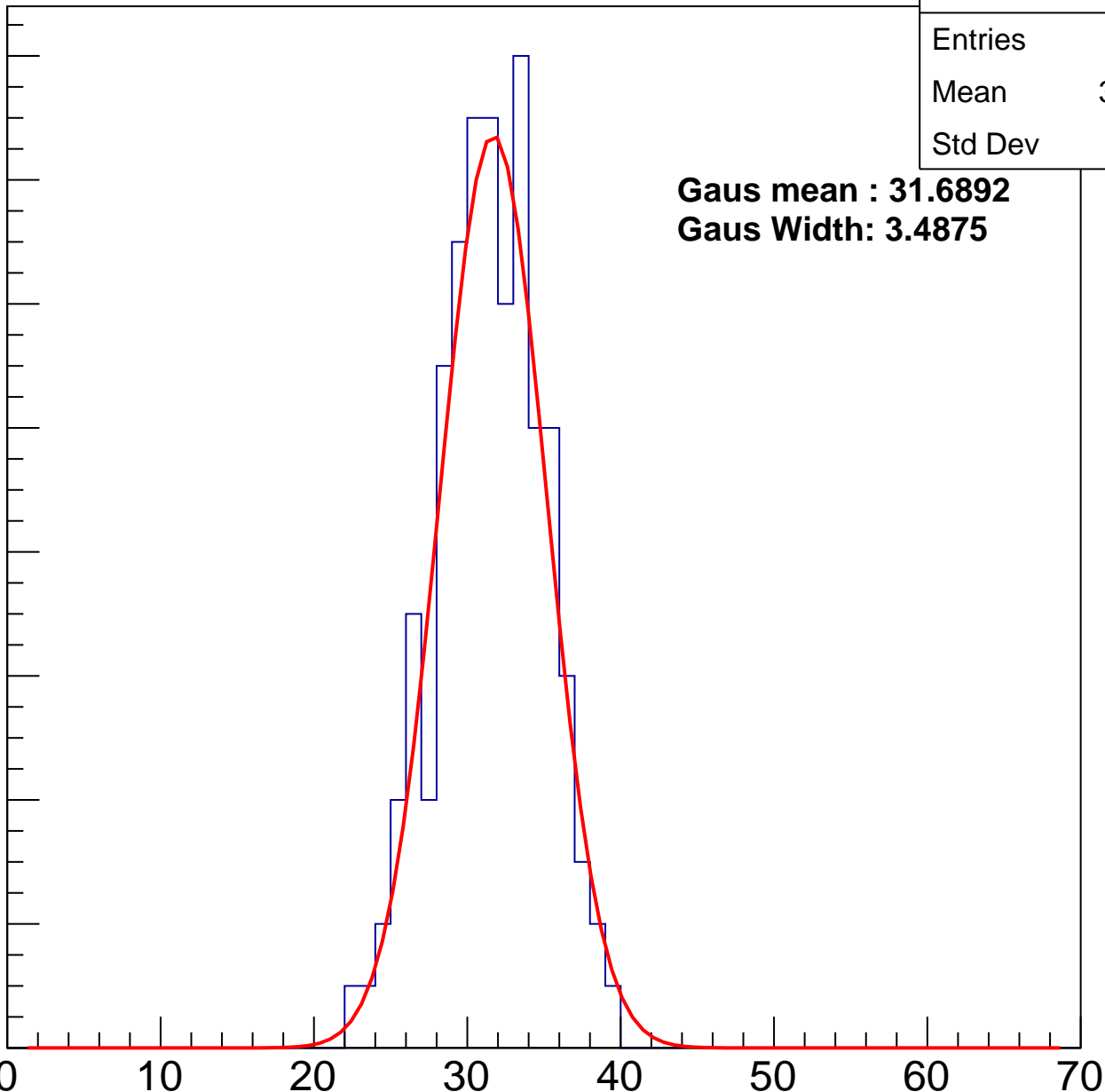
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 133 |
| Mean | 31.03 |
| Std Dev | 3.4 |

Gaus mean : 31.6892

Gaus Width: 3.4875

ampl

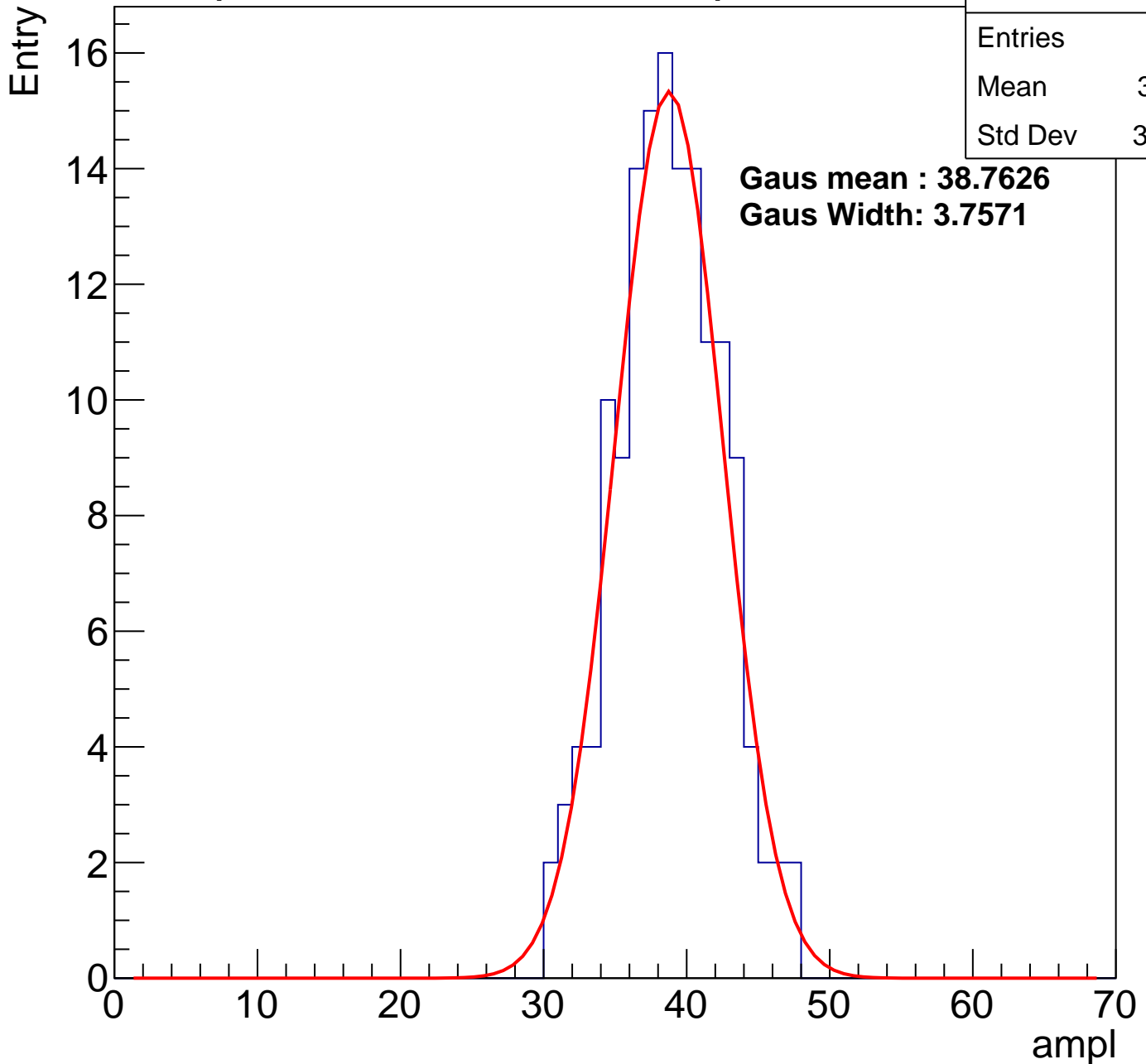


B0L101S, U22-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 38.31 |
| Std Dev | 3.626 |

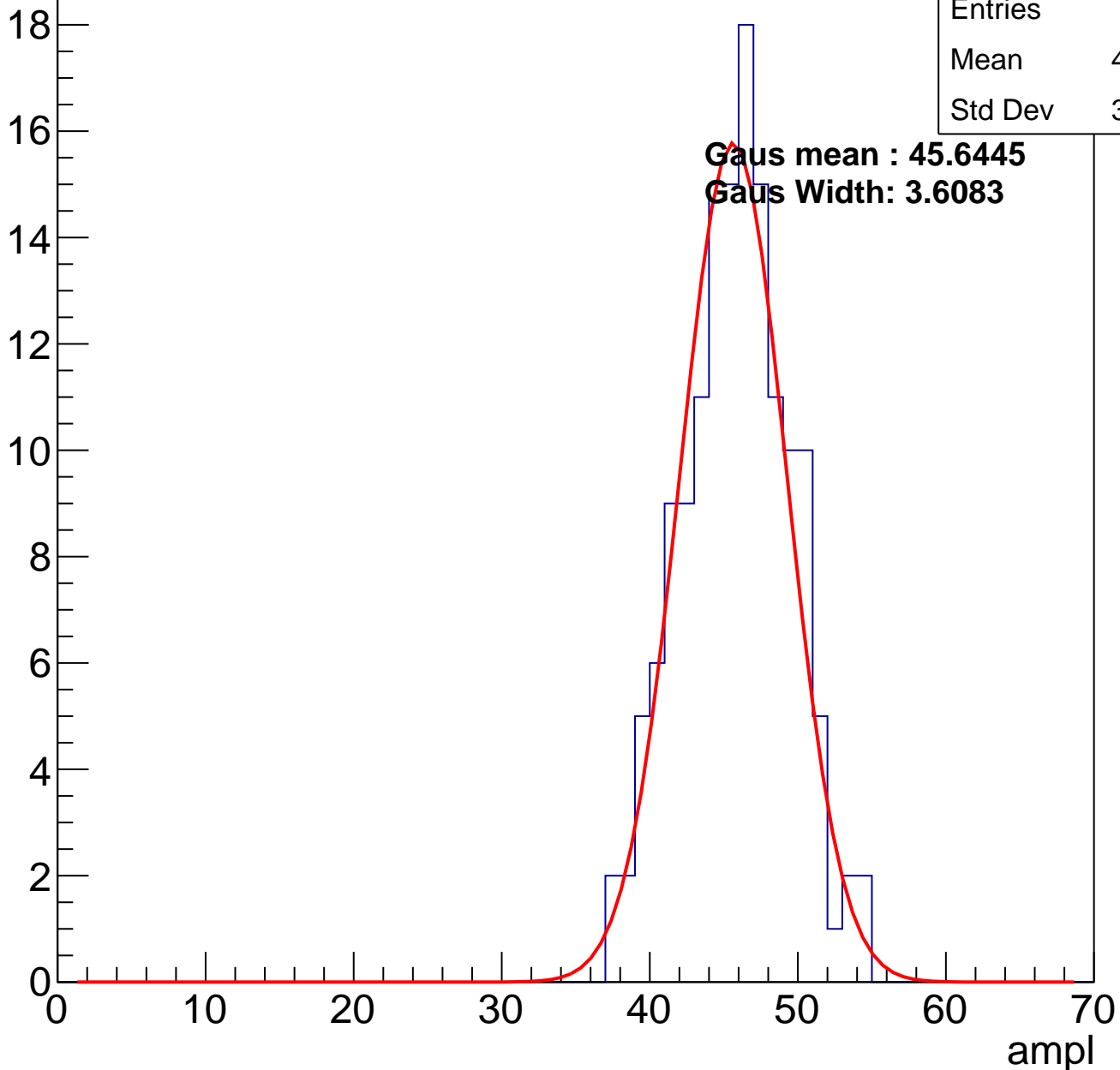
Gaus mean : 38.7626
Gaus Width: 3.7571



B0L101S, U22-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 52.48 |
| Std Dev | 3.27 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

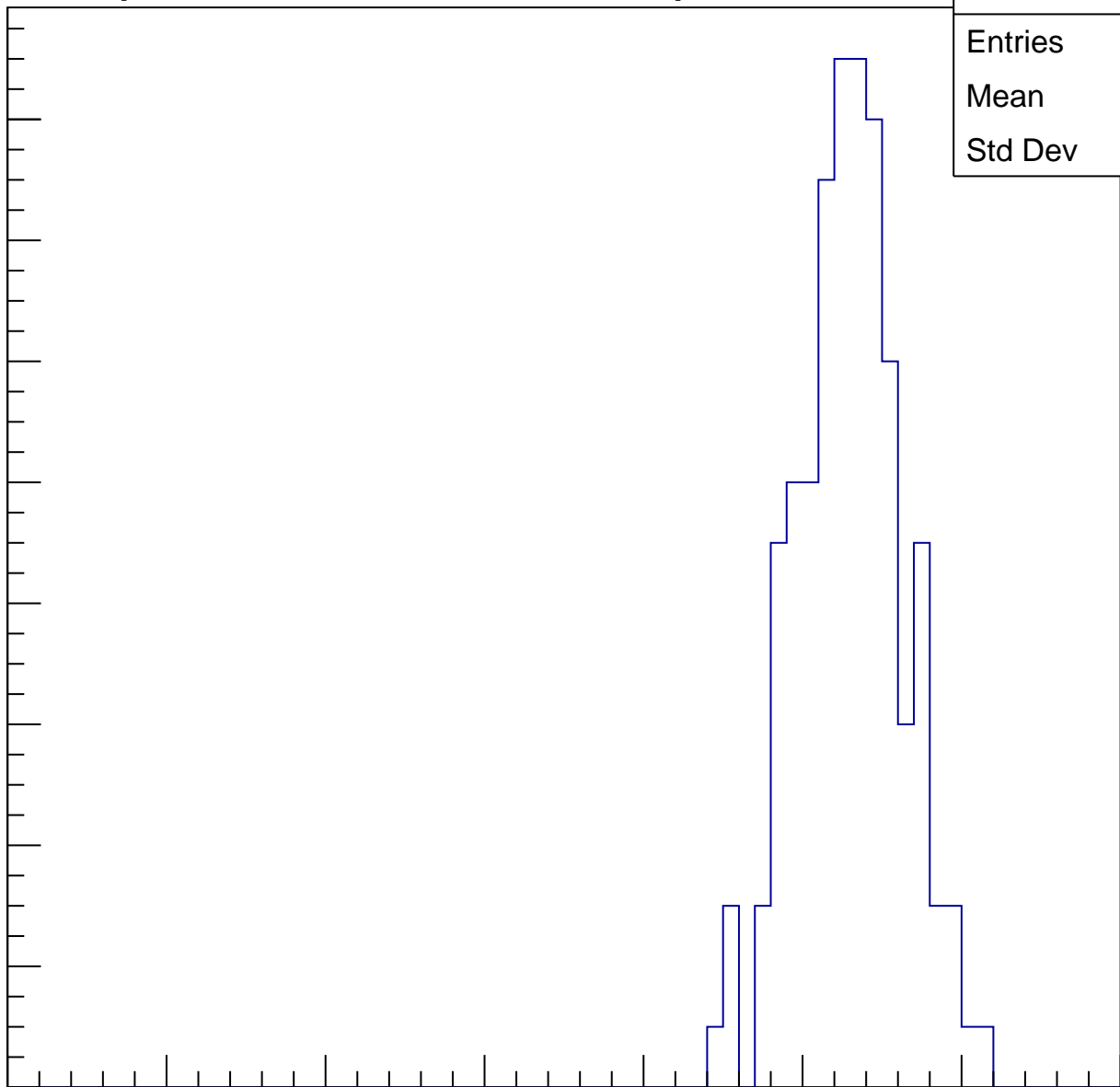
40

50

60

70

ampl



B0L101S, U22-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 94 |
| Mean | 57.76 |
| Std Dev | 6.557 |

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B0L101S, U22-ch11, adc5

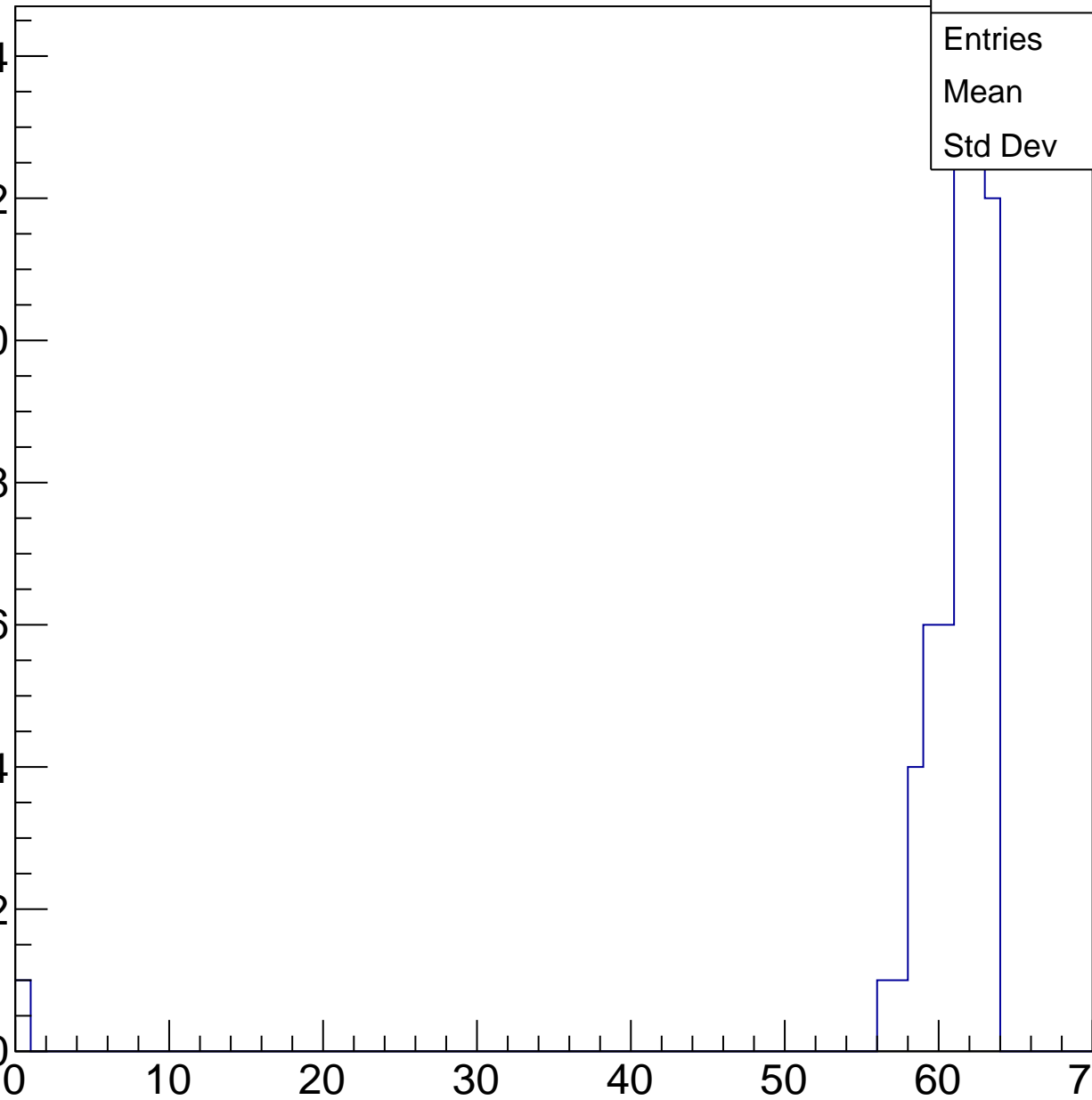
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 59.91 |
| Std Dev | 8.116 |

ampl



B0L101S, U22-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 30.11 |
| Std Dev | 4.185 |

Gaus mean : 30.8343

Gaus Width: 3.2770

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

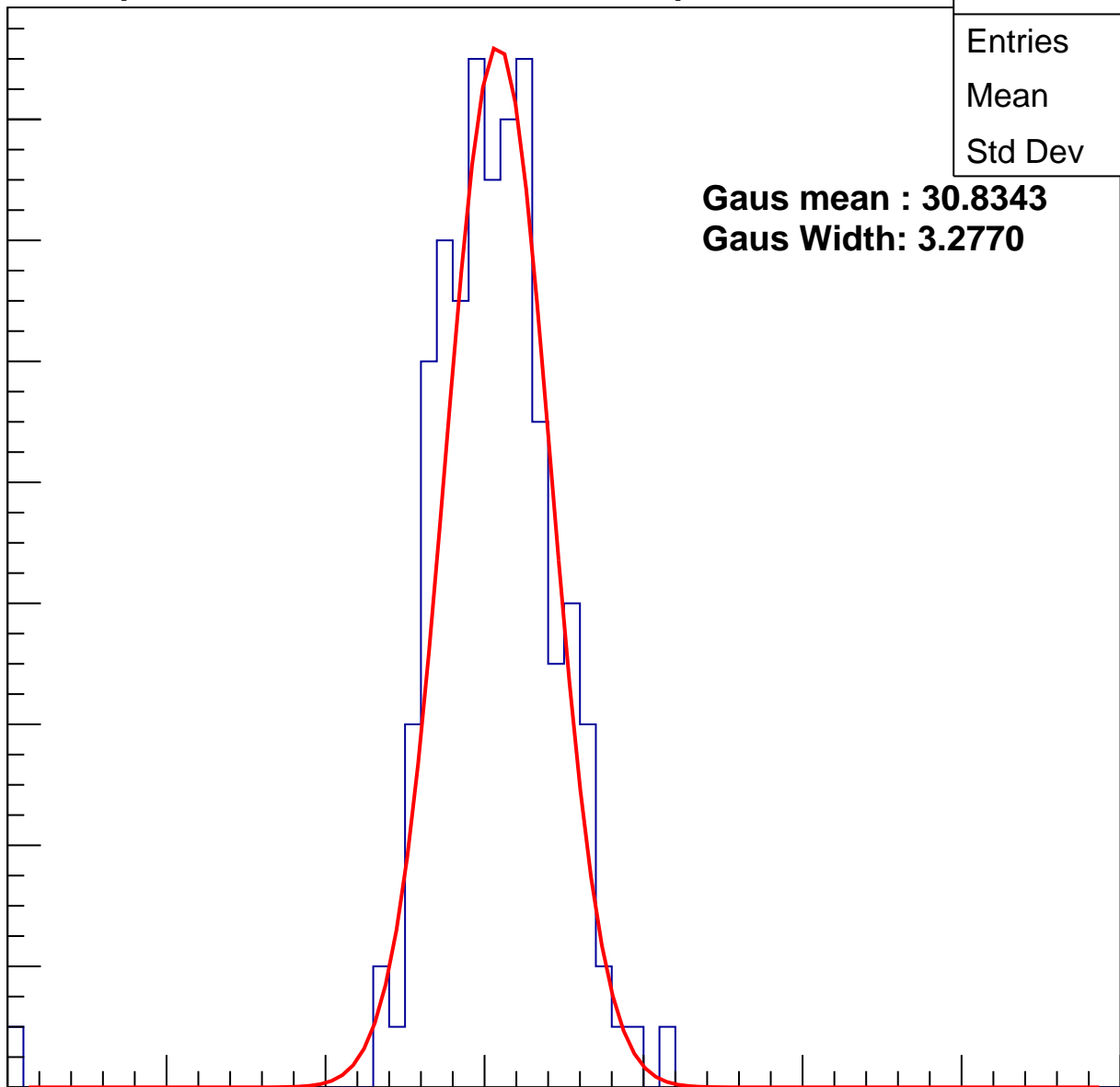
40

50

60

70

ampl



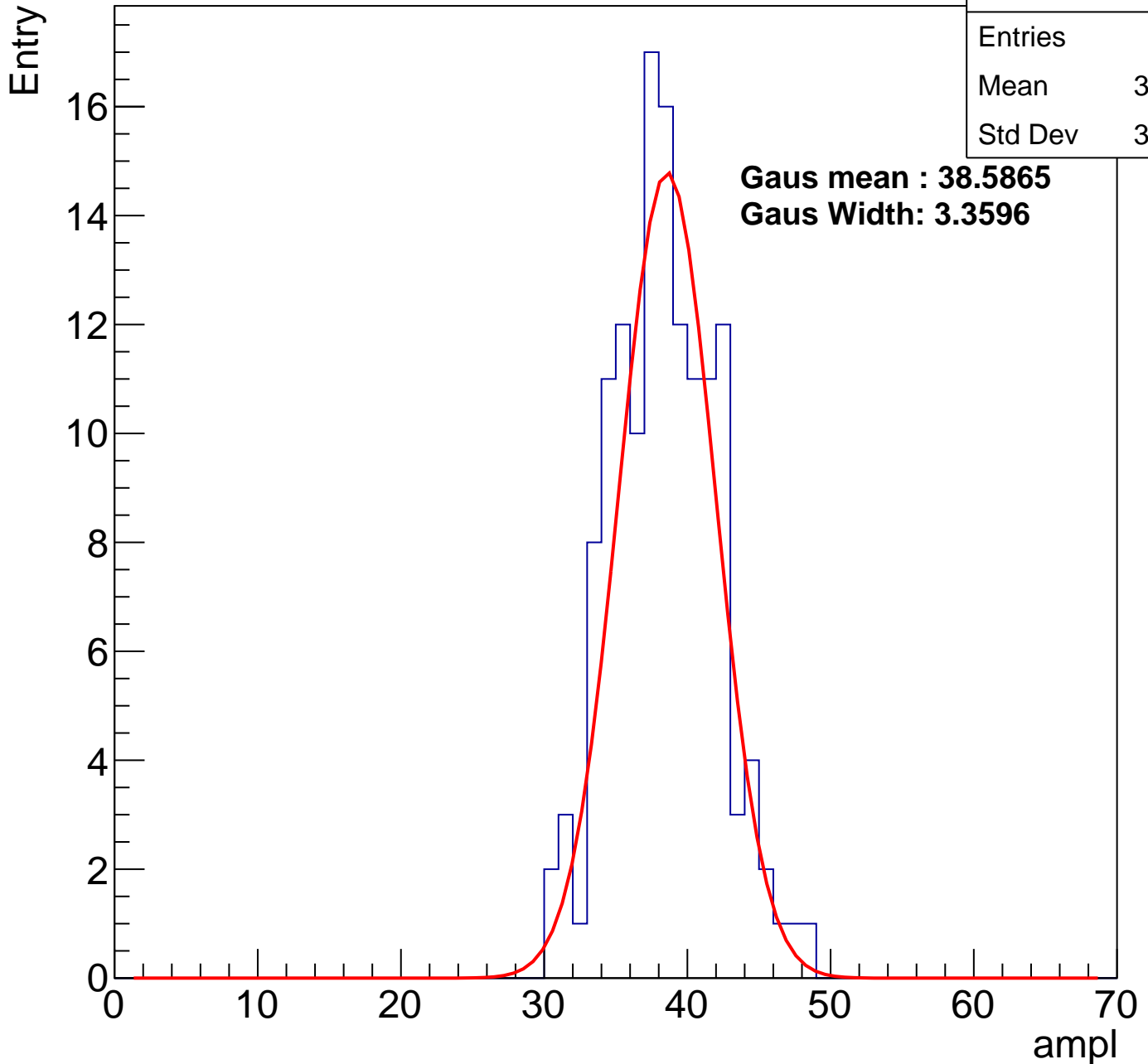
B0L101S, U22-ch12, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 37.96 |
| Std Dev | 3.572 |

Gaus mean : 38.5865

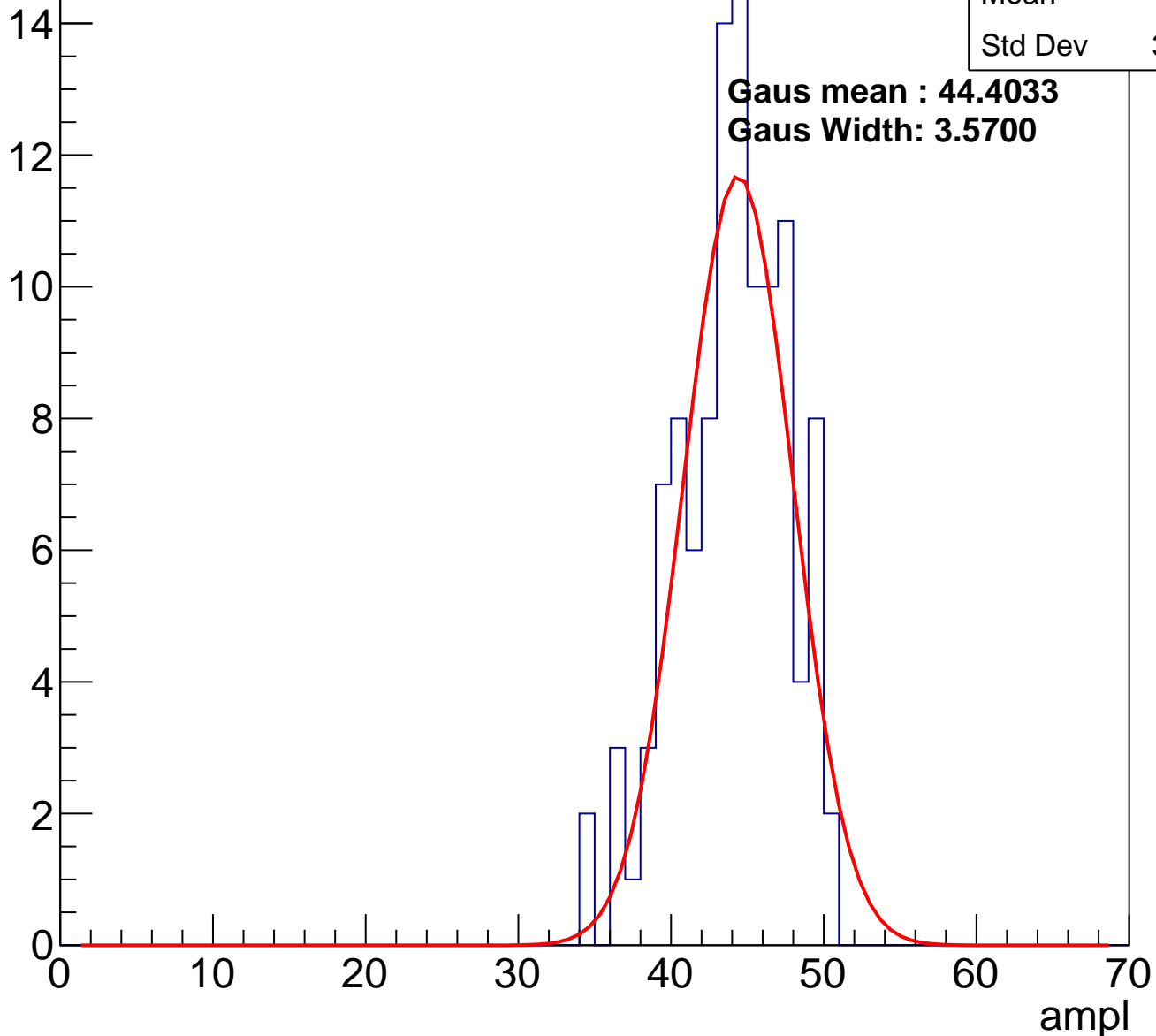
Gaus Width: 3.3596



B0L101S, U22-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

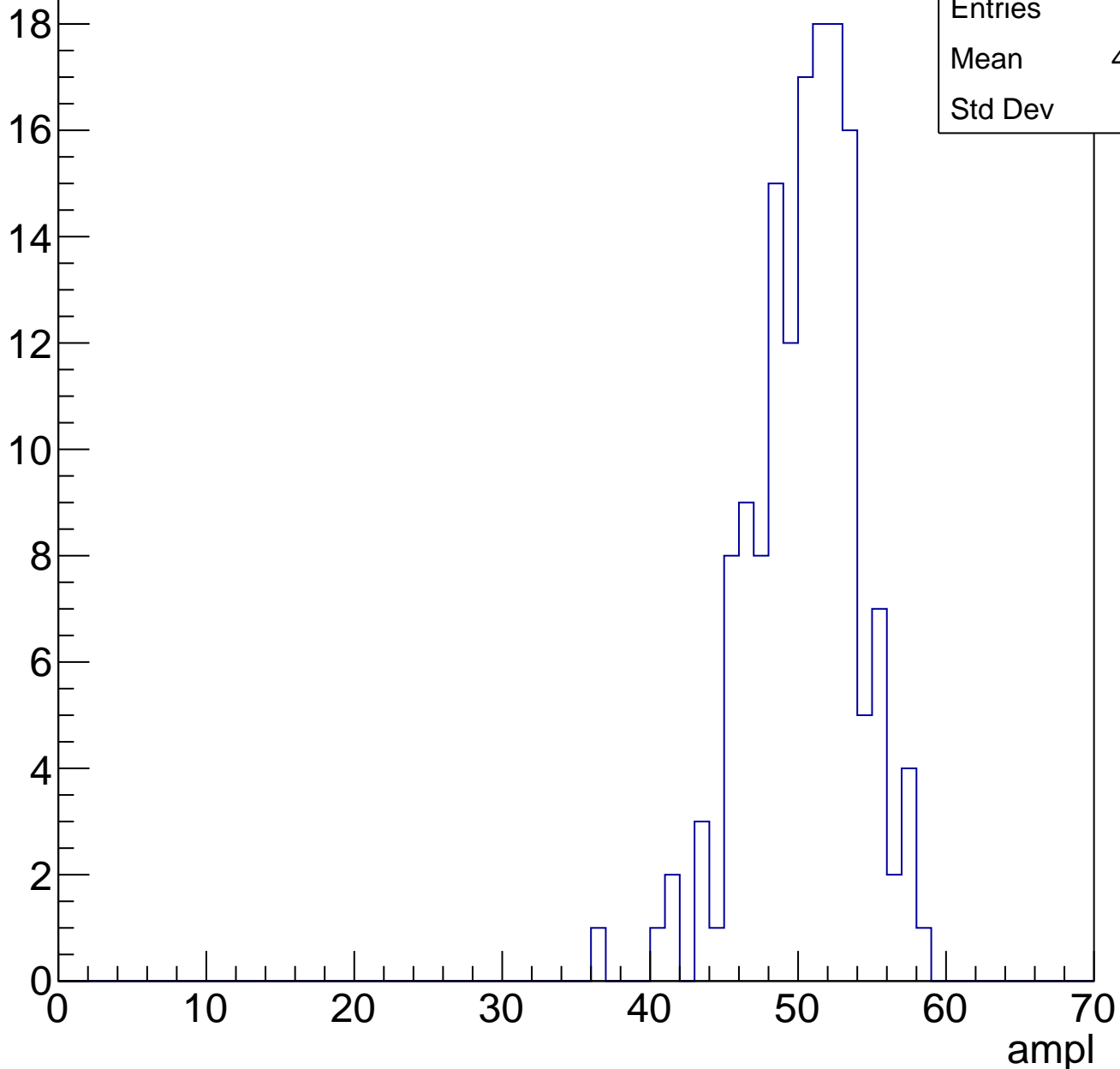


B0L101S, U22-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 49.96 |
| Std Dev | 3.63 |

Entry



B0L101S, U22-ch12, adc4

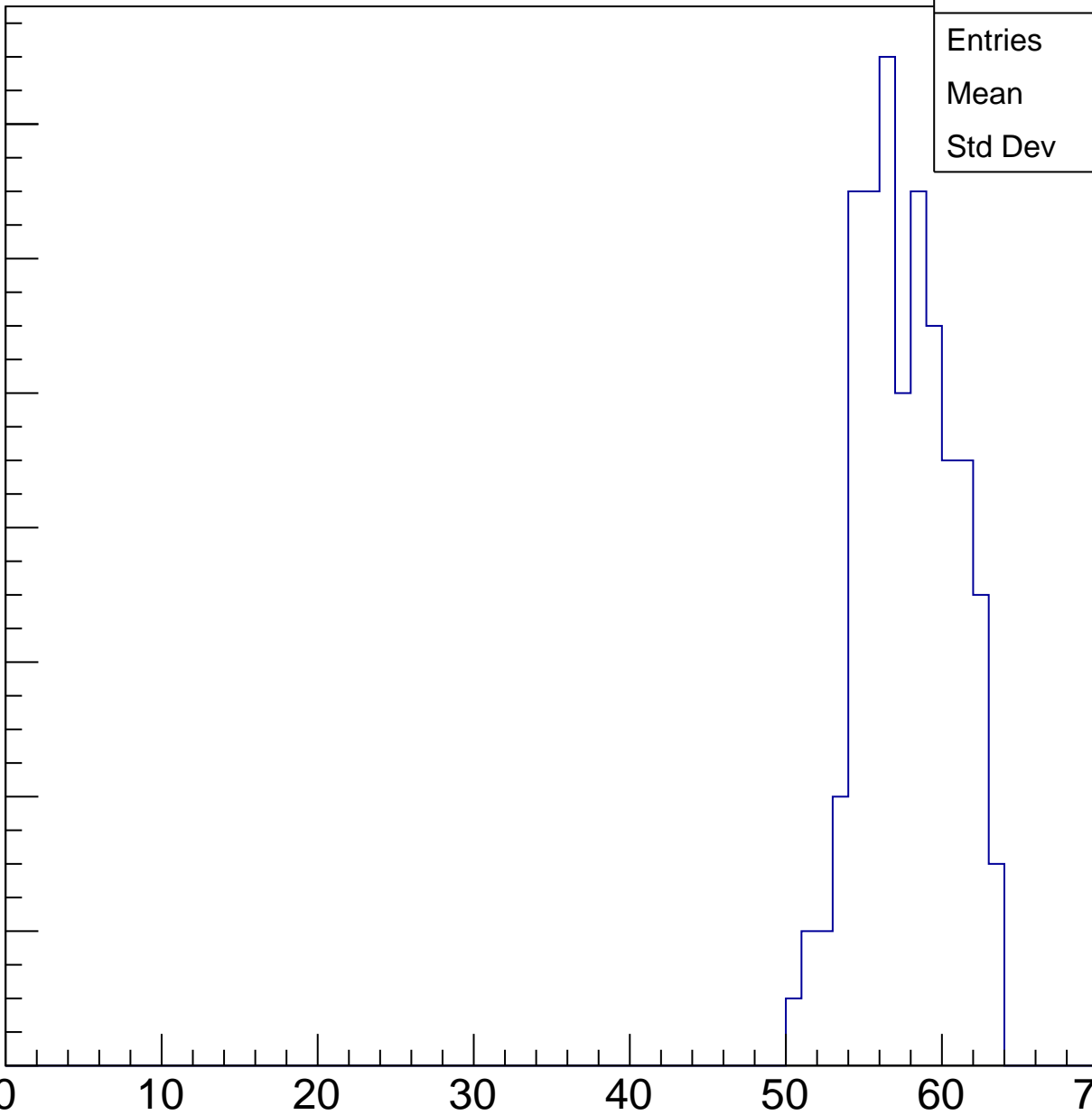
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 112 |
| Mean | 57.23 |
| Std Dev | 2.955 |

ampl



B0L101S, U22-ch12, adc5

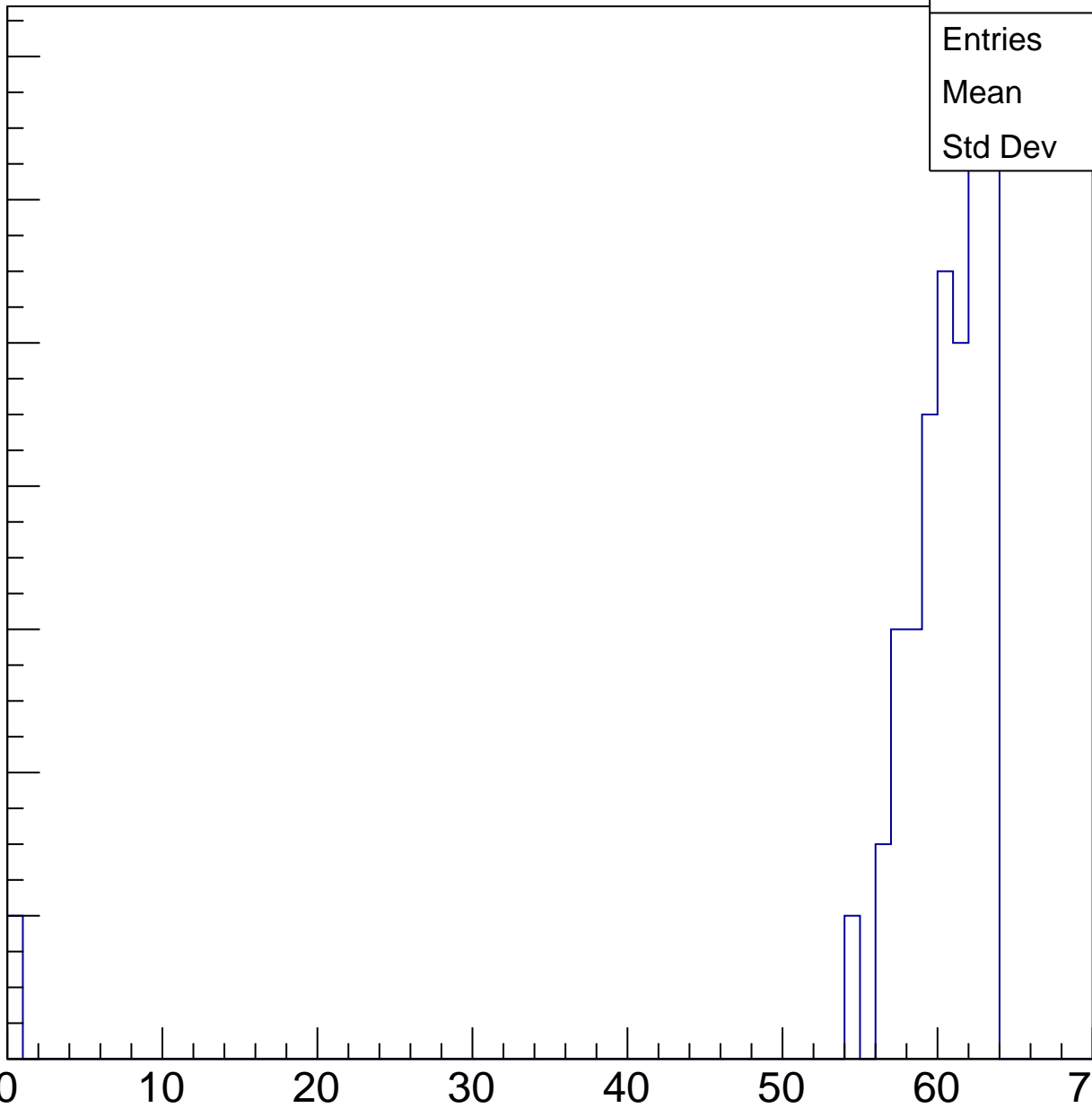
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 58.62 |
| Std Dev | 9.901 |

ampl



B0L101S, U22-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B0L101S, U22-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 29.48 |
| Std Dev | 4.183 |

Gaus mean : 30.2184

Gaus Width: 3.4854

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

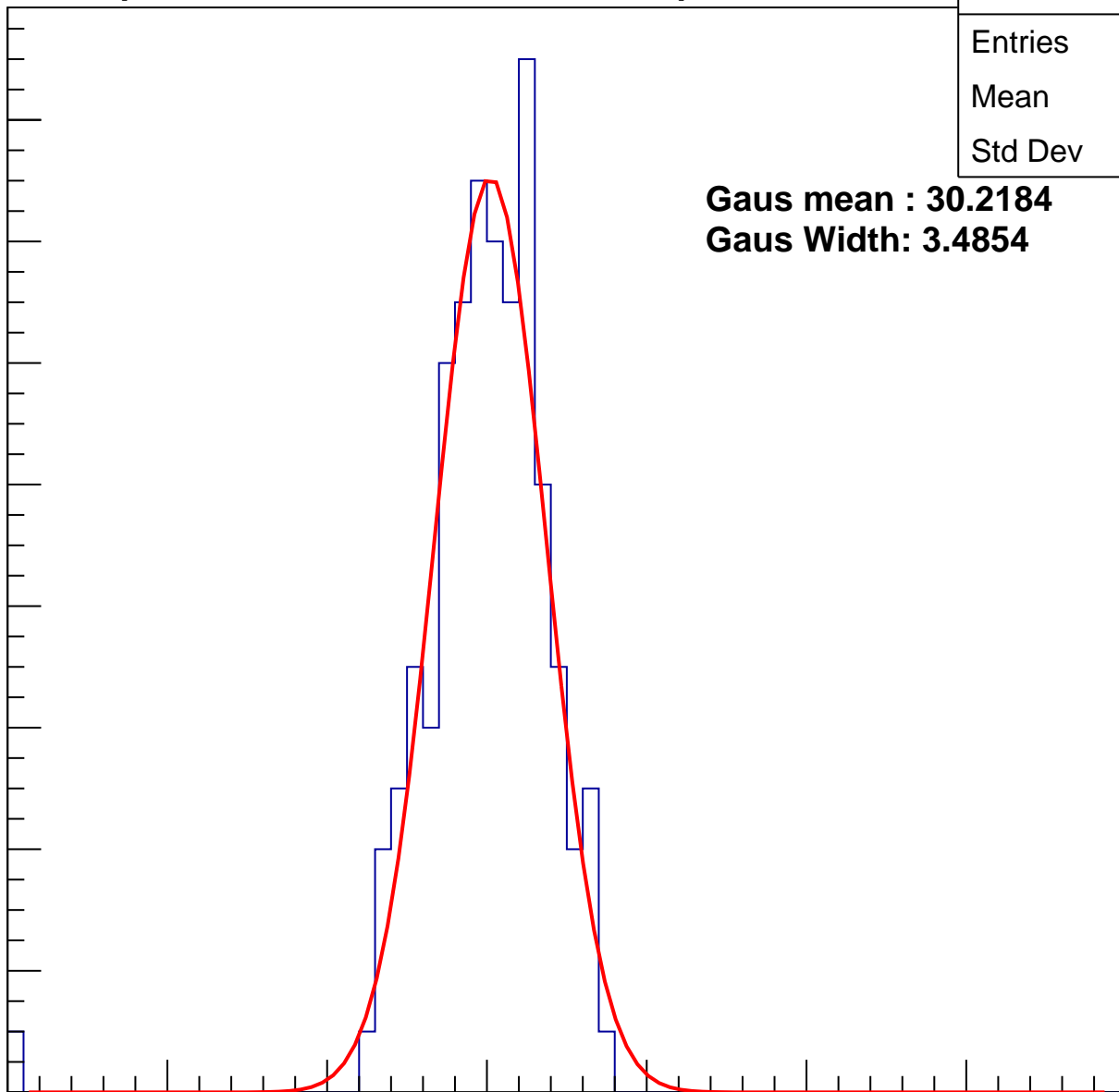
40

50

60

70

ampl



B0L101S, U22-ch13, adc1

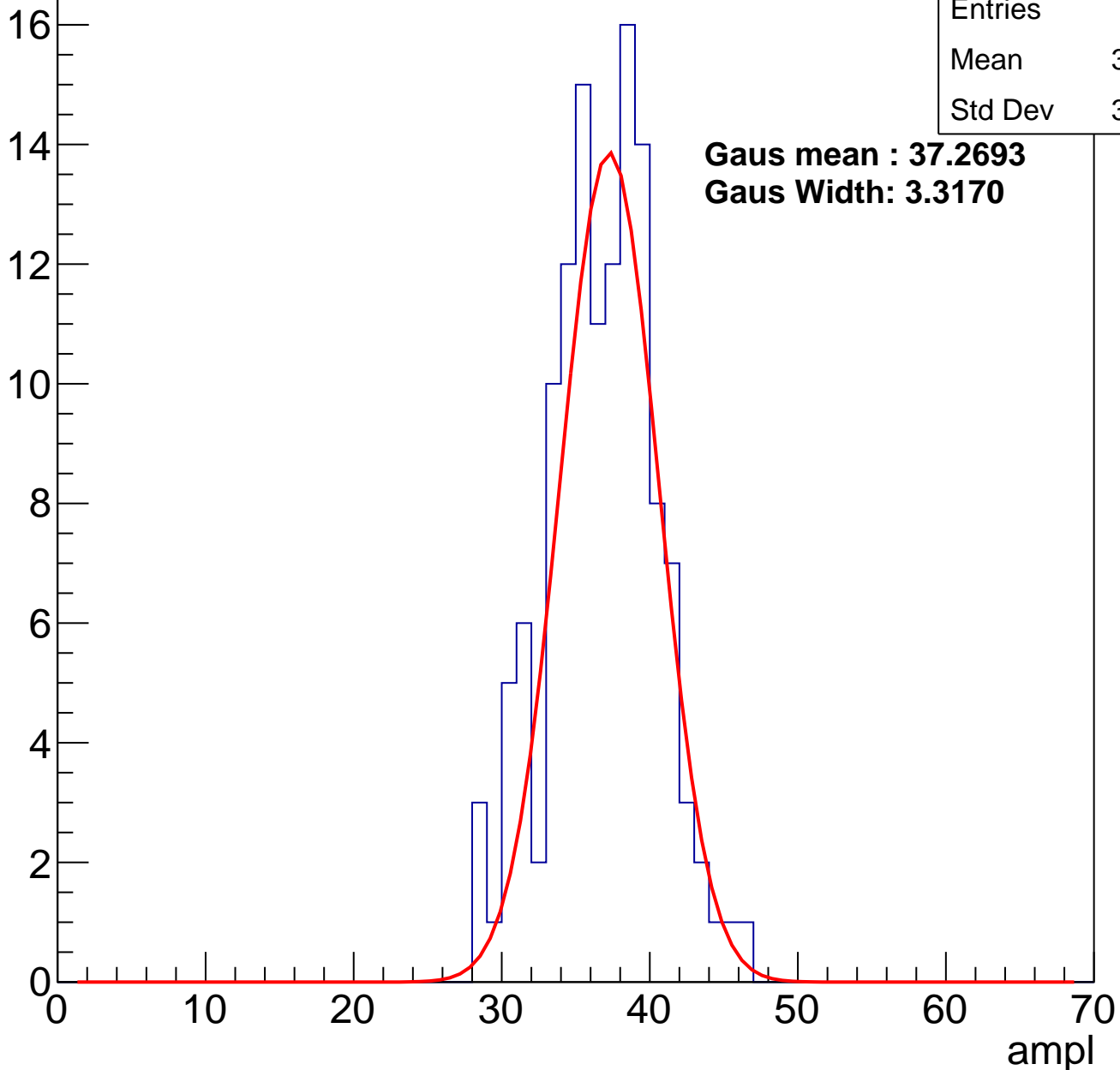
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 36.34 |
| Std Dev | 3.613 |

Gaus mean : 37.2693

Gaus Width: 3.3170

Entry



B0L101S, U22-ch13, adc2

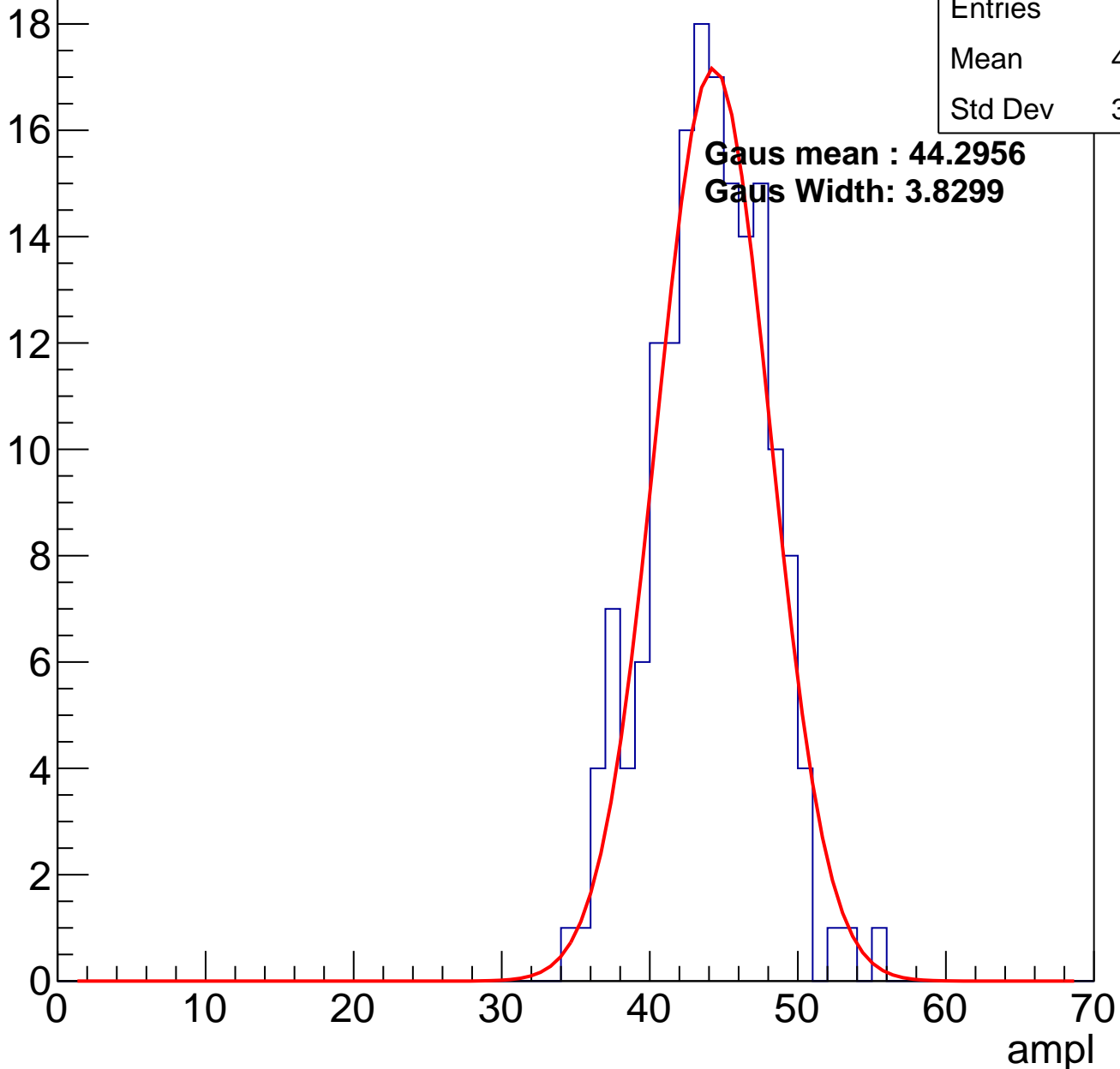
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 167 |
| Mean | 43.59 |
| Std Dev | 3.794 |

Gaus mean : 44.2956

Gaus Width: 3.8299

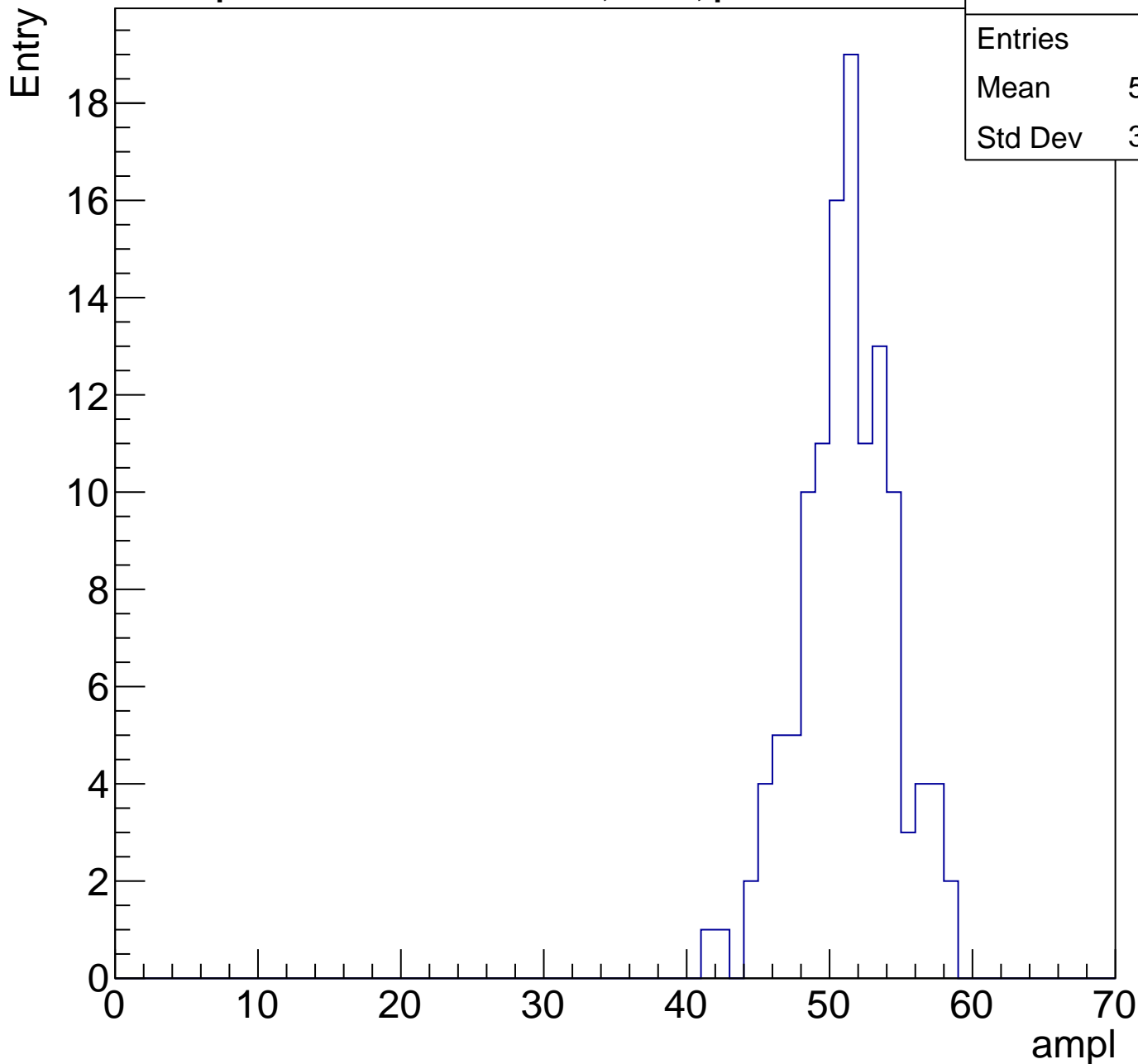
Entry



B0L101S, U22-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

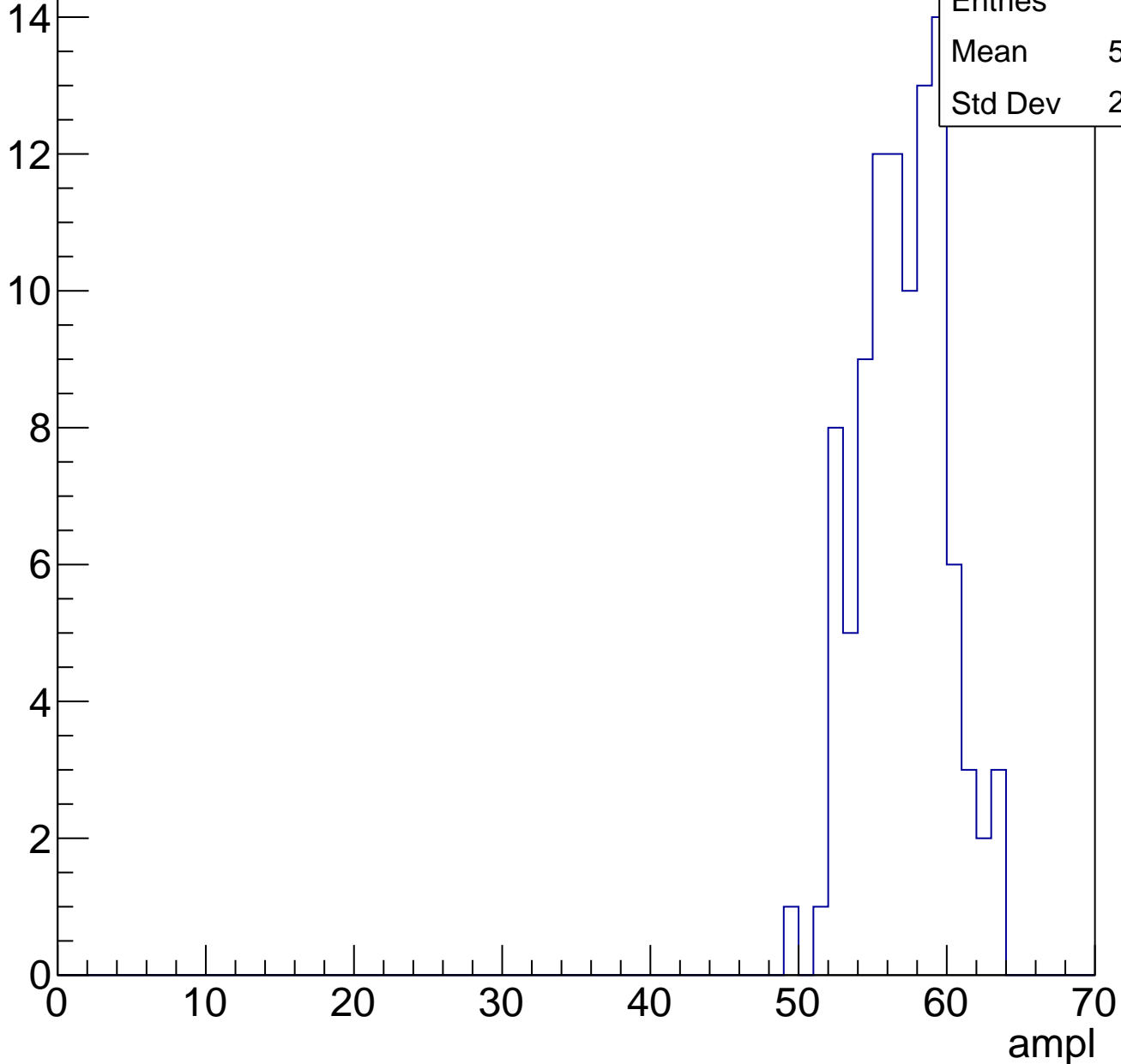
| | |
|---------|-------|
| Entries | 121 |
| Mean | 50.73 |
| Std Dev | 3.293 |



B0L101S, U22-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch13, adc5

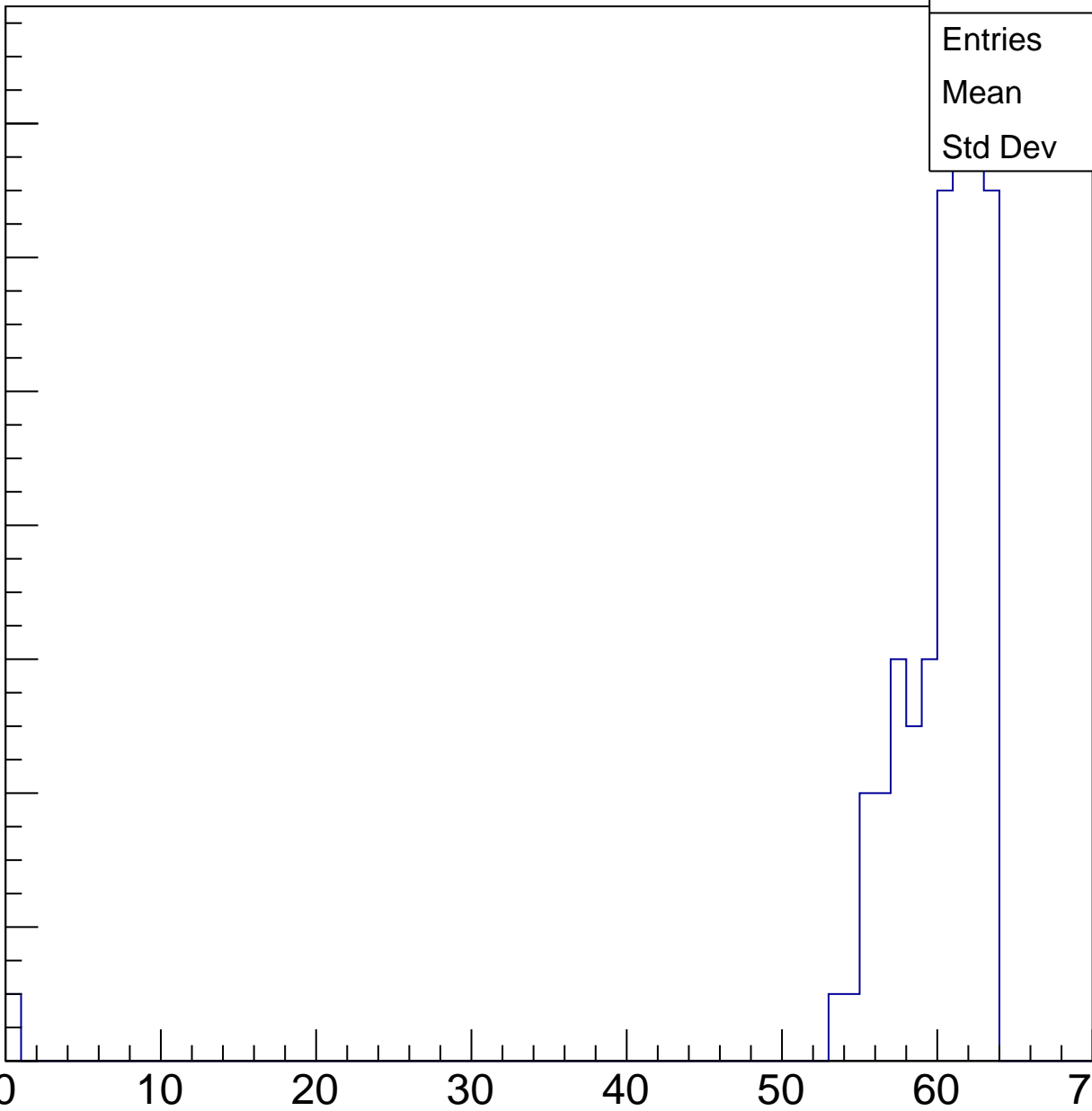
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 59.27 |
| Std Dev | 7.004 |

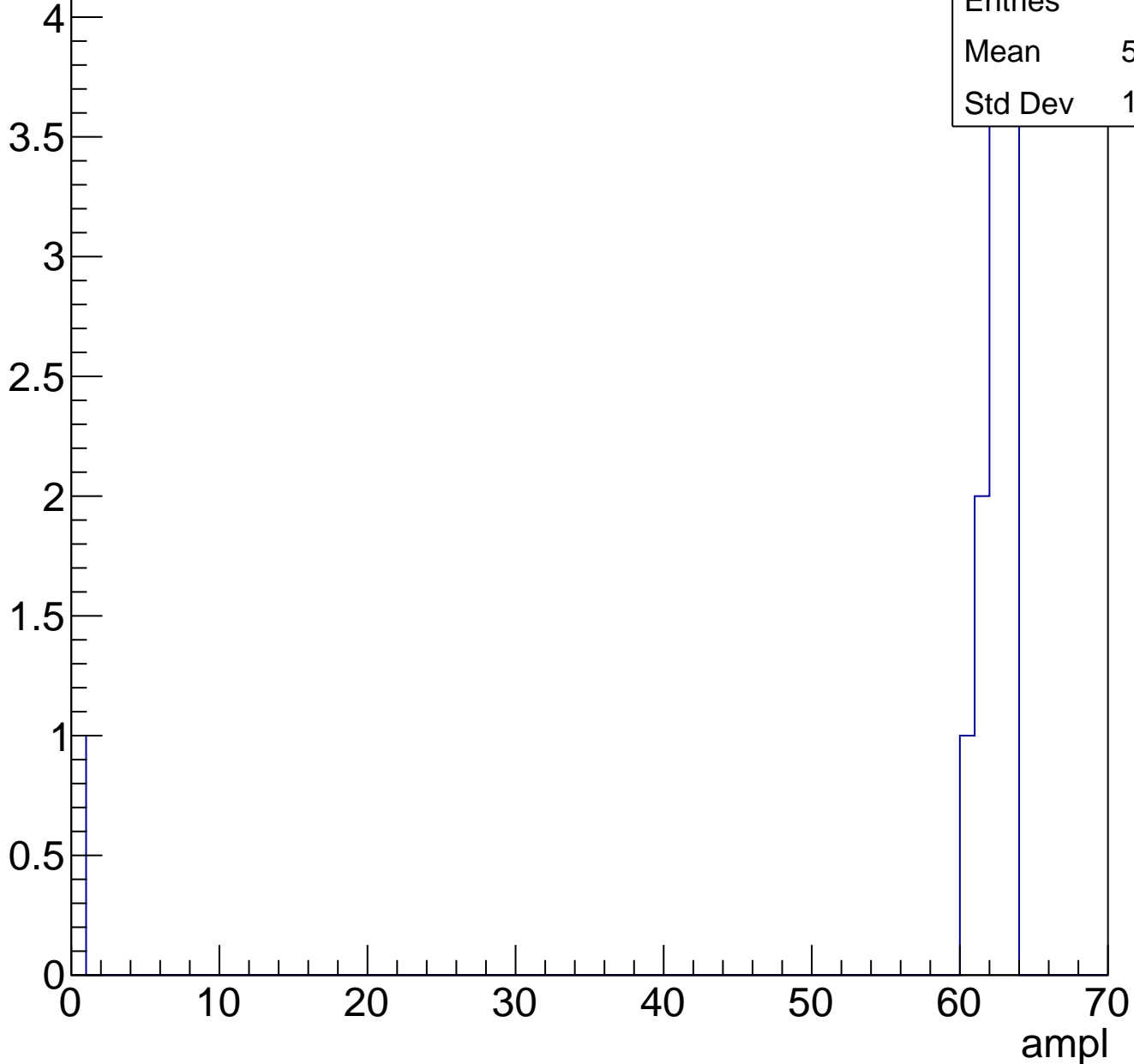
ampl



B0L101S, U22-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch13, adc7

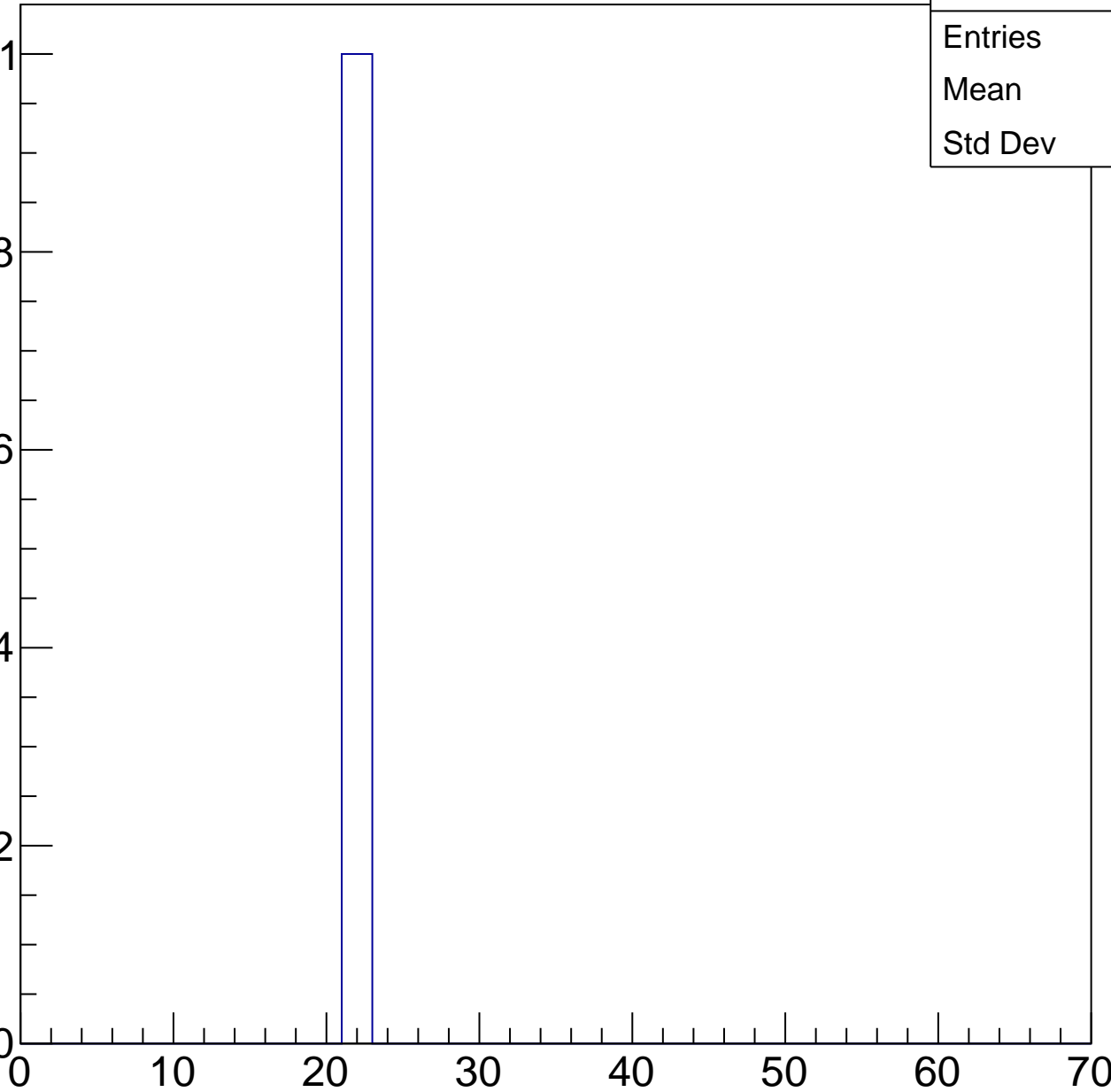
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 2 |
| Mean | 21.5 |
| Std Dev | 0.5 |

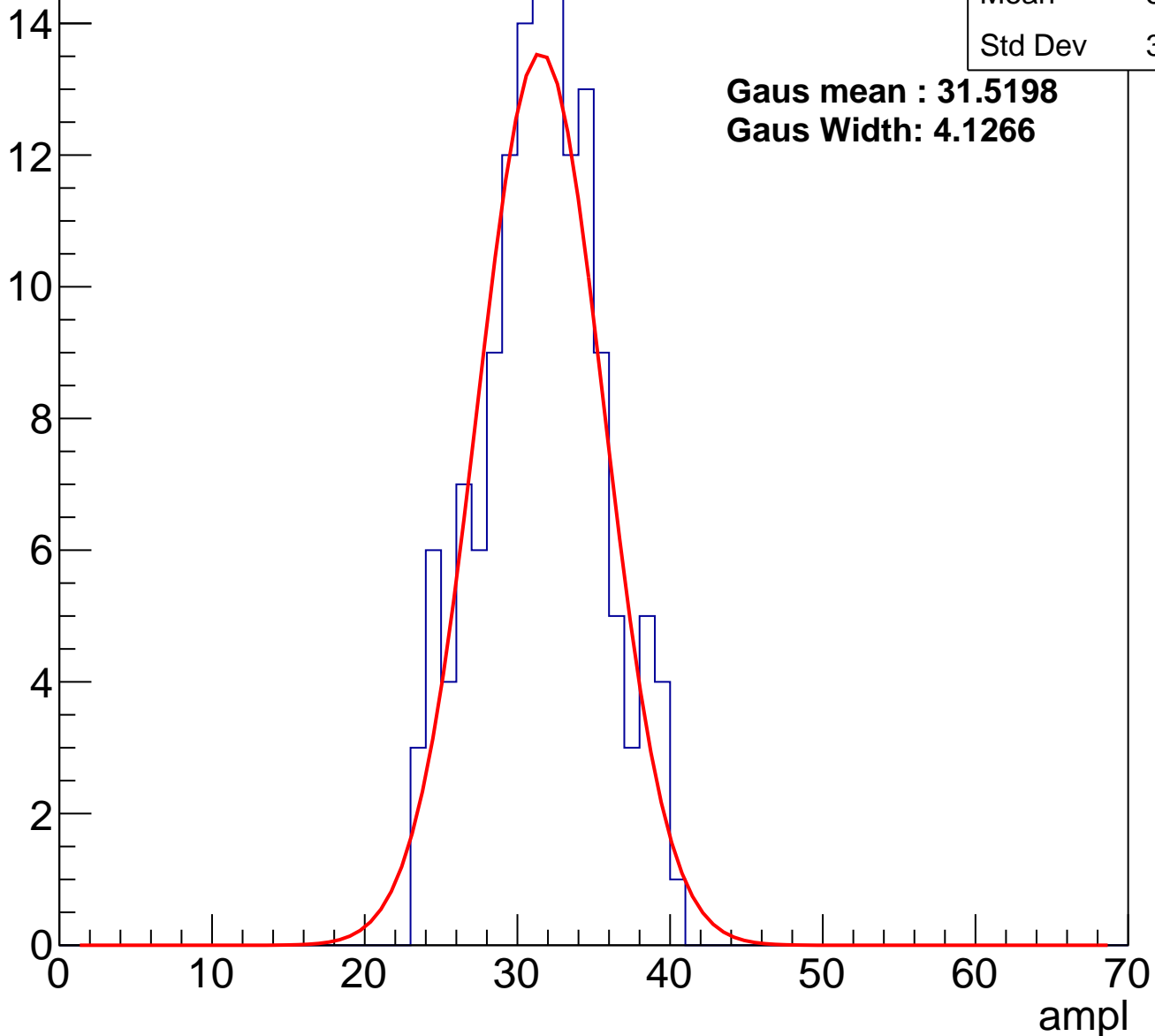
ampl



B0L101S, U22-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch14, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 38.47 |
| Std Dev | 3.765 |

Gaus mean : 38.8469

Gaus Width: 3.9280

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

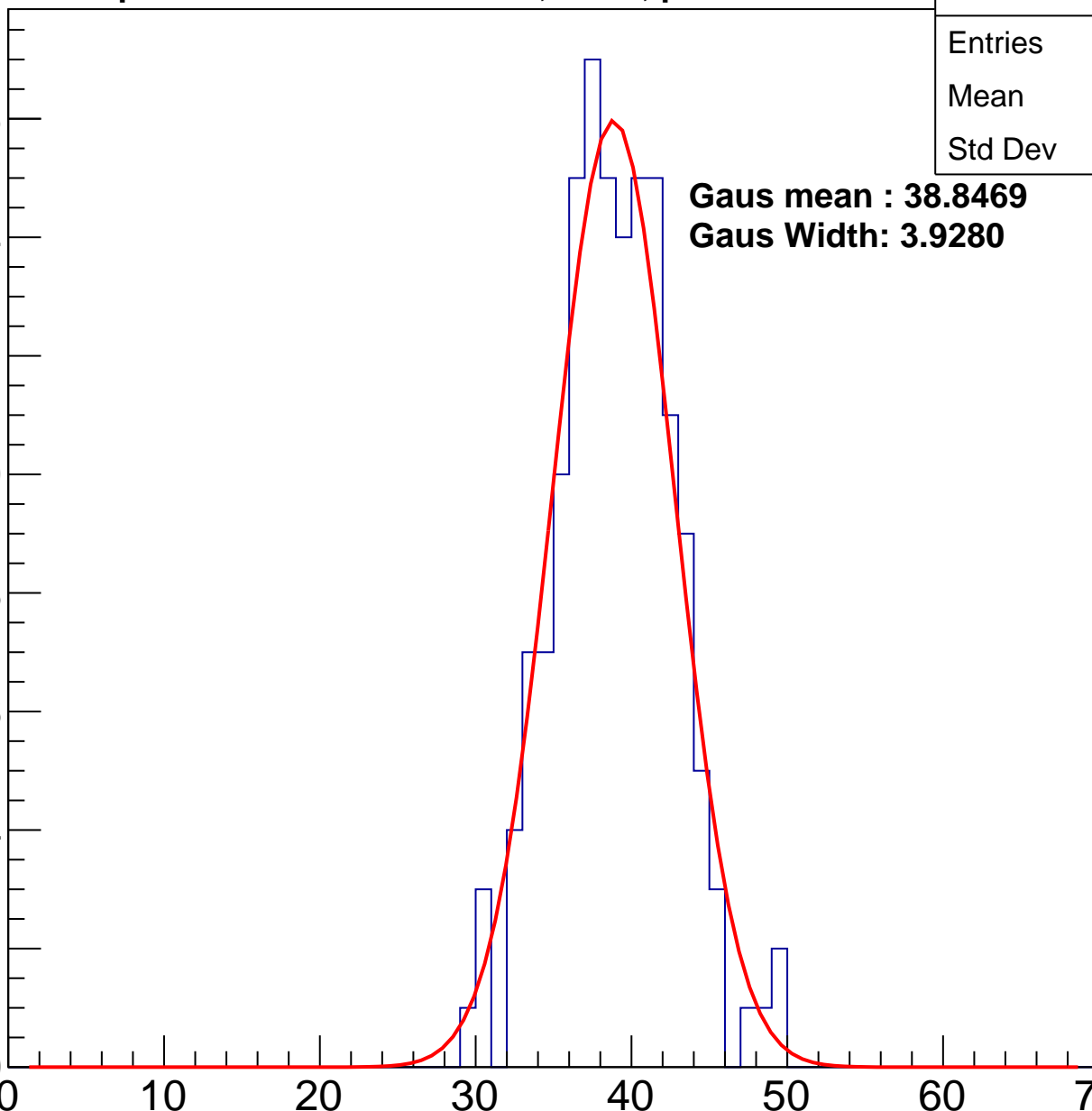
40

50

60

70

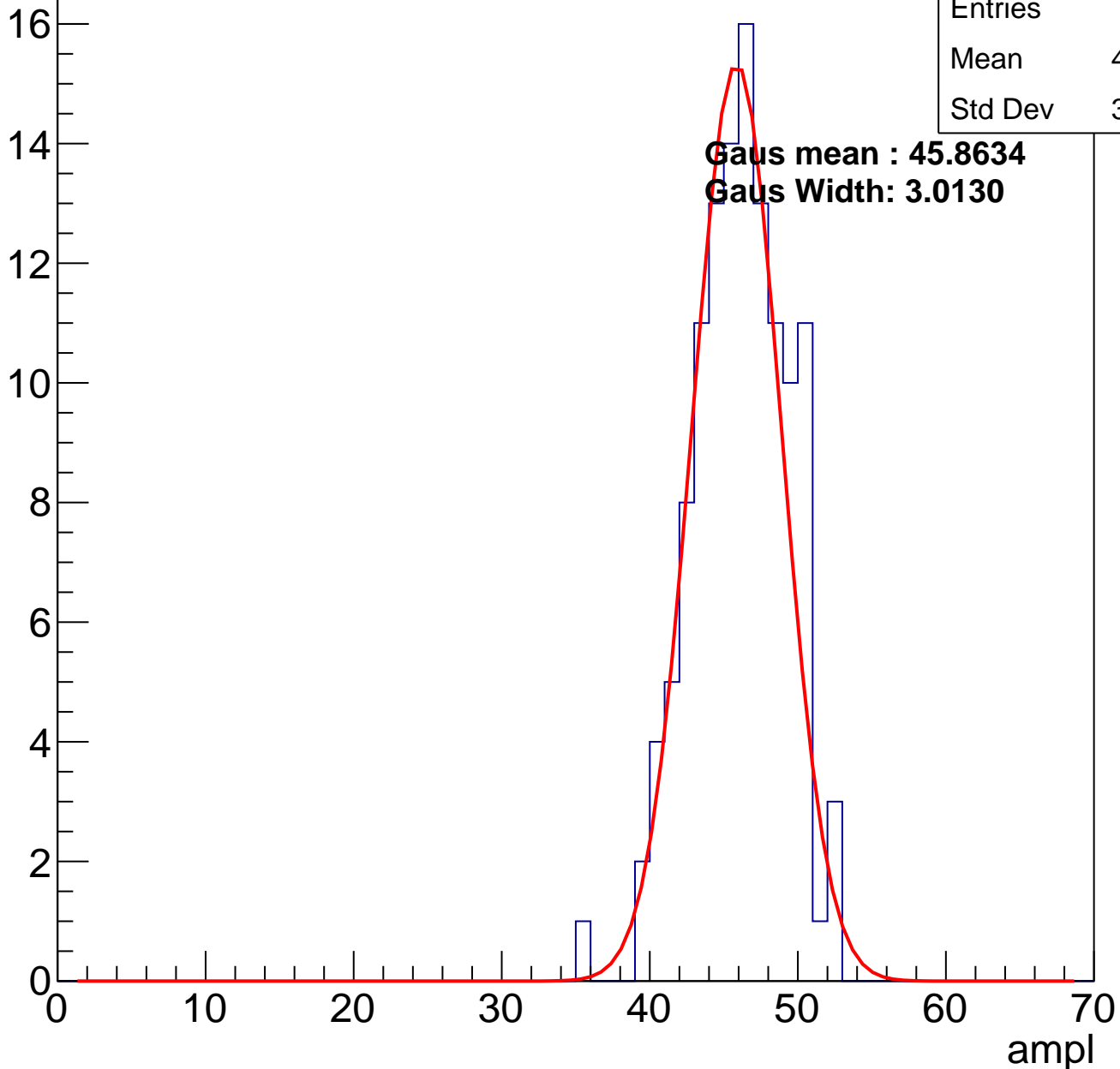
ampl



B0L101S, U22-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

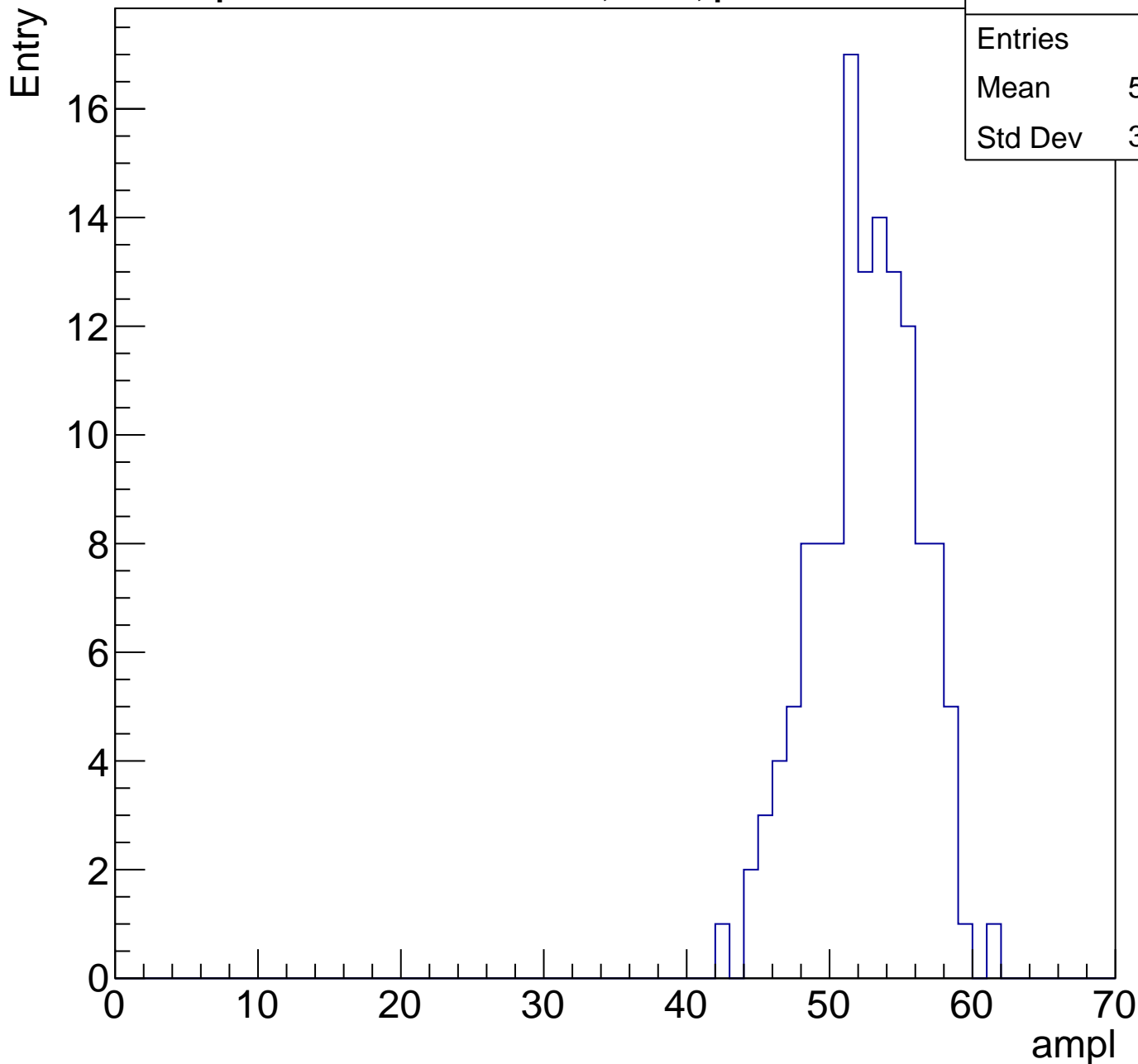
Entry



B0L101S, U22-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 52.07 |
| Std Dev | 3.607 |



B0L101S, U22-ch14, adc4

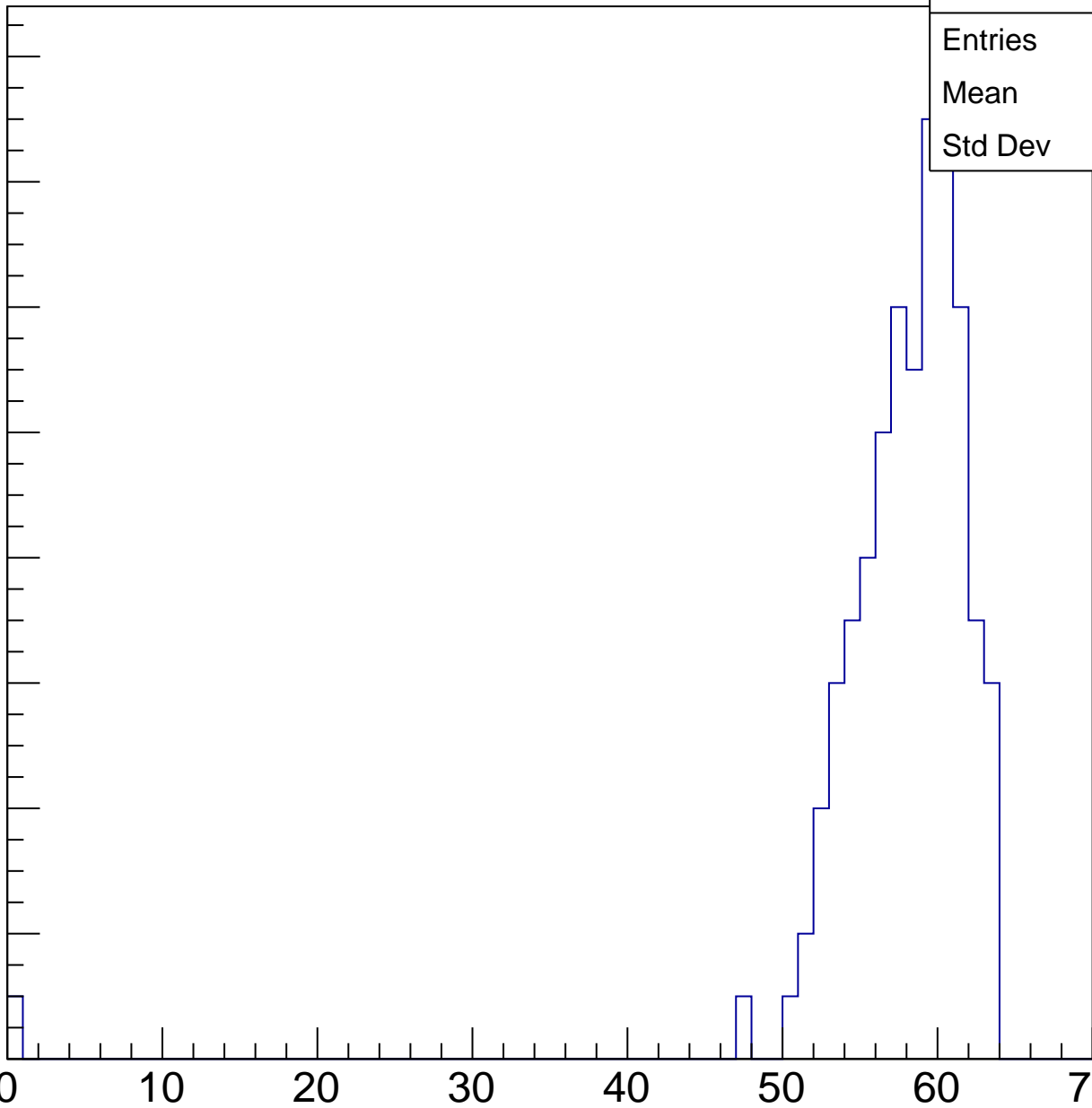
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 119 |
| Mean | 57.26 |
| Std Dev | 6.191 |

ampl



B0L101S, U22-ch14, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 46 |
| Mean | 59.76 |
| Std Dev | 9.113 |

B0L101S, U22-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 30.52 |
| Std Dev | 3.374 |

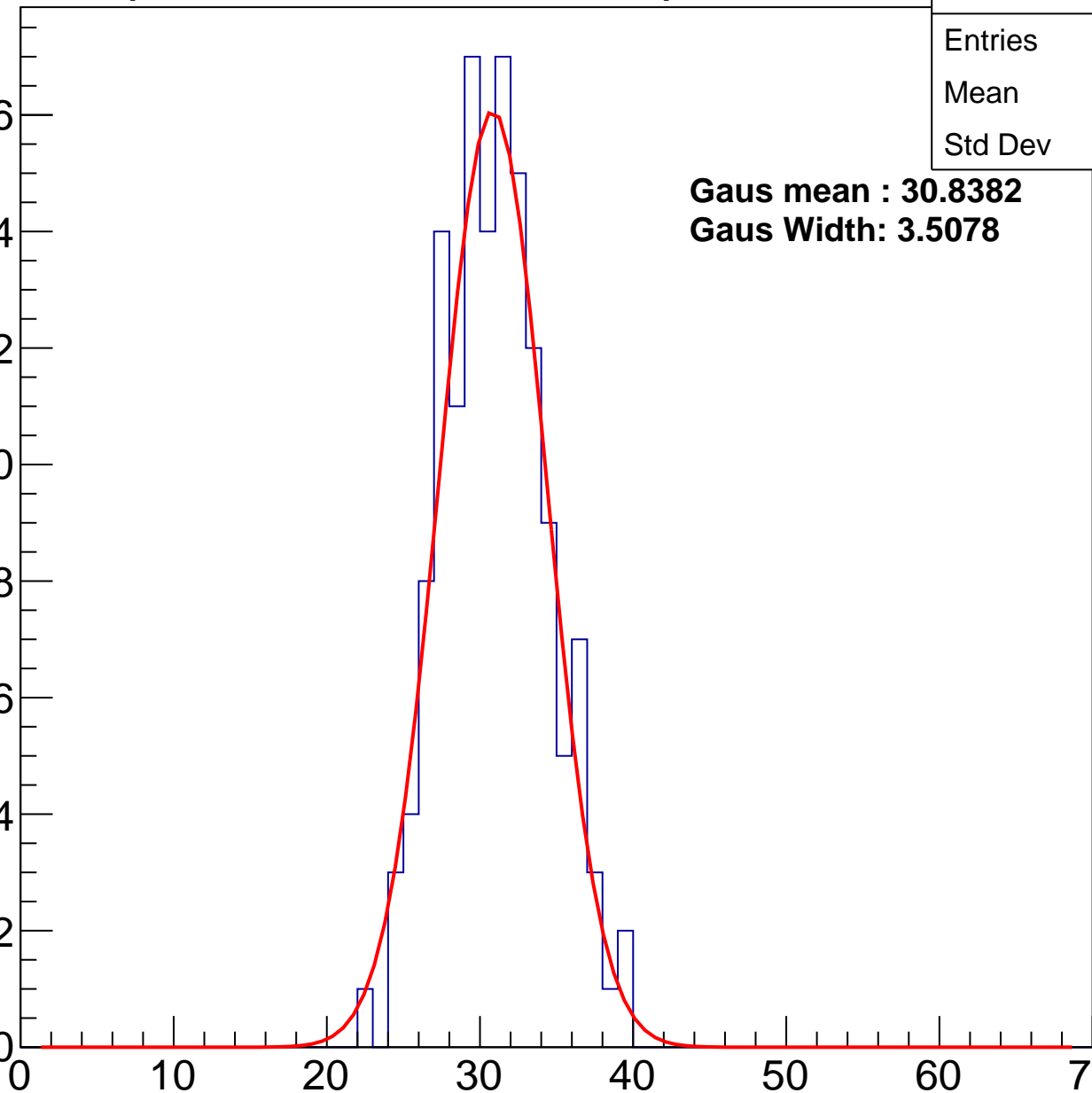
Gaus mean : 30.8382

Gaus Width: 3.5078

Entry

16
14
12
10
8
6
4
2
0

ampl

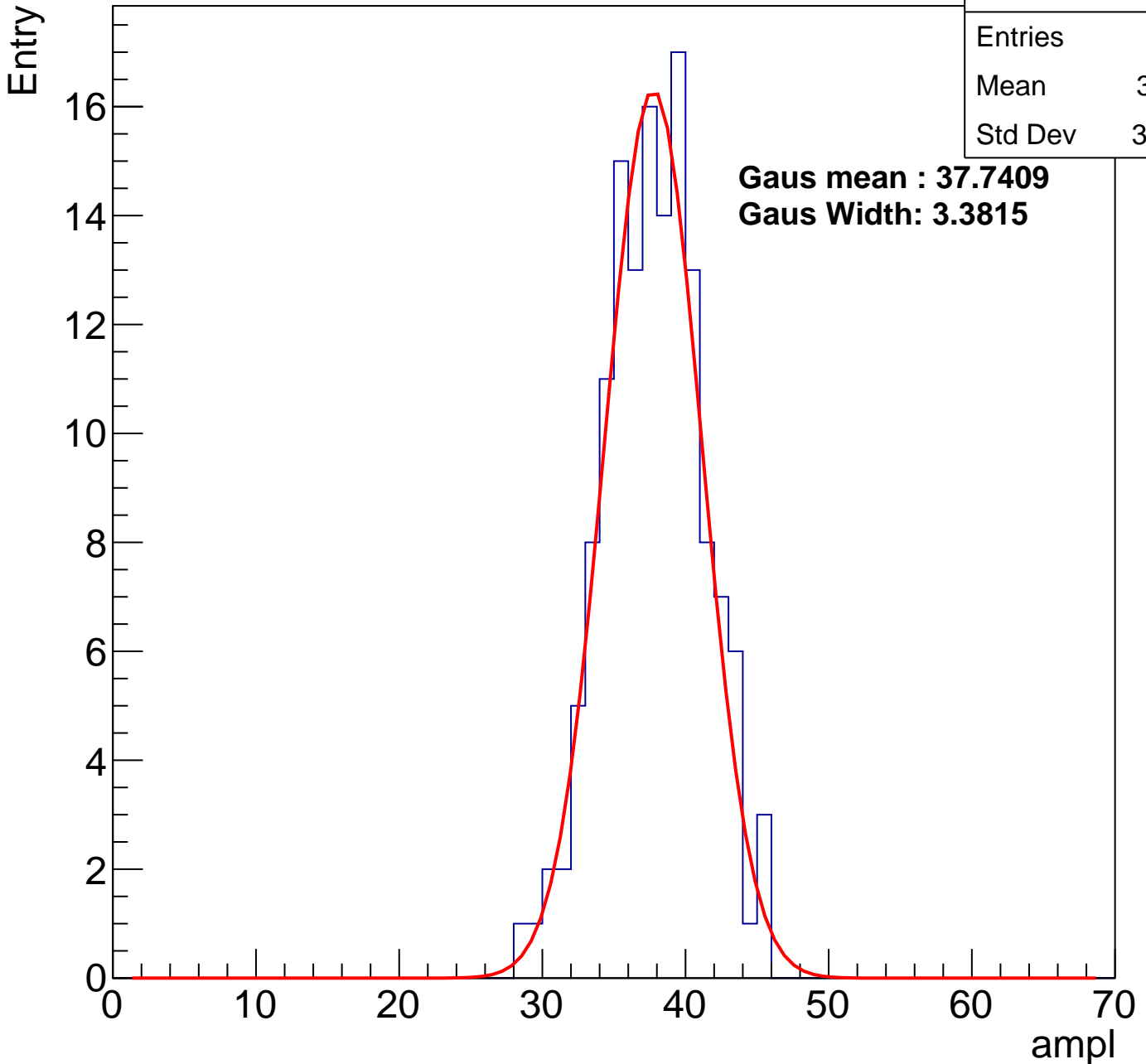


B0L101S, U22-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 37.31 |
| Std Dev | 3.433 |

Gaus mean : 37.7409
Gaus Width: 3.3815



B0L101S, U22-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

121

Mean

44.04

Std Dev

3.081

Gaus mean : 44.6644

Gaus Width: 2.6854

0

10

20

30

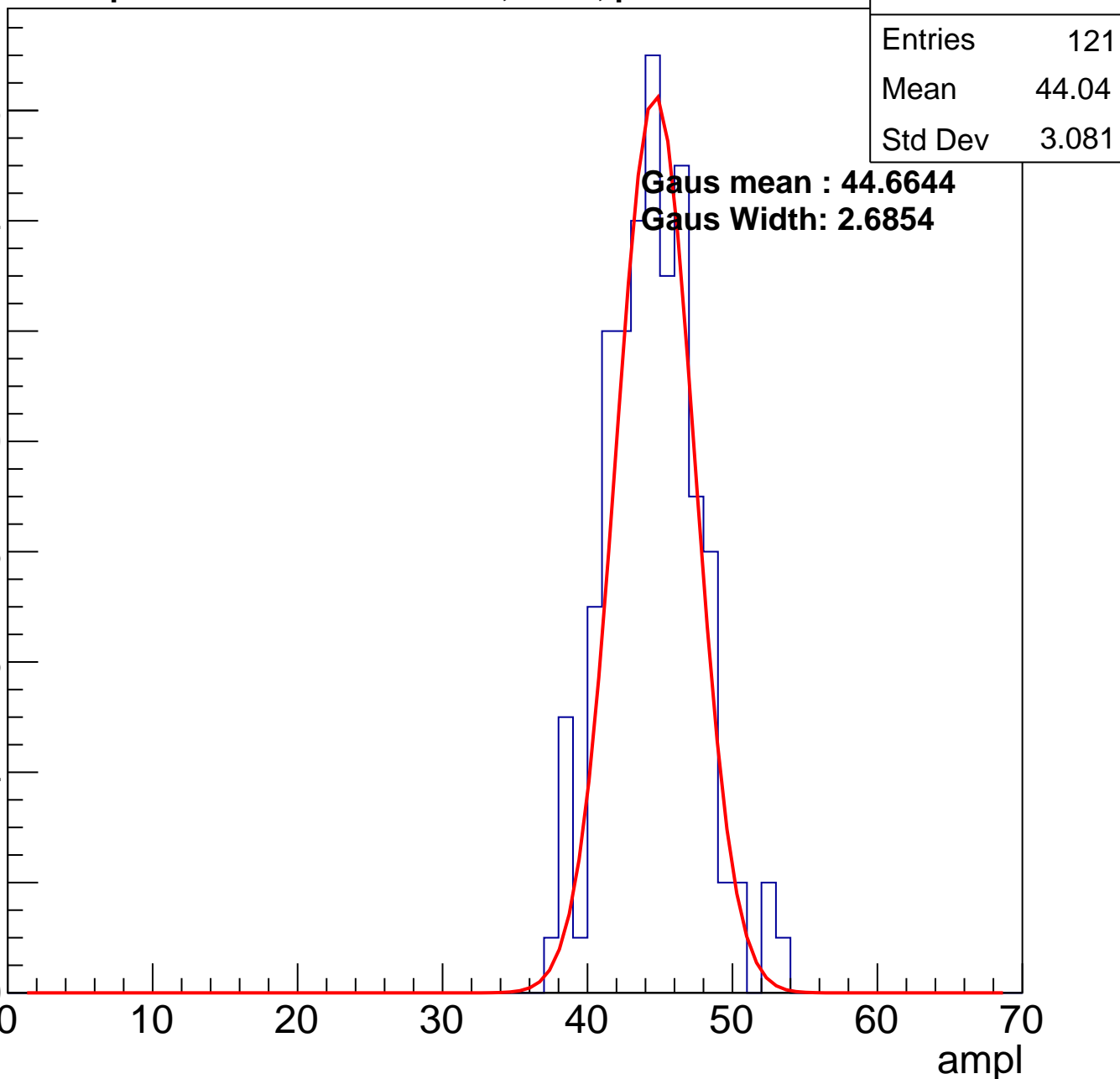
40

50

60

70

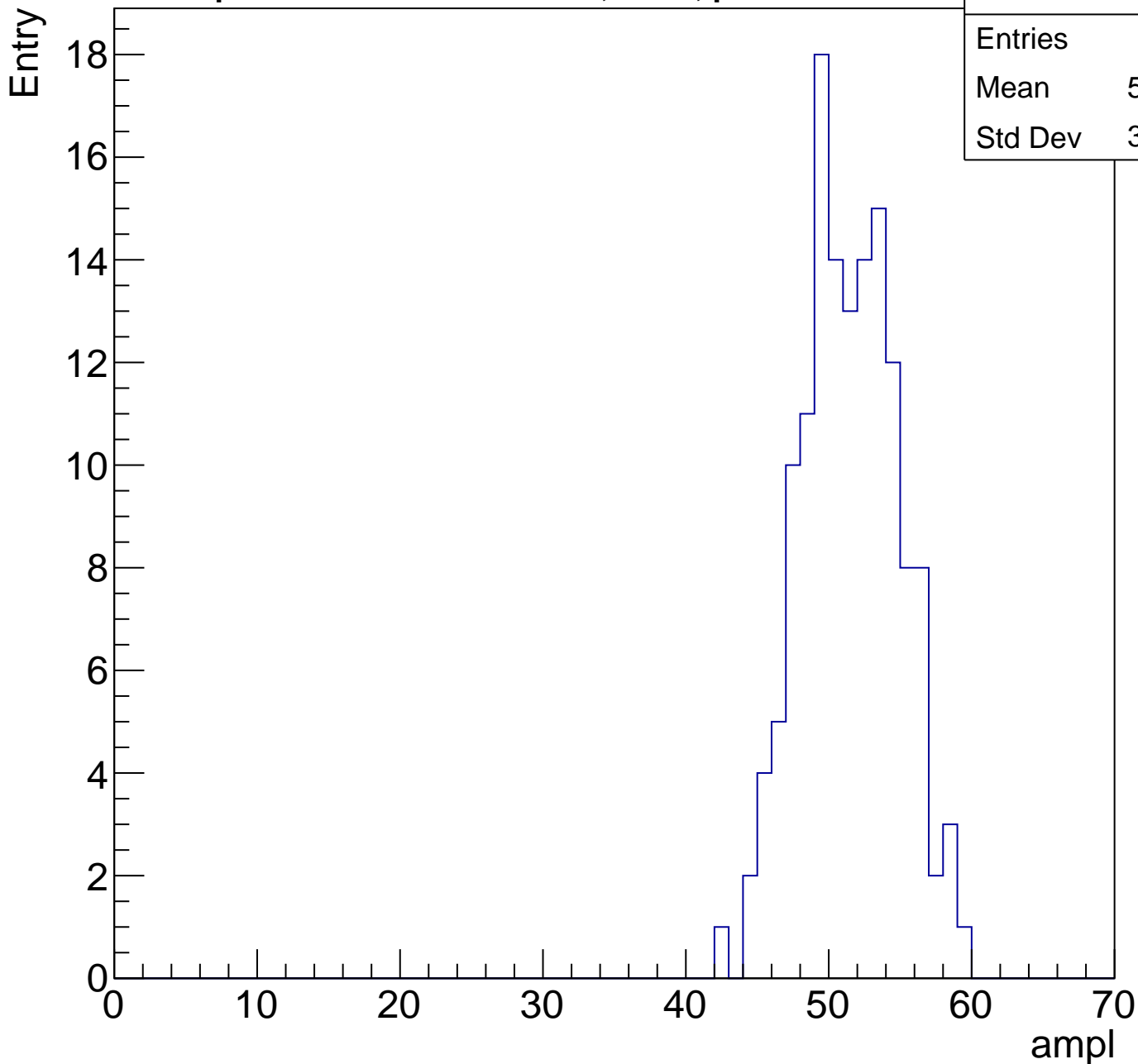
ampl



B0L101S, U22-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 50.99 |
| Std Dev | 3.354 |



B0L101S, U22-ch15, adc4

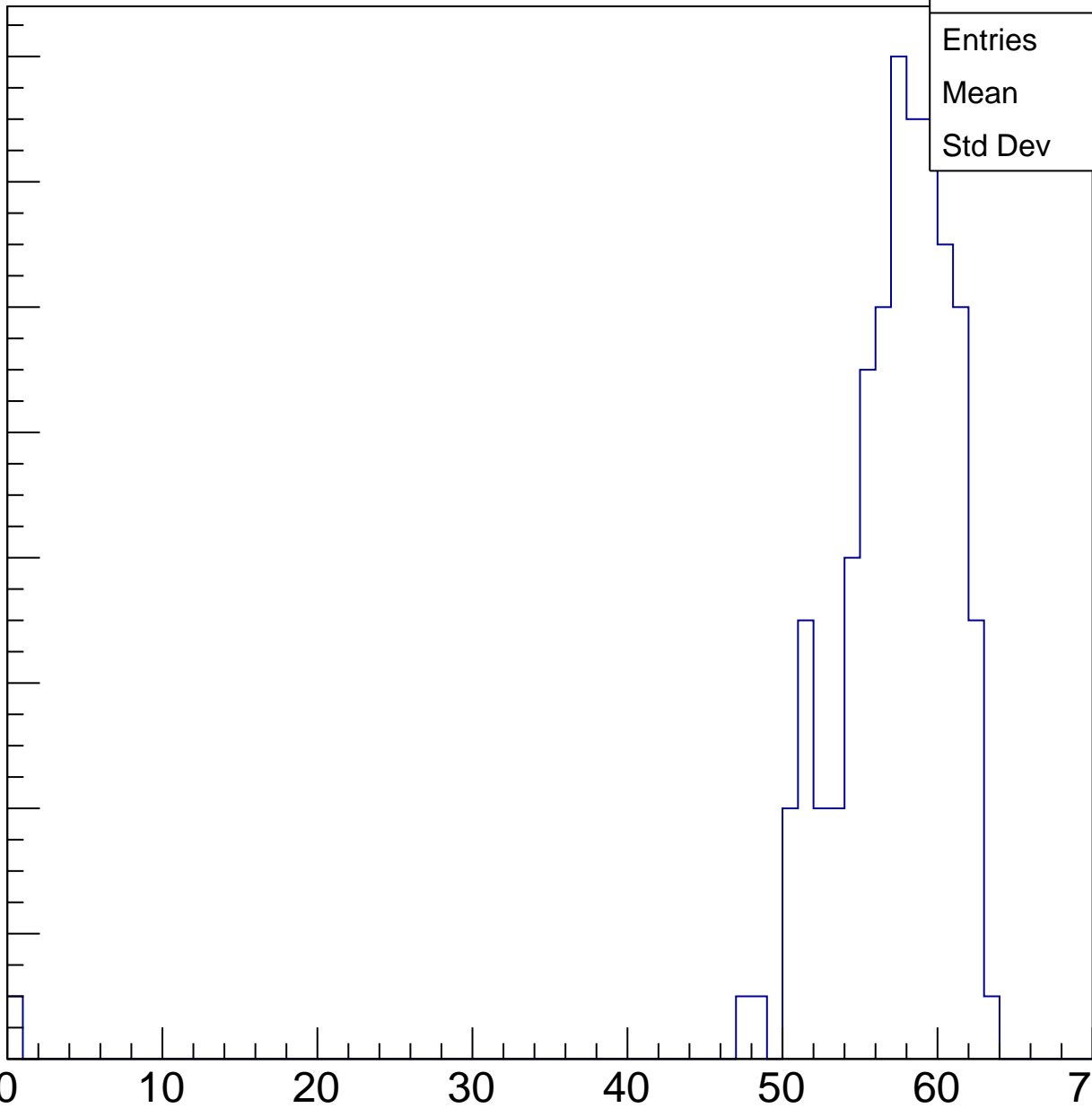
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 132 |
| Mean | 56.49 |
| Std Dev | 5.987 |

ampl



B0L101S, U22-ch15, adc5

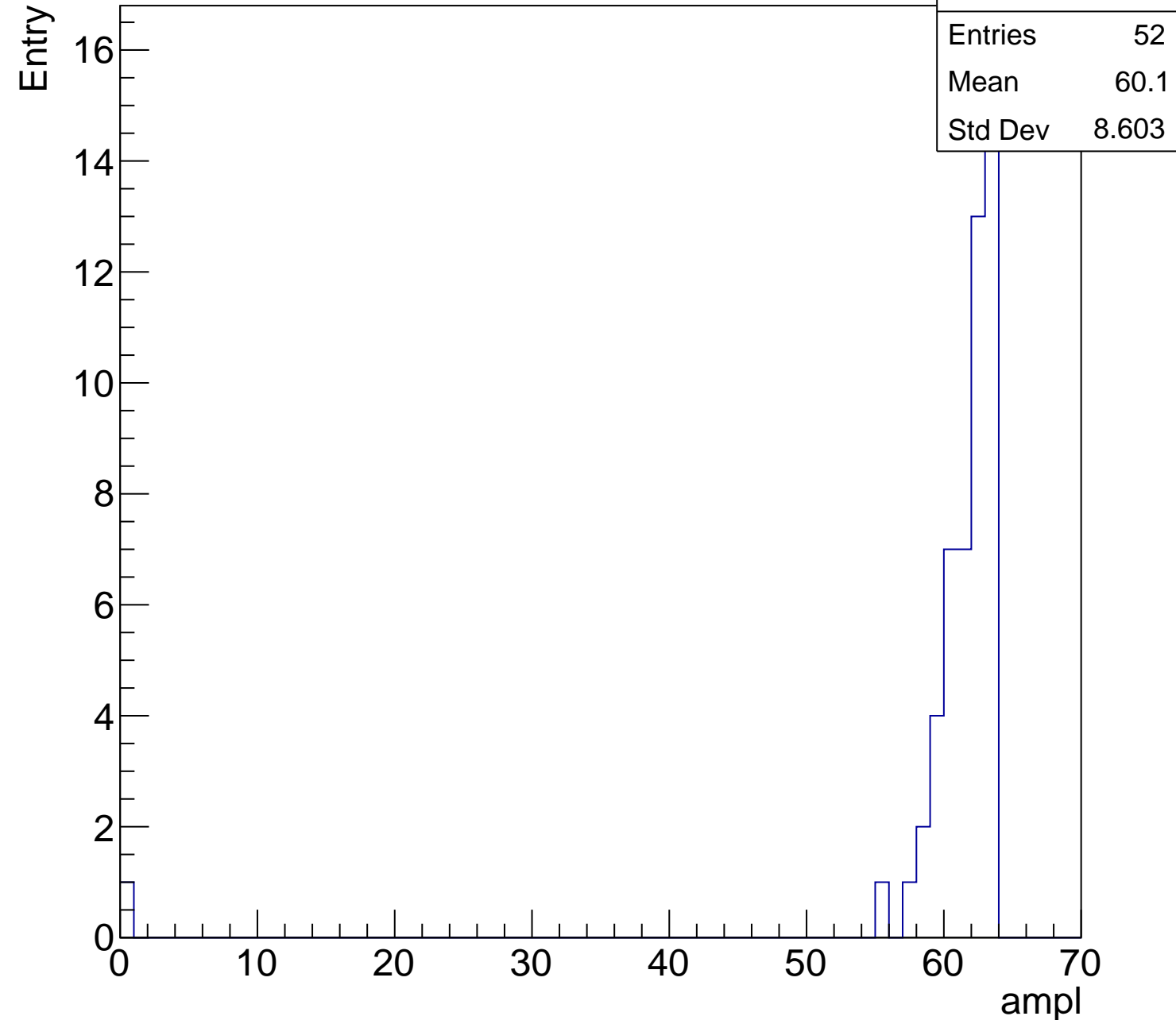
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 60.1 |
| Std Dev | 8.603 |

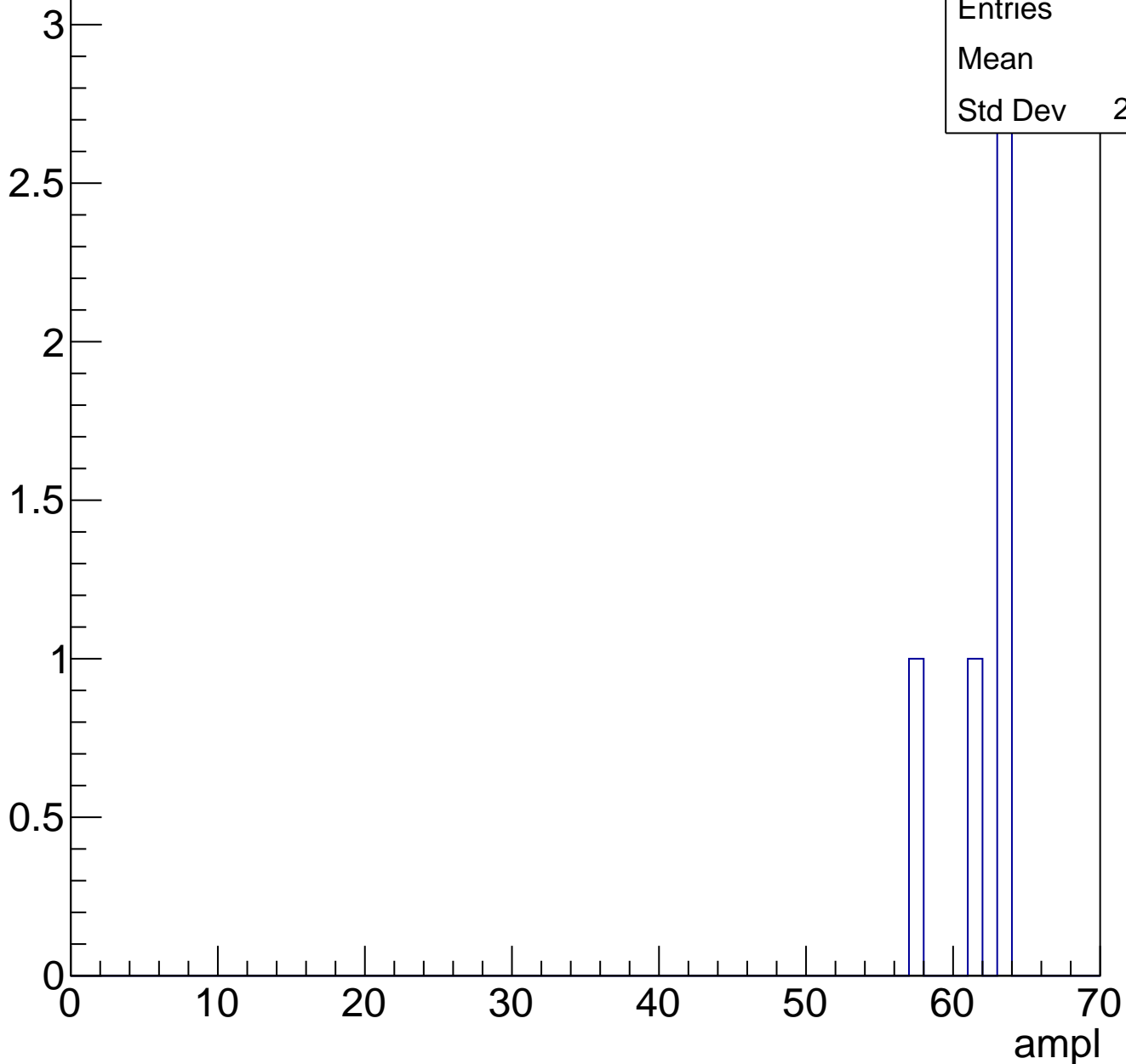
ampl



B0L101S, U22-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

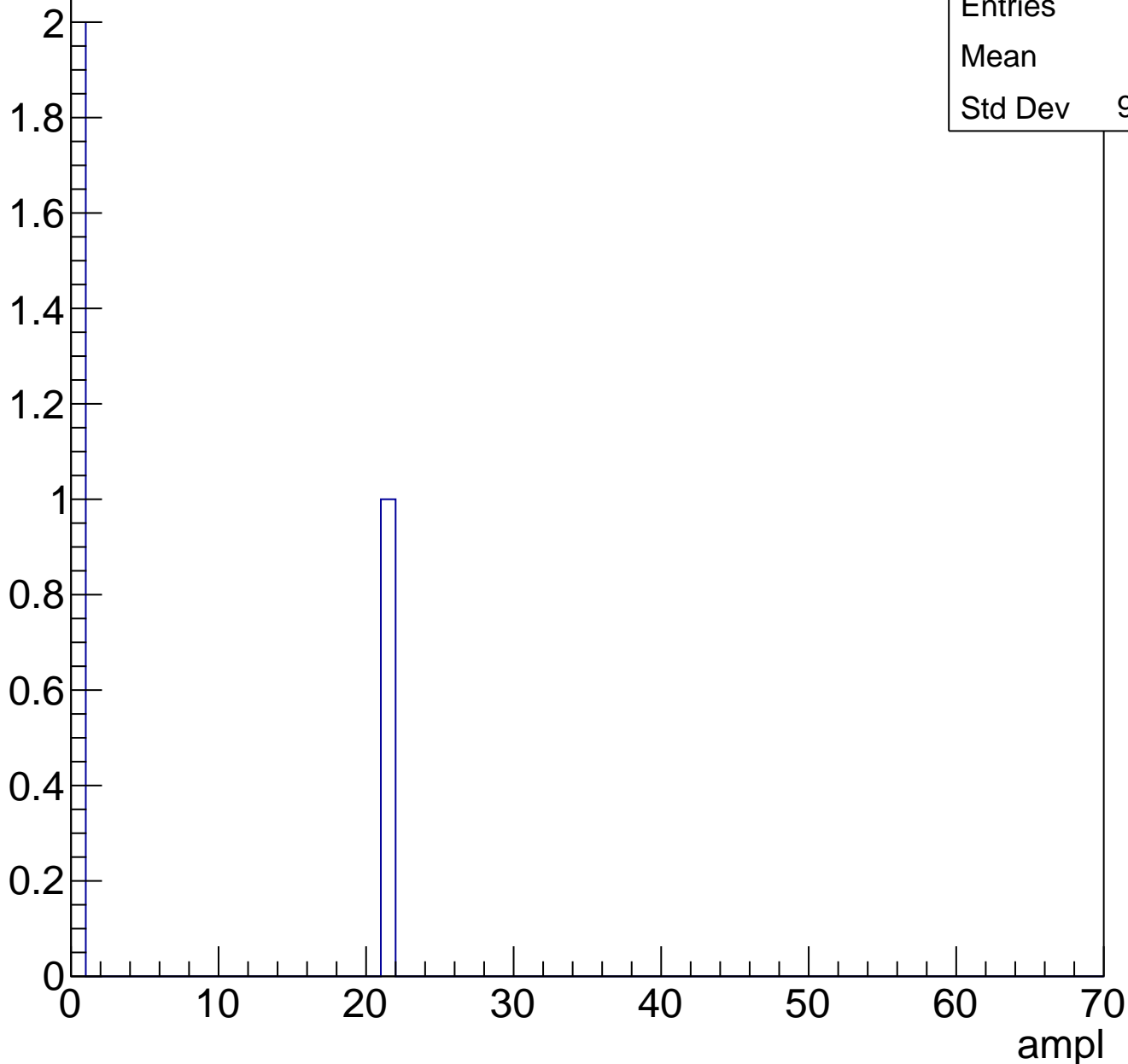
Entry



B0L101S, U22-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7 |
| Std Dev | 9.899 |

B0L101S, U22-ch16, adc0

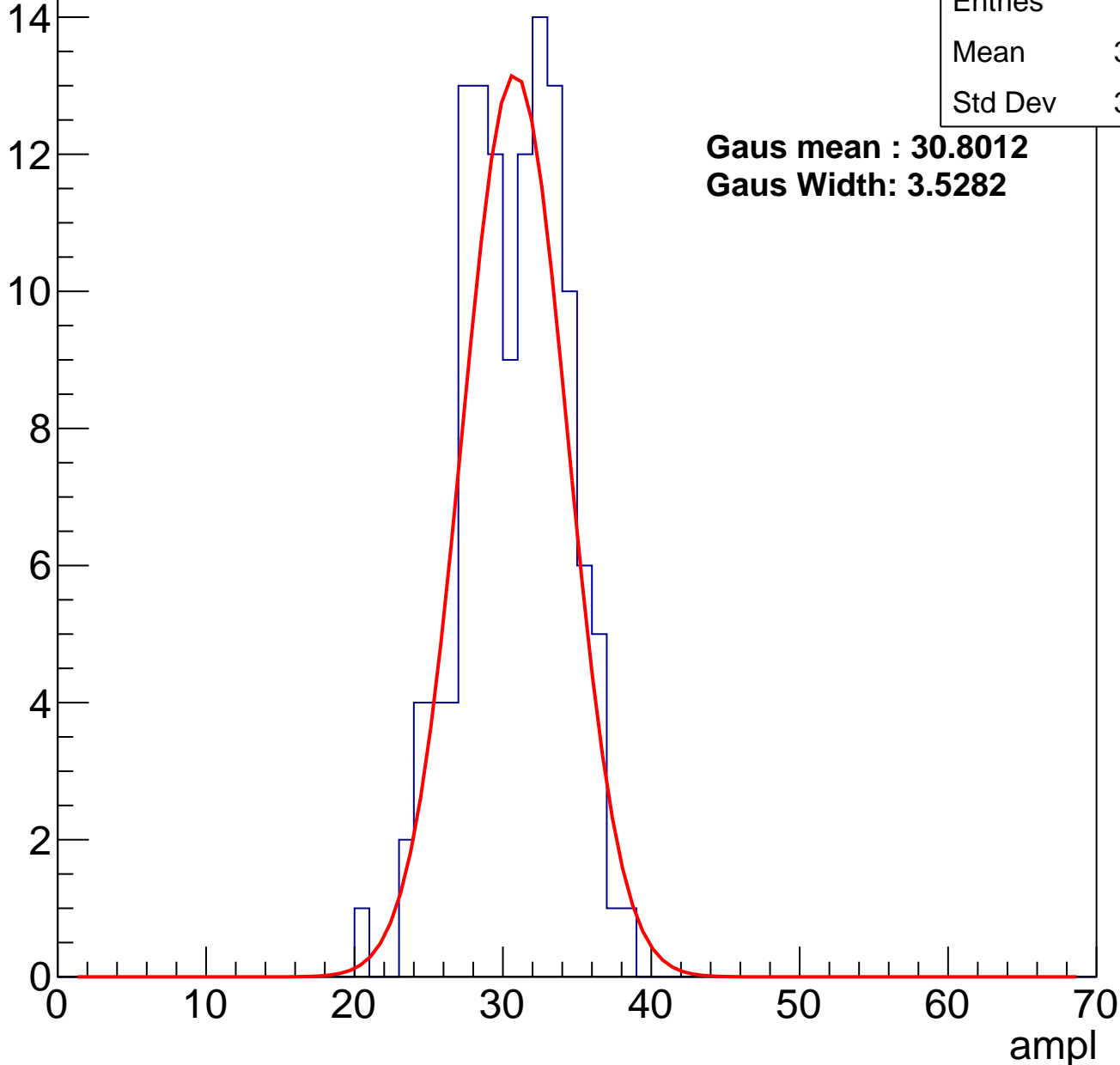
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 30.27 |
| Std Dev | 3.439 |

Gaus mean : 30.8012

Gaus Width: 3.5282

Entry



B0L101S, U22-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

37.4

Std Dev

3.509

Gaus mean : 37.8793

Gaus Width: 3.5762

0

10

20

30

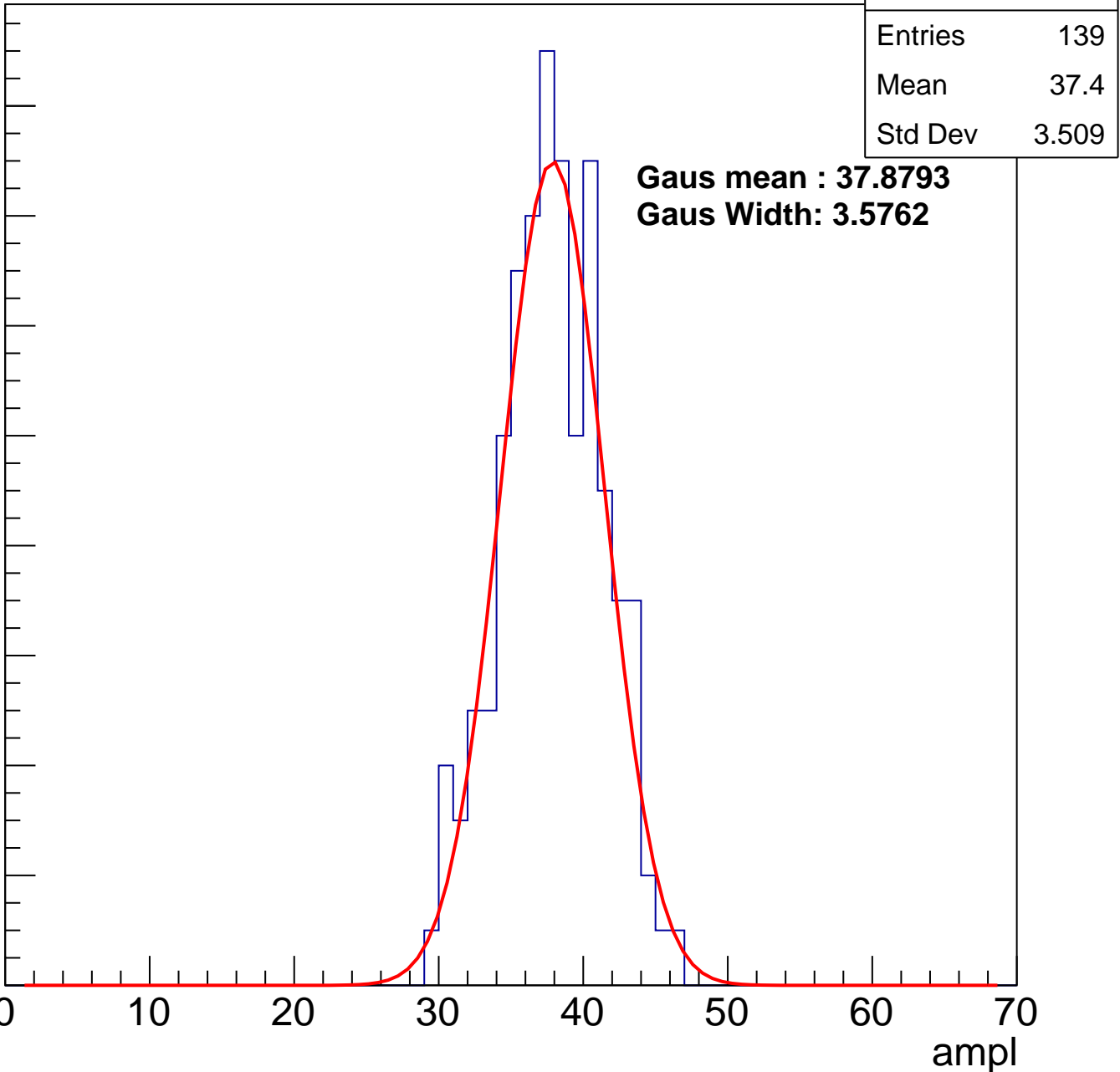
40

50

60

70

ampl



B0L101S, U22-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

124

Mean

43.97

Std Dev

3.711

Gaus mean : 44.5866

Gaus Width: 3.8729

0

10

20

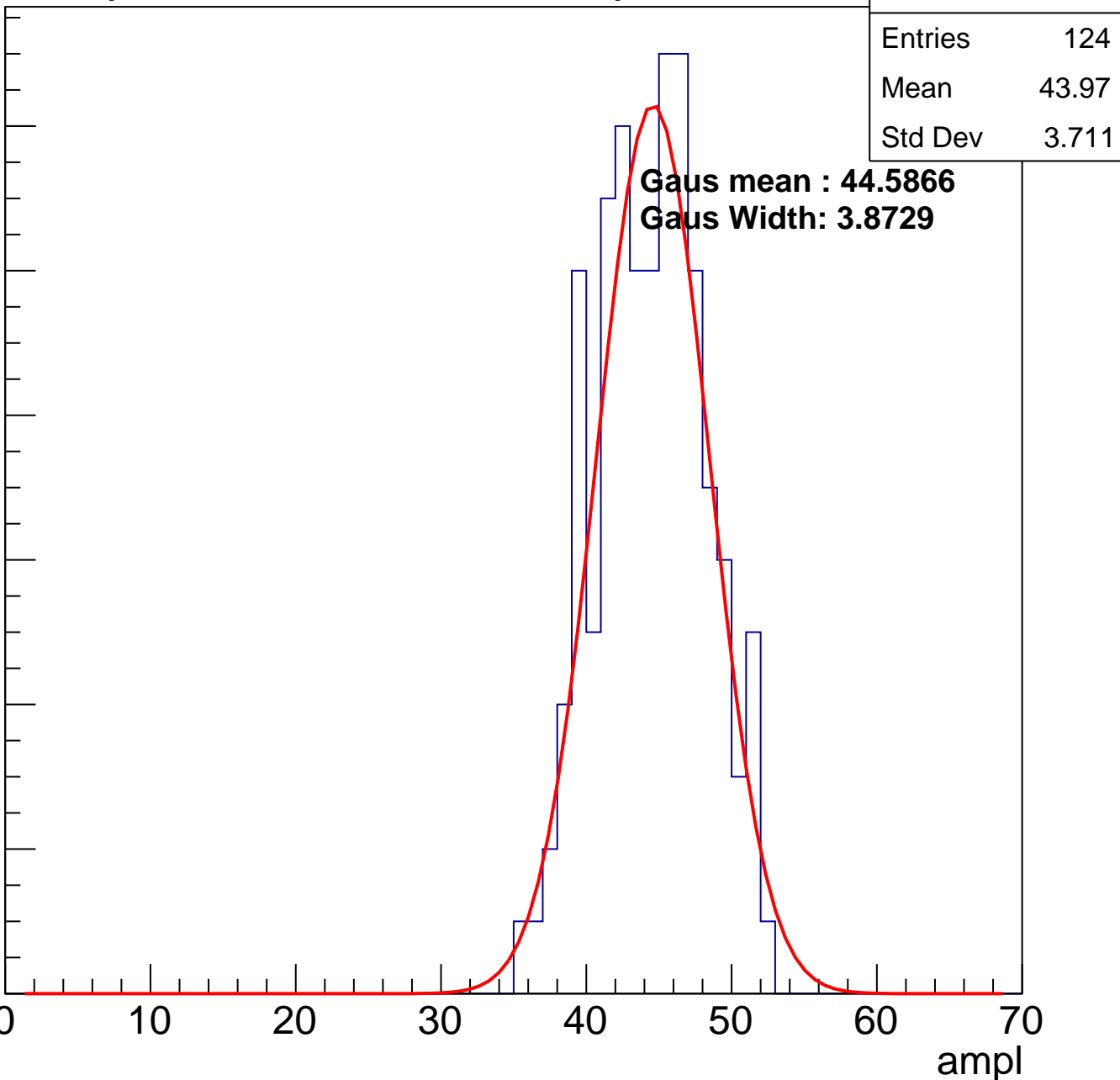
30

40

50

60

ampl

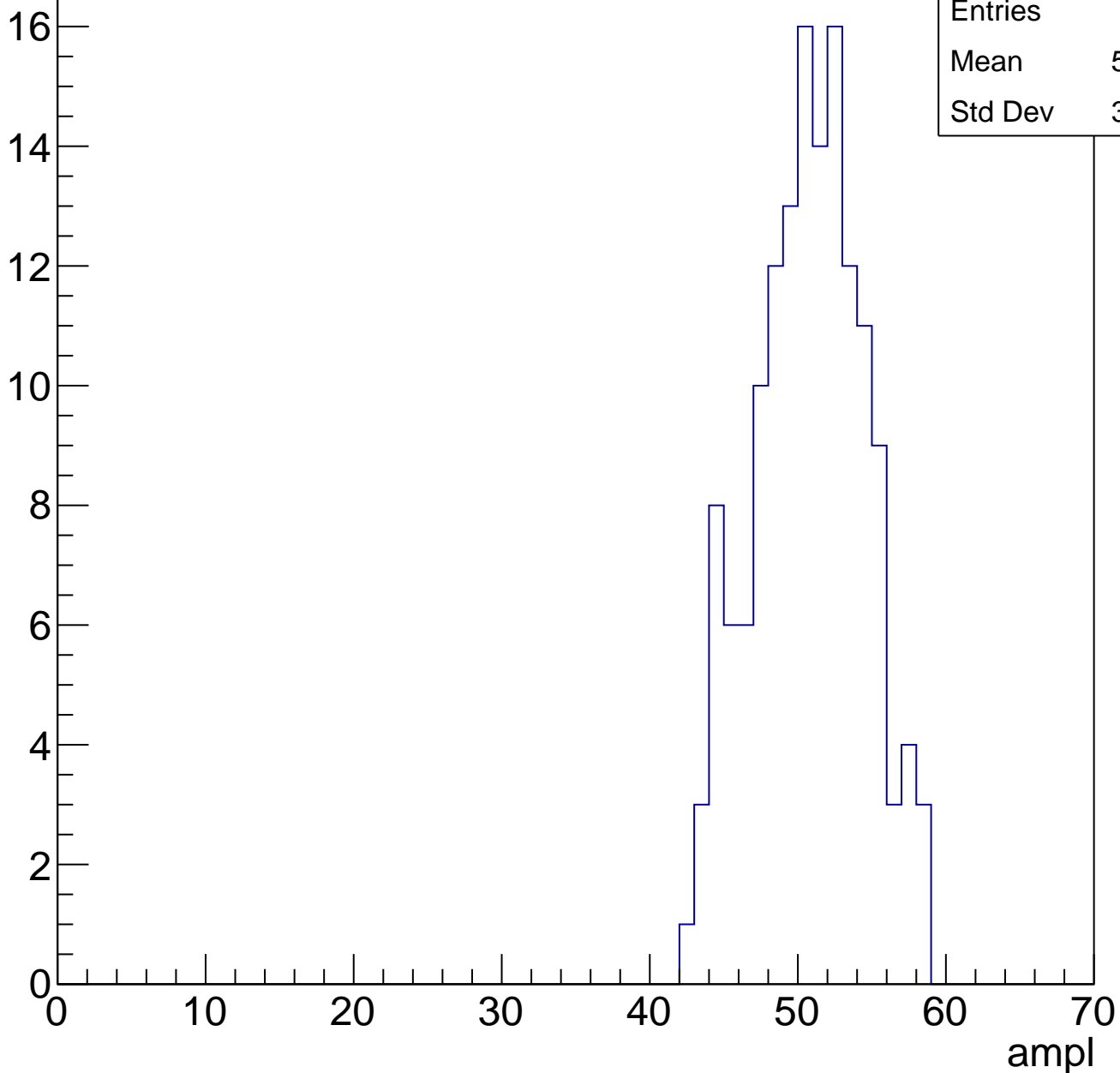


B0L101S, U22-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 147 |
| Mean | 50.29 |
| Std Dev | 3.657 |

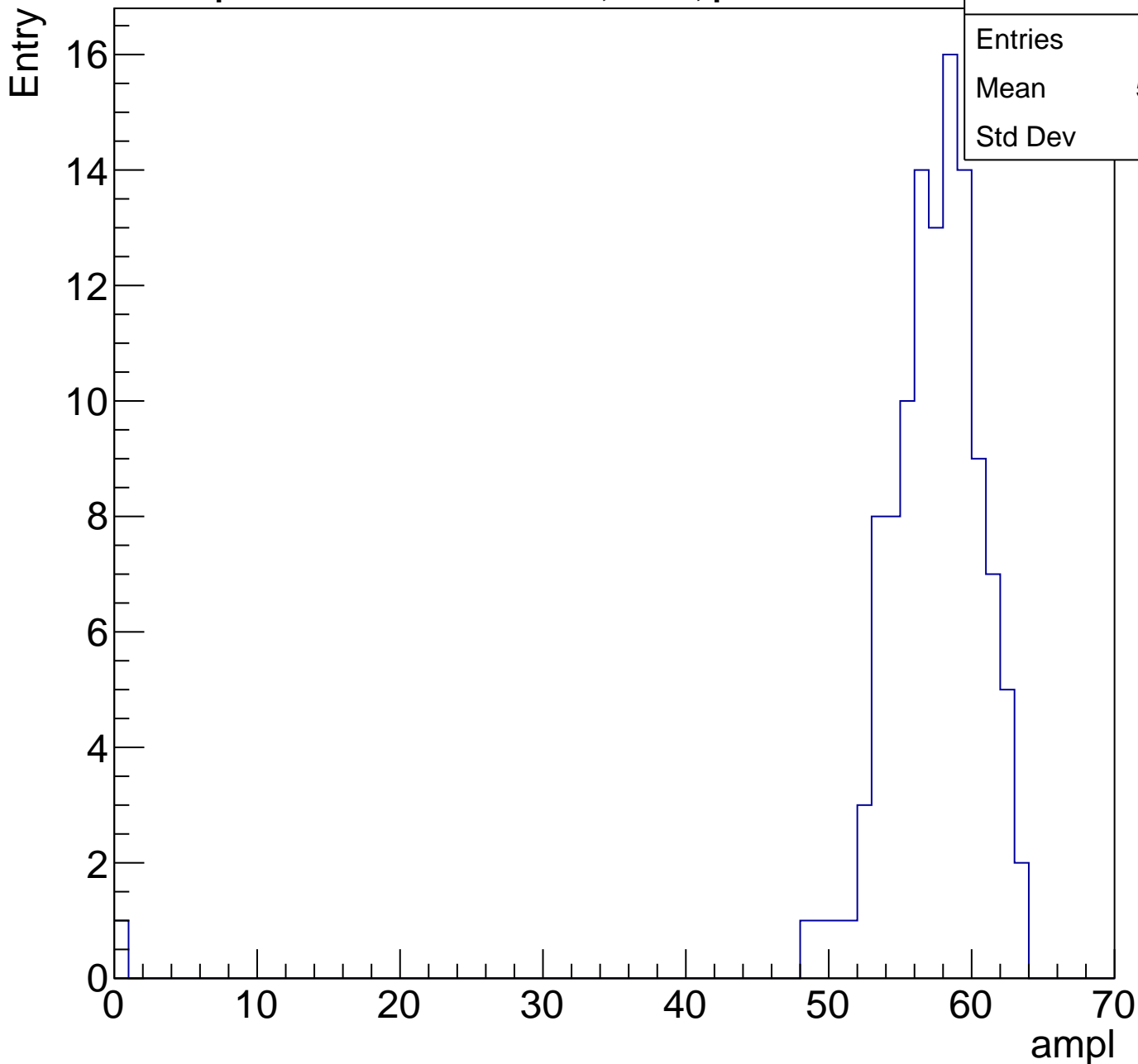
Entry



B0L101S, U22-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 114 |
| Mean | 56.51 |
| Std Dev | 6.1 |



B0L101S, U22-ch16, adc5

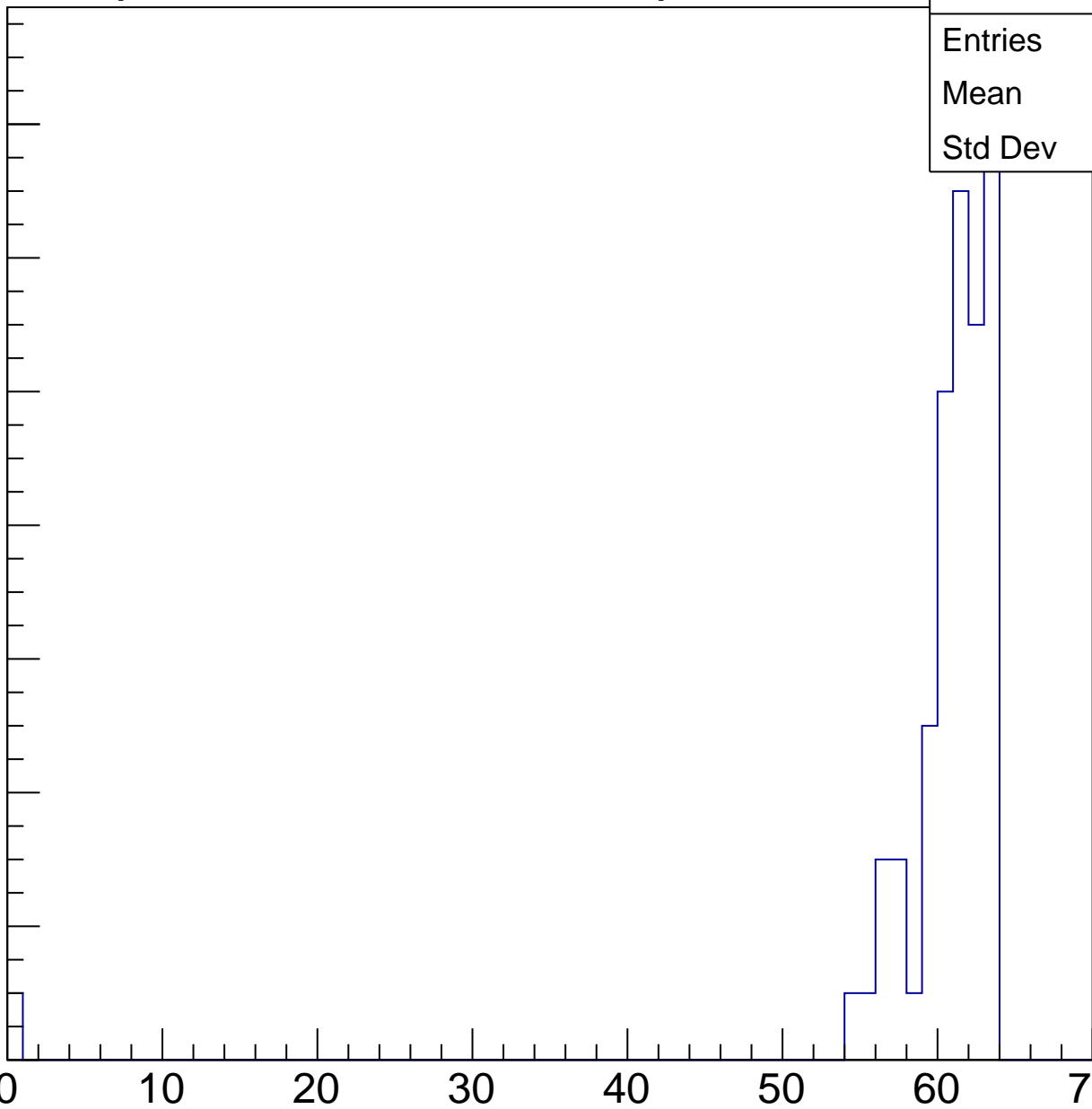
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 59.7 |
| Std Dev | 7.838 |

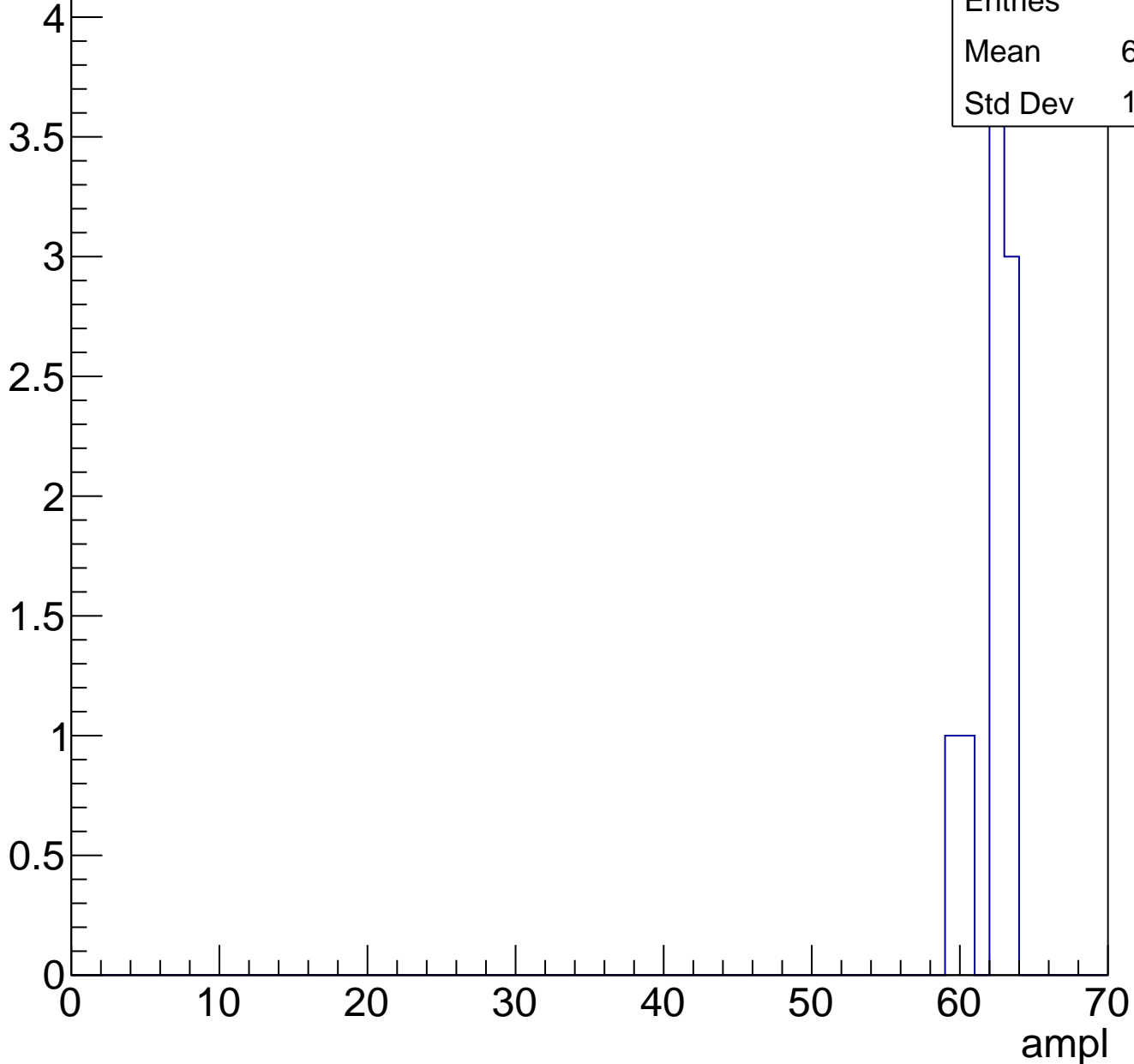
ampl



B0L101S, U22-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B0L101S, U22-ch17, adc0

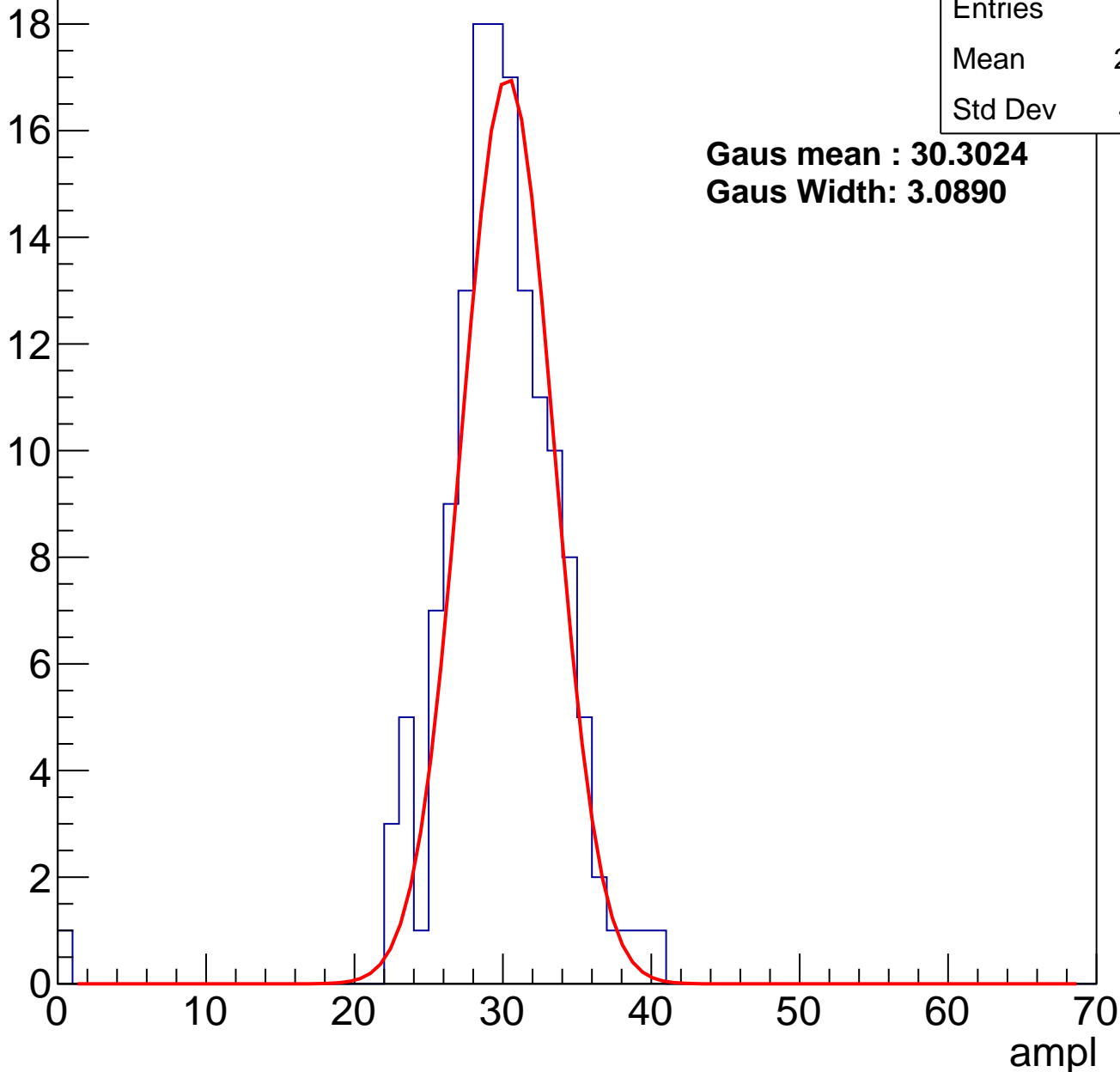
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 29.37 |
| Std Dev | 4.241 |

Gaus mean : 30.3024

Gaus Width: 3.0890

Entry

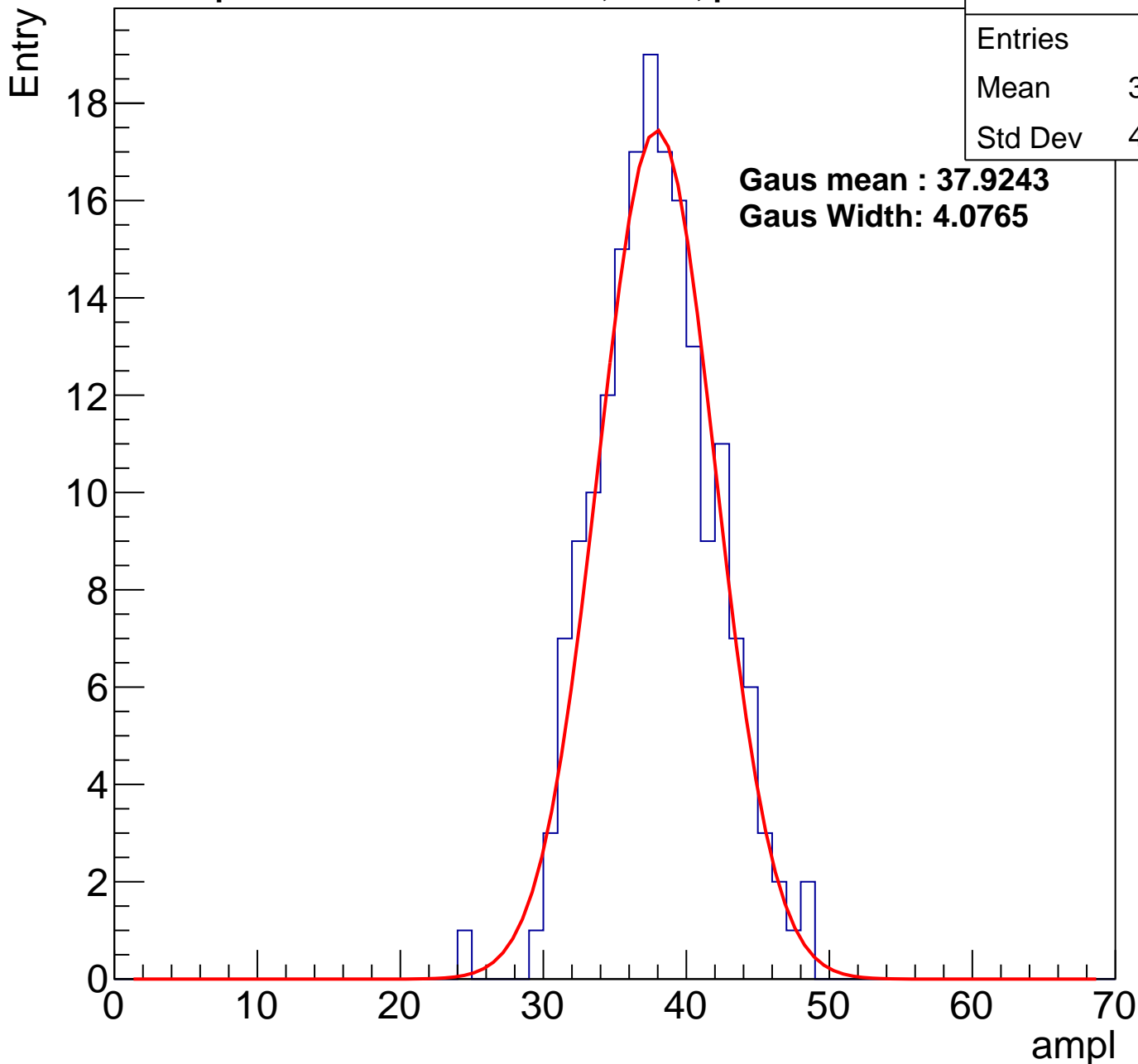


B0L101S, U22-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 37.47 |
| Std Dev | 4.085 |

Gaus mean : 37.9243
Gaus Width: 4.0765



B0L101S, U22-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

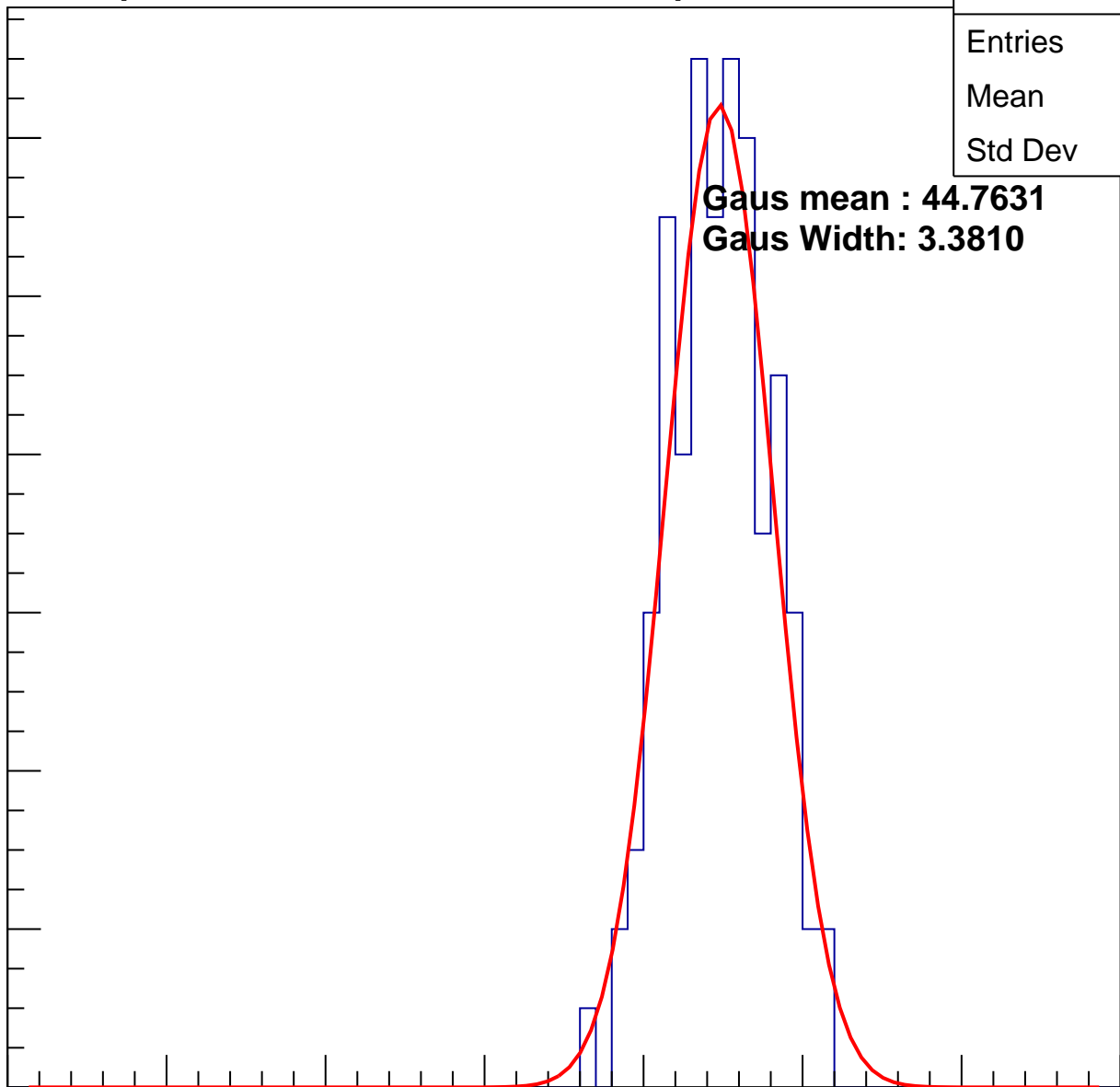
0 10 20 30 40 50 60 70

ampl

| | |
|---------|-------|
| Entries | 106 |
| Mean | 44.27 |
| Std Dev | 3.119 |

Gaus mean : 44.7631

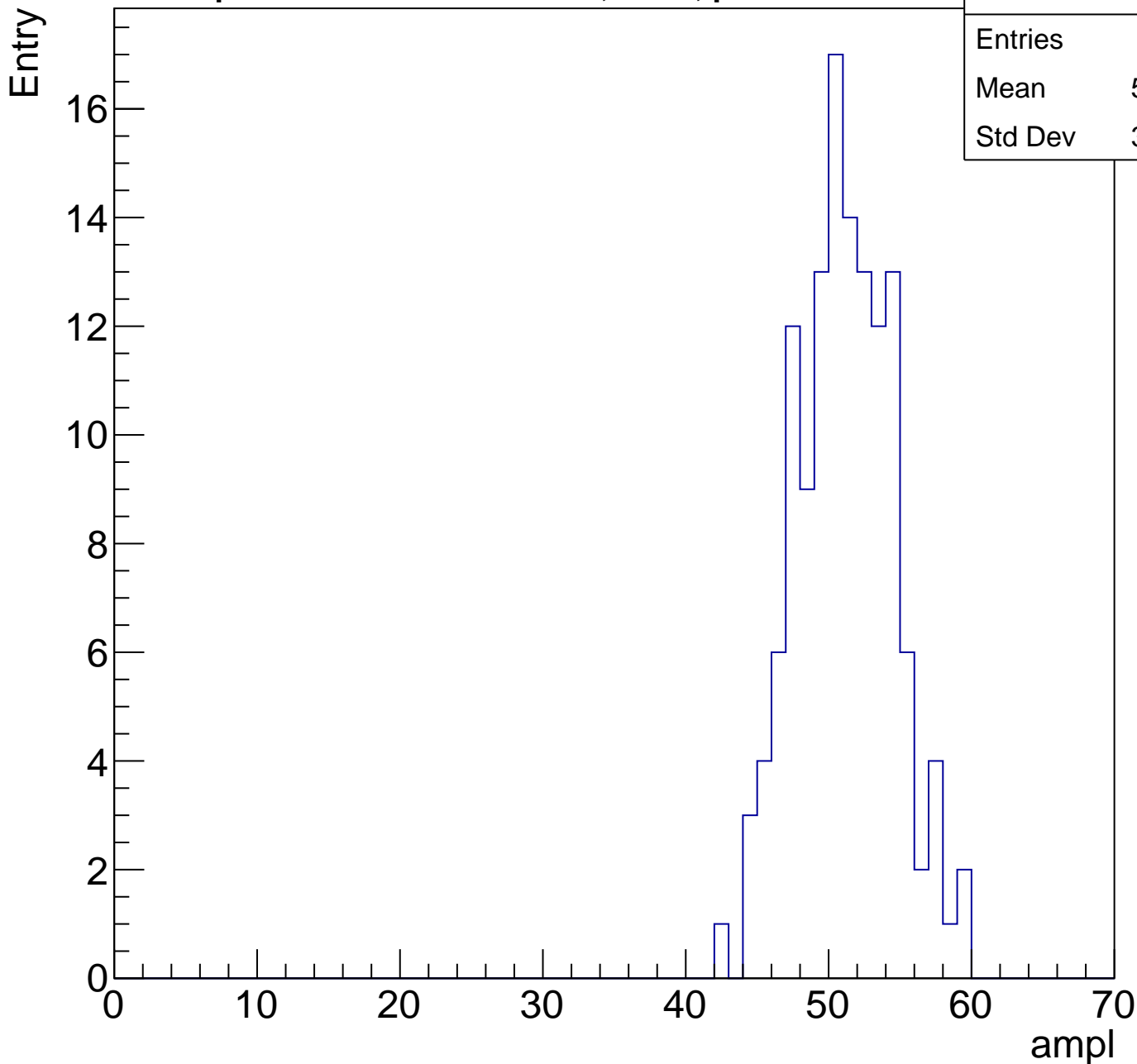
Gaus Width: 3.3810



B0L101S, U22-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 50.66 |
| Std Dev | 3.364 |



B0L101S, U22-ch17, adc4

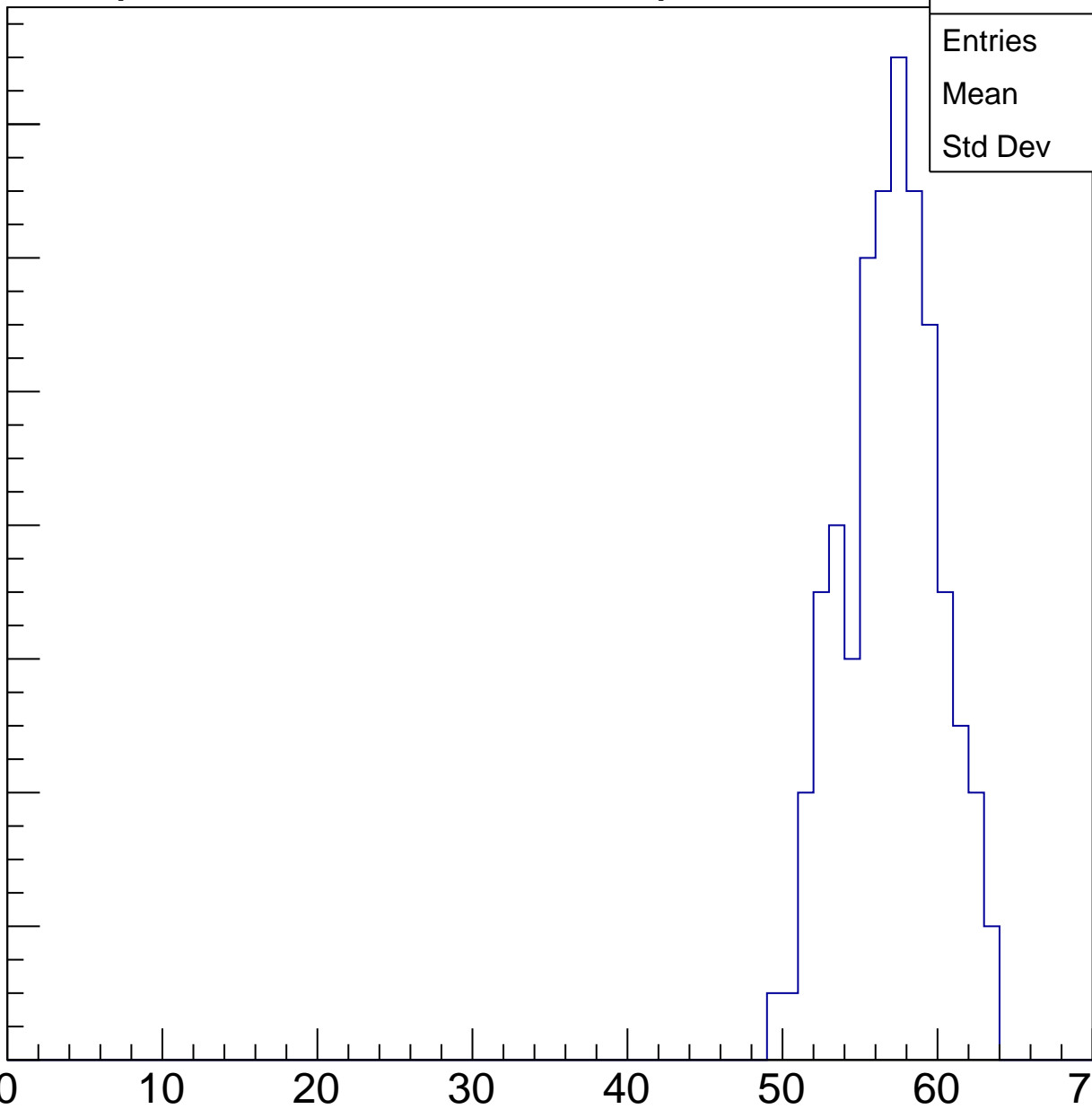
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 109 |
| Mean | 56.51 |
| Std Dev | 3.061 |

ampl



B0L101S, U22-ch17, adc5

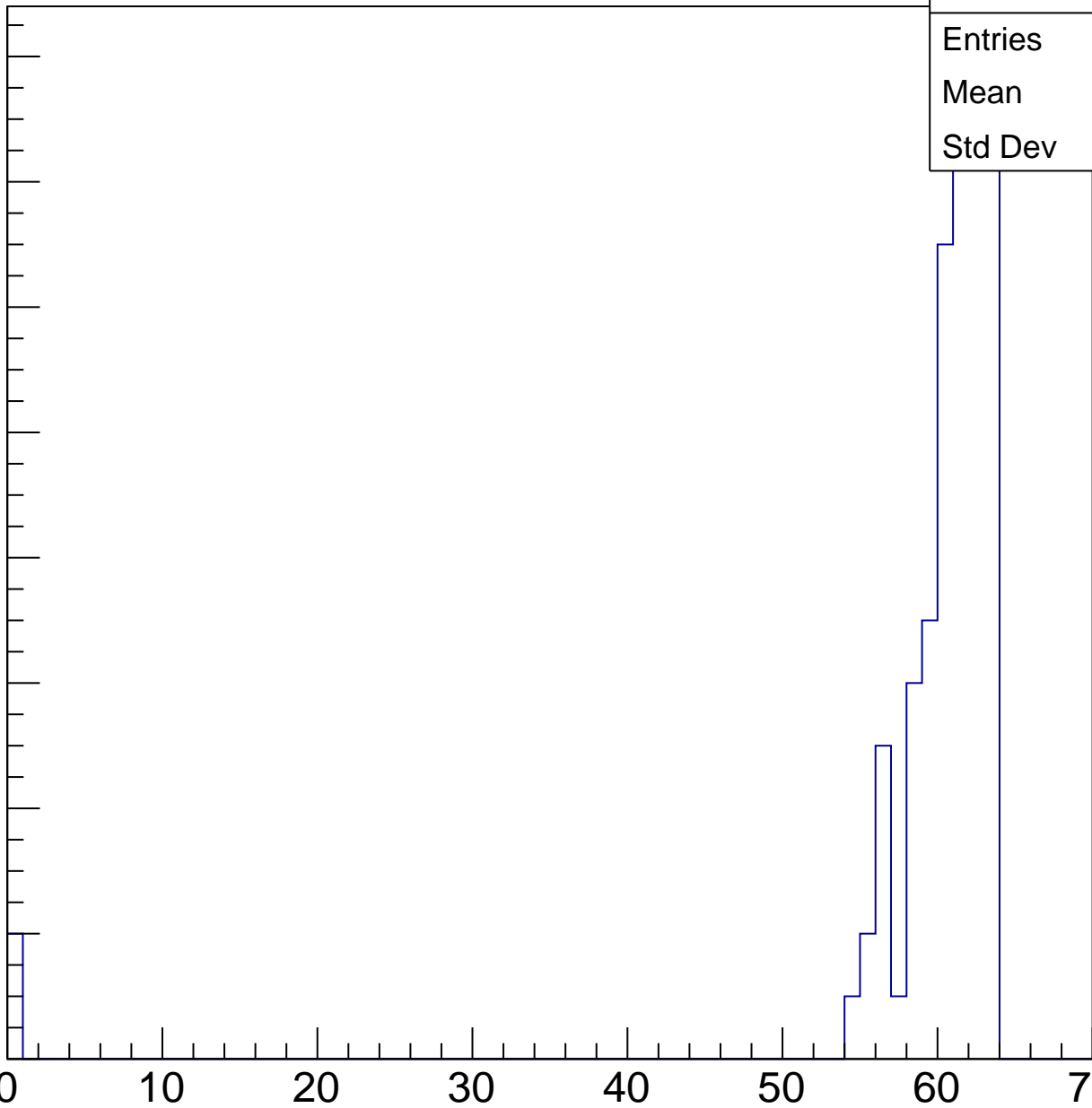
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 58.96 |
| Std Dev | 9.525 |

ampl



B0L101S, U22-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.667 |
| Std Dev | 9.428 |

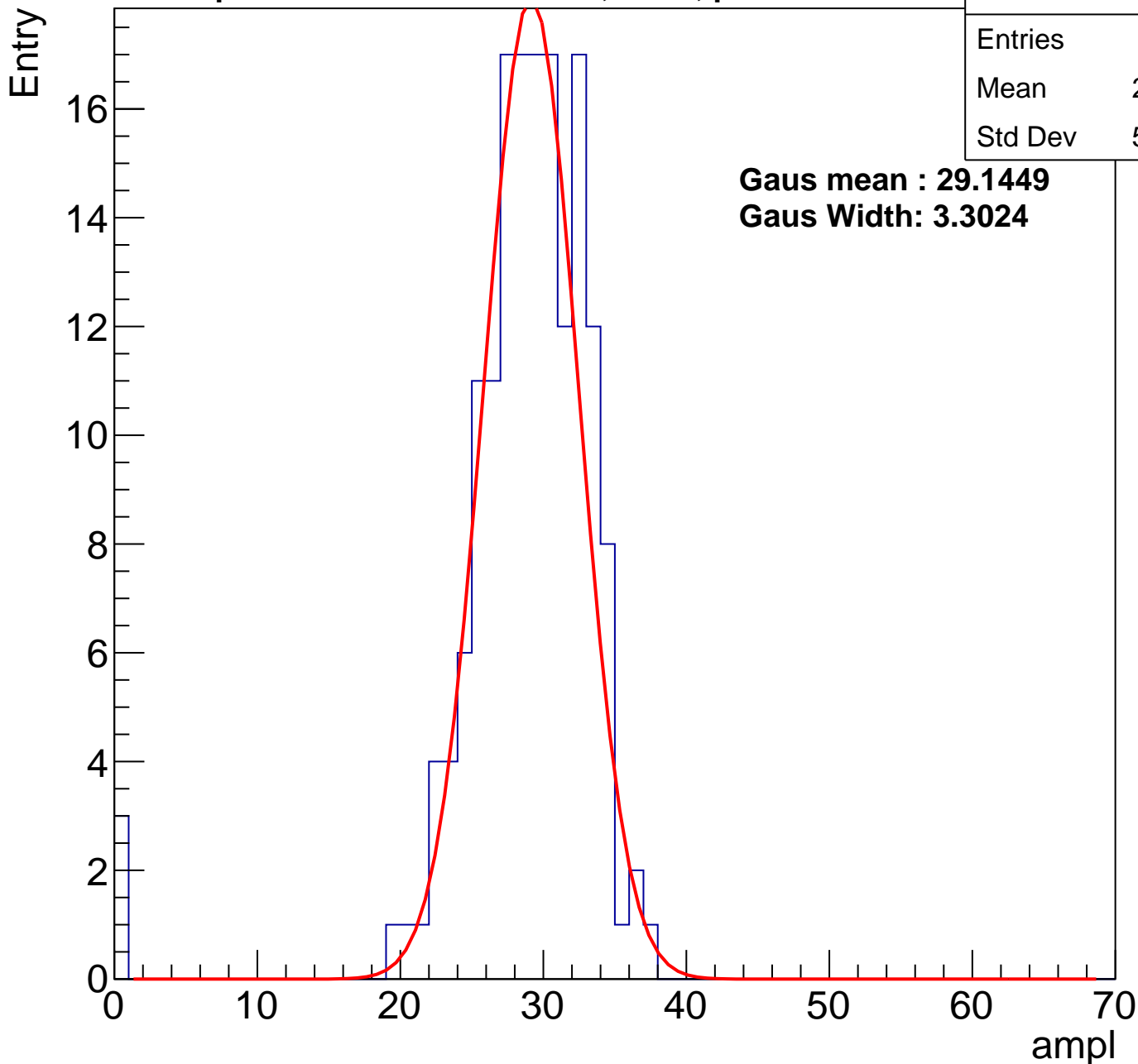
B0L101S, U22-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 163 |
| Mean | 28.29 |
| Std Dev | 5.162 |

Gaus mean : 29.1449

Gaus Width: 3.3024



B0L101S, U22-ch18, adc1

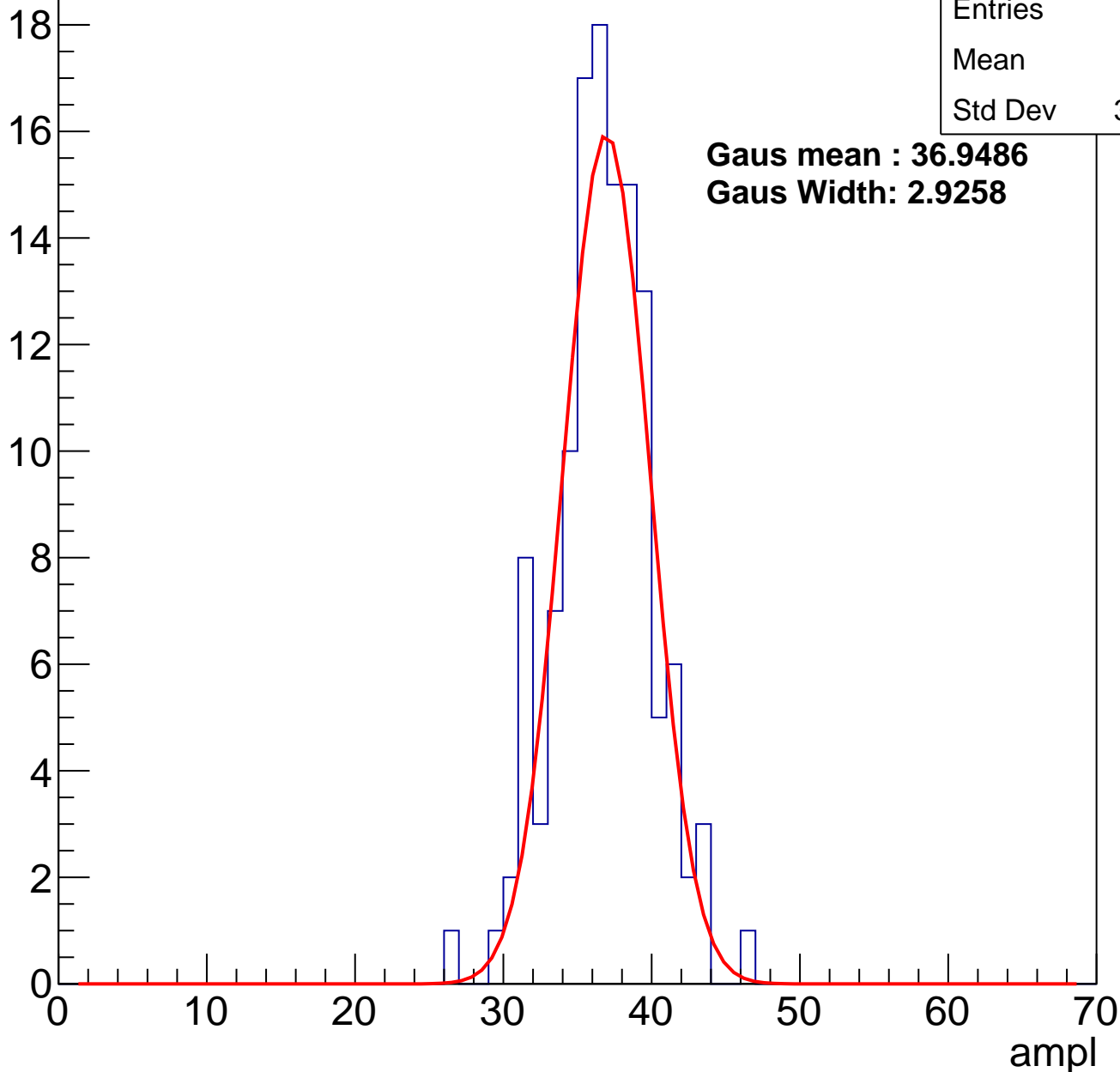
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 127 |
| Mean | 36.3 |
| Std Dev | 3.222 |

Gaus mean : 36.9486

Gaus Width: 2.9258

Entry



B0L101S, U22-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

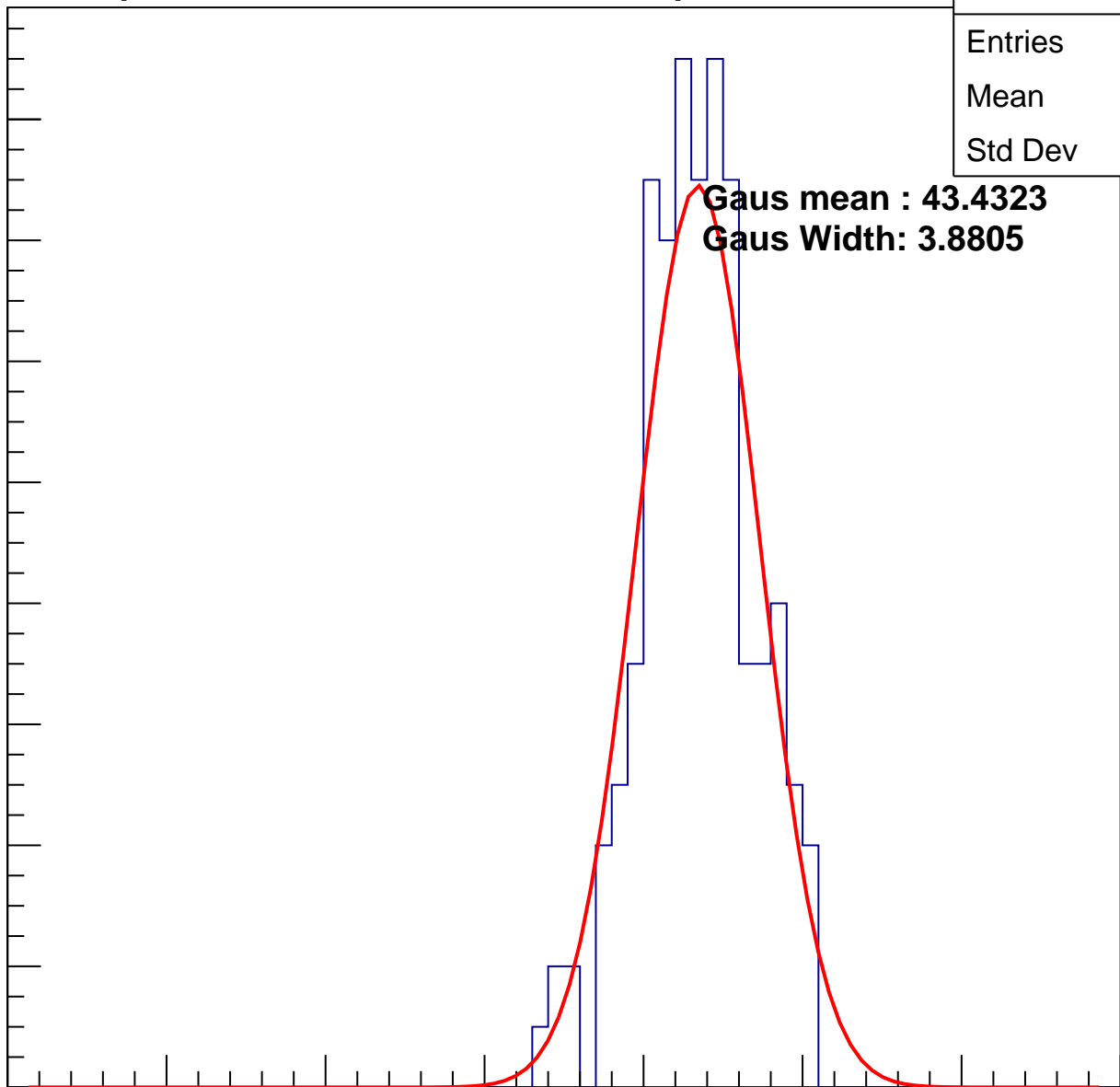
| | |
|---------|-------|
| Entries | 145 |
| Mean | 42.88 |
| Std Dev | 3.528 |

Gaus mean : 43.4323

Gaus Width: 3.8805

0 10 20 30 40 50 60 70

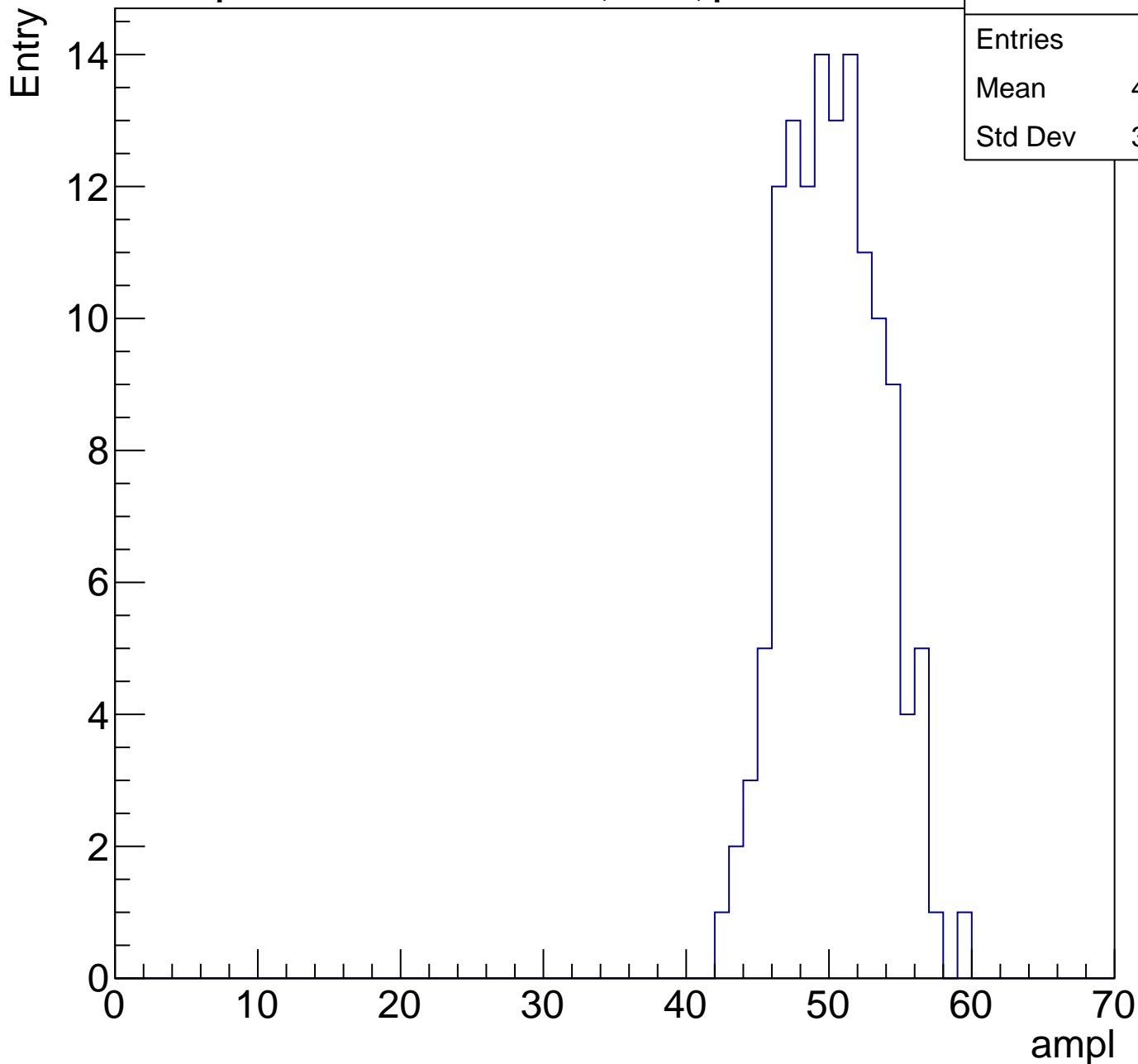
ampl



B0L101S, U22-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

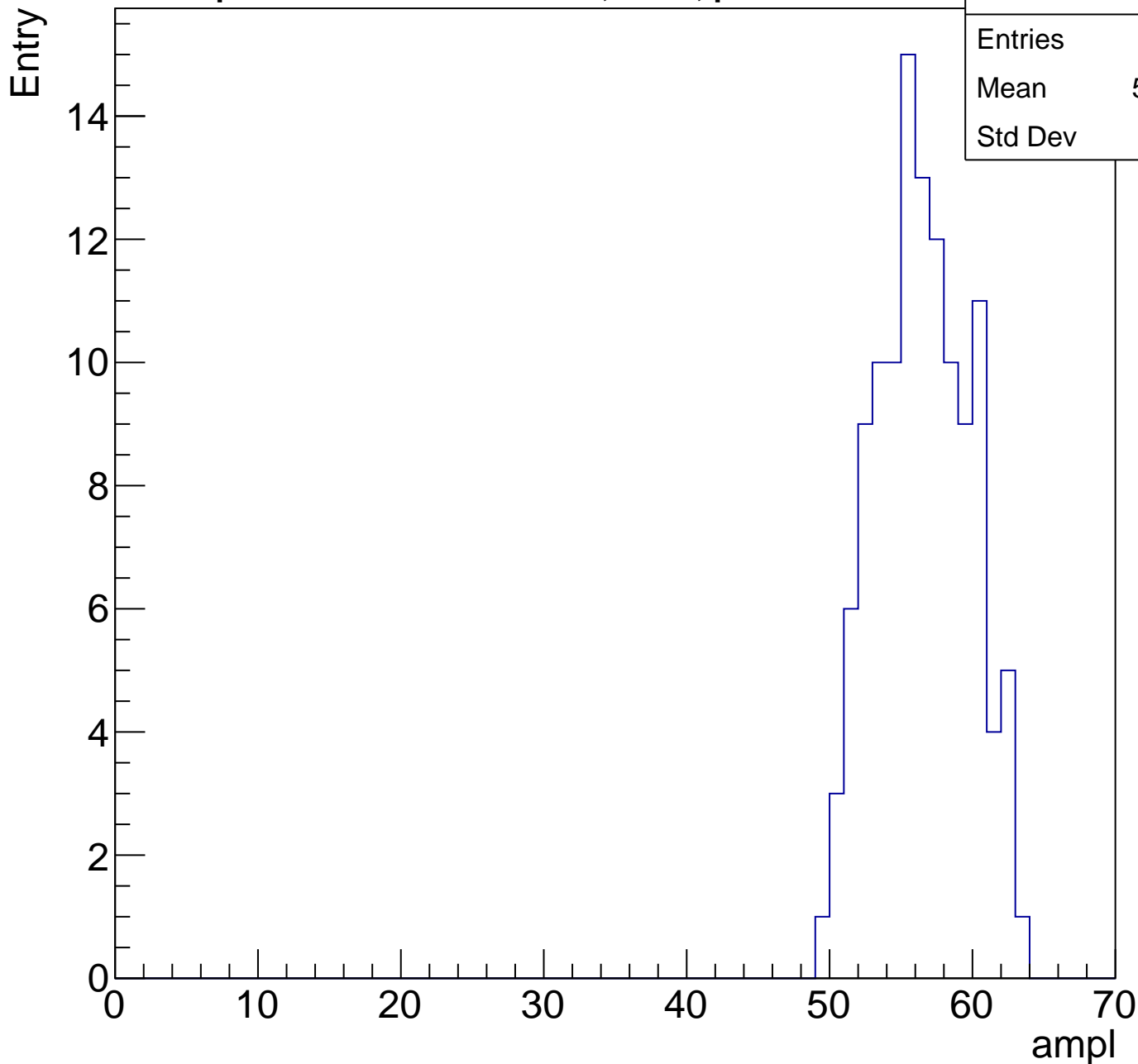
| | |
|---------|-------|
| Entries | 130 |
| Mean | 49.83 |
| Std Dev | 3.356 |



B0L101S, U22-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 119 |
| Mean | 56.03 |
| Std Dev | 3.22 |



B0L101S, U22-ch18, adc5

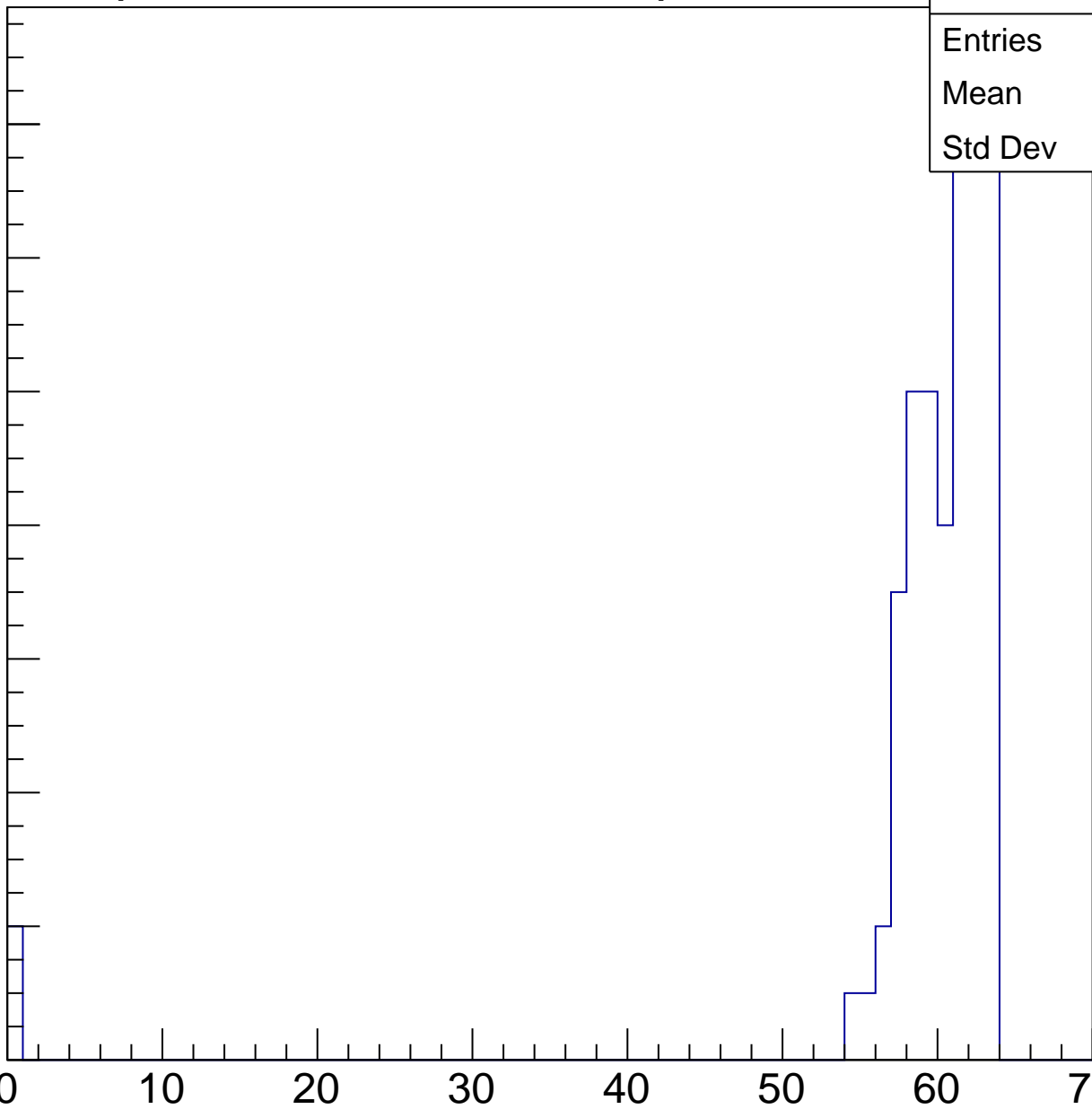
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 84 |
| Mean | 58.75 |
| Std Dev | 9.431 |

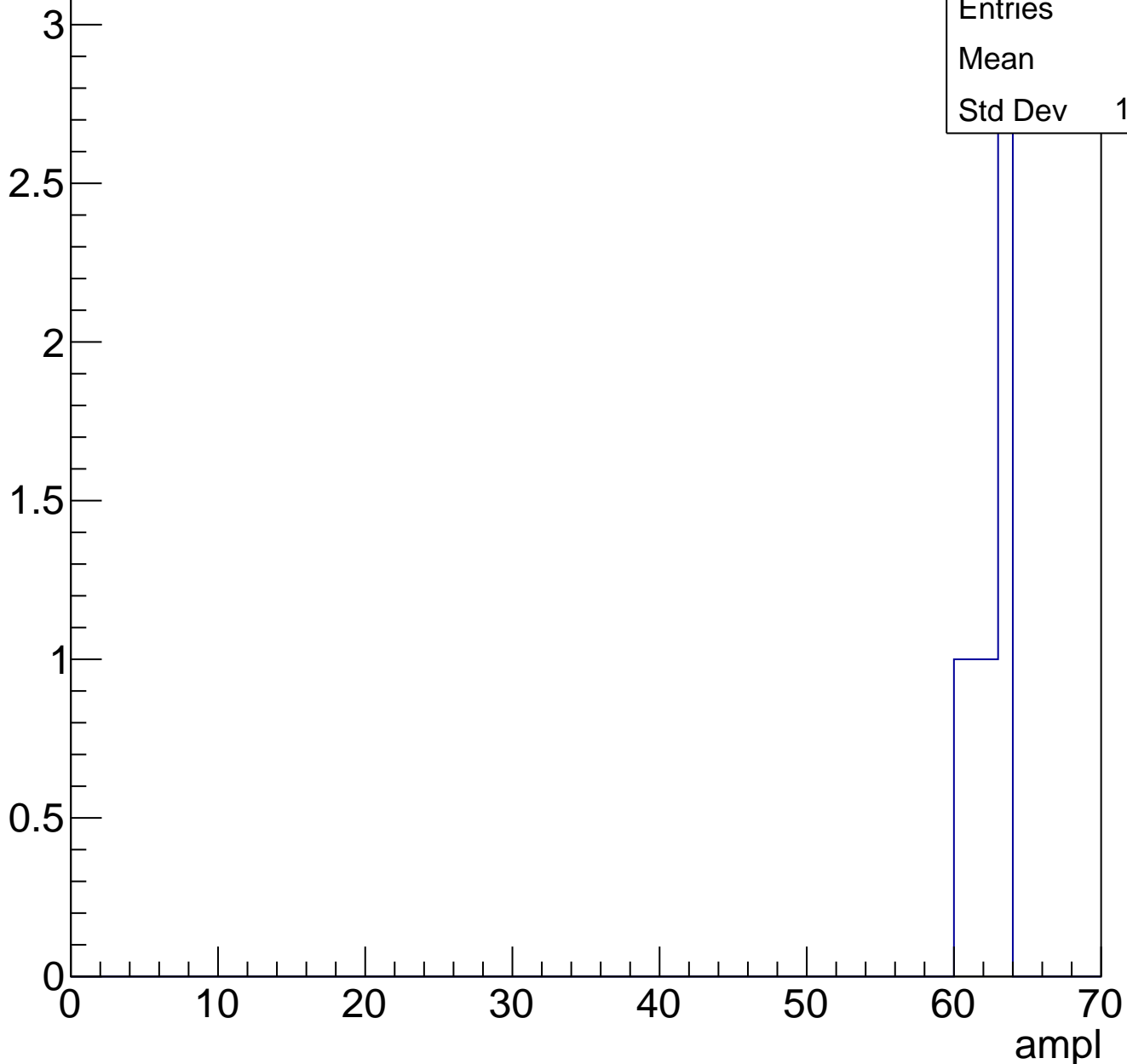
ampl



B0L101S, U22-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



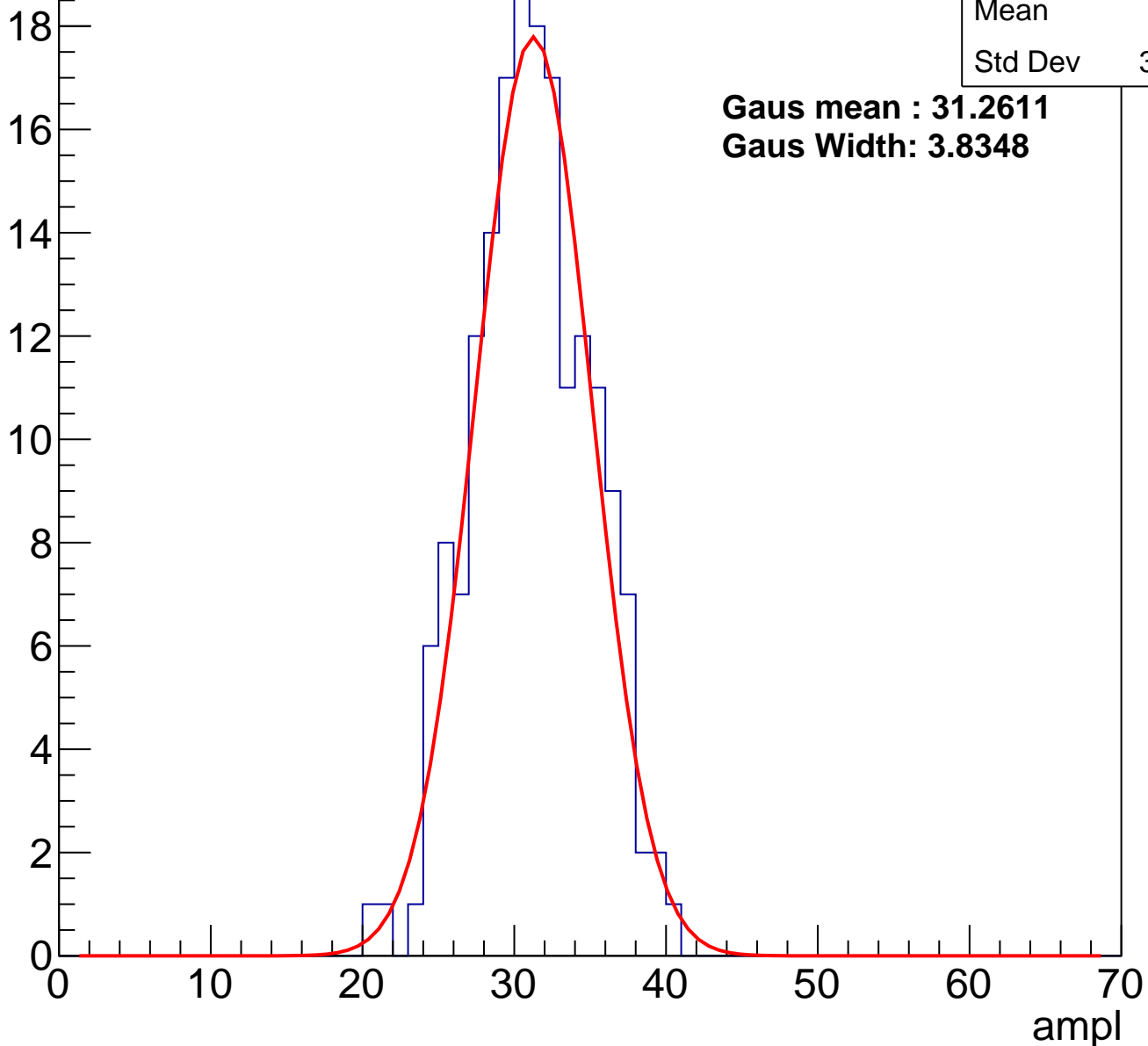
B0L101S, U22-ch19, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 176 |
| Mean | 30.7 |
| Std Dev | 3.814 |

Gaus mean : 31.2611
Gaus Width: 3.8348

Entry



B0L101S, U22-ch19, adc1

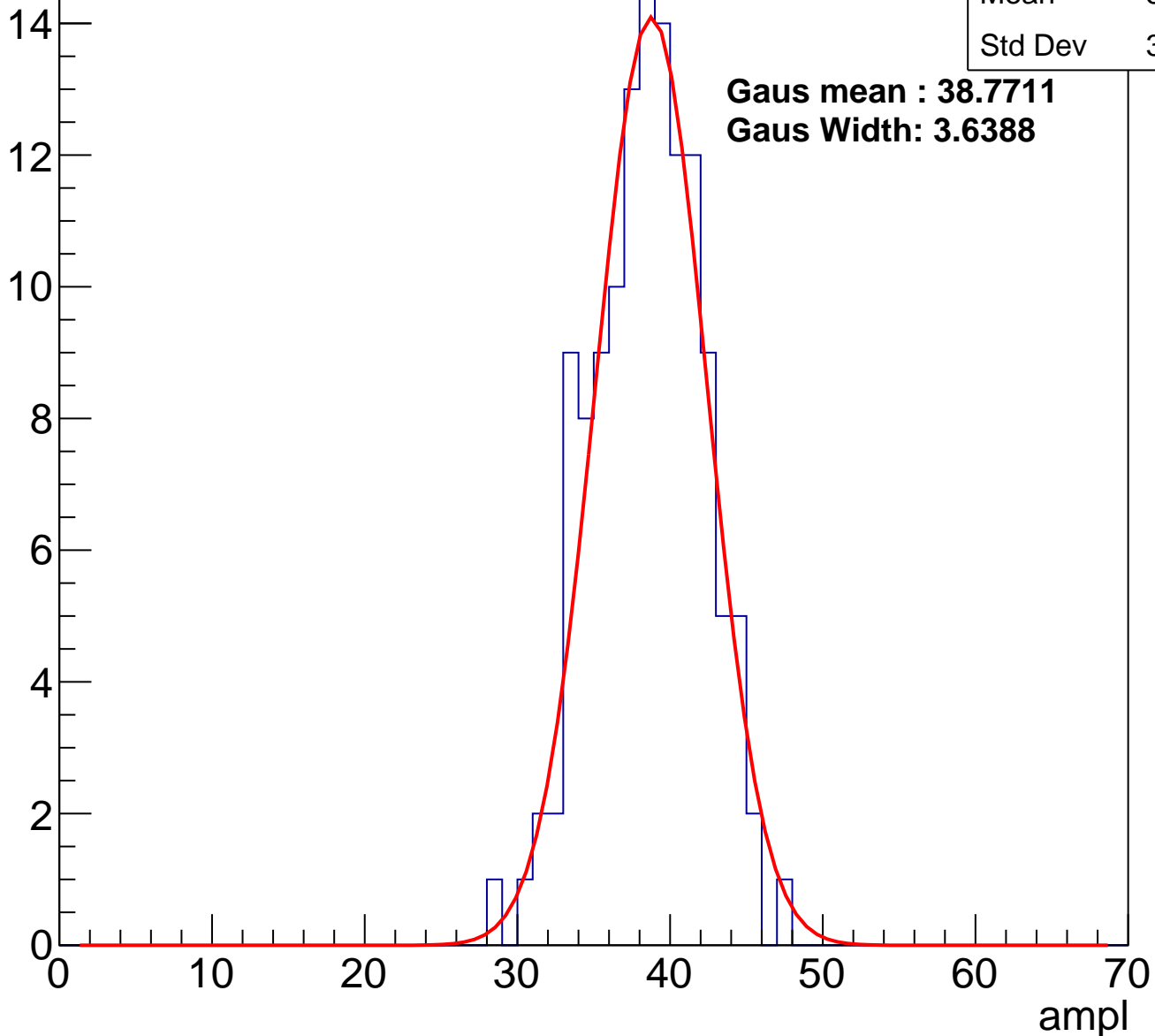
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 38.05 |
| Std Dev | 3.529 |

Gaus mean : 38.7711

Gaus Width: 3.6388

Entry



B0L101S, U22-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

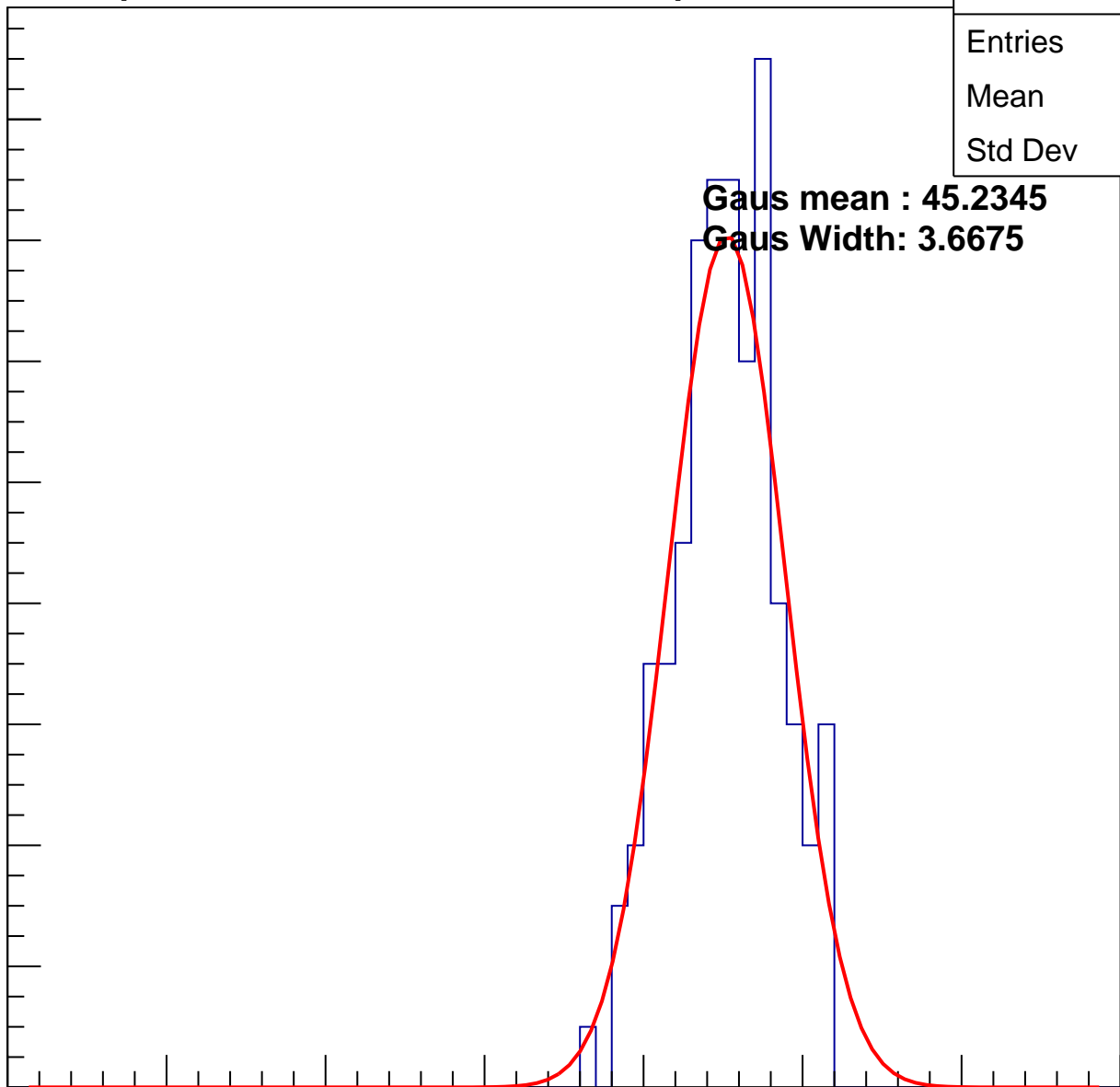
| | |
|---------|-------|
| Entries | 128 |
| Mean | 44.71 |
| Std Dev | 3.265 |

Gaus mean : 45.2345

Gaus Width: 3.6675

0 10 20 30 40 50 60 70

ampl

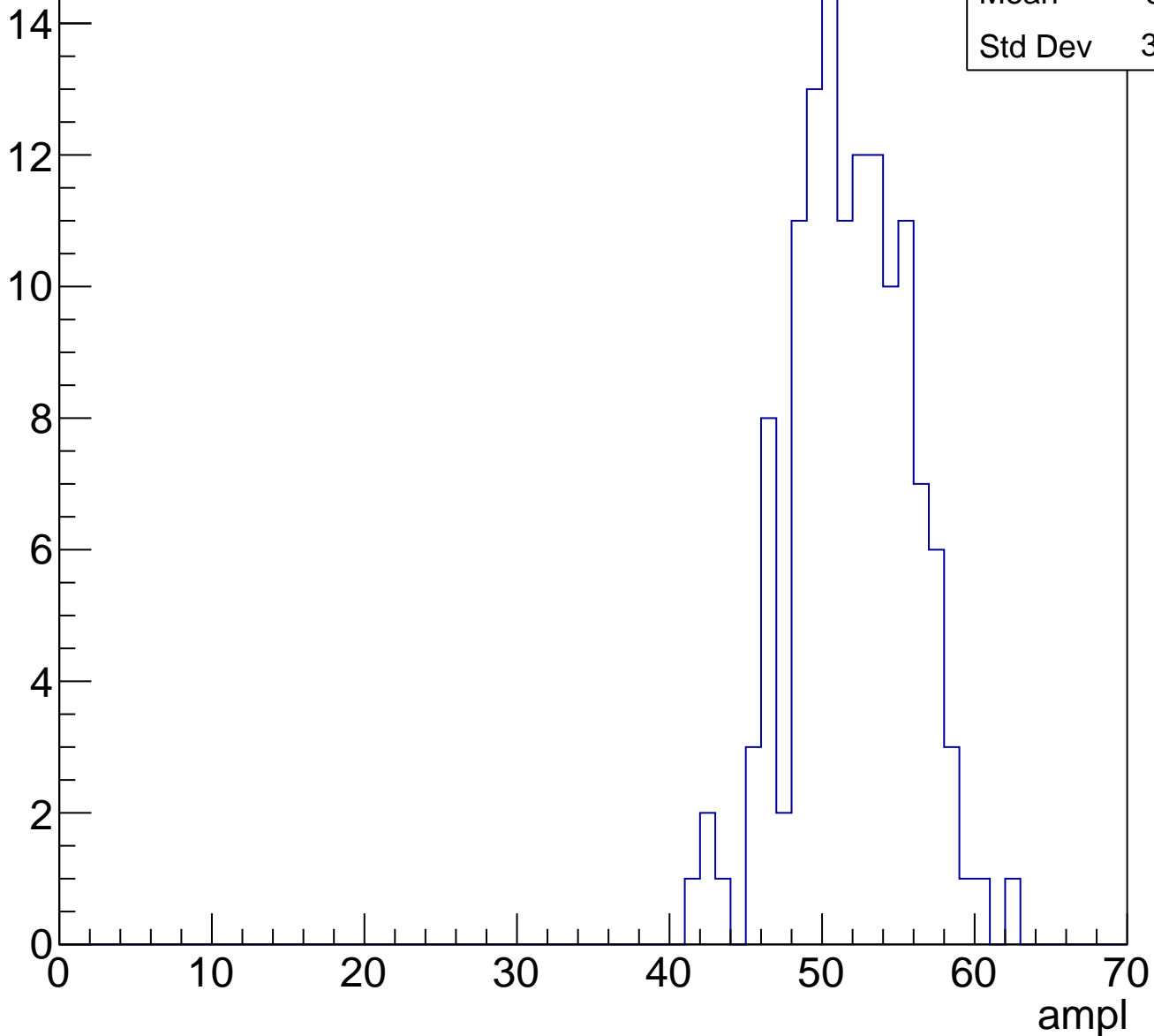


B0L101S, U22-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 51.41 |
| Std Dev | 3.848 |

Entry



B0L101S, U22-ch19, adc4

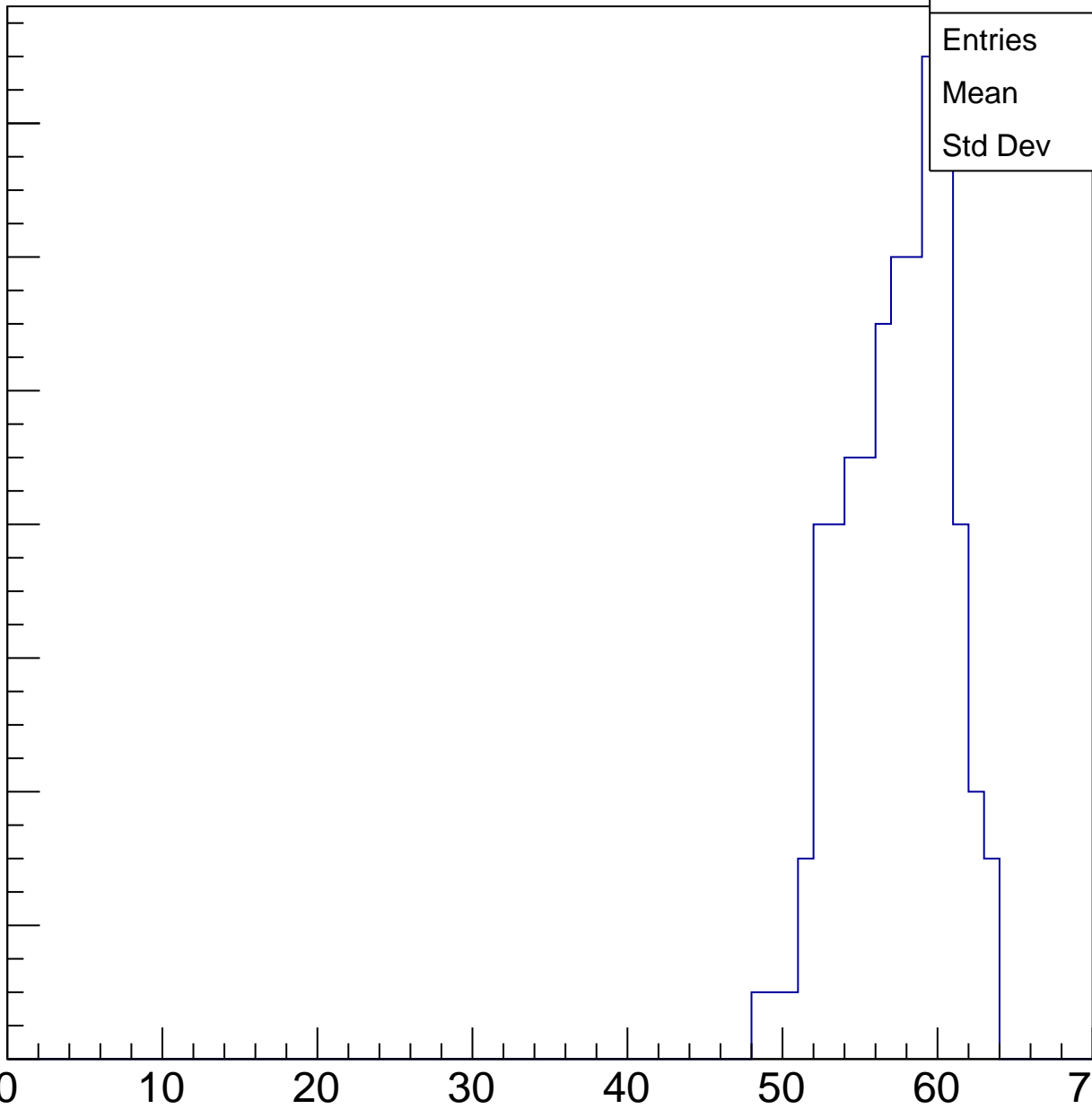
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 119 |
| Mean | 56.87 |
| Std Dev | 3.292 |

ampl



B0L101S, U22-ch19, adc5

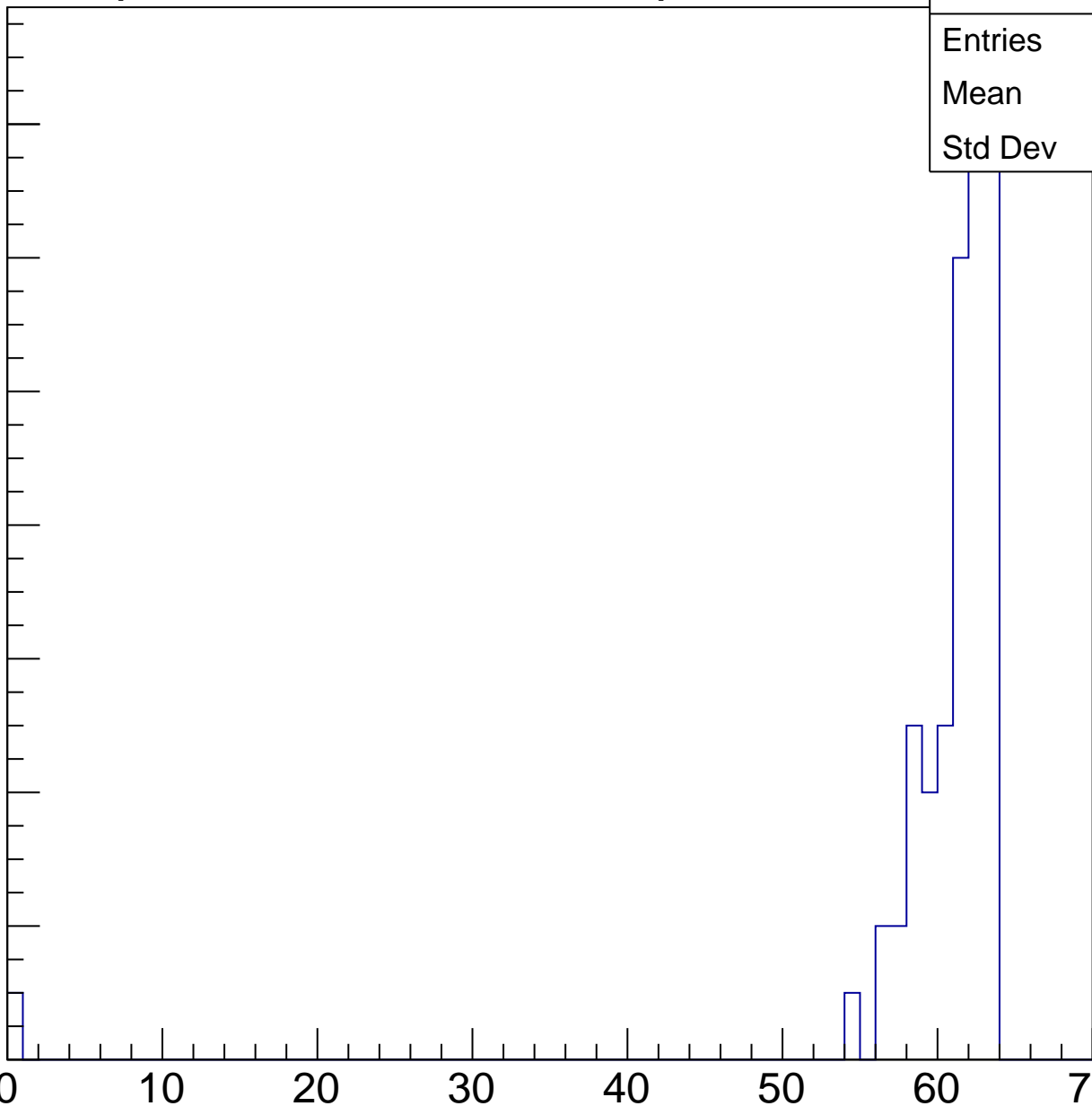
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.89 |
| Std Dev | 7.948 |

ampl



B0L101S, U22-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 22 |
| Std Dev | 0 |

ampl

B0L101S, U22-ch20, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 89 |
| Mean | 28.69 |
| Std Dev | 2.909 |

Gaus mean : 28.9480

Gaus Width: 3.0755

Entry

12

10

8

6

4

2

0

0

10

20

30

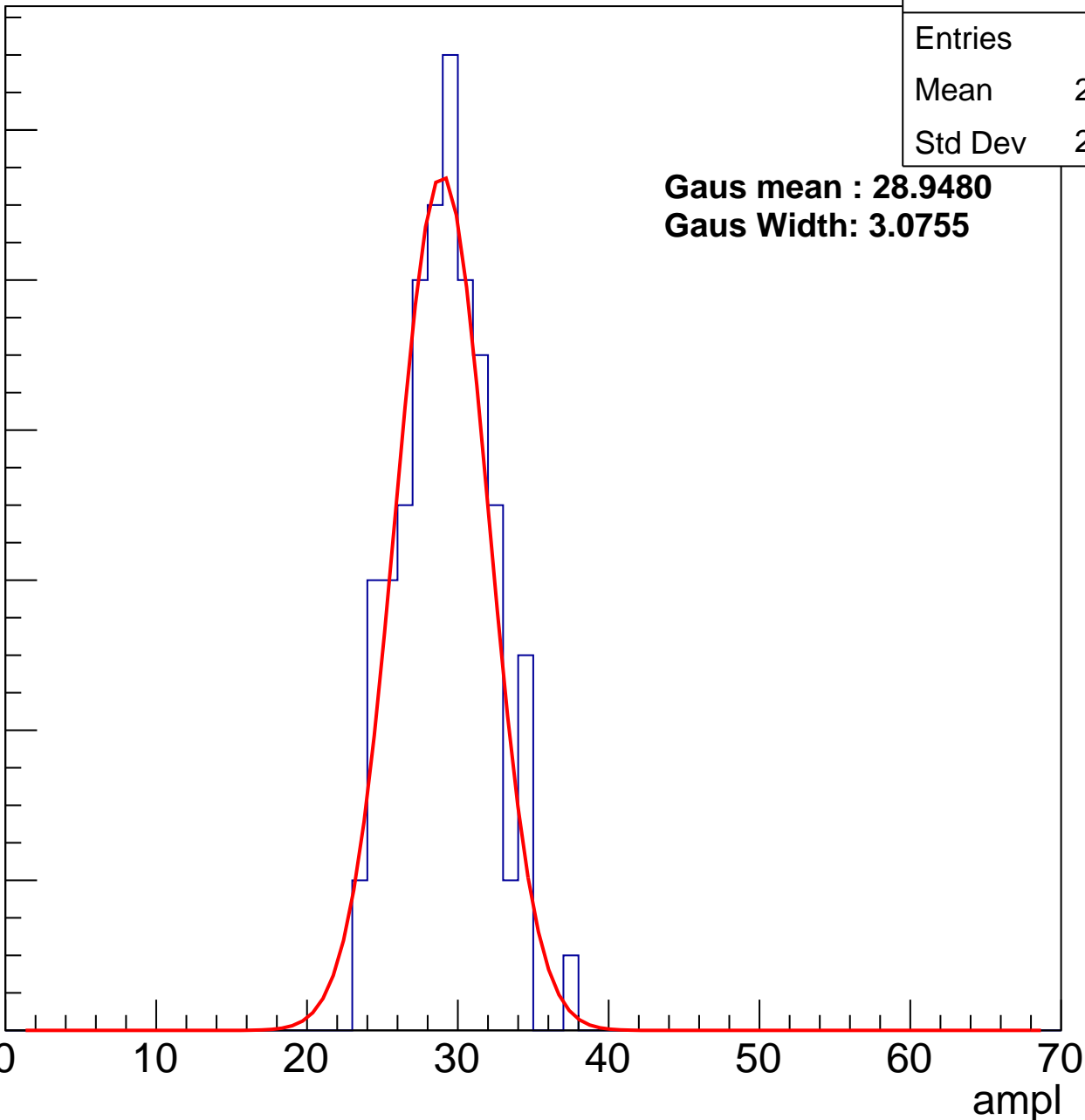
40

50

60

70

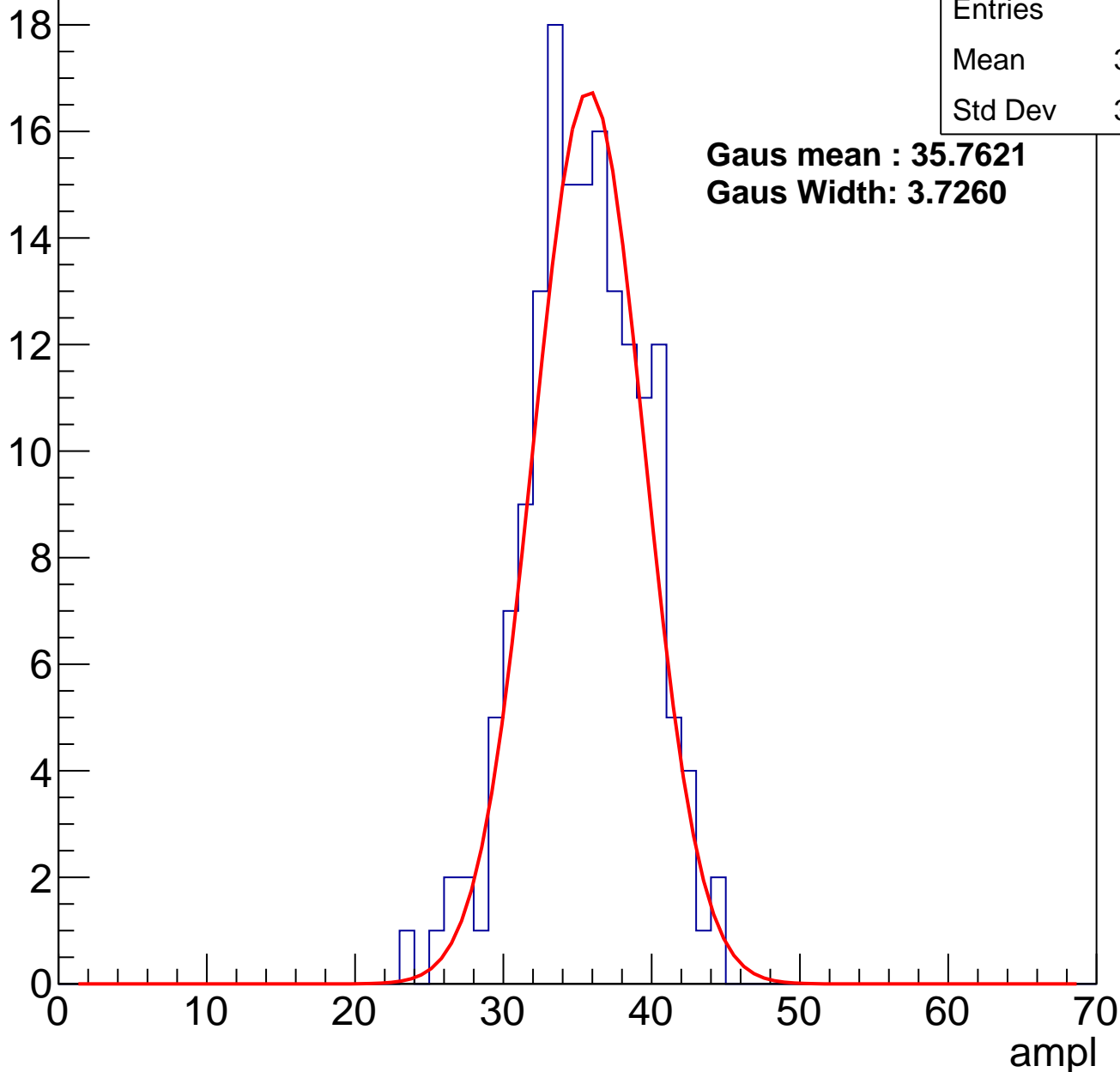
ampl



B0L101S, U22-ch20, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch20, adc2

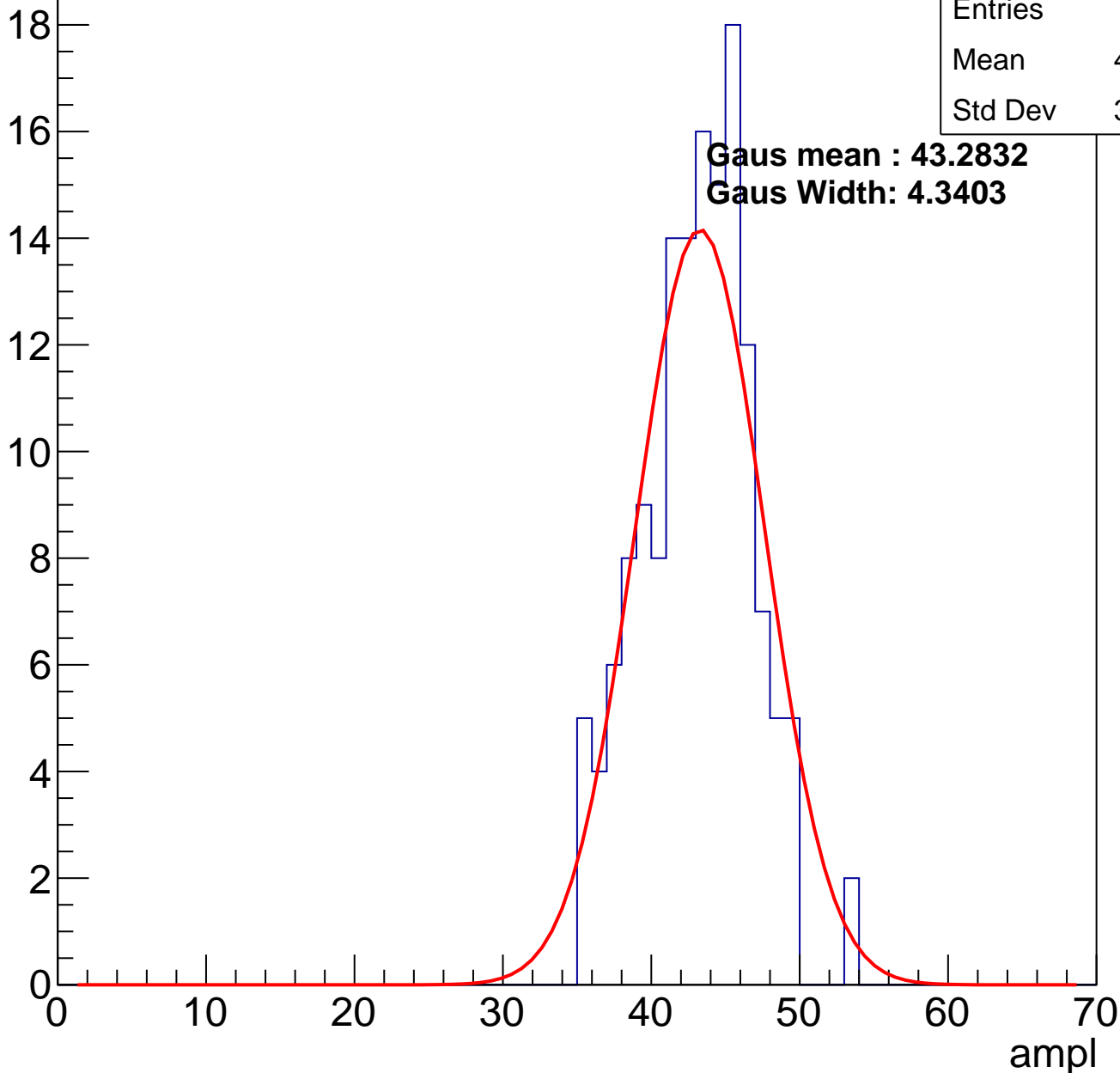
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 42.62 |
| Std Dev | 3.684 |

Gaus mean : 43.2832

Gaus Width: 4.3403

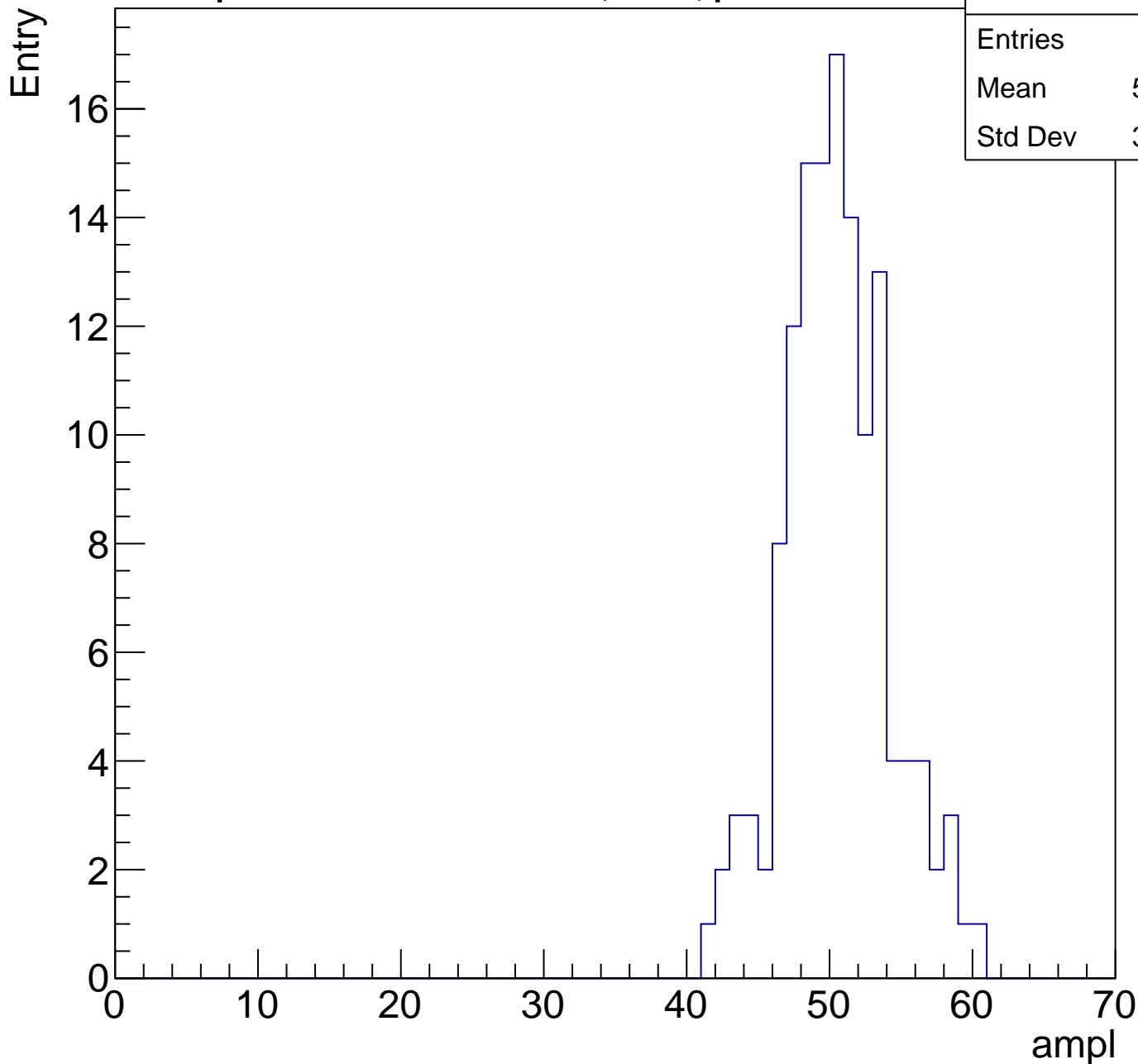
Entry



B0L101S, U22-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 50.02 |
| Std Dev | 3.639 |



B0L101S, U22-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

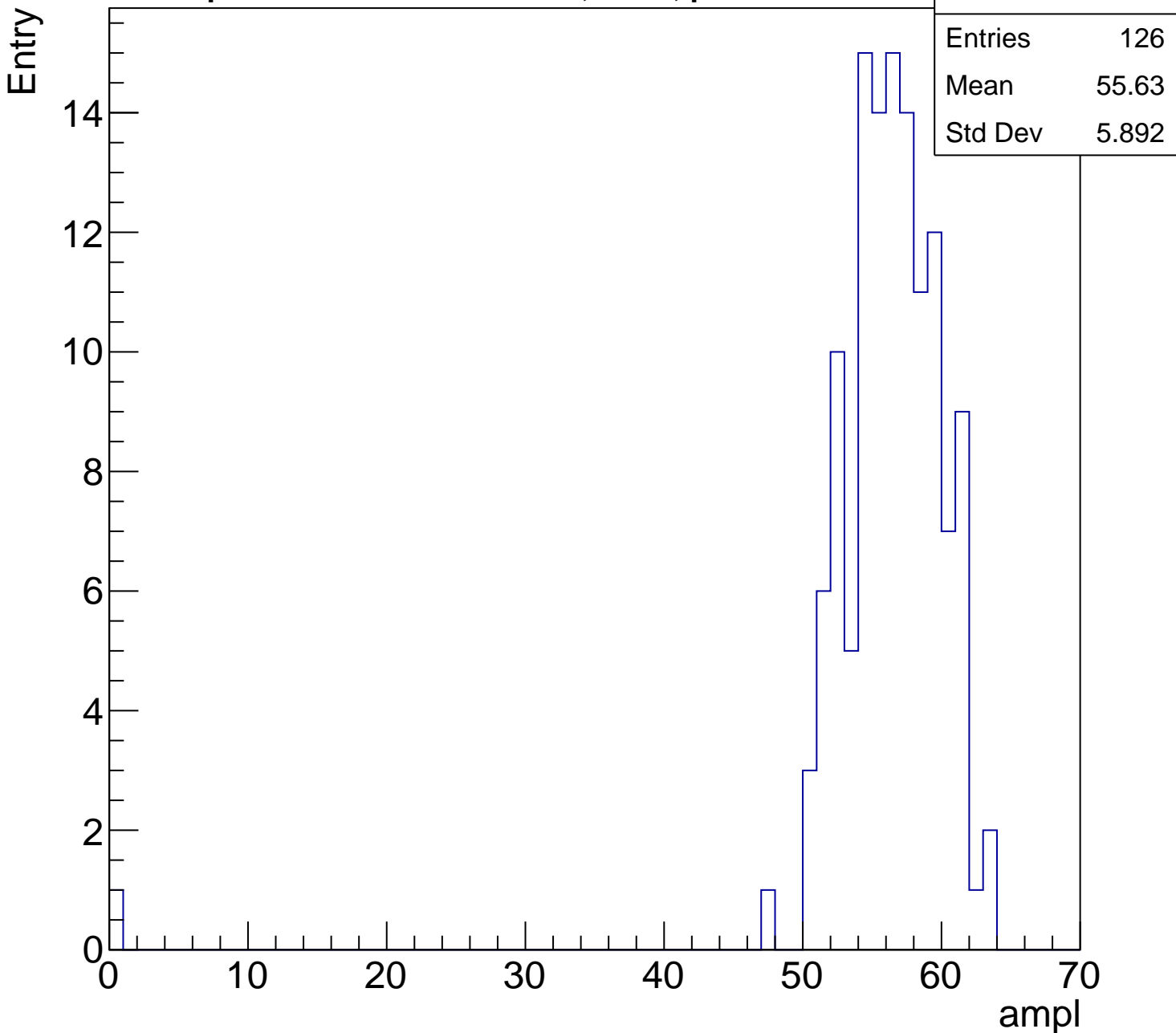
| | |
|---------|-------|
| Entries | 126 |
| Mean | 55.63 |
| Std Dev | 5.892 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

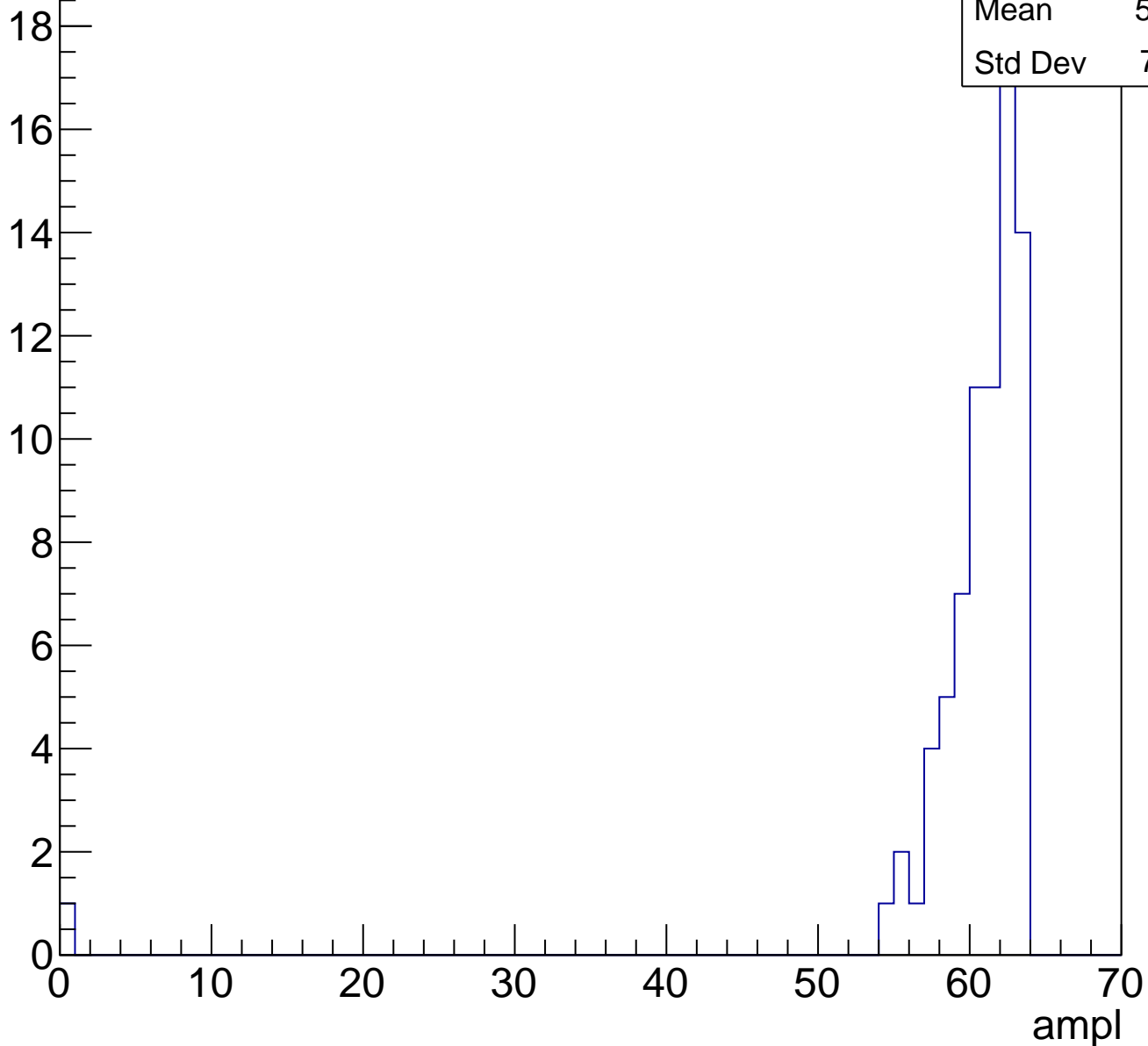
ampl



B0L101S, U22-ch20, adc5

calib_packv5_042523_0143.root, FC#1, port C1

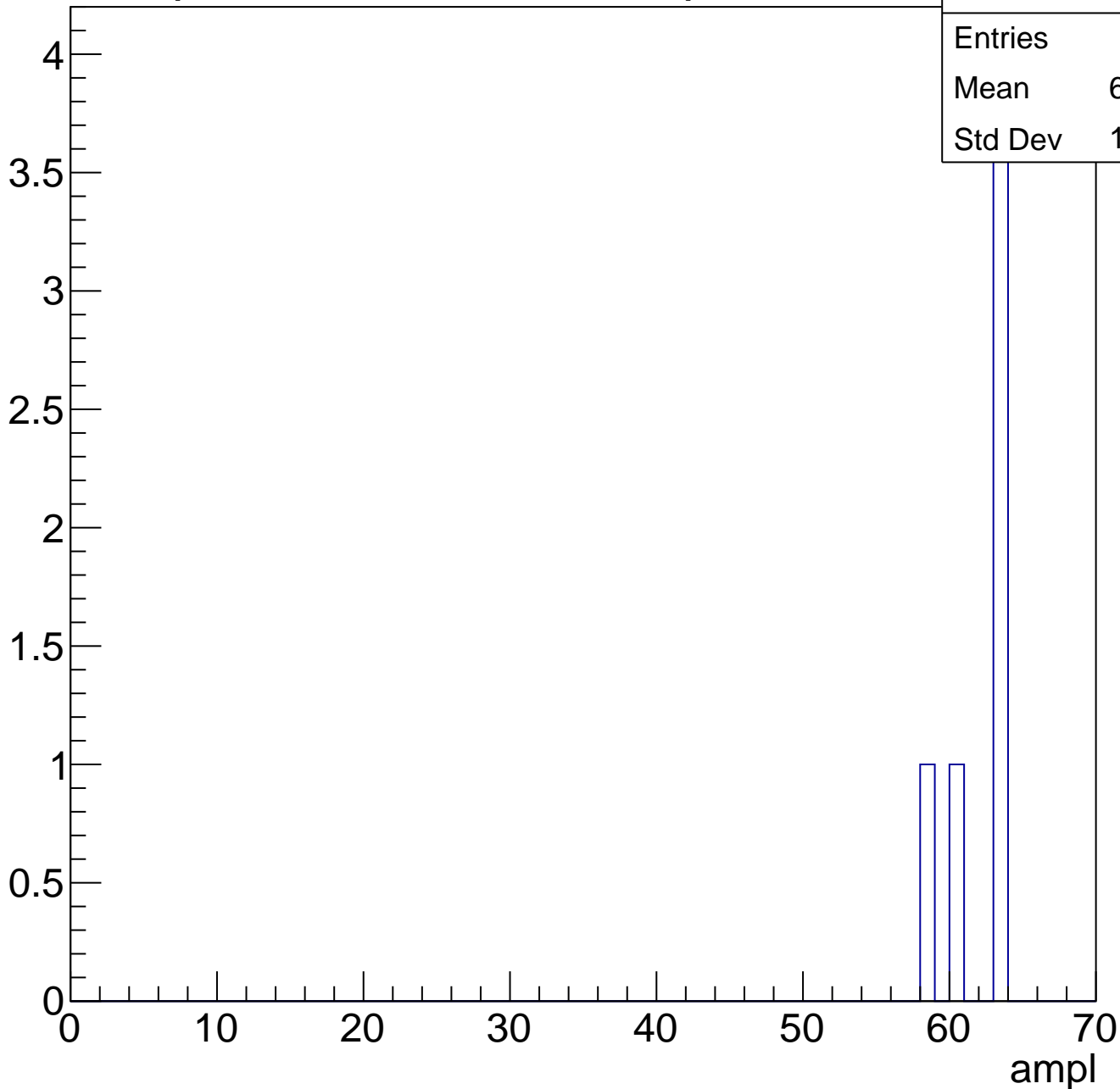
Entry



B0L101S, U22-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch21, adc0

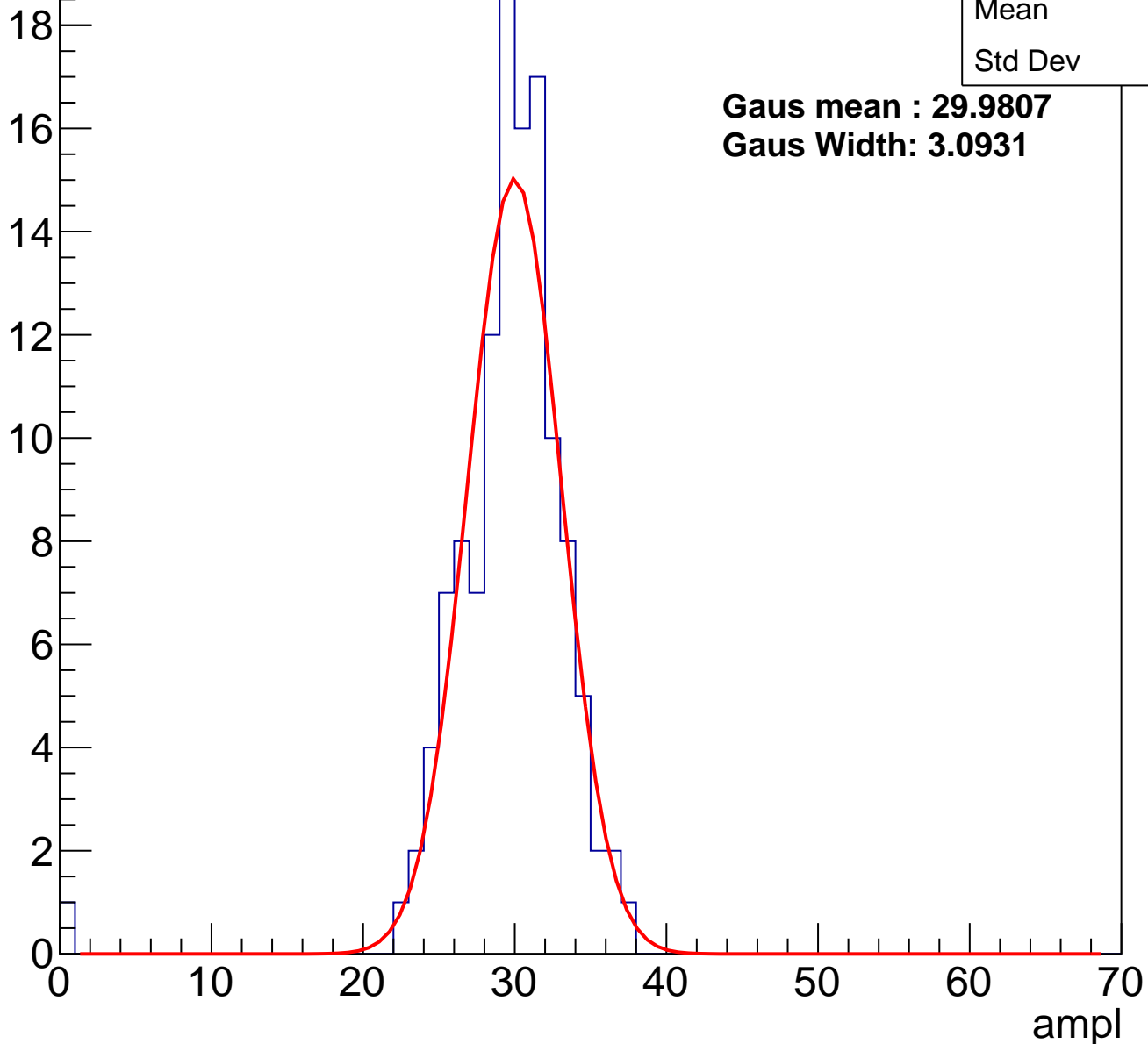
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 122 |
| Mean | 29.2 |
| Std Dev | 3.99 |

Gaus mean : 29.9807

Gaus Width: 3.0931

Entry



B0L101S, U22-ch21, adc1

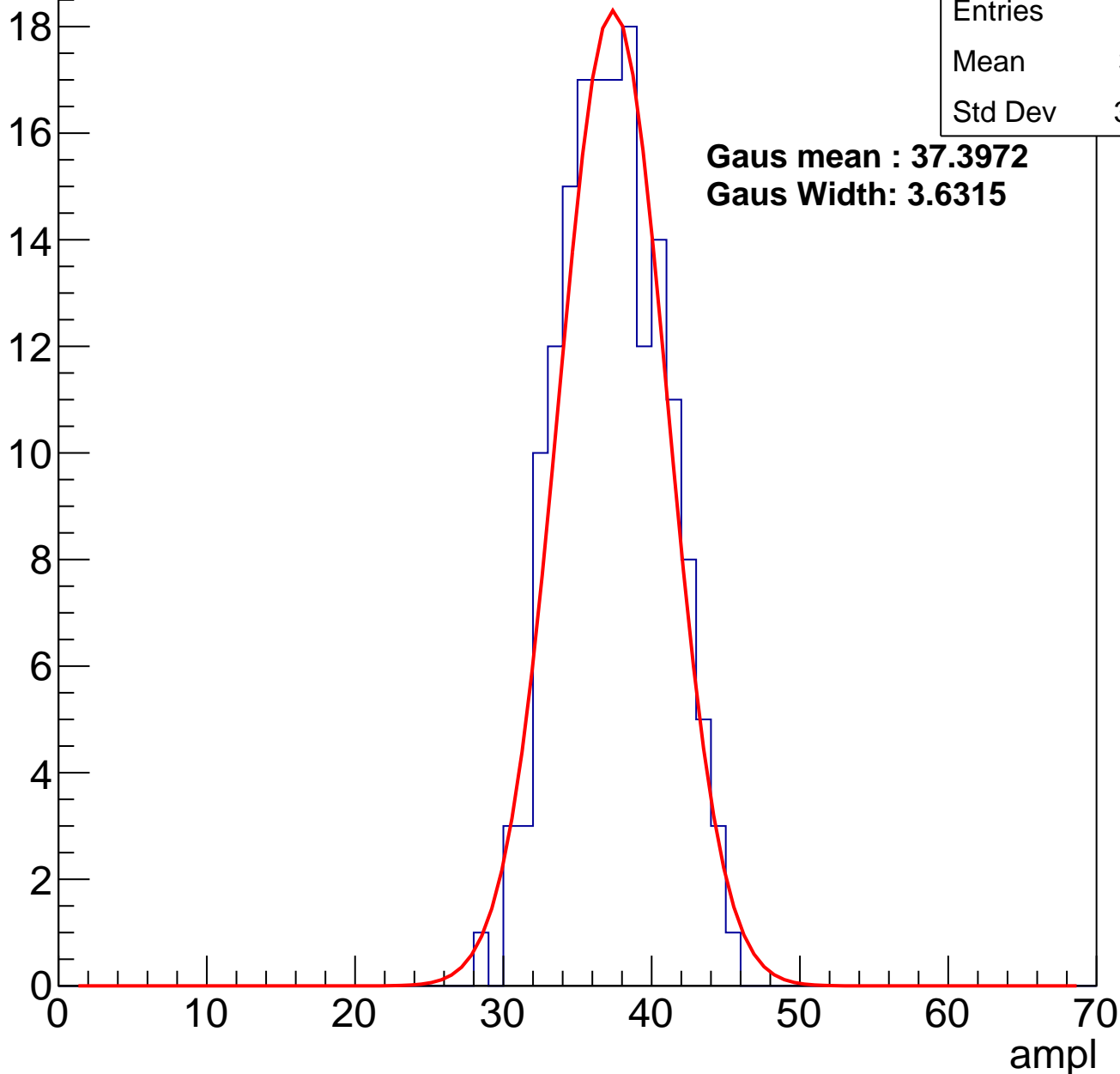
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 167 |
| Mean | 36.91 |
| Std Dev | 3.422 |

Gaus mean : 37.3972

Gaus Width: 3.6315

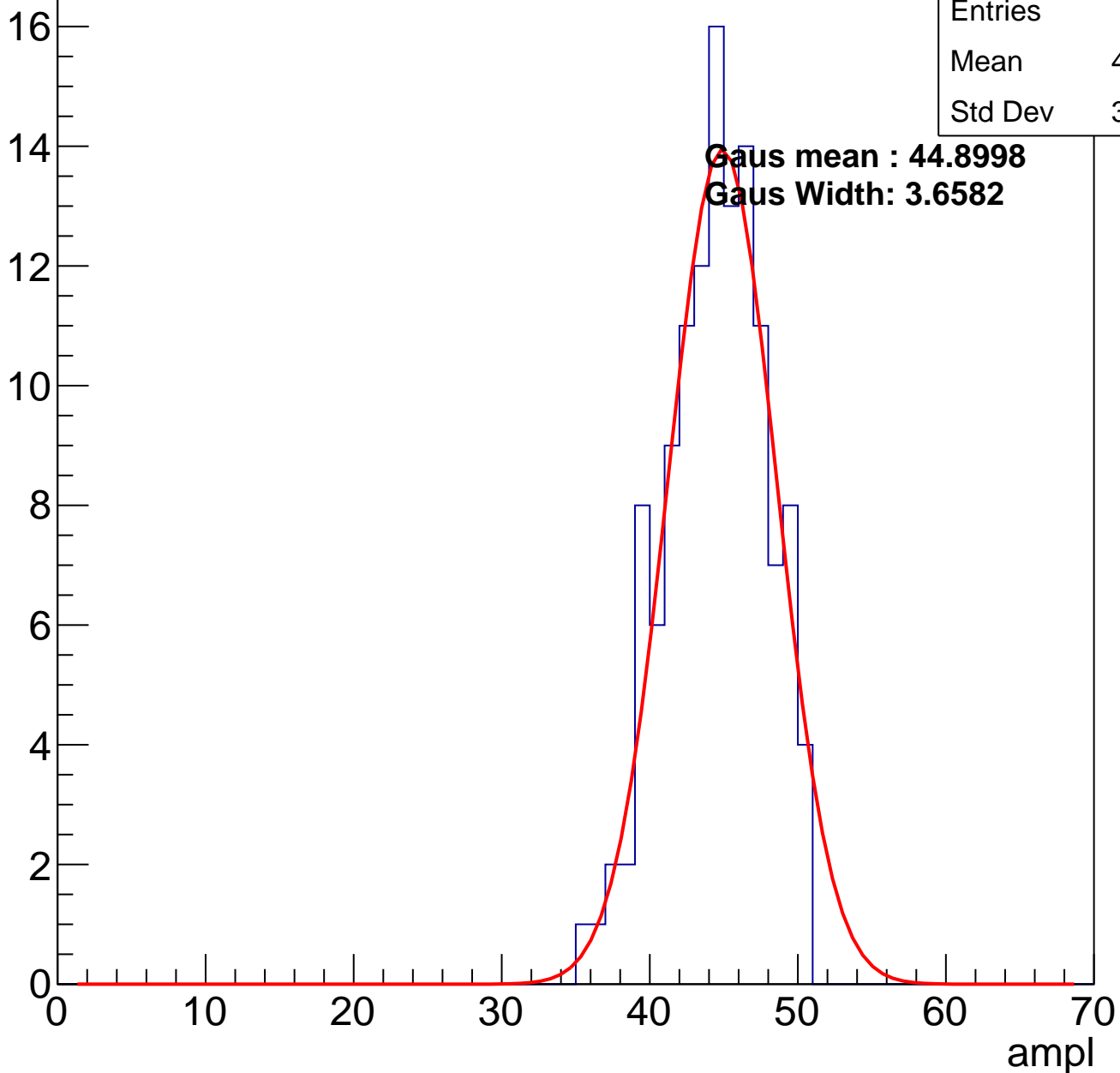
Entry



B0L101S, U22-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

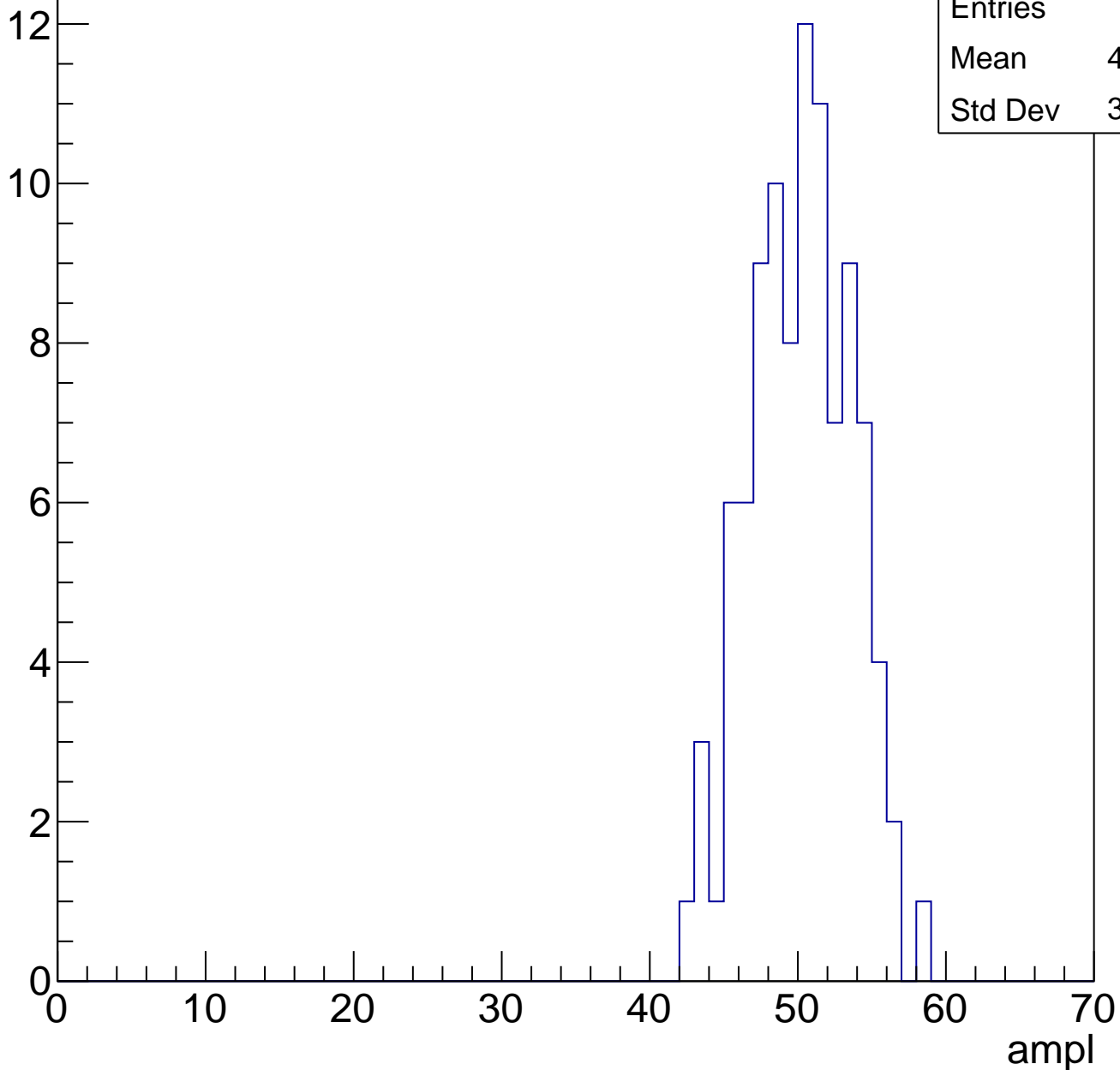


B0L101S, U22-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 97 |
| Mean | 49.75 |
| Std Dev | 3.343 |

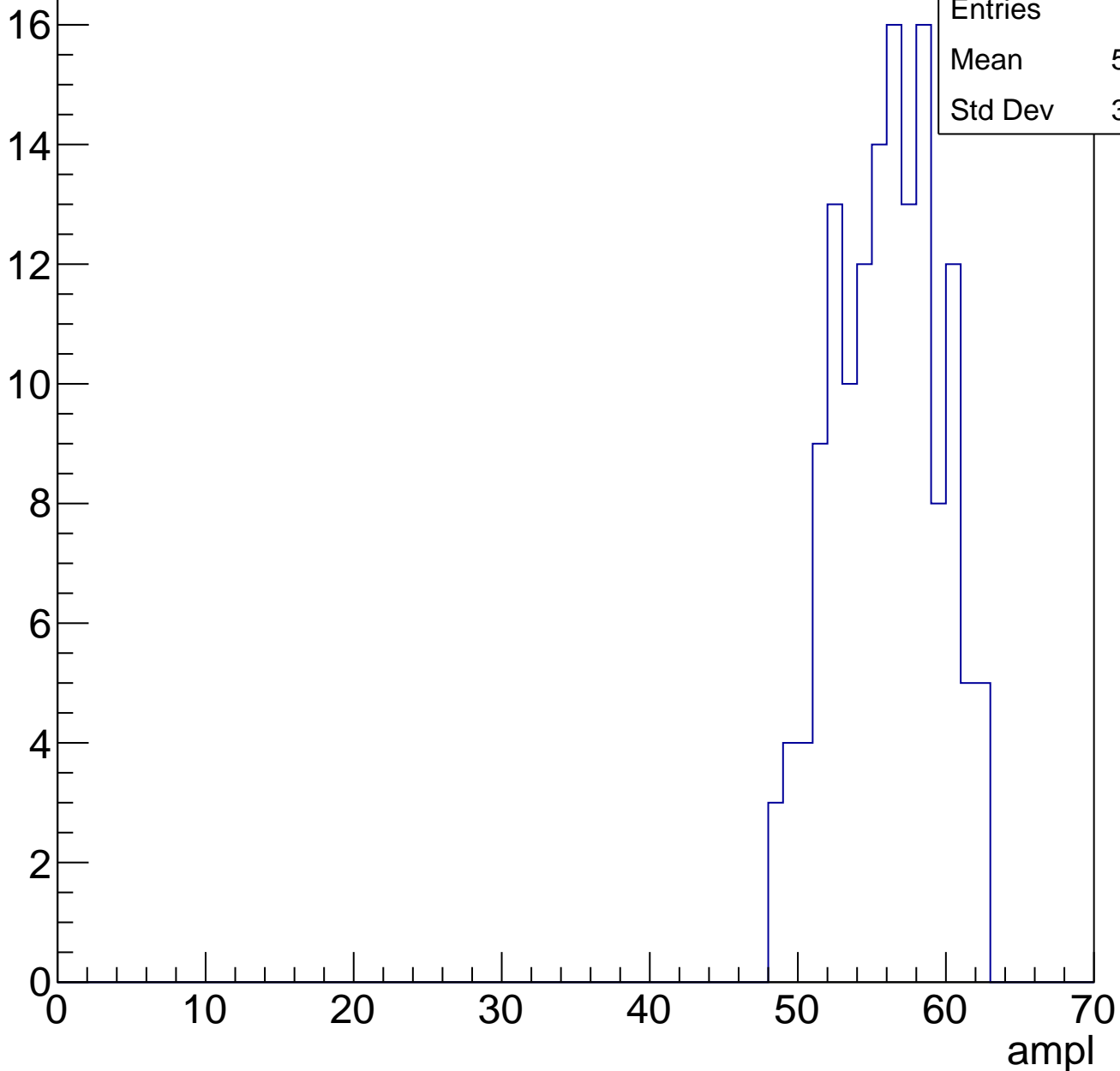
Entry



B0L101S, U22-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 144 |
| Mean | 55.52 |
| Std Dev | 3.466 |

B0L101S, U22-ch21, adc5

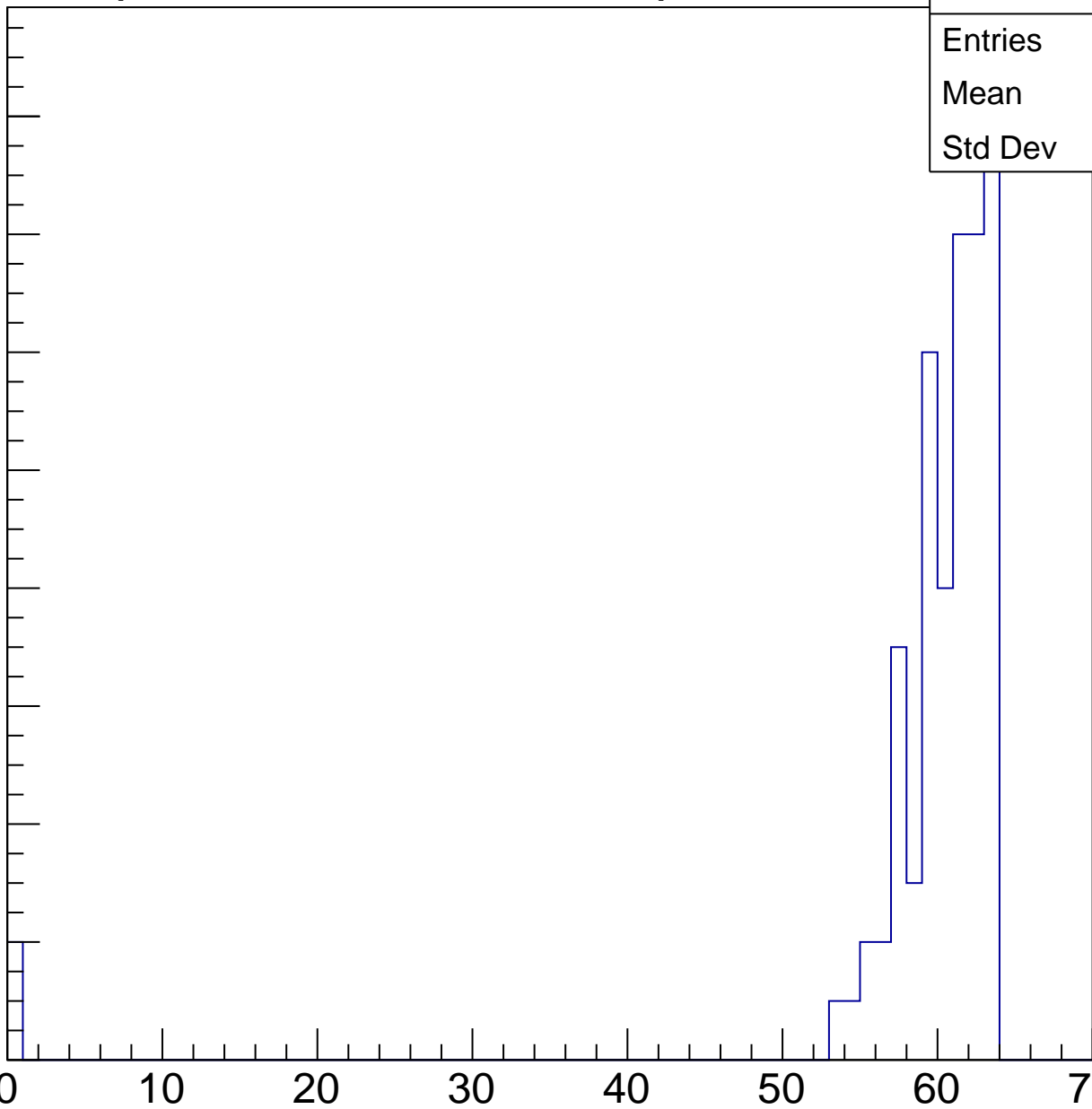
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 58.83 |
| Std Dev | 9.546 |

ampl



B0L101S, U22-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



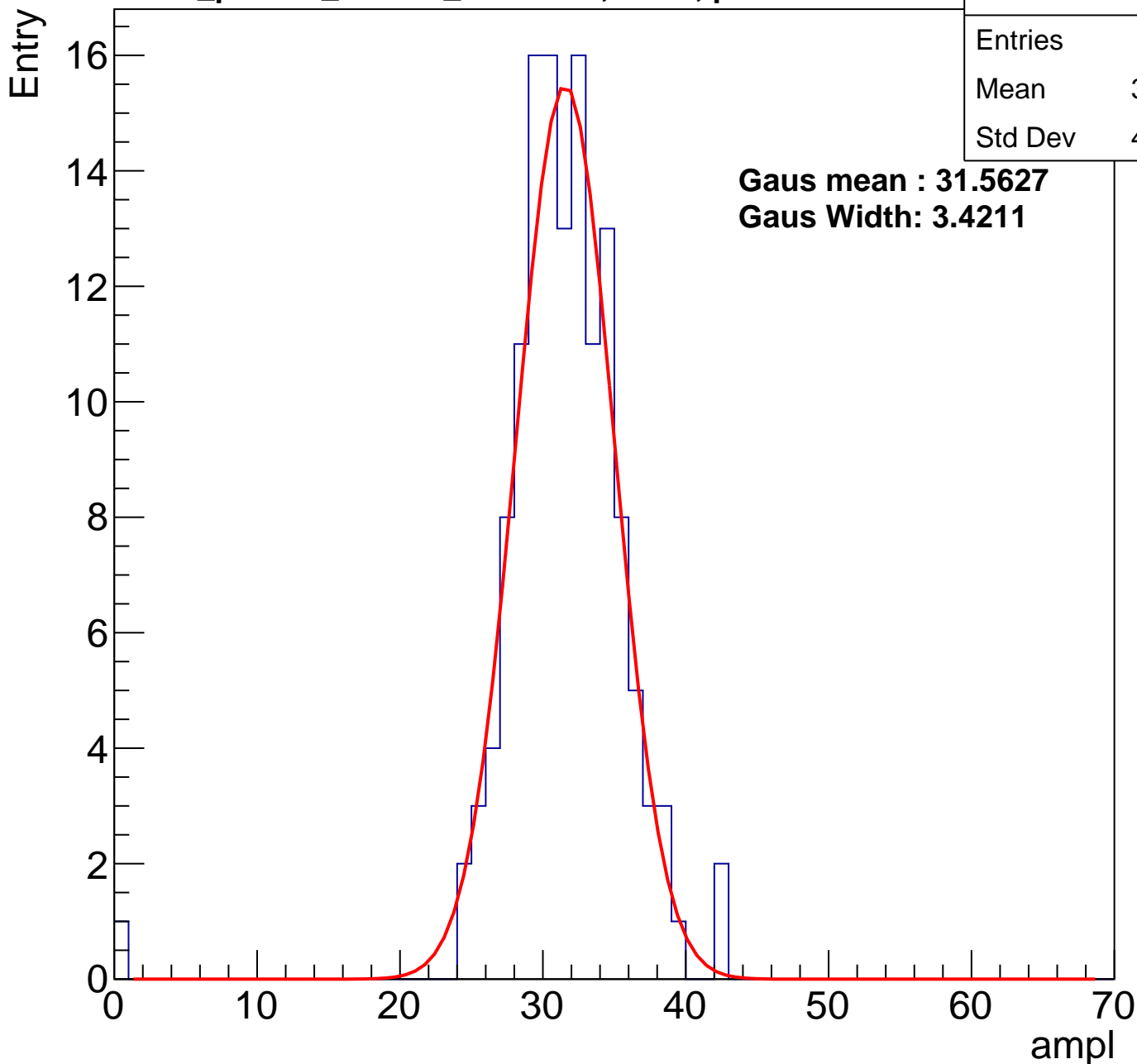
B0L101S, U22-ch22, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 31.05 |
| Std Dev | 4.343 |

Gaus mean : 31.5627

Gaus Width: 3.4211



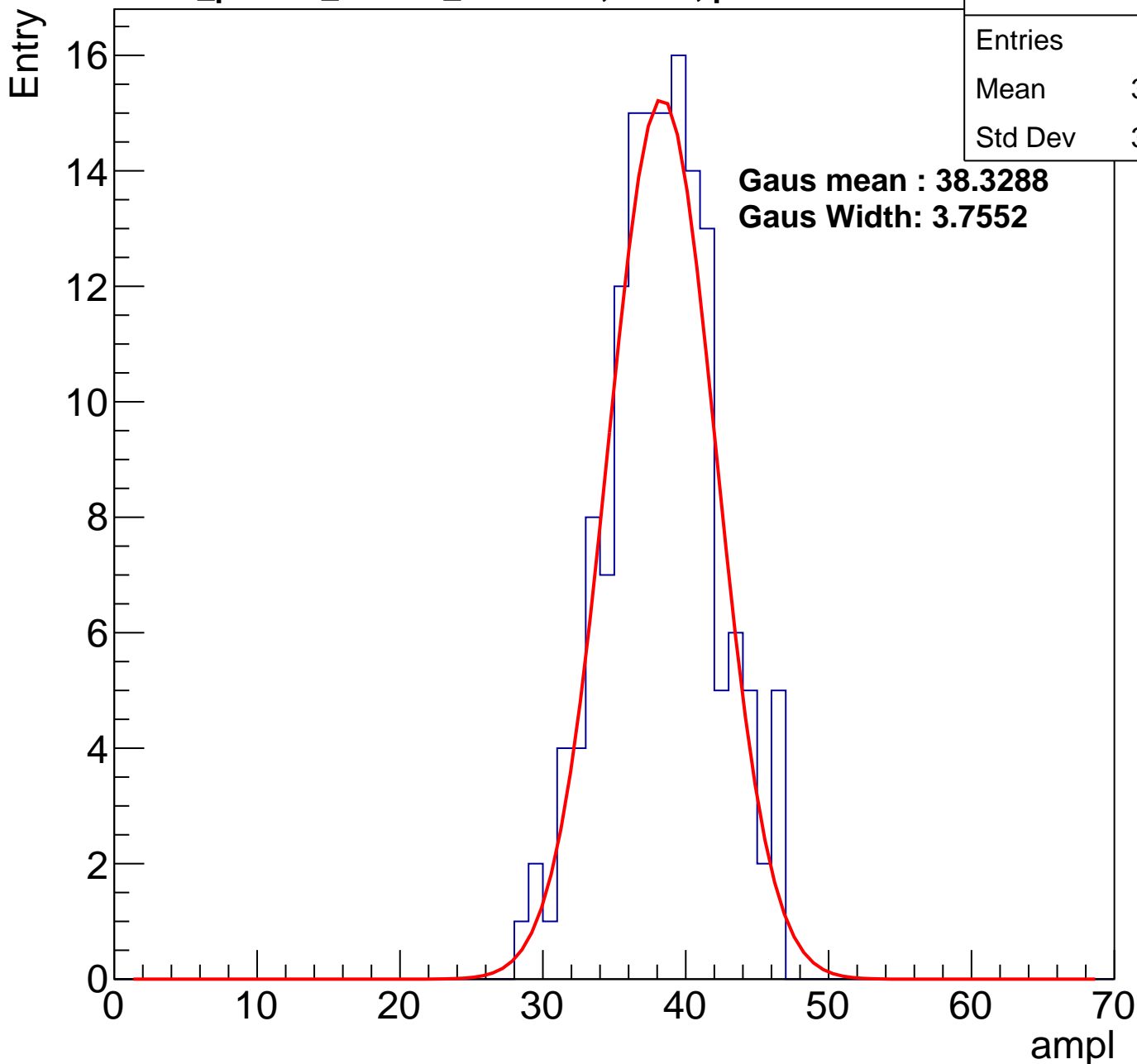
B0L101S, U22-ch22, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 37.87 |
| Std Dev | 3.808 |

Gaus mean : 38.3288

Gaus Width: 3.7552



B0L101S, U22-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

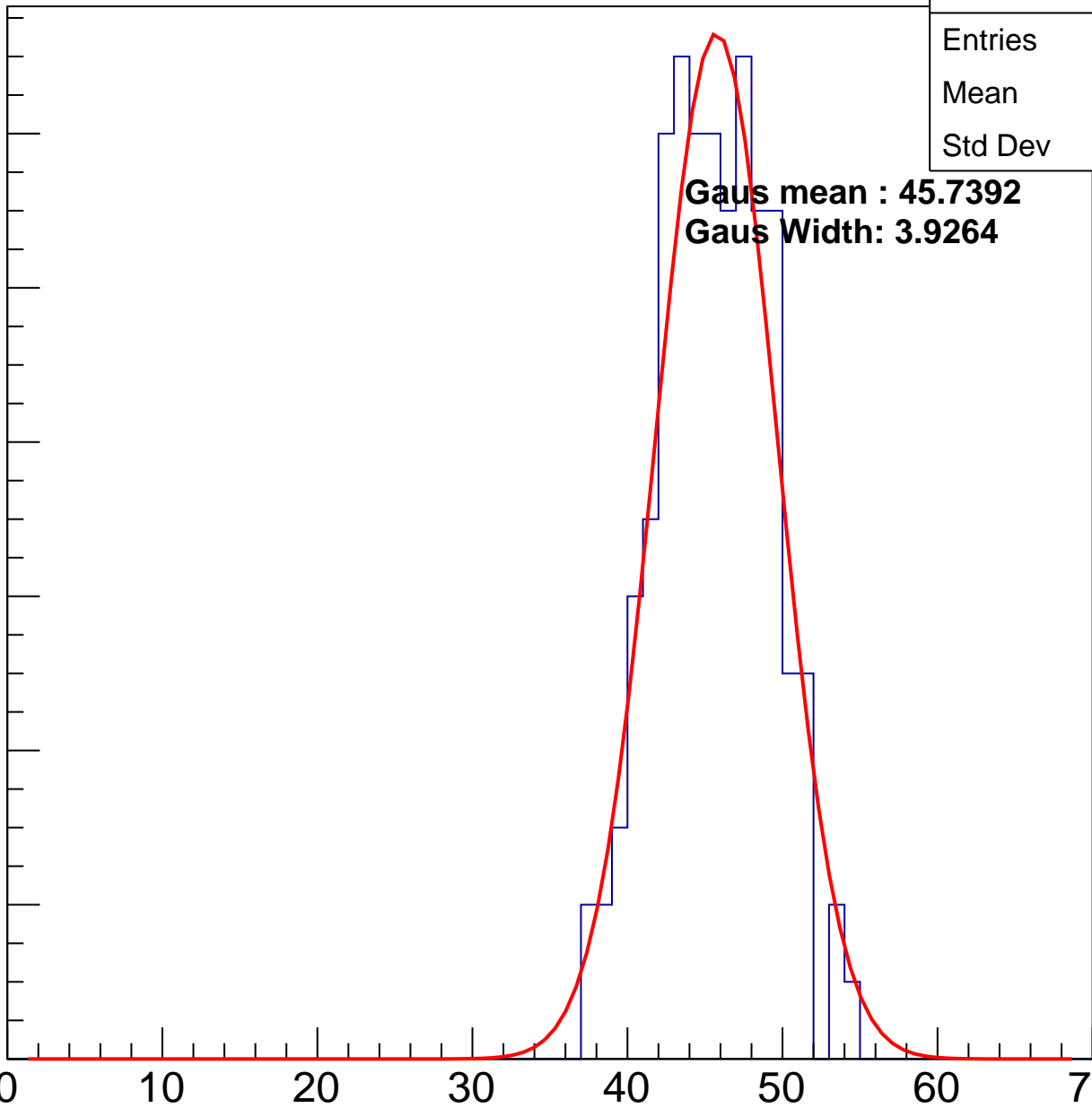
| | |
|---------|-------|
| Entries | 128 |
| Mean | 45.11 |
| Std Dev | 3.556 |

Gaus mean : 45.7392

Gaus Width: 3.9264

0 10 20 30 40 50 60 70

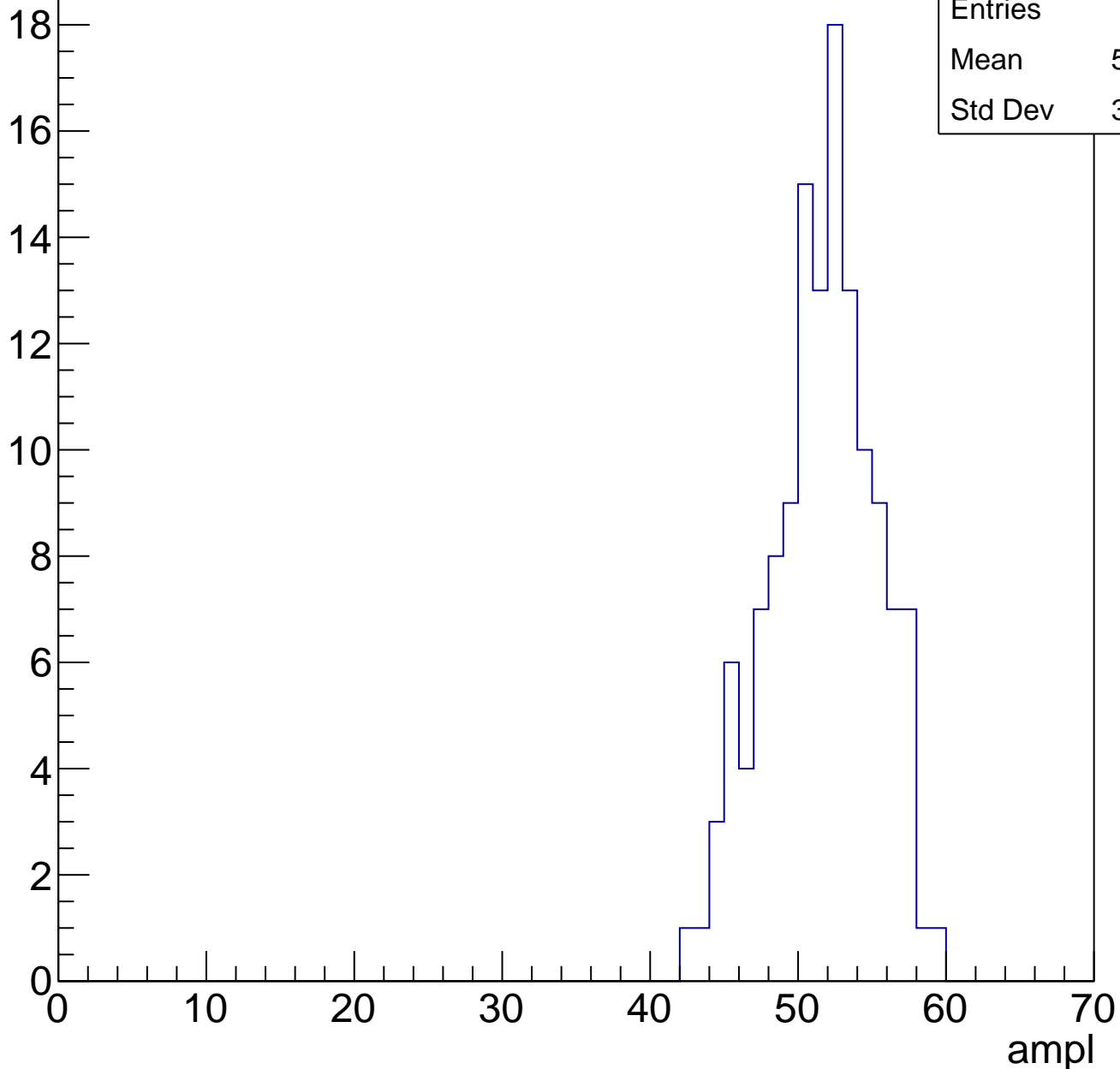
ampl



B0L101S, U22-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch22, adc4

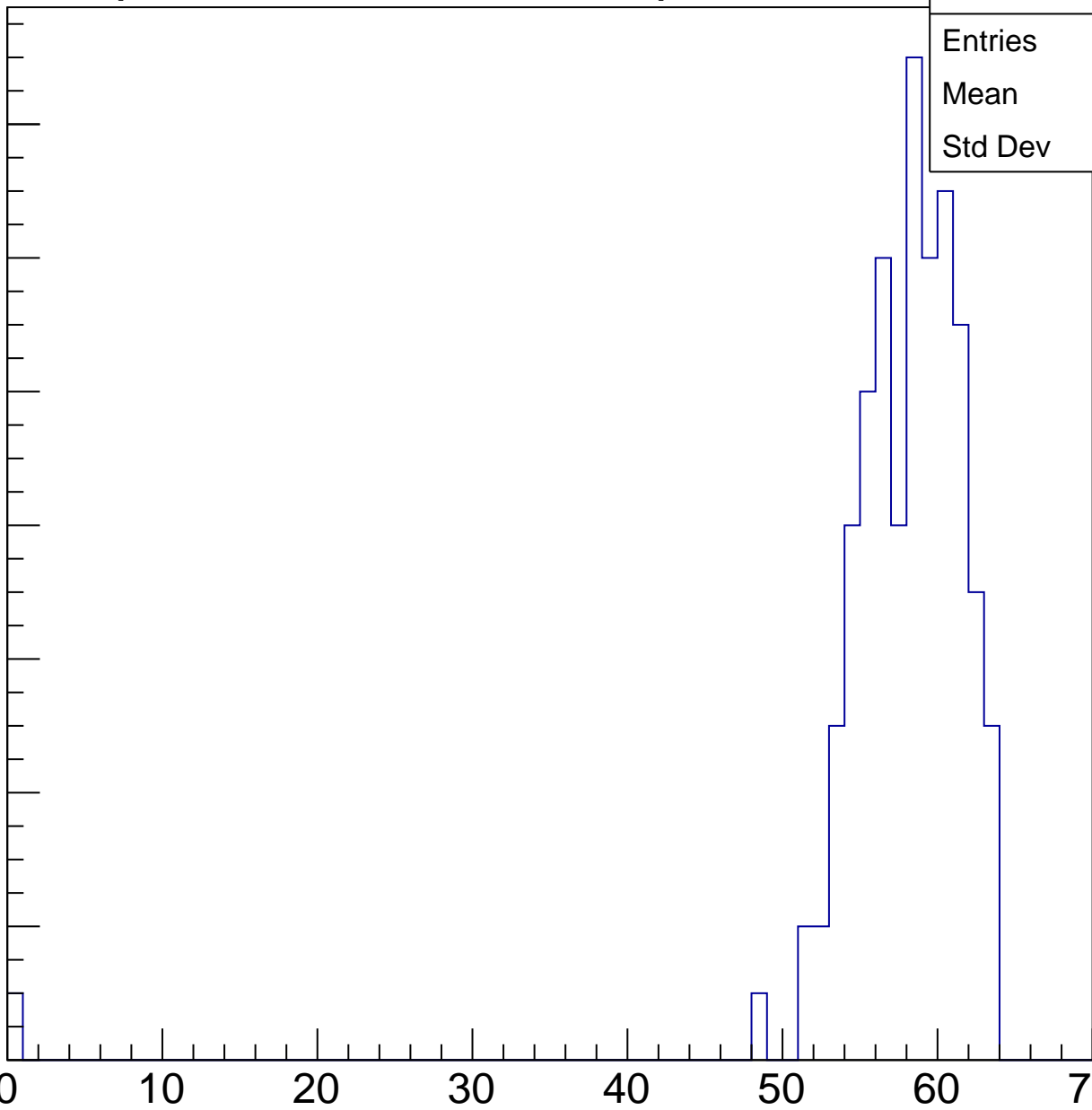
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 112 |
| Mean | 57.21 |
| Std Dev | 6.241 |

ampl



B0L101S, U22-ch22, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

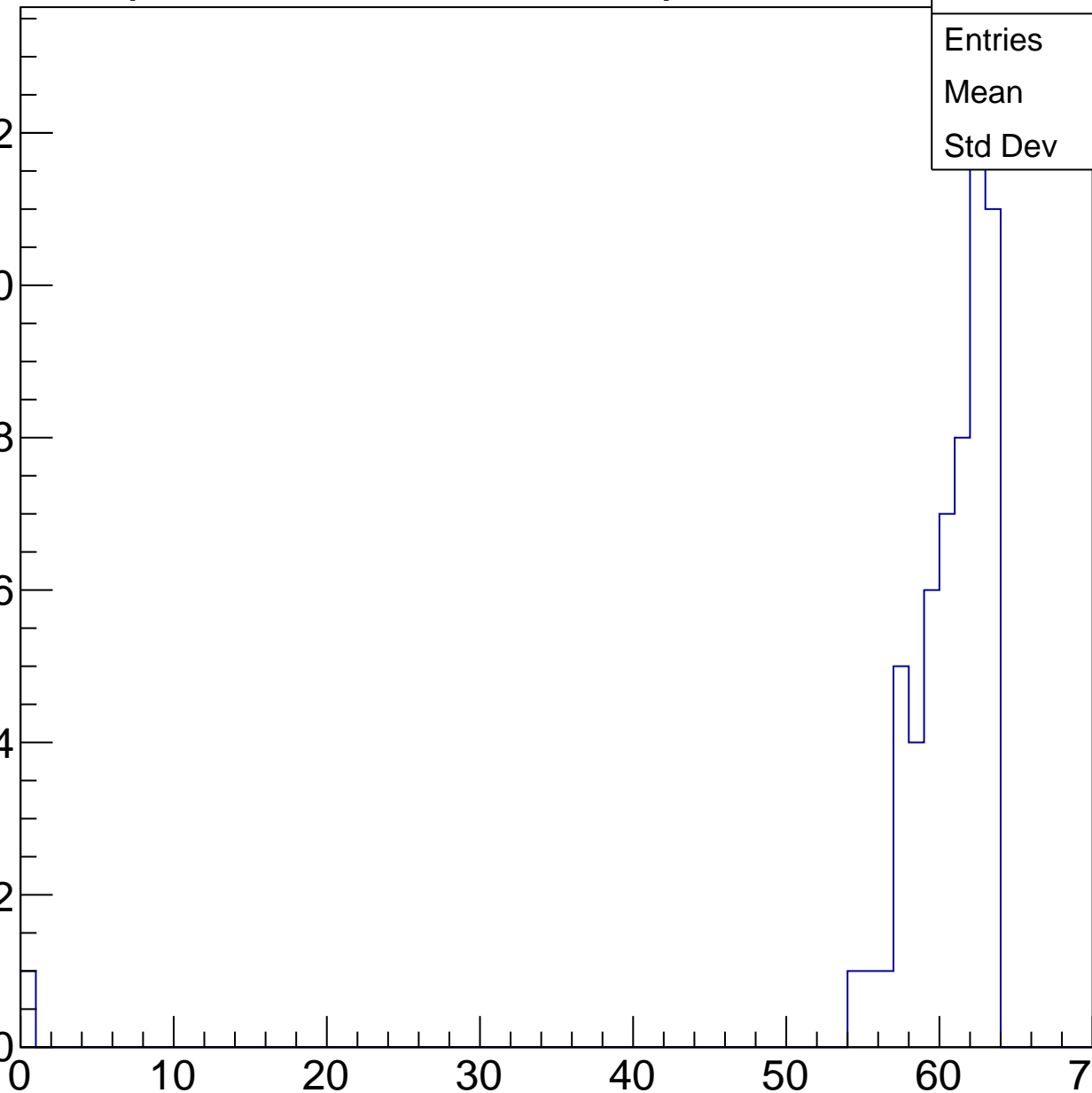
4

2

0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 59.36 |
| Std Dev | 8.178 |

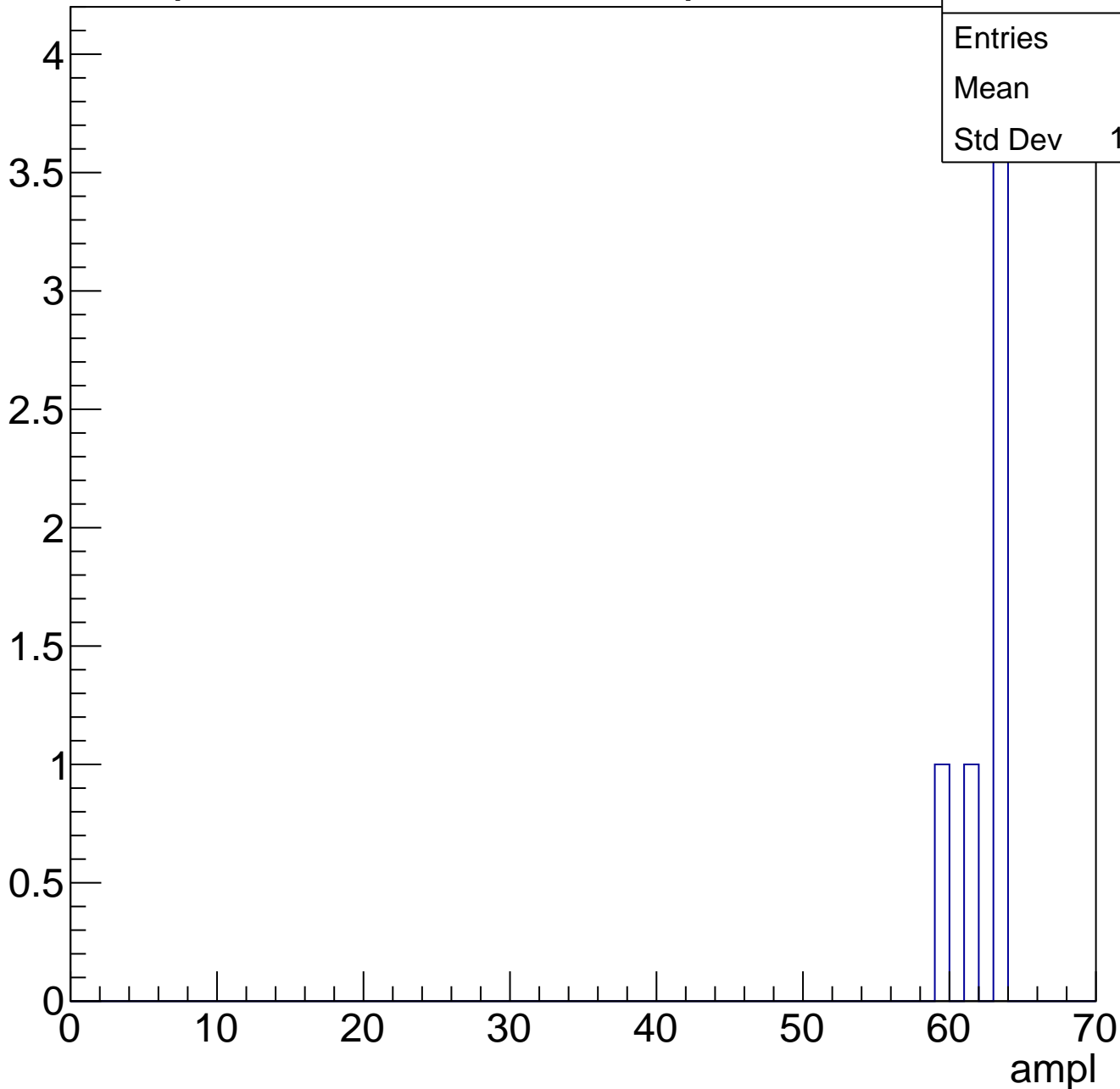
ampl



B0L101S, U22-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 12.5 |
| Std Dev | 12.5 |

B0L101S, U22-ch23, adc0

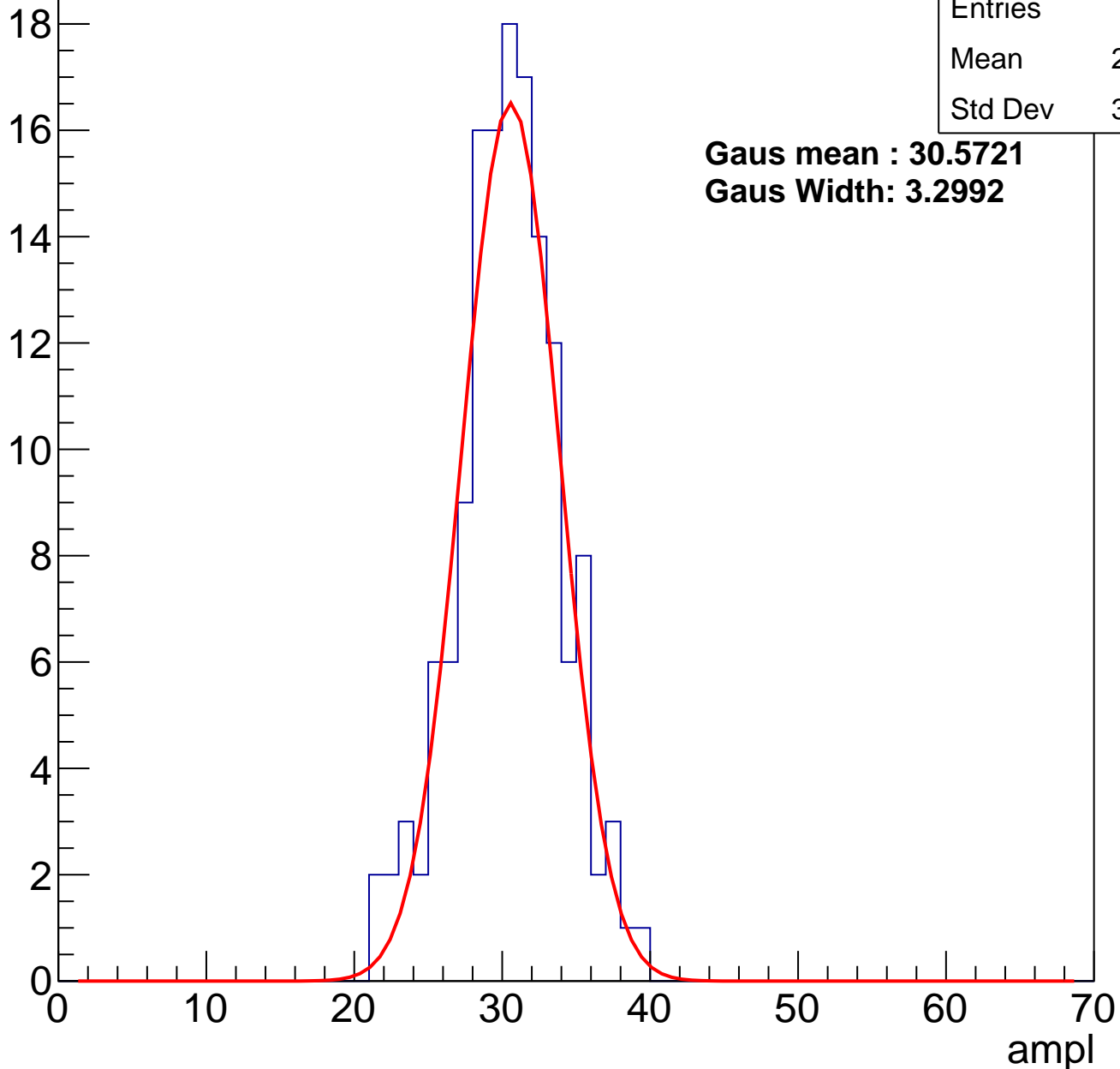
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 29.99 |
| Std Dev | 3.493 |

Gaus mean : 30.5721

Gaus Width: 3.2992

Entry



B0L101S, U22-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 36.83 |
| Std Dev | 3.564 |

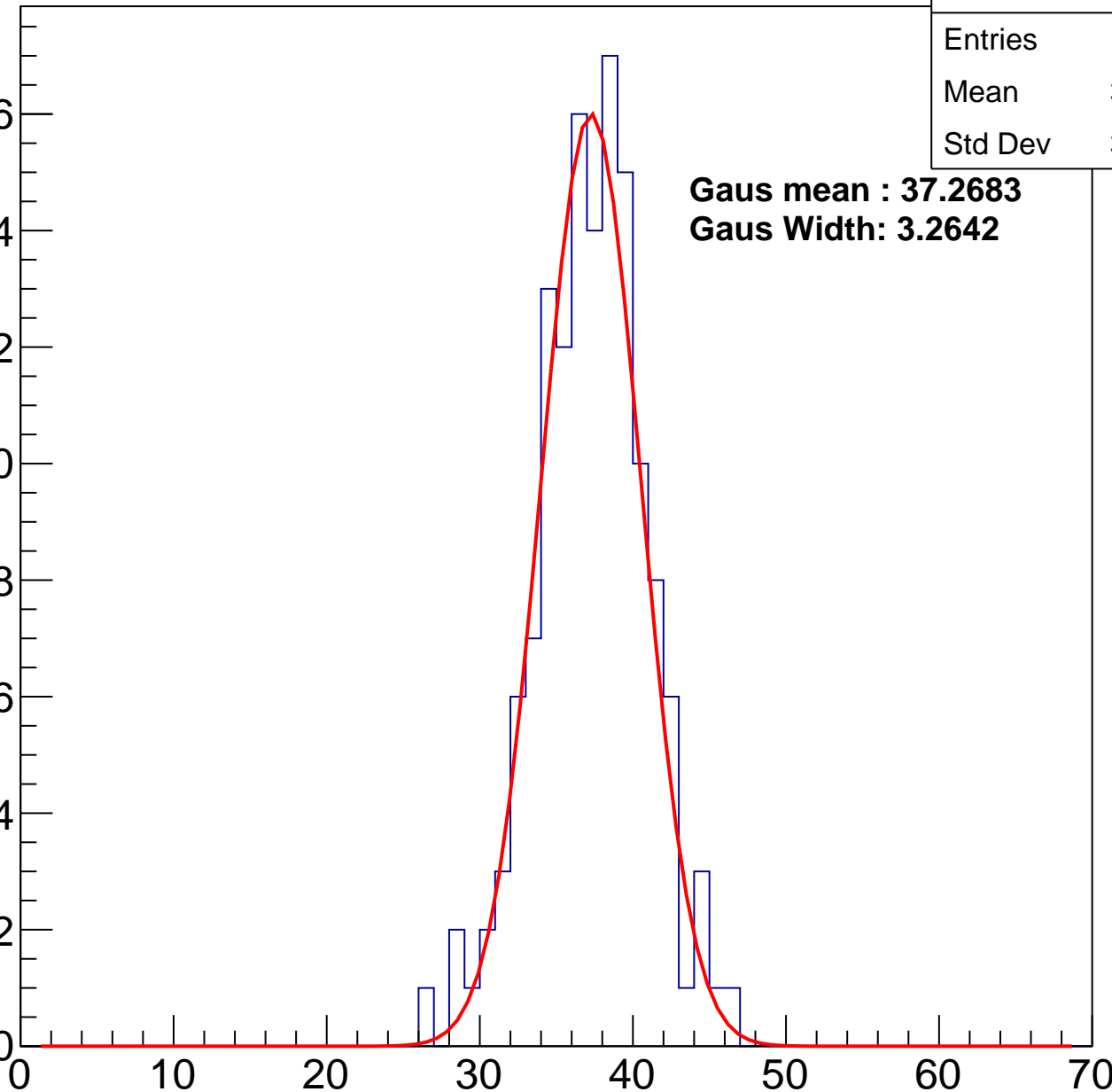
Gaus mean : 37.2683

Gaus Width: 3.2642

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 43.86 |
| Std Dev | 3.477 |

Gaus mean : 44.4205

Gaus Width: 3.4424

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

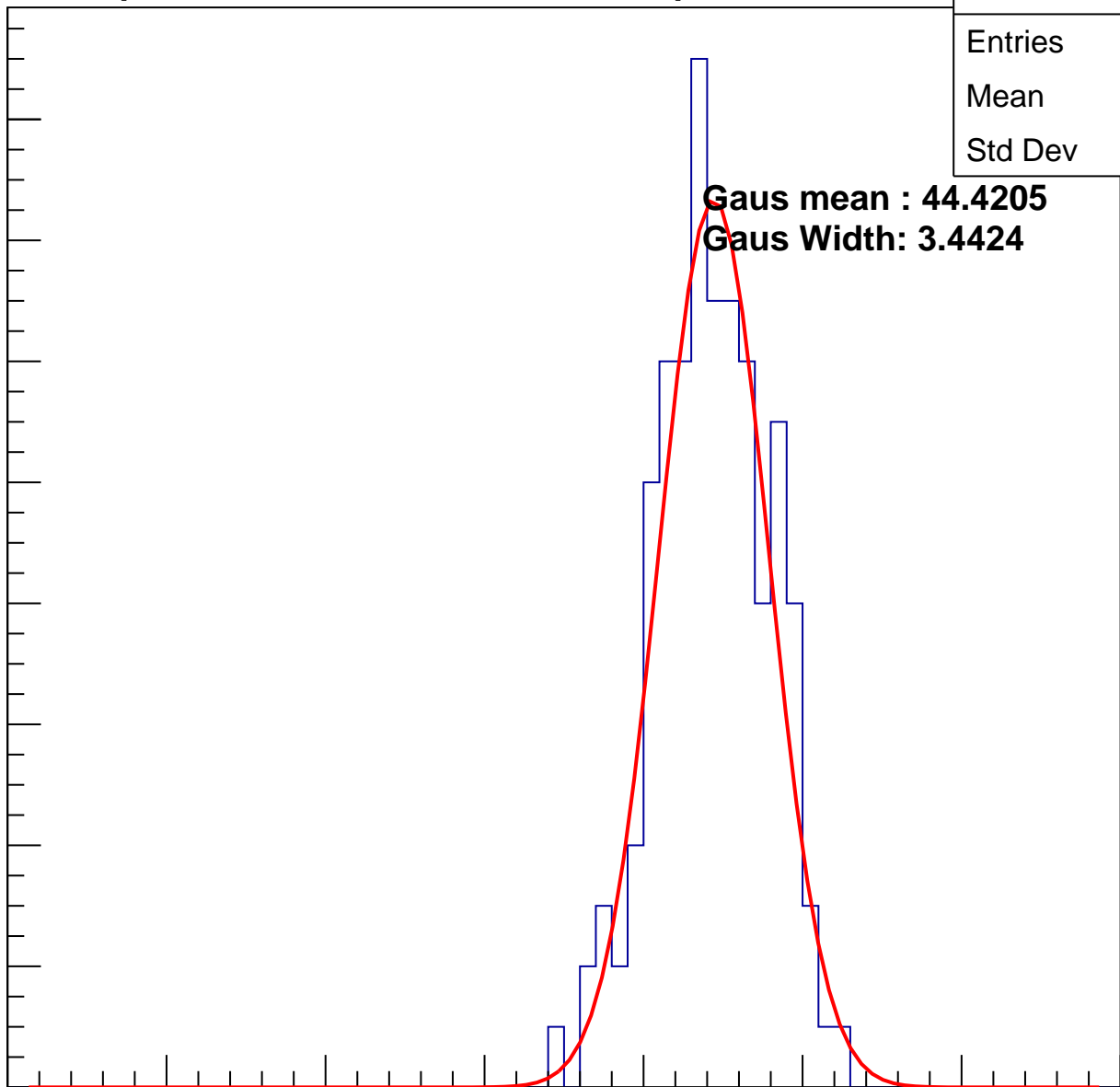
40

50

60

70

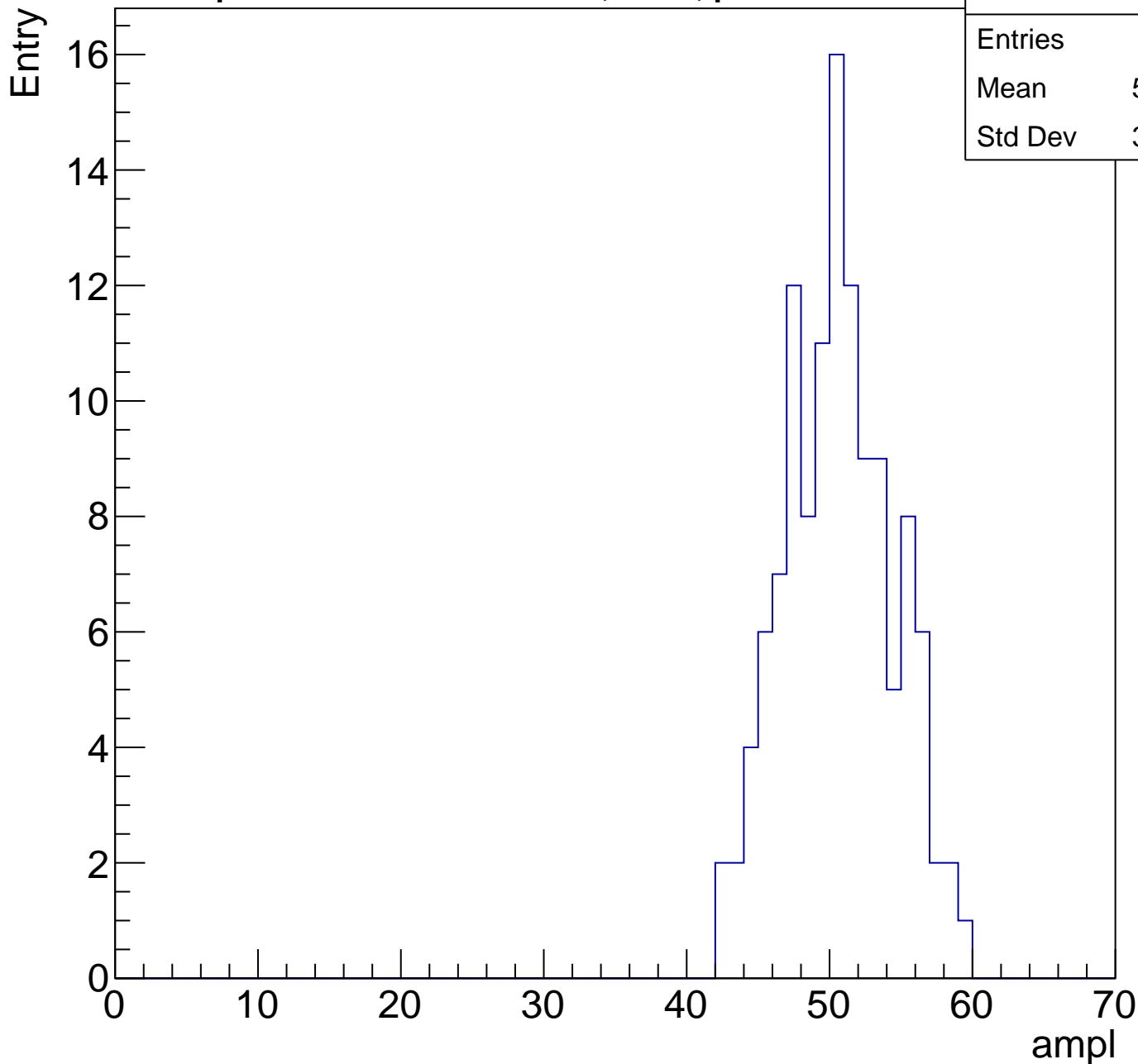
ampl



B0L101S, U22-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 50.14 |
| Std Dev | 3.747 |



B0L101S, U22-ch23, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 55.44 |
| Std Dev | 5.989 |

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch23, adc5

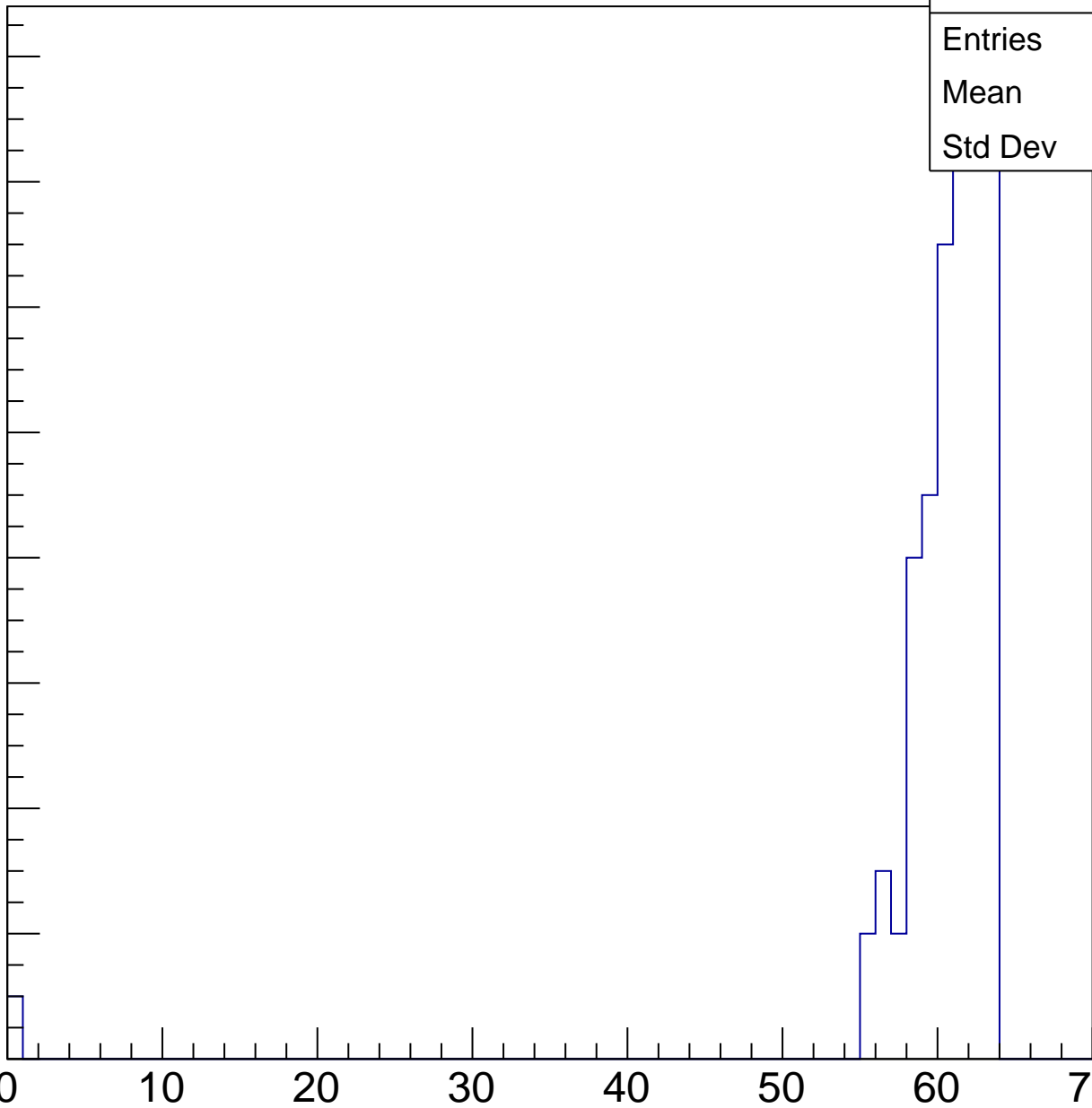
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 59.79 |
| Std Dev | 6.839 |

ampl



B0L101S, U22-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

B0L101S, U22-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch24, adc0

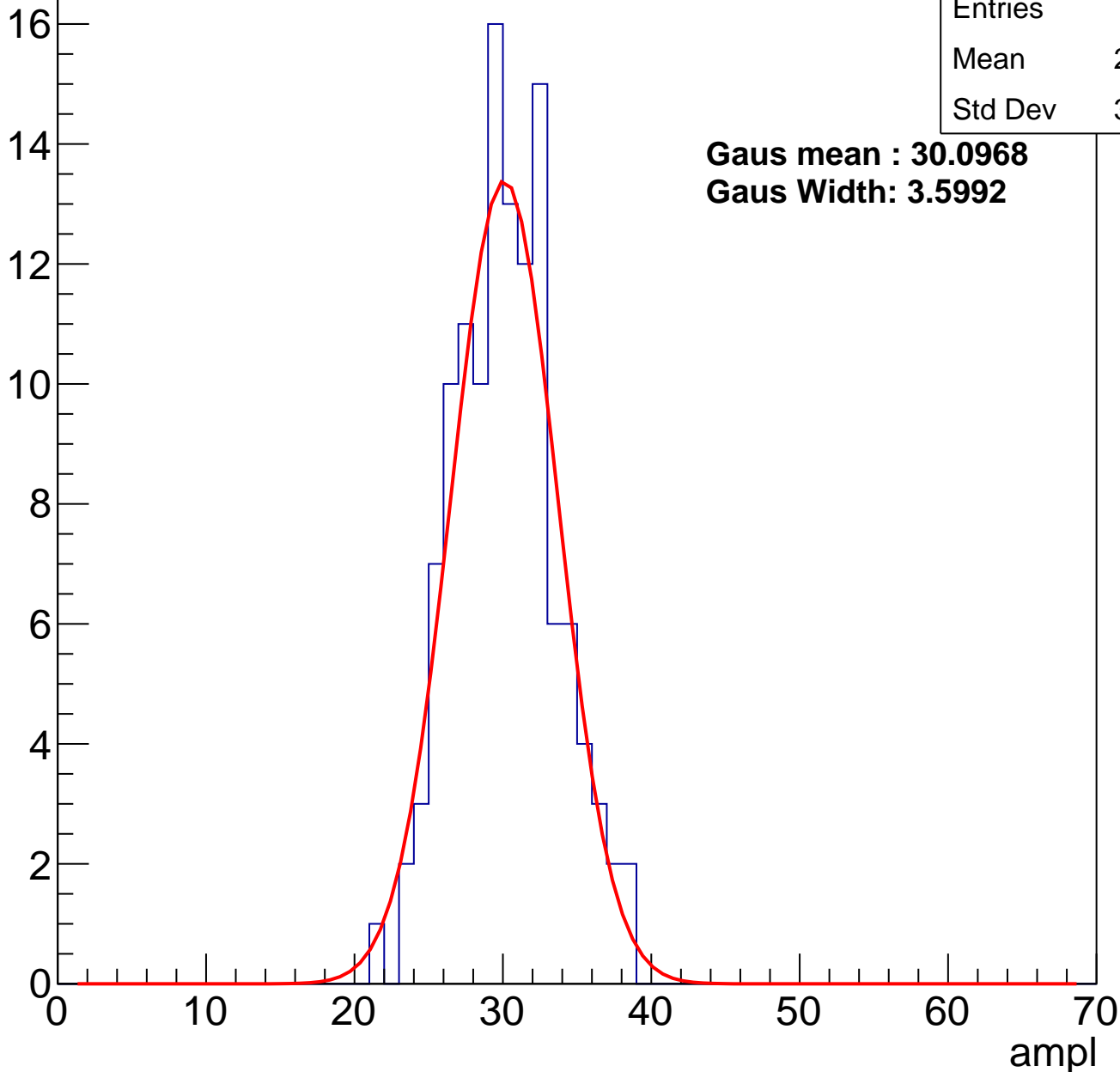
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 29.73 |
| Std Dev | 3.403 |

Gaus mean : 30.0968

Gaus Width: 3.5992

Entry



B0L101S, U22-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 36.28 |
| Std Dev | 3.419 |

Gaus mean : 36.7320

Gaus Width: 3.6666

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

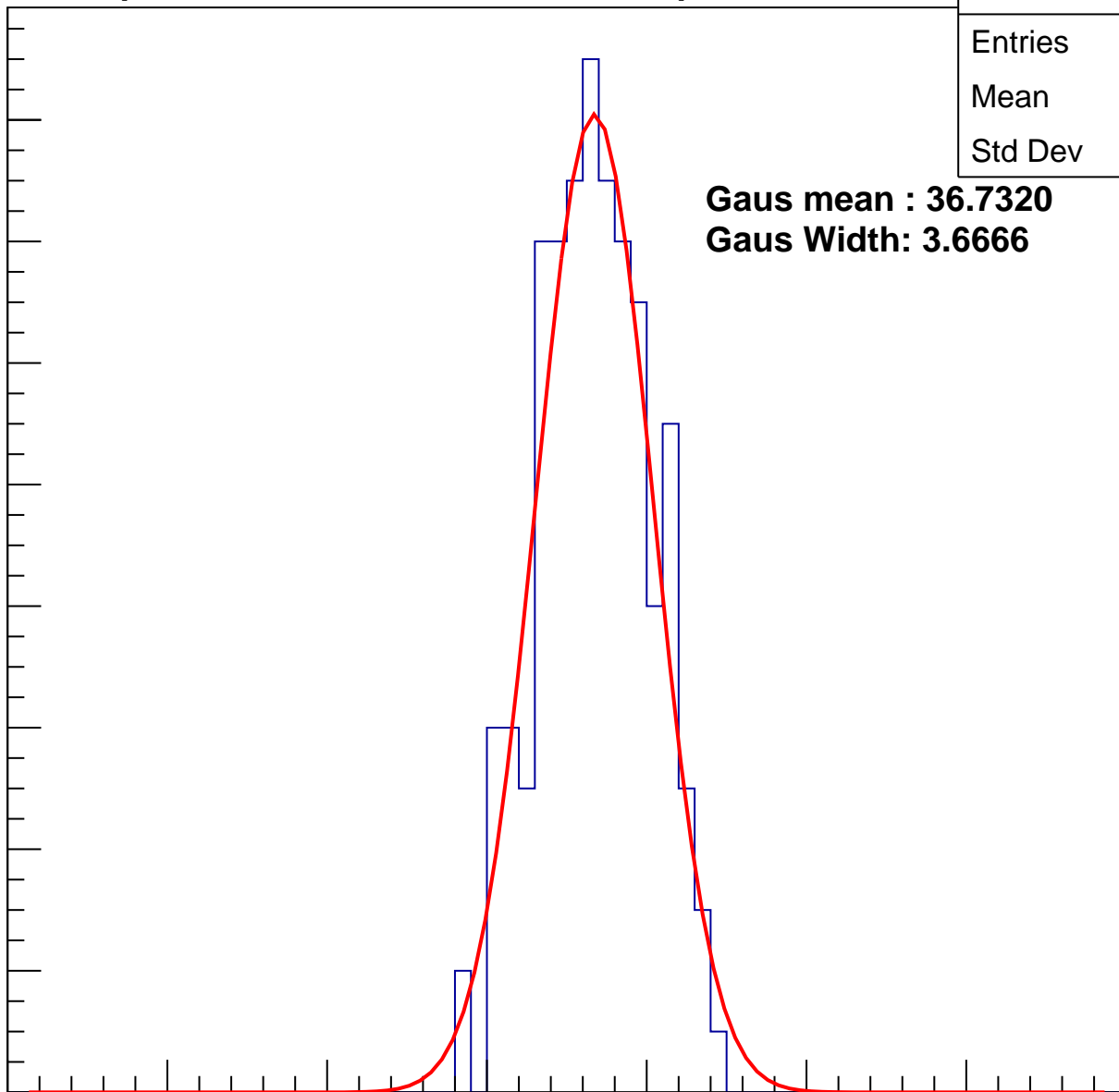
40

50

60

70

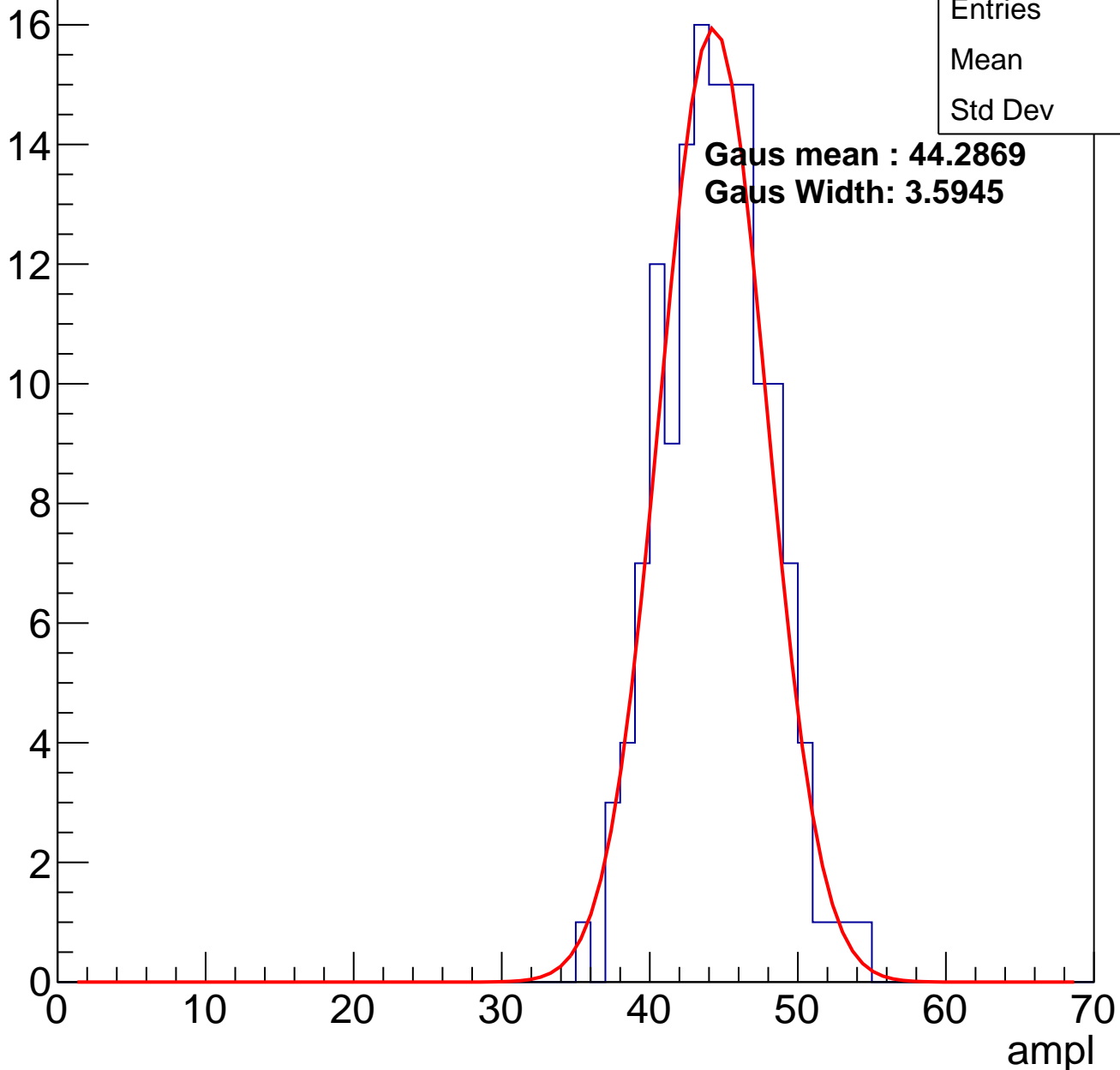
ampl



B0L101S, U22-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|------|
| Entries | 146 |
| Mean | 44 |
| Std Dev | 3.54 |

B0L101S, U22-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

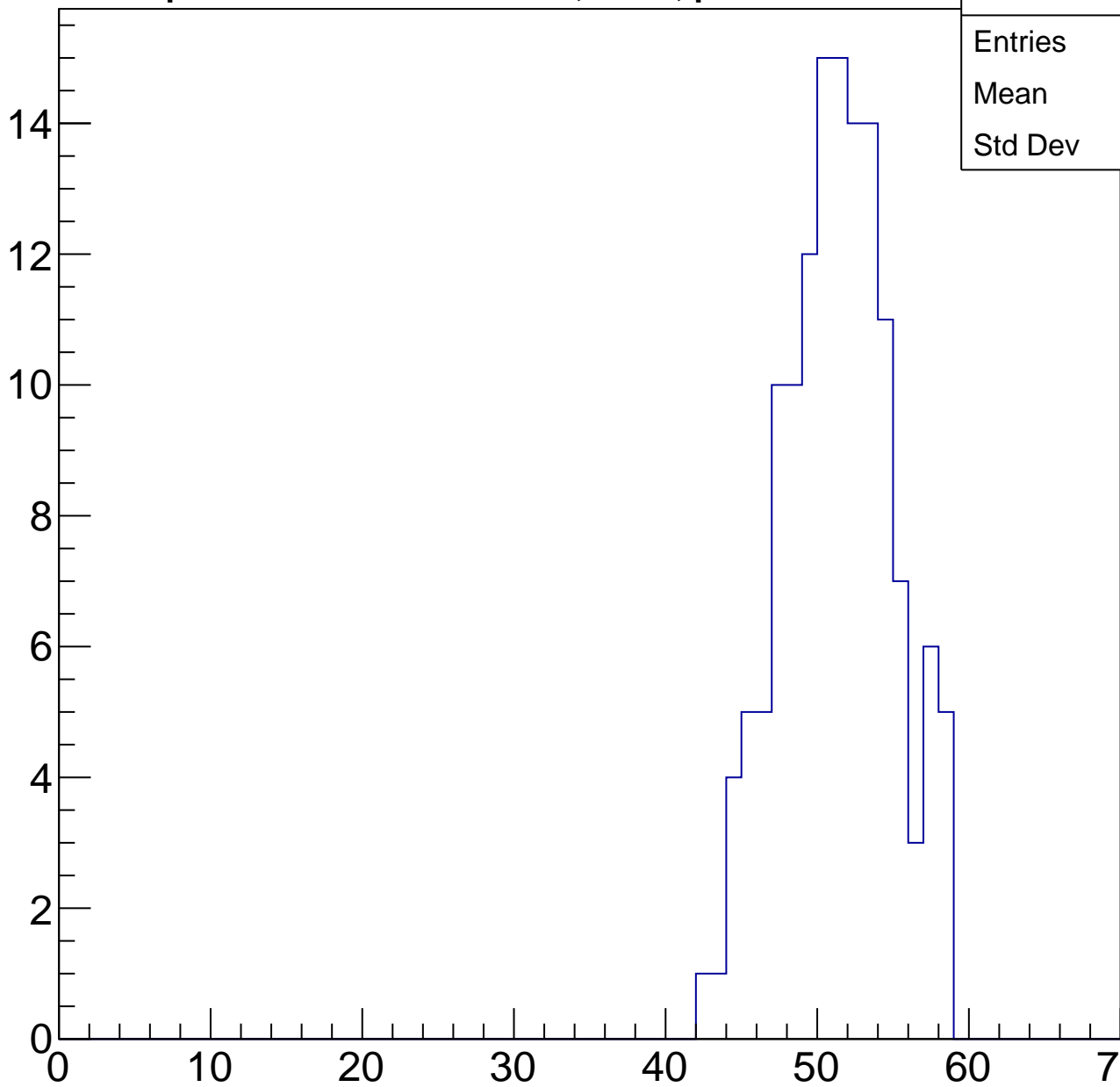
| | |
|---------|-------|
| Entries | 138 |
| Mean | 50.86 |
| Std Dev | 3.584 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch24, adc4

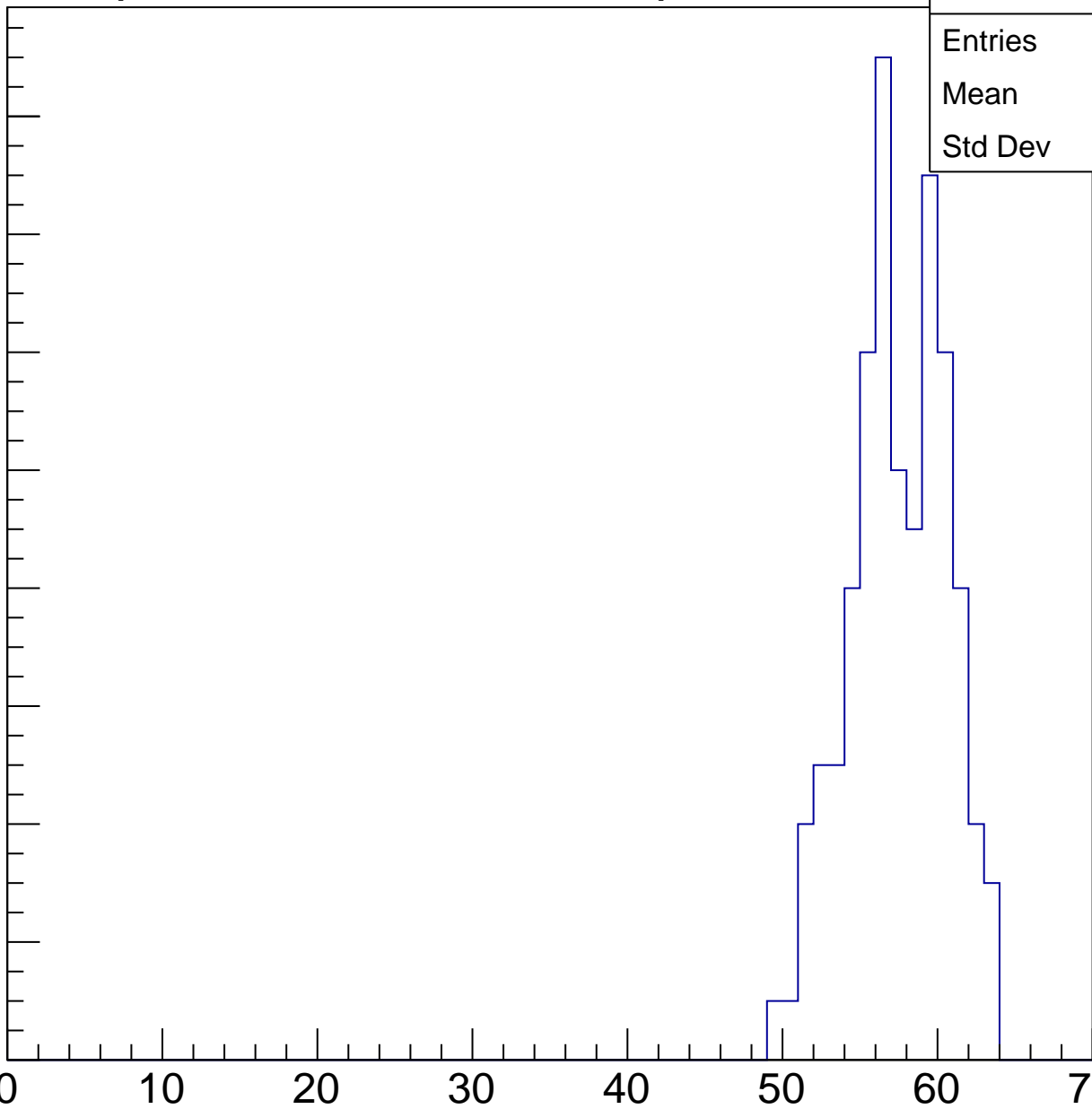
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 114 |
| Mean | 56.96 |
| Std Dev | 3.123 |

ampl



B0L101S, U22-ch24, adc5

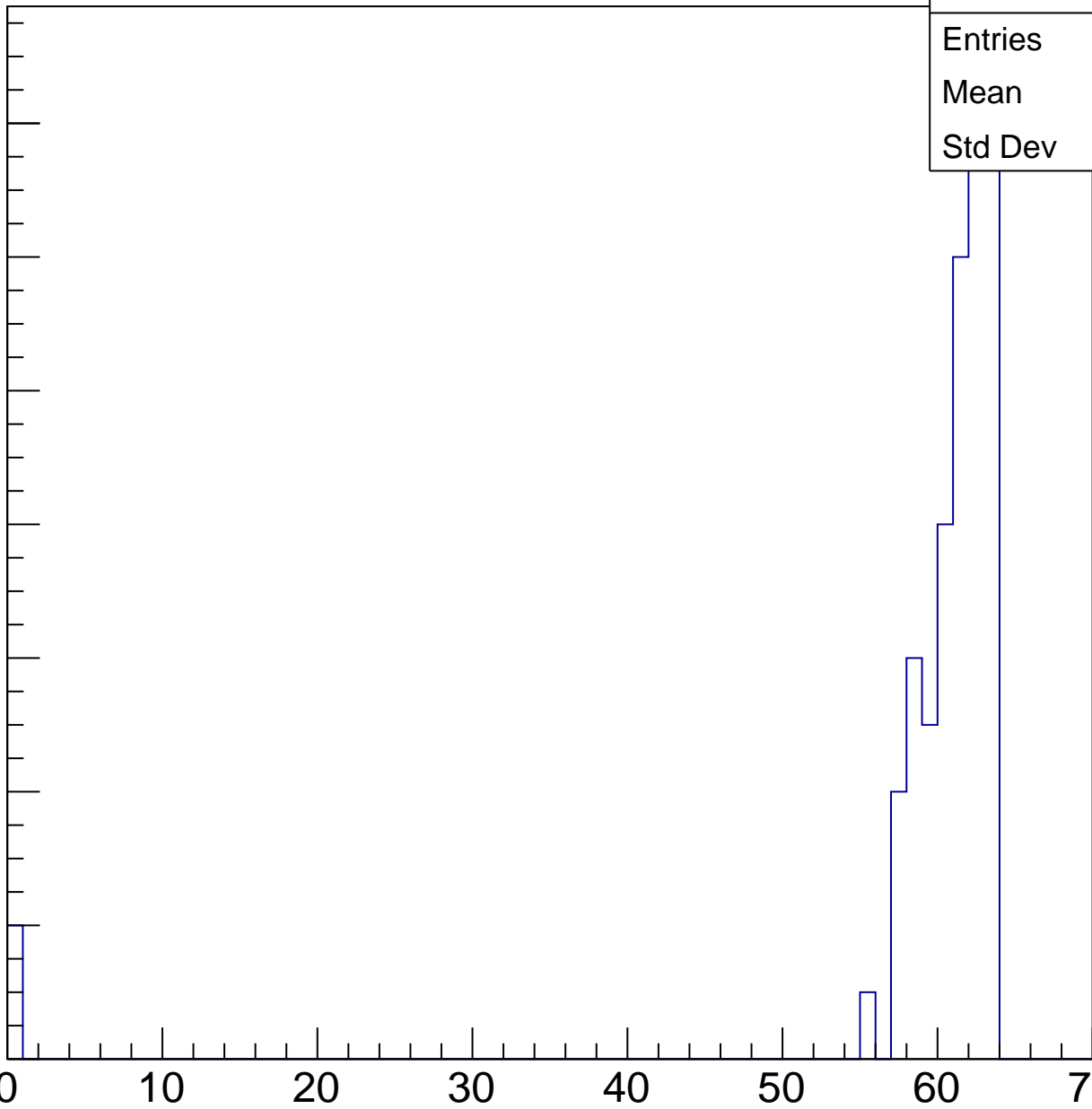
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 67 |
| Mean | 58.96 |
| Std Dev | 10.52 |

ampl



B0L101S, U22-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

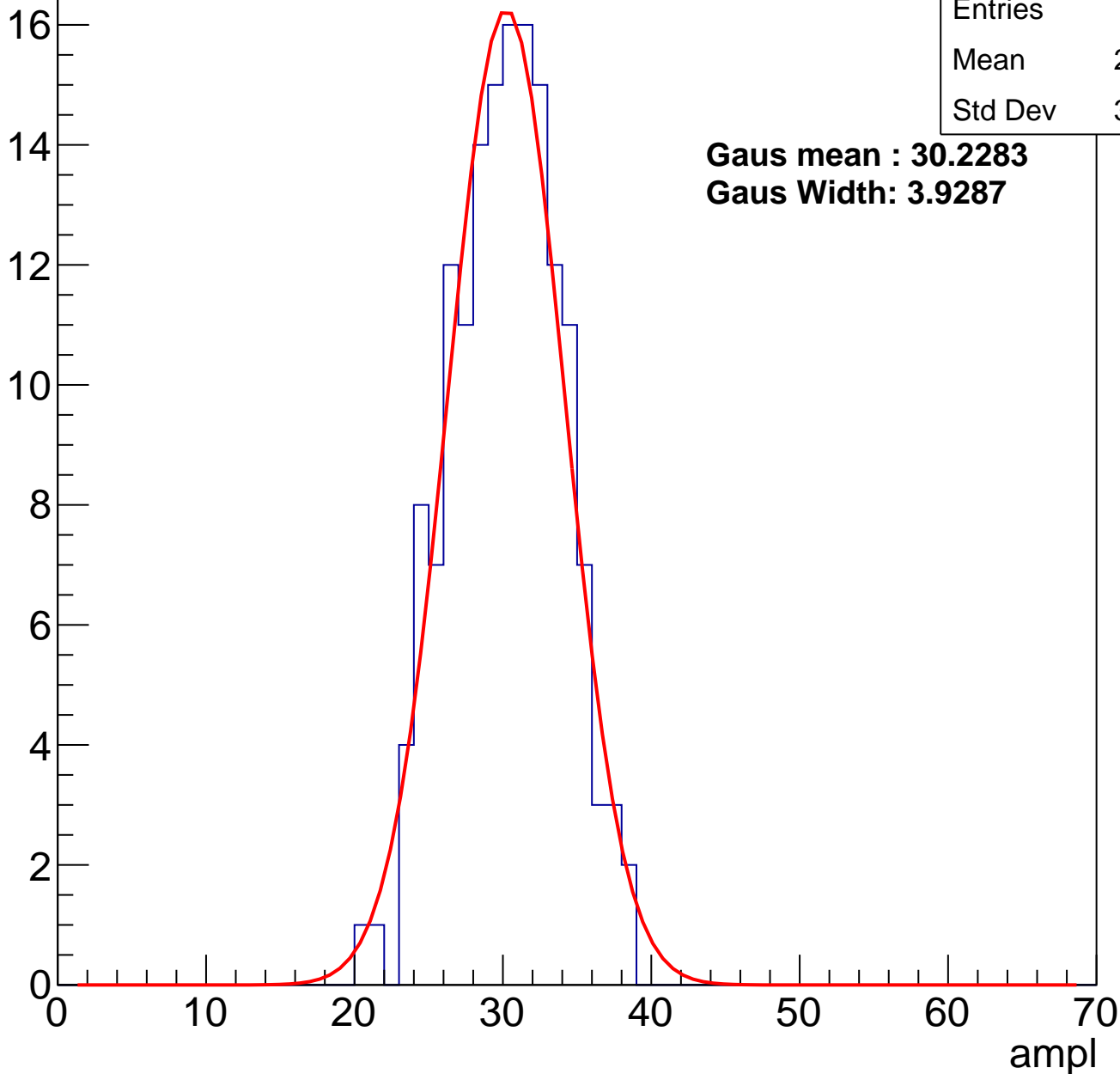
Entry



B0L101S, U22-ch25, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch25, adc1

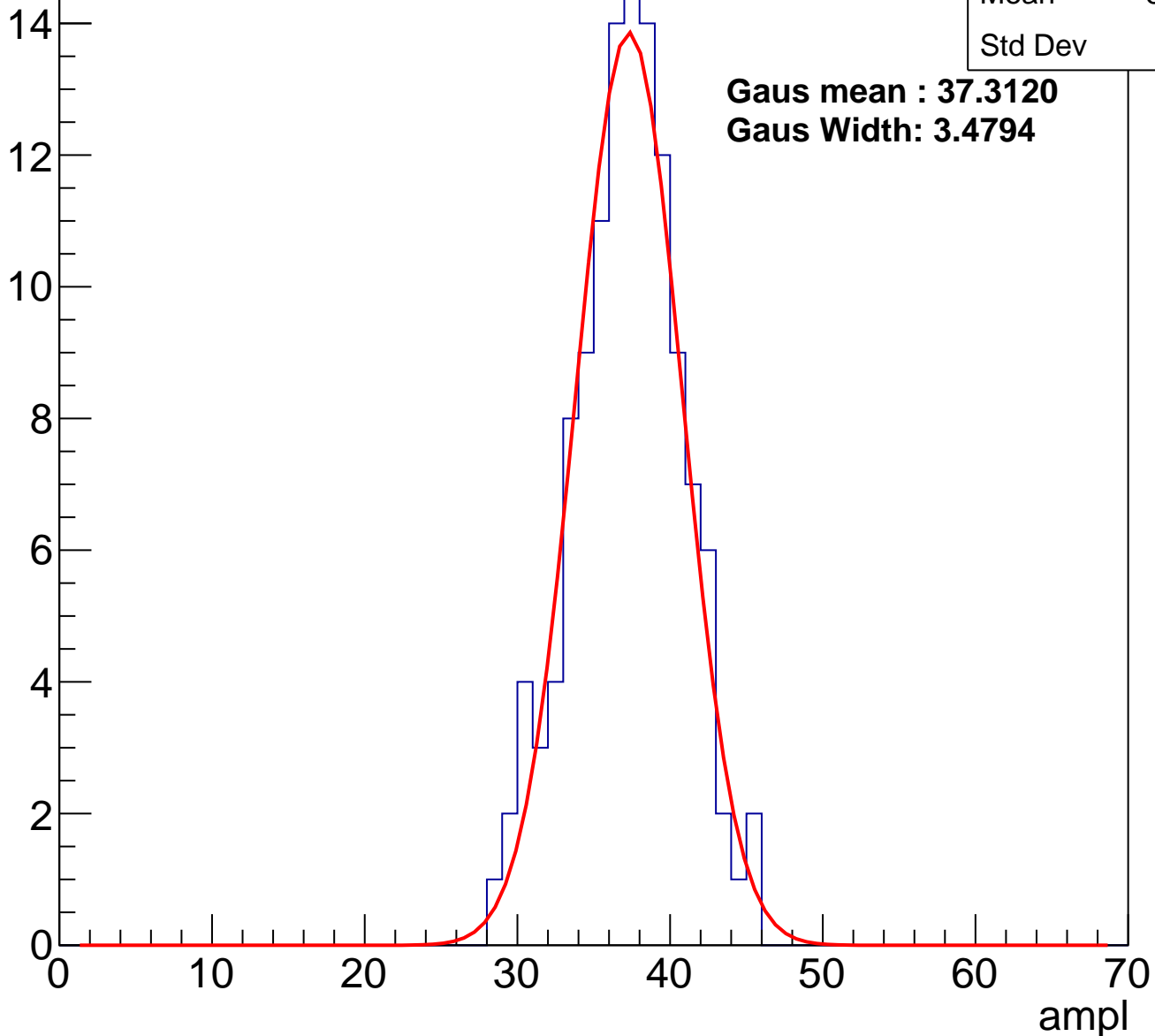
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 36.77 |
| Std Dev | 3.51 |

Gaus mean : 37.3120

Gaus Width: 3.4794

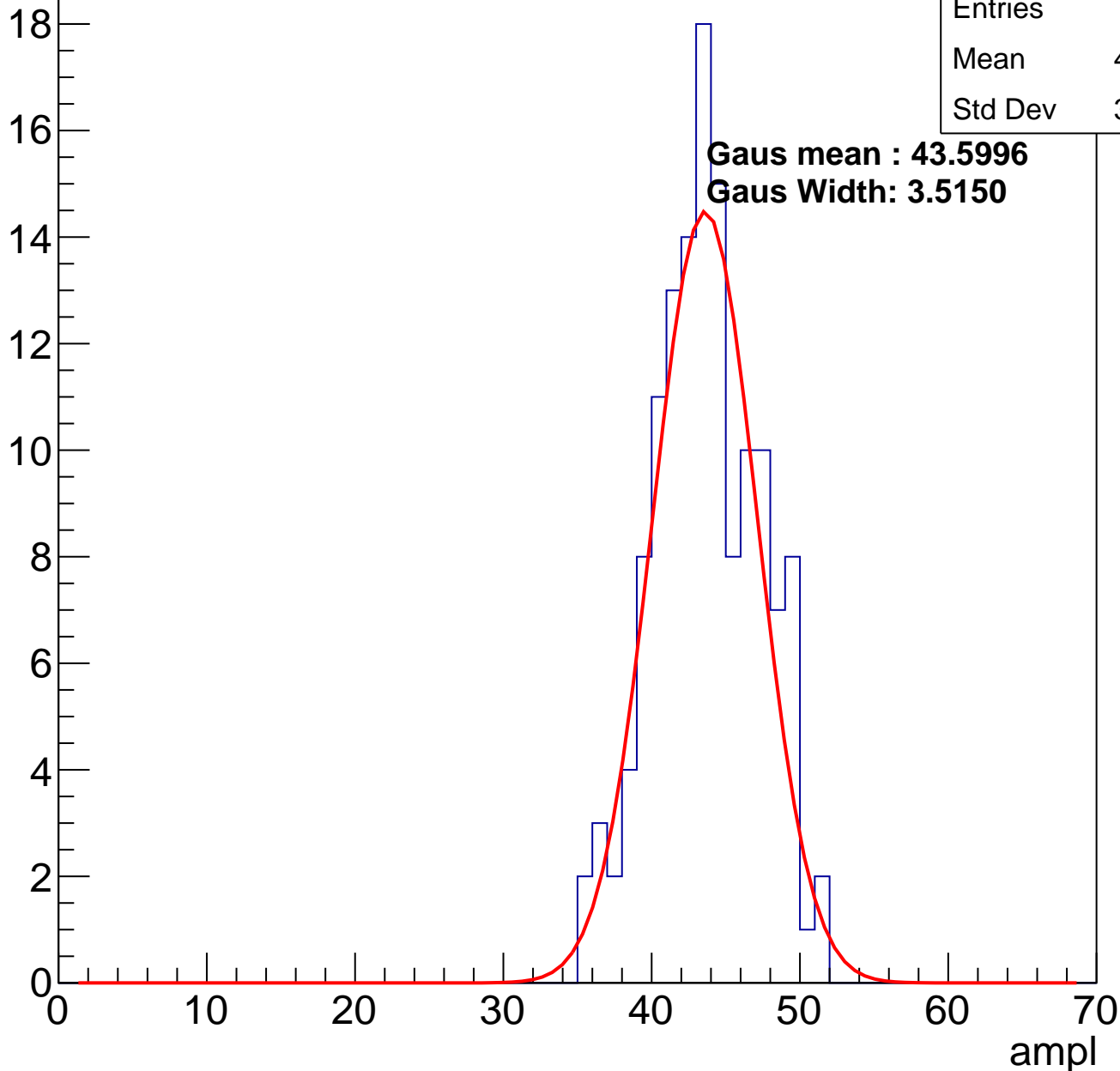
Entry



B0L101S, U22-ch25, adc2

calib_packv5_042523_0143.root, FC#1, port C1

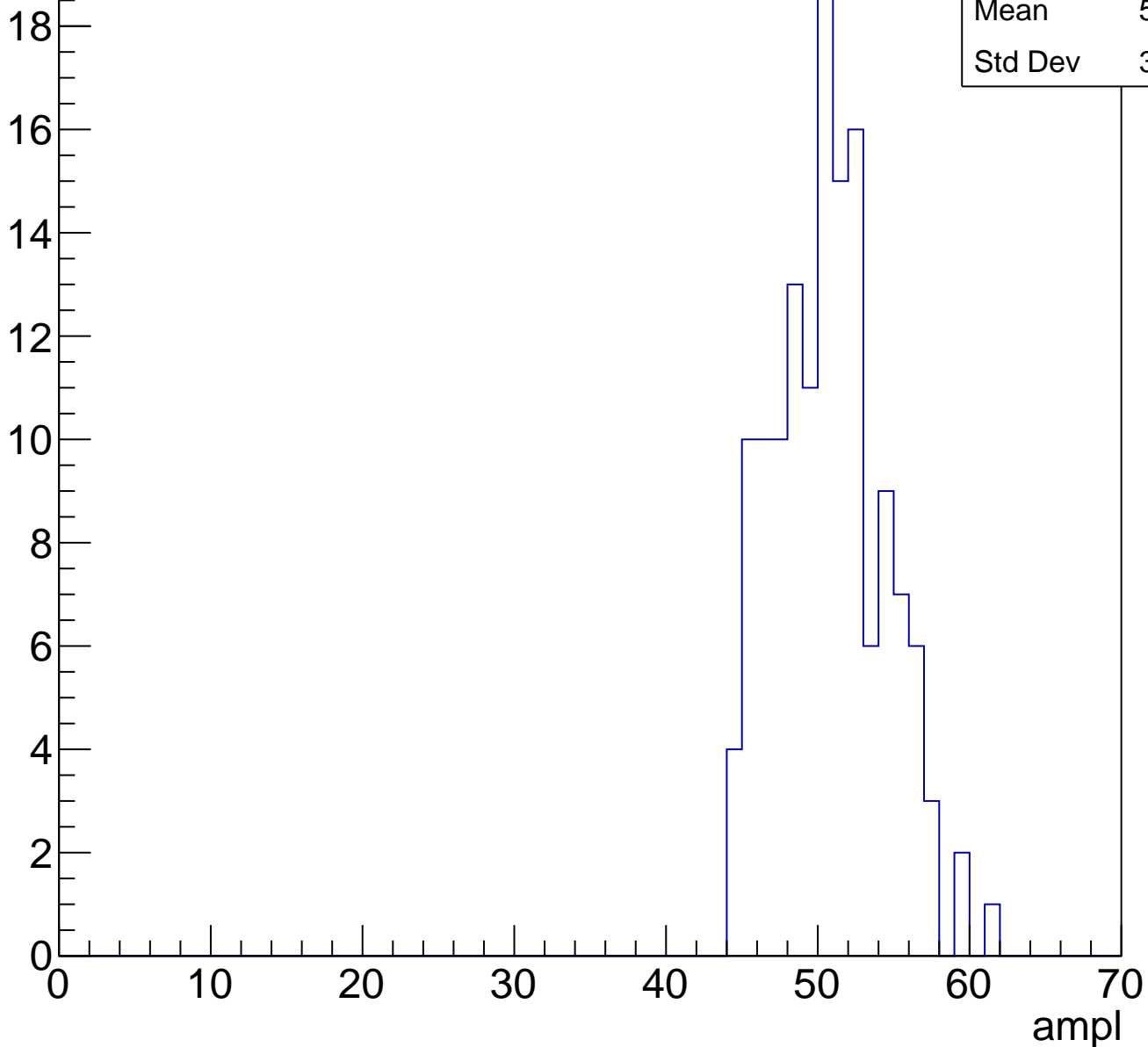
Entry



B0L101S, U22-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

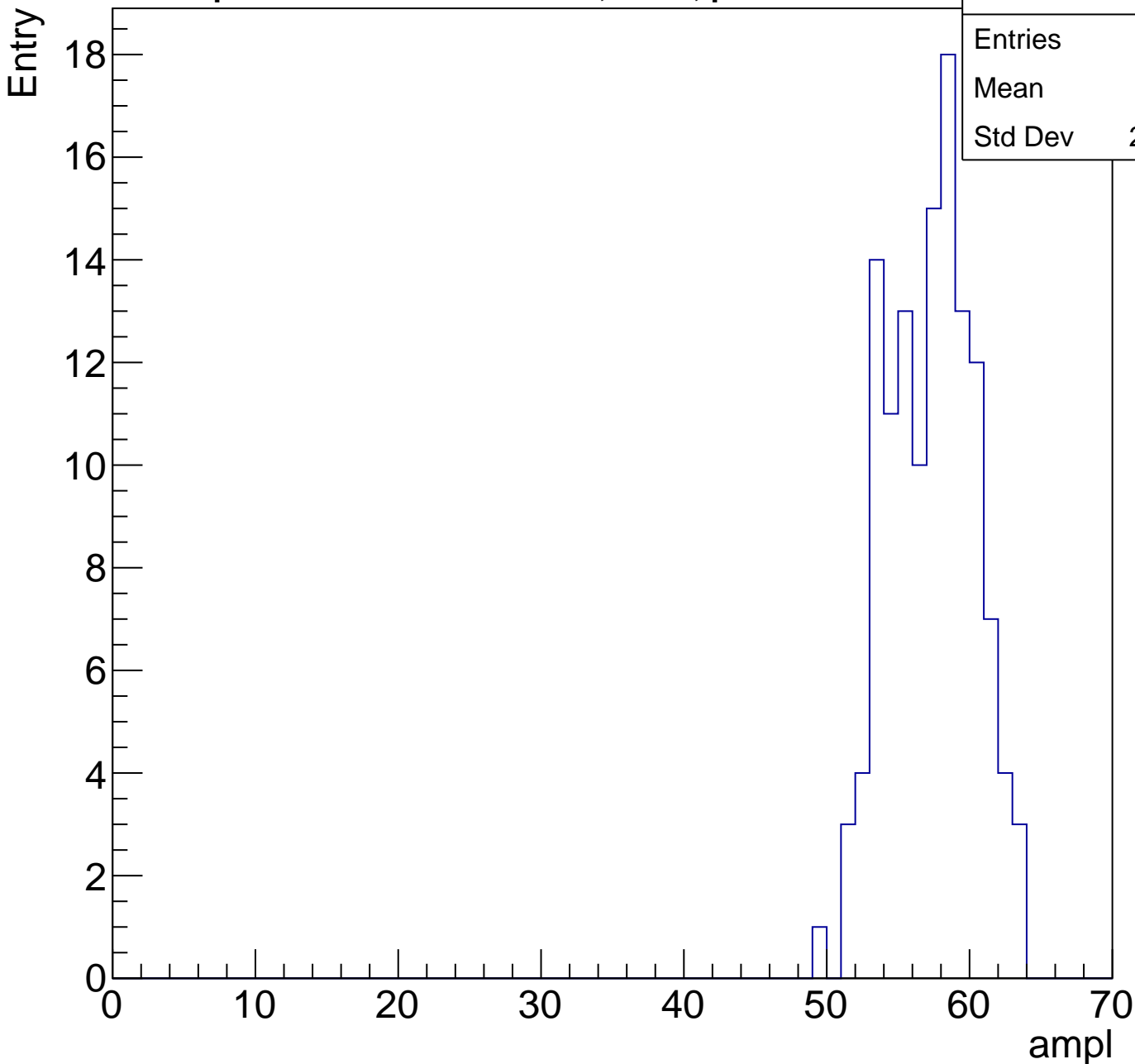
Entry



B0L101S, U22-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 128 |
| Mean | 56.8 |
| Std Dev | 2.998 |



B0L101S, U22-ch25, adc5

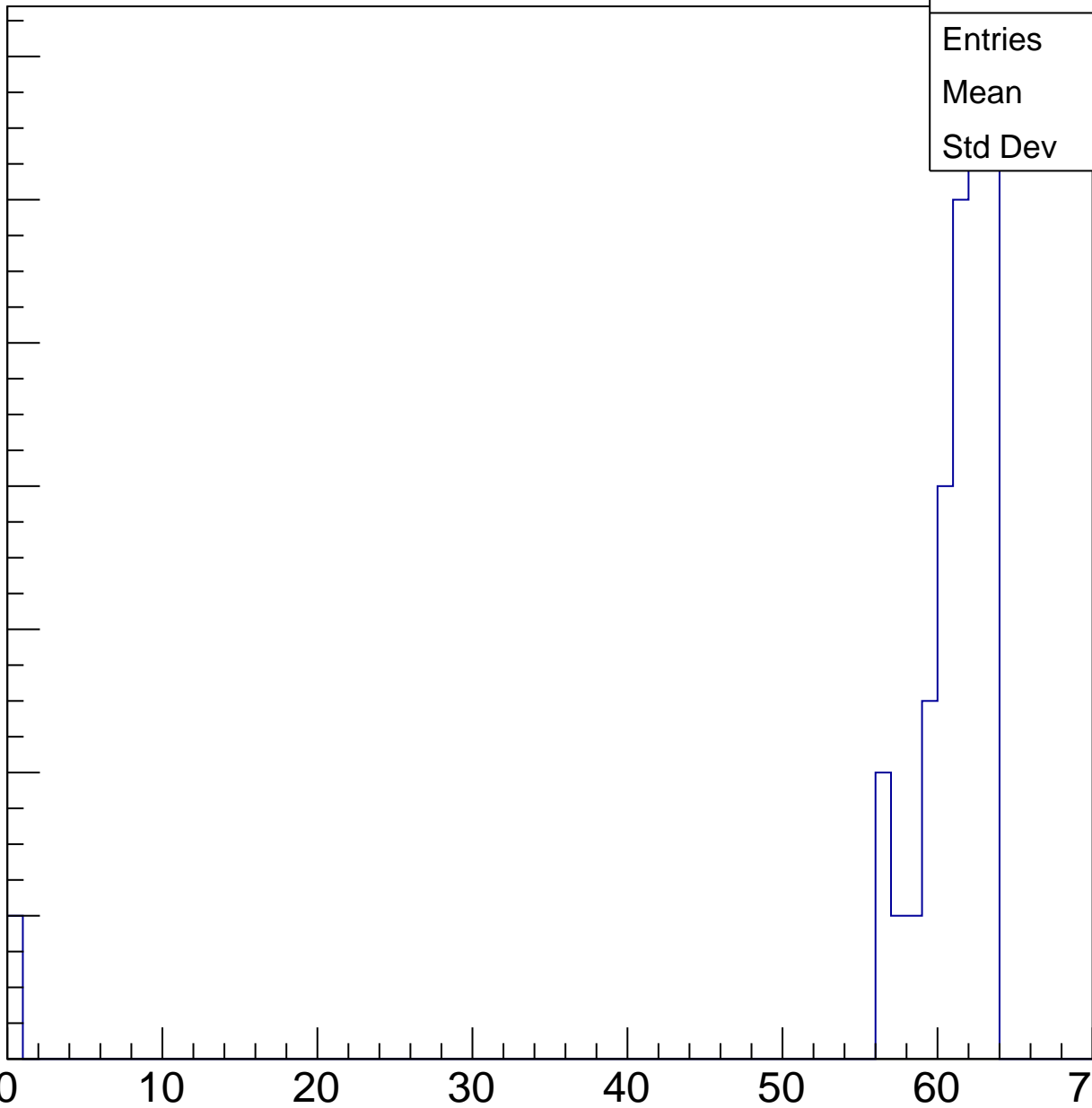
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 58.84 |
| Std Dev | 10.92 |

ampl



B0L101S, U22-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

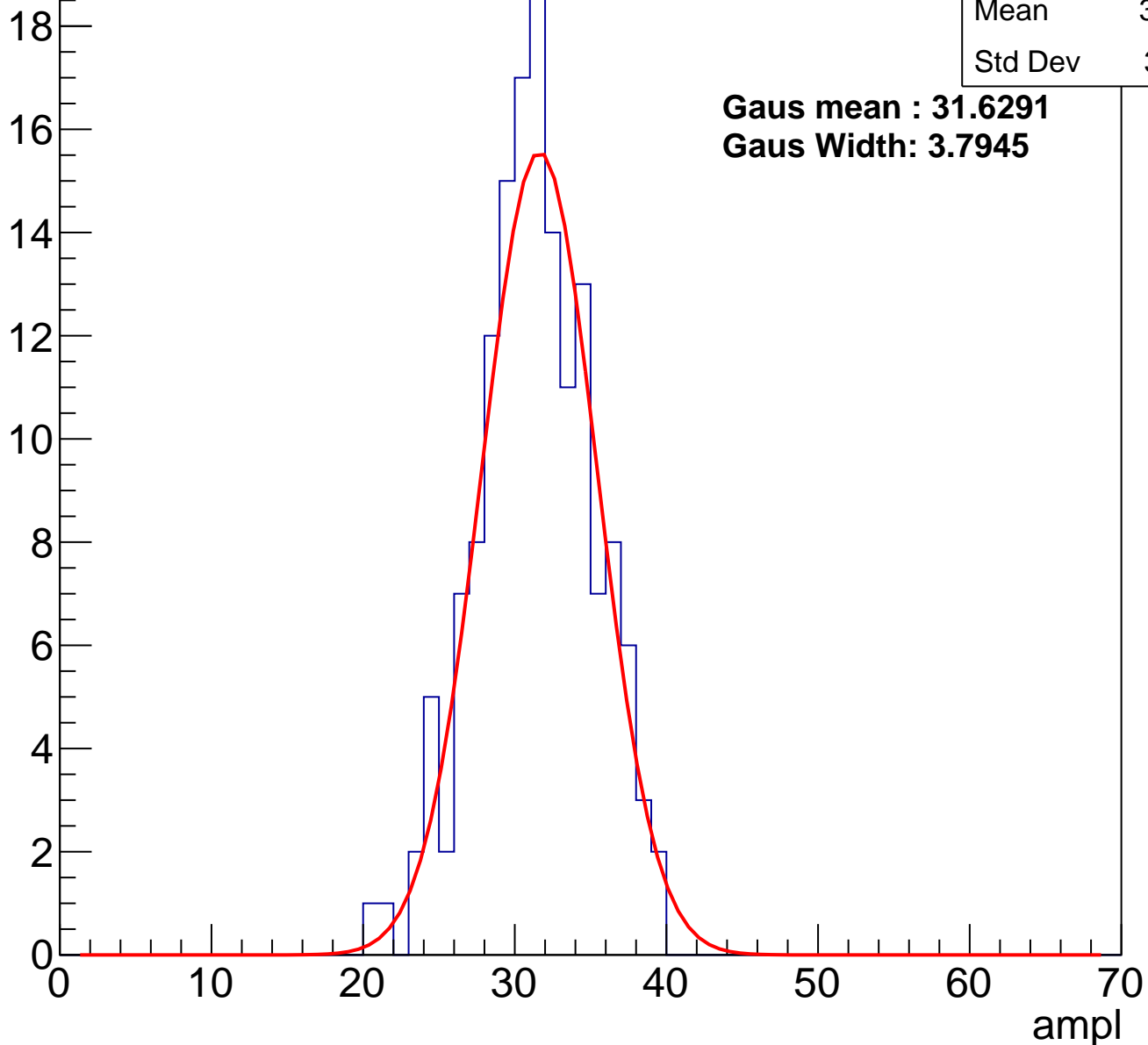


| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch26, adc1

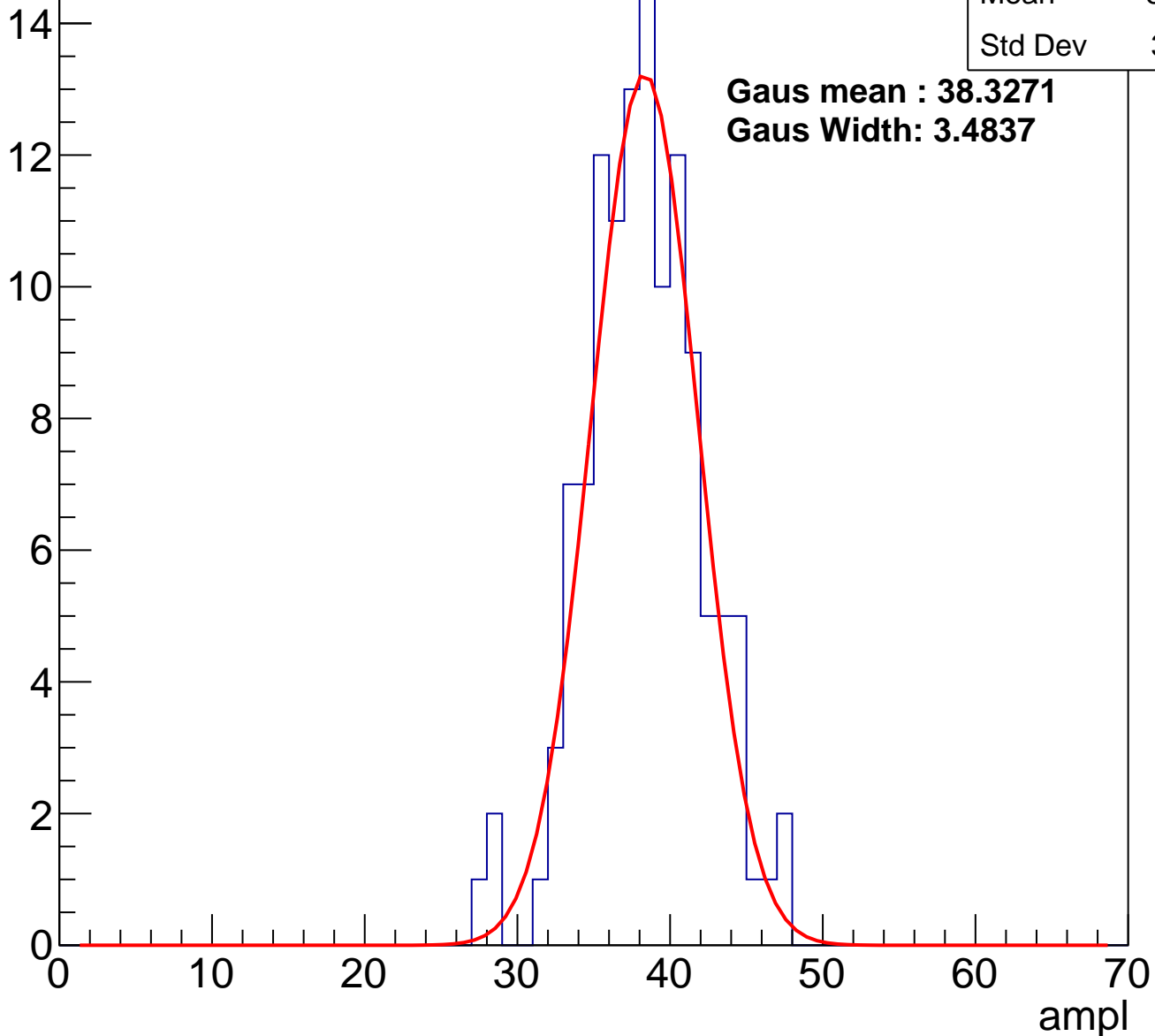
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 37.83 |
| Std Dev | 3.761 |

Gaus mean : 38.3271

Gaus Width: 3.4837

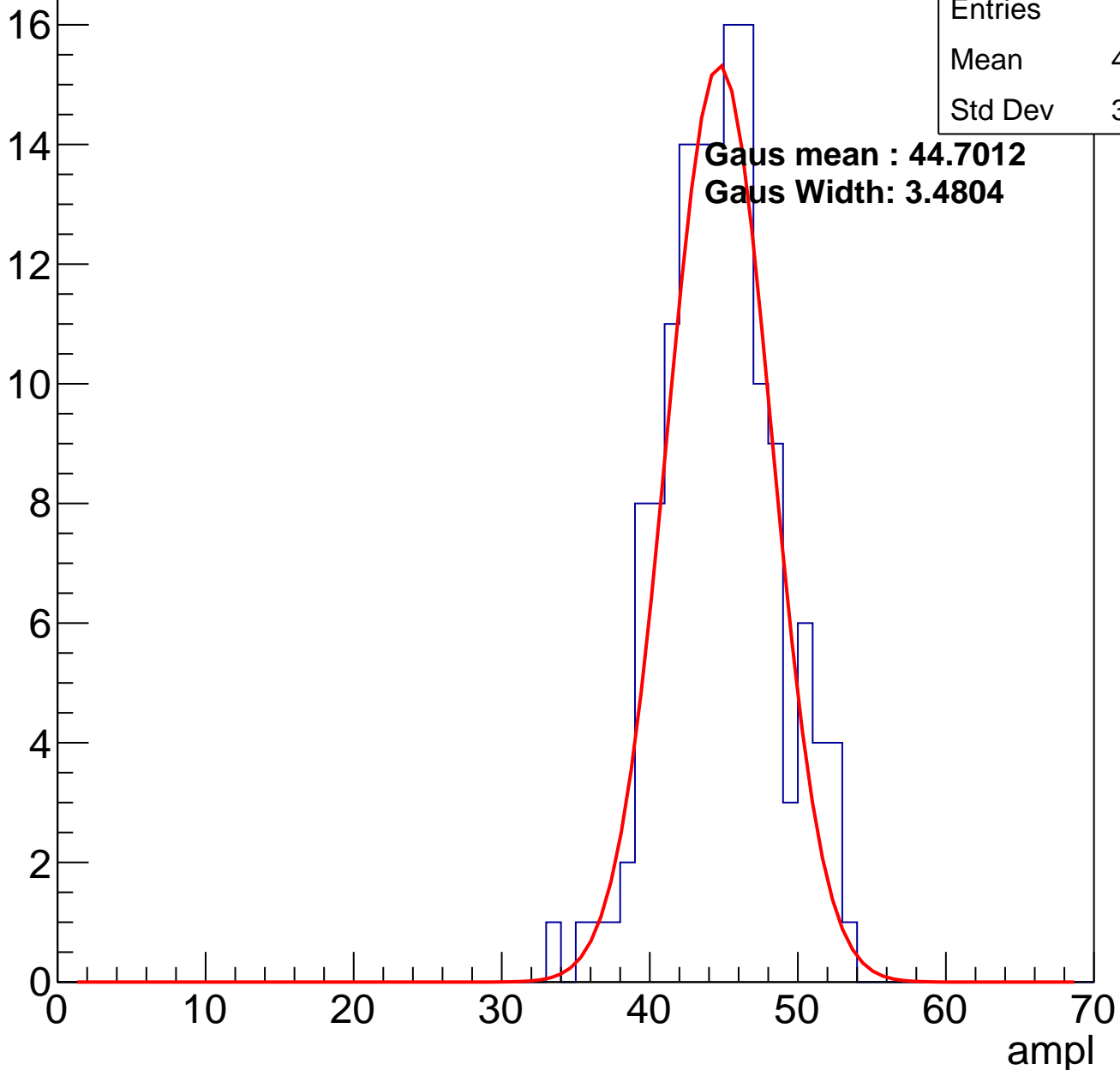
Entry



B0L101S, U22-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

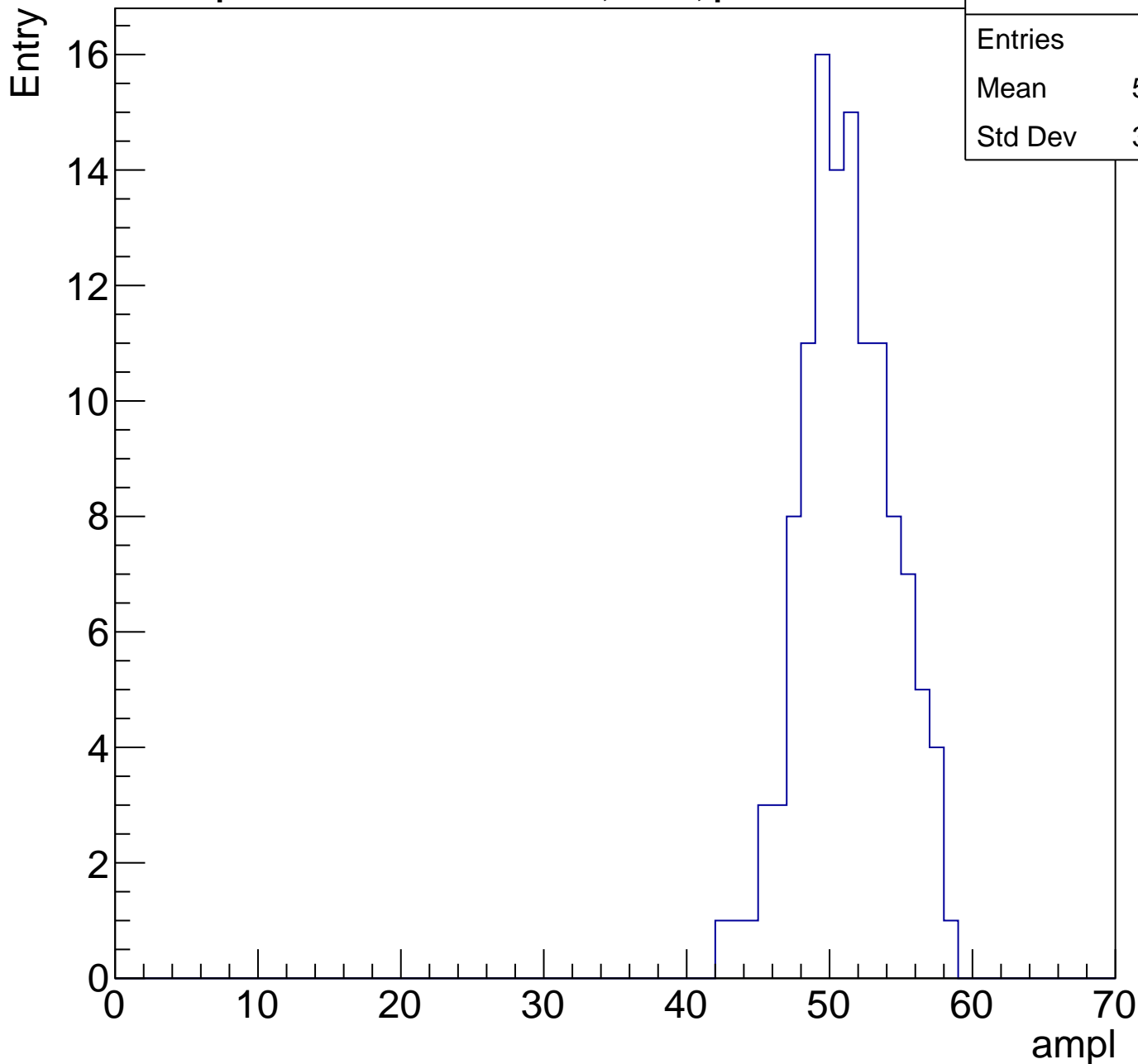
Entry



B0L101S, U22-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 120 |
| Mean | 50.77 |
| Std Dev | 3.205 |

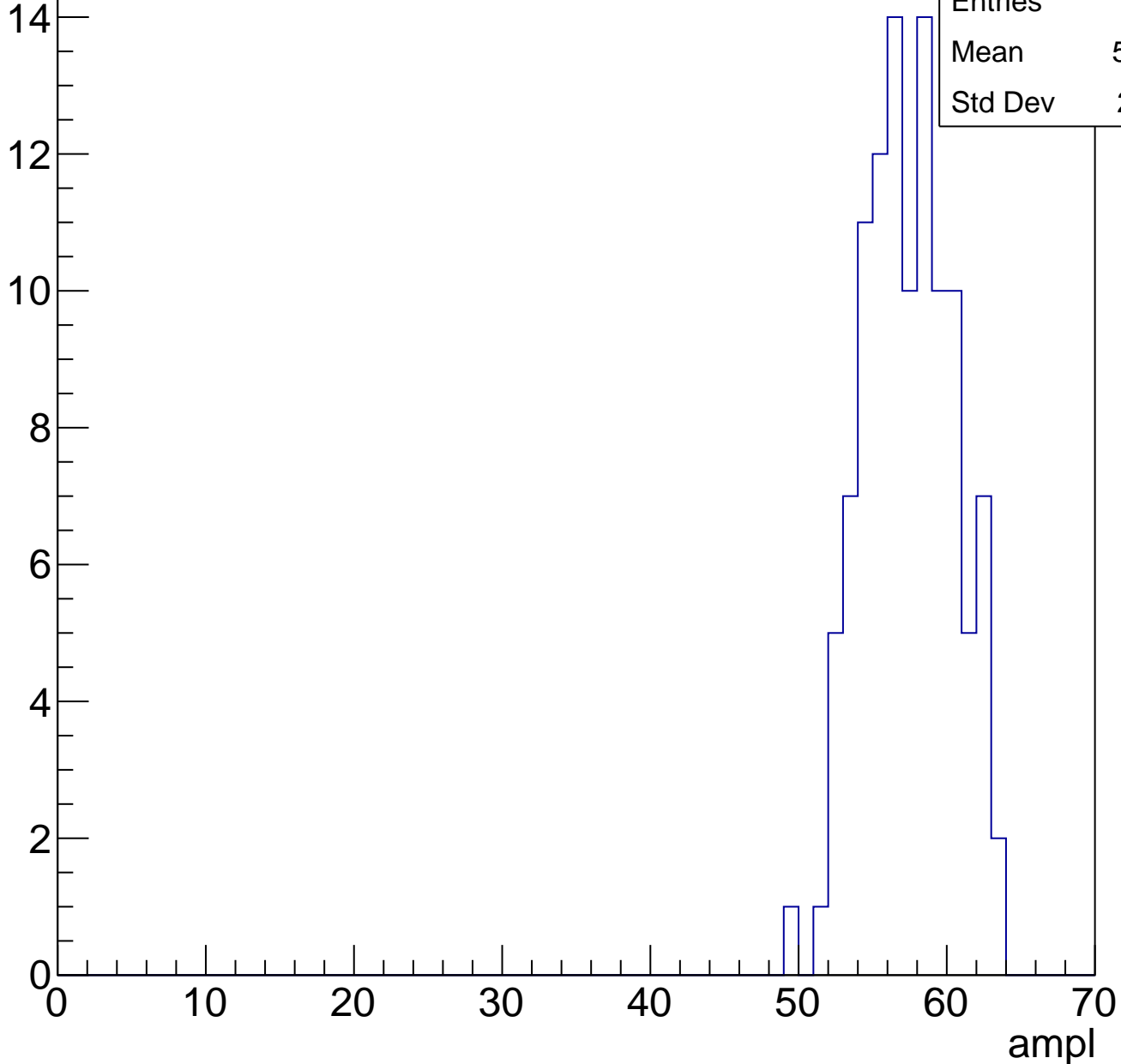


B0L101S, U22-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 109 |
| Mean | 56.94 |
| Std Dev | 2.981 |

Entry



B0L101S, U22-ch26, adc5

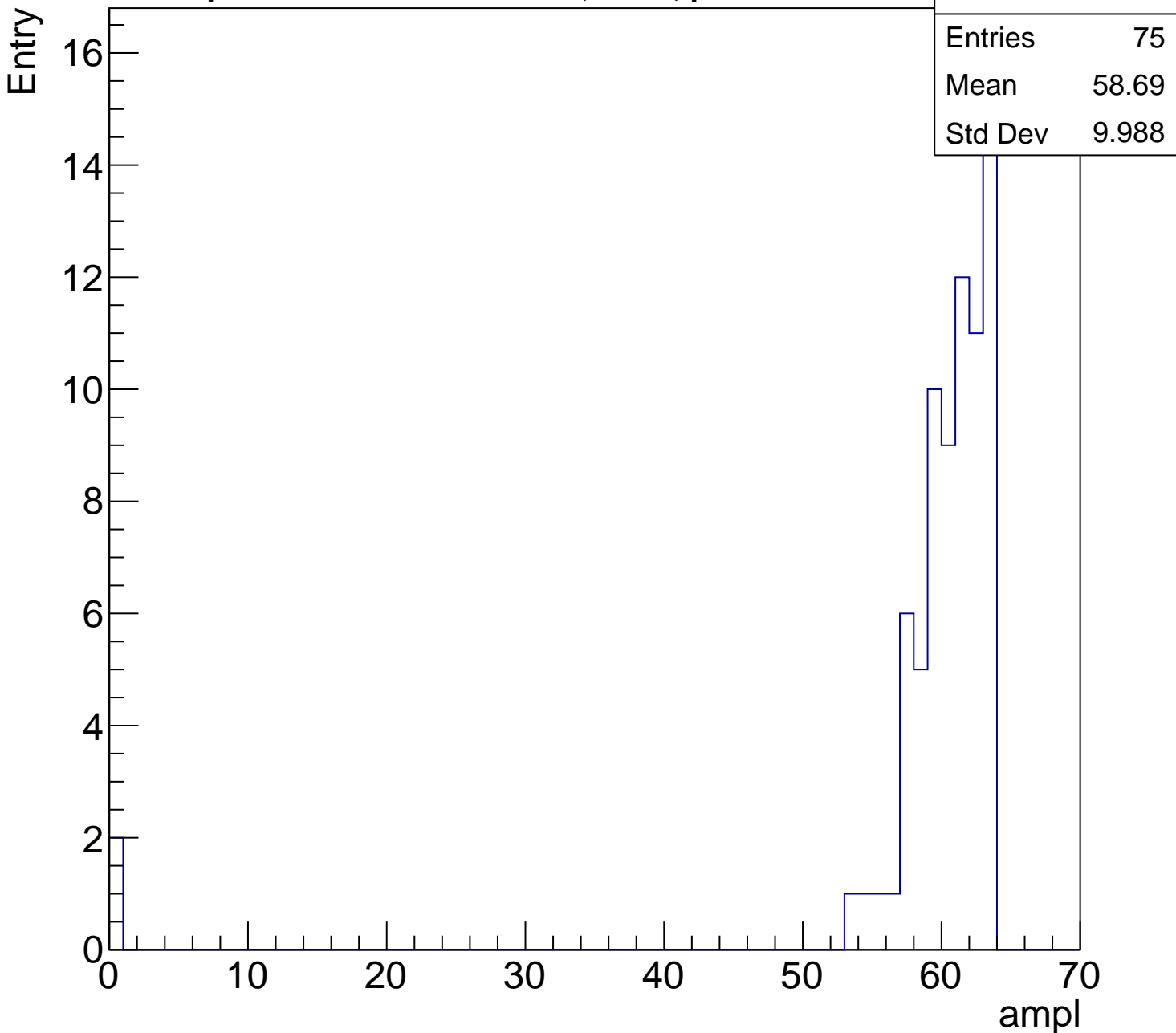
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 75 |
| Mean | 58.69 |
| Std Dev | 9.988 |

ampl



B0L101S, U22-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

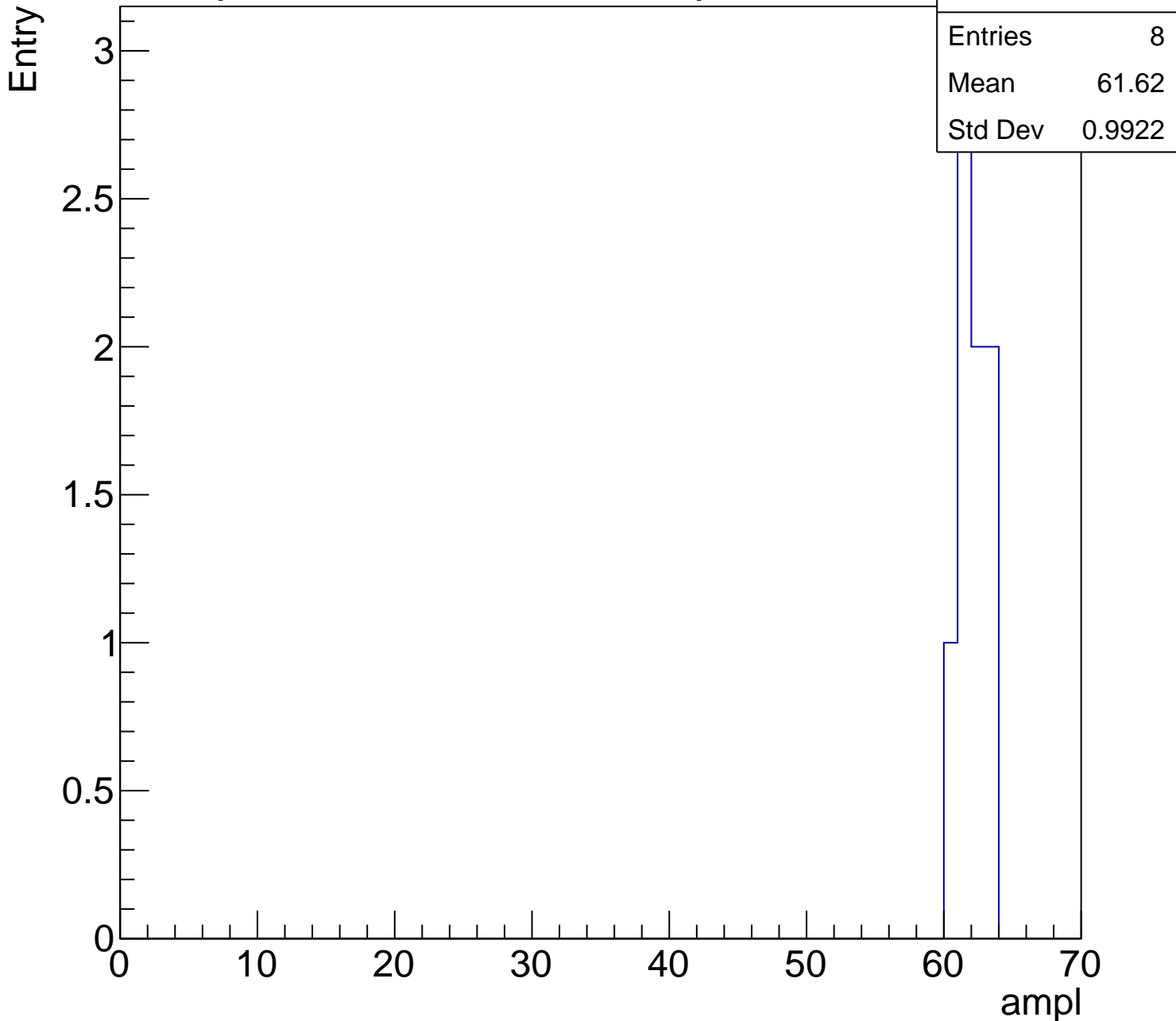
8

Mean

61.62

Std Dev

0.9922



B0L101S, U22-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

ampl

B0L101S, U22-ch27, adc0

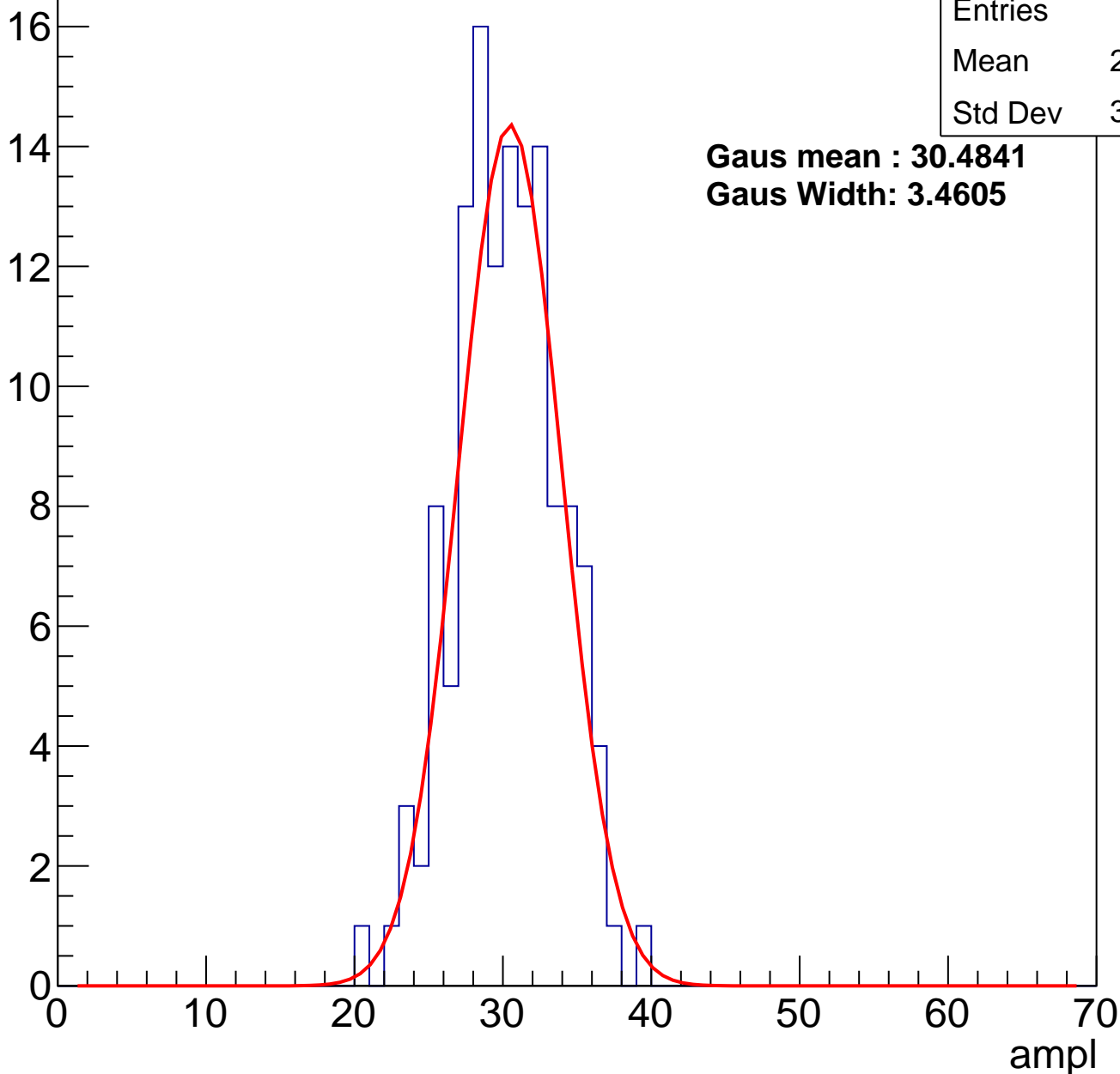
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 29.83 |
| Std Dev | 3.462 |

Gaus mean : 30.4841

Gaus Width: 3.4605

Entry



B0L101S, U22-ch27, adc1

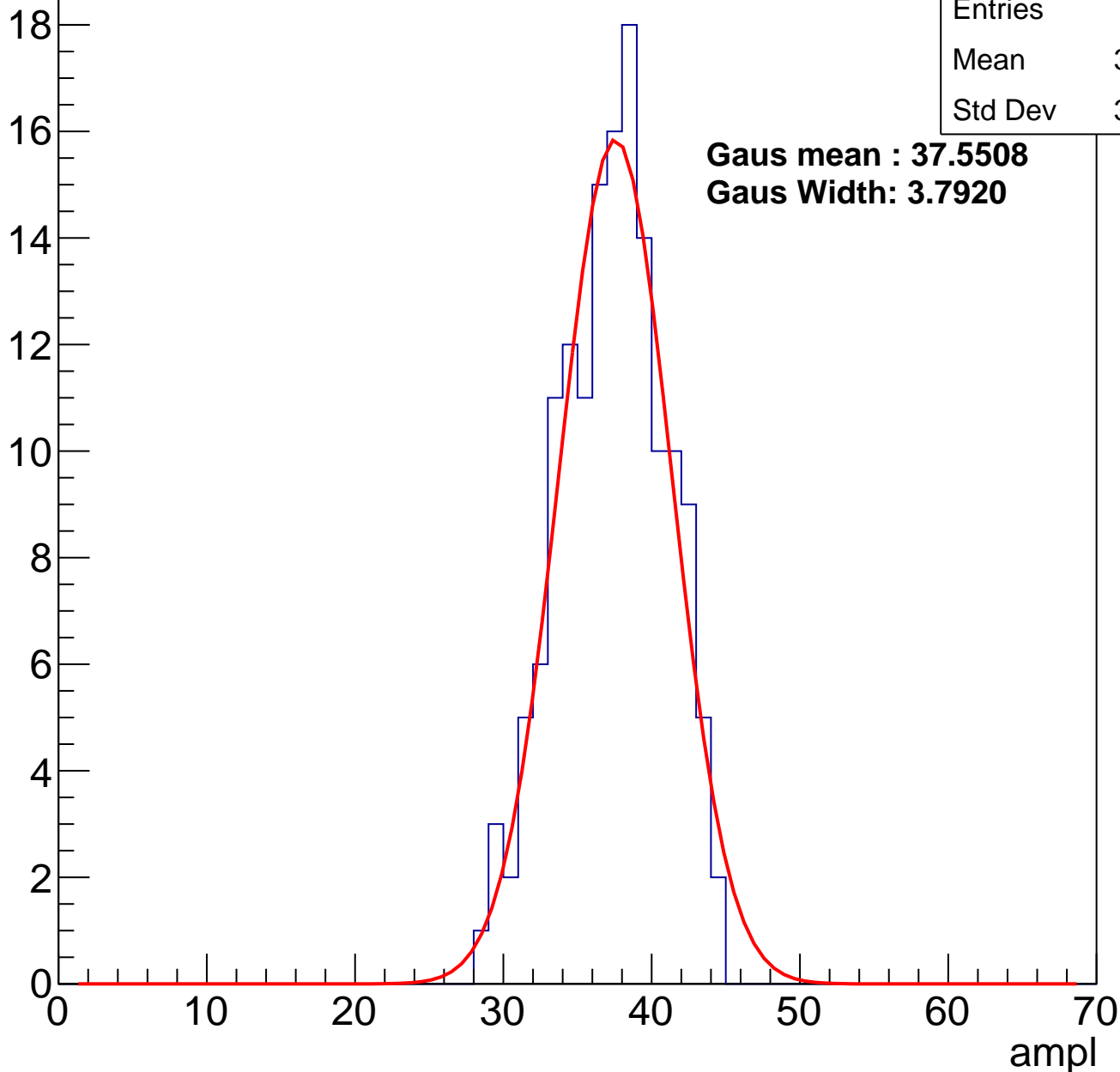
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 36.87 |
| Std Dev | 3.529 |

Gaus mean : 37.5508

Gaus Width: 3.7920

Entry



B0L101S, U22-ch27, adc2

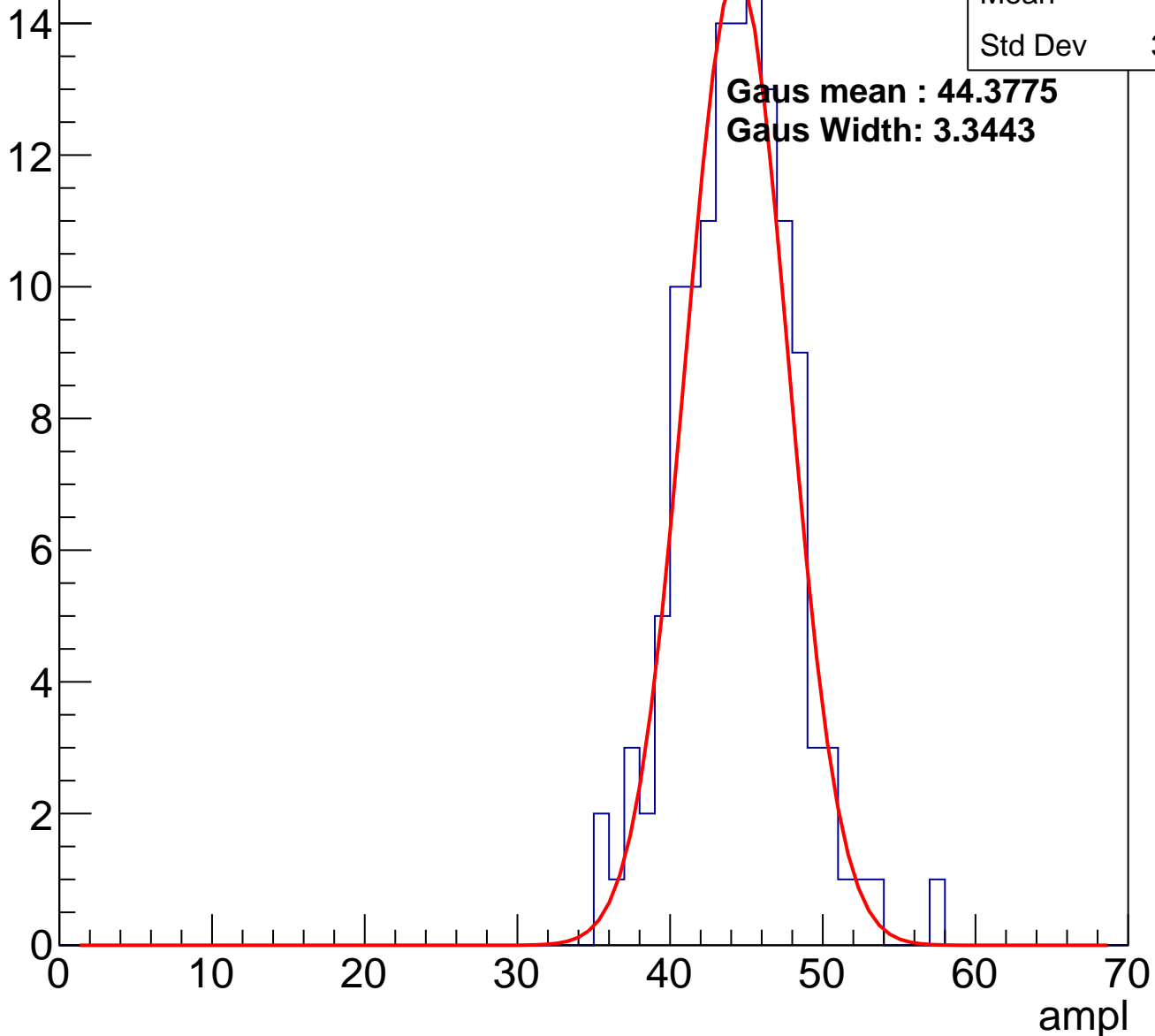
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 43.92 |
| Std Dev | 3.651 |

Gaus mean : 44.3775

Gaus Width: 3.3443

Entry



B0L101S, U22-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

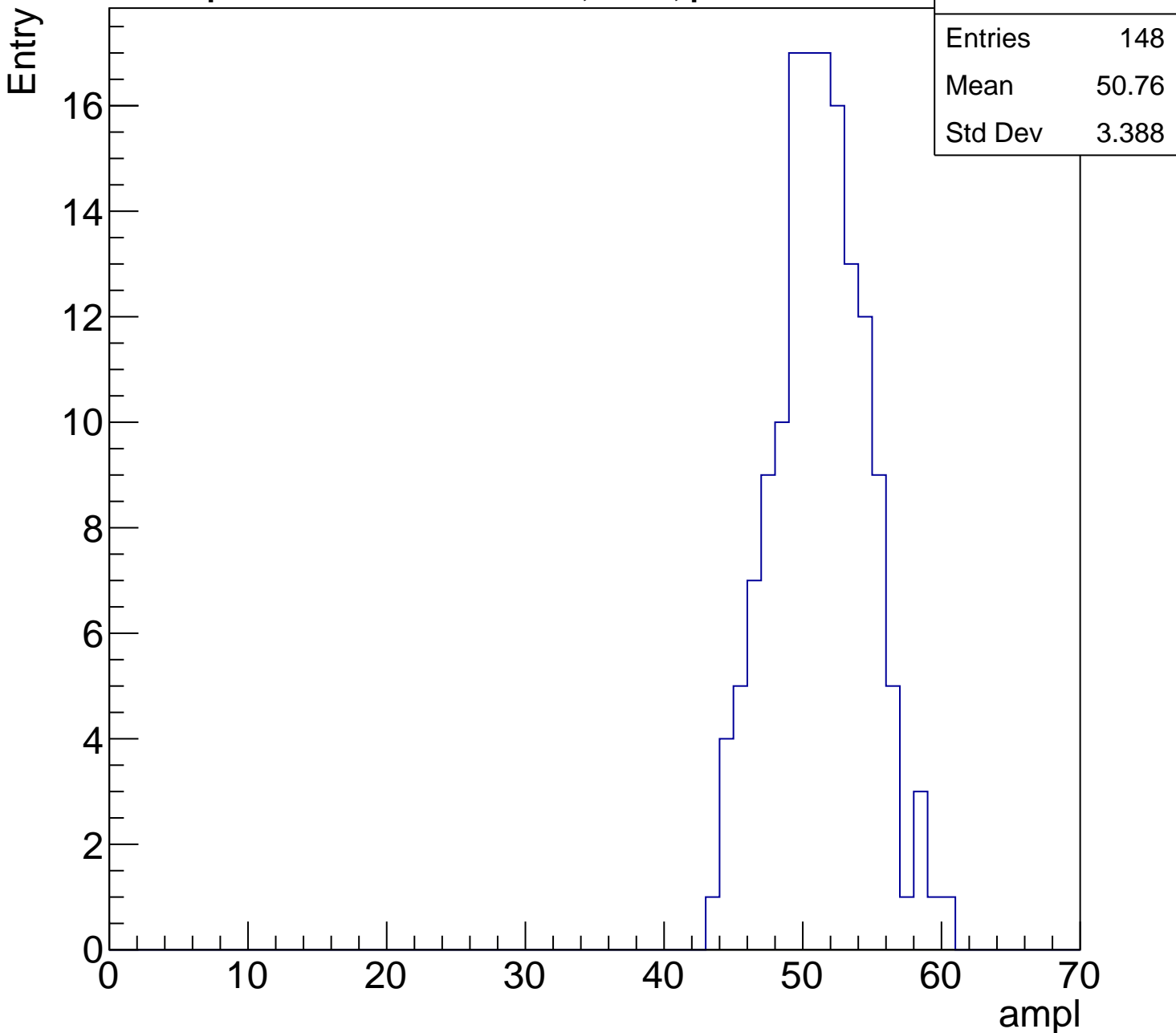
| | |
|---------|-------|
| Entries | 148 |
| Mean | 50.76 |
| Std Dev | 3.388 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

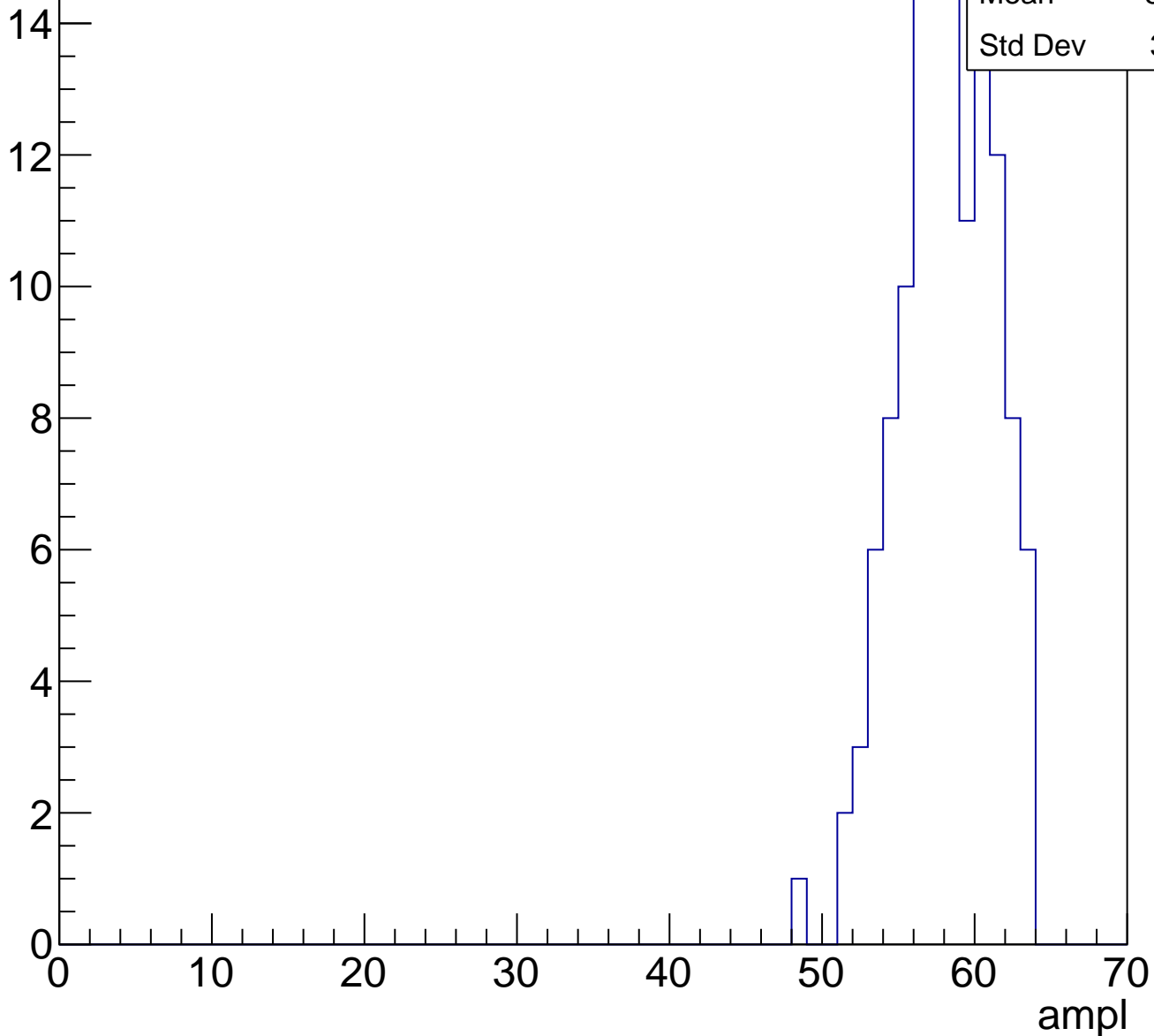


B0L101S, U22-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 126 |
| Mean | 57.67 |
| Std Dev | 3.071 |

Entry



B0L101S, U22-ch27, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.53 |
| Std Dev | 11.75 |

ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl

B0L101S, U22-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch28, adc0

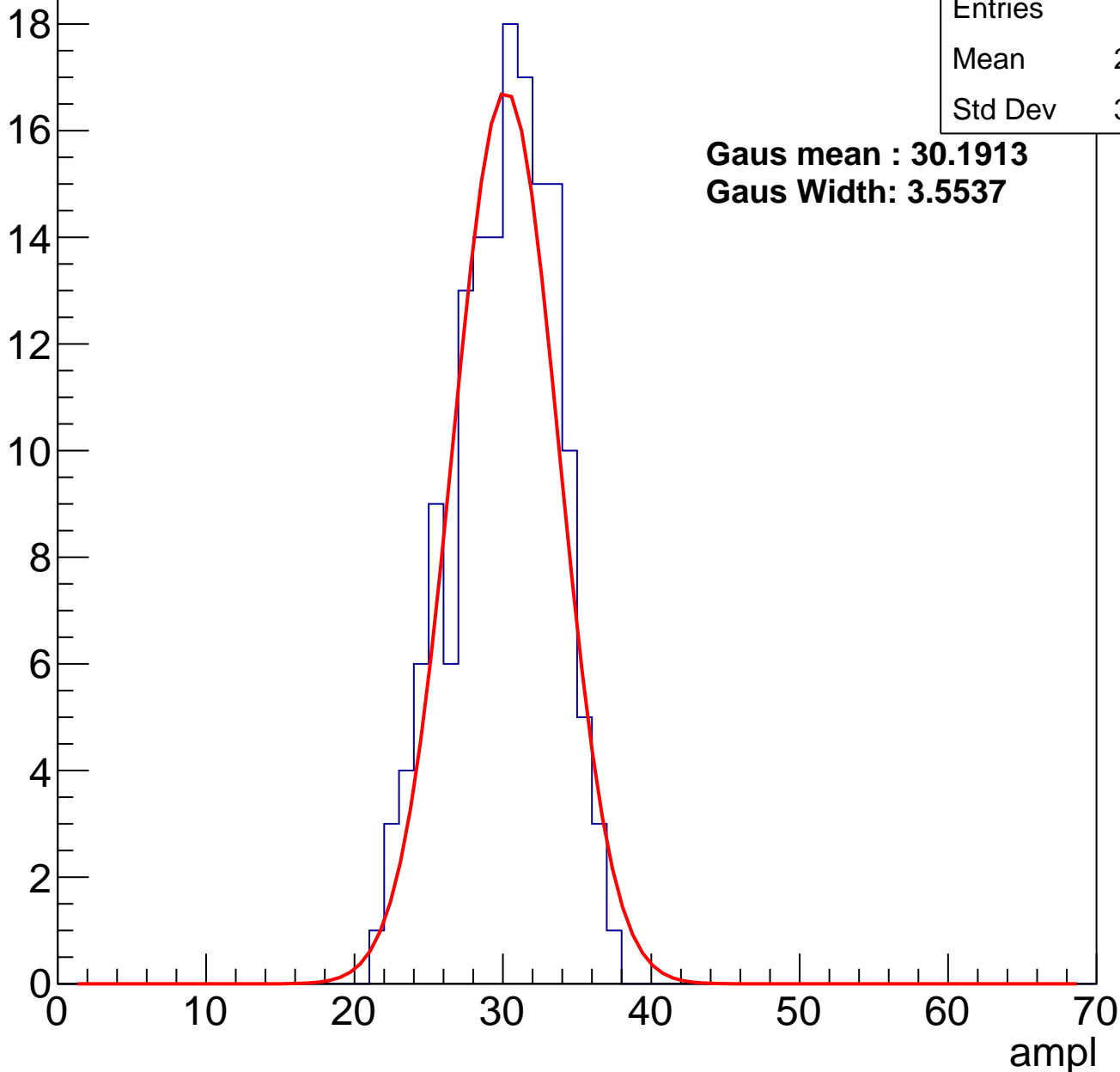
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 154 |
| Mean | 29.58 |
| Std Dev | 3.437 |

Gaus mean : 30.1913

Gaus Width: 3.5537

Entry



B0L101S, U22-ch28, adc1

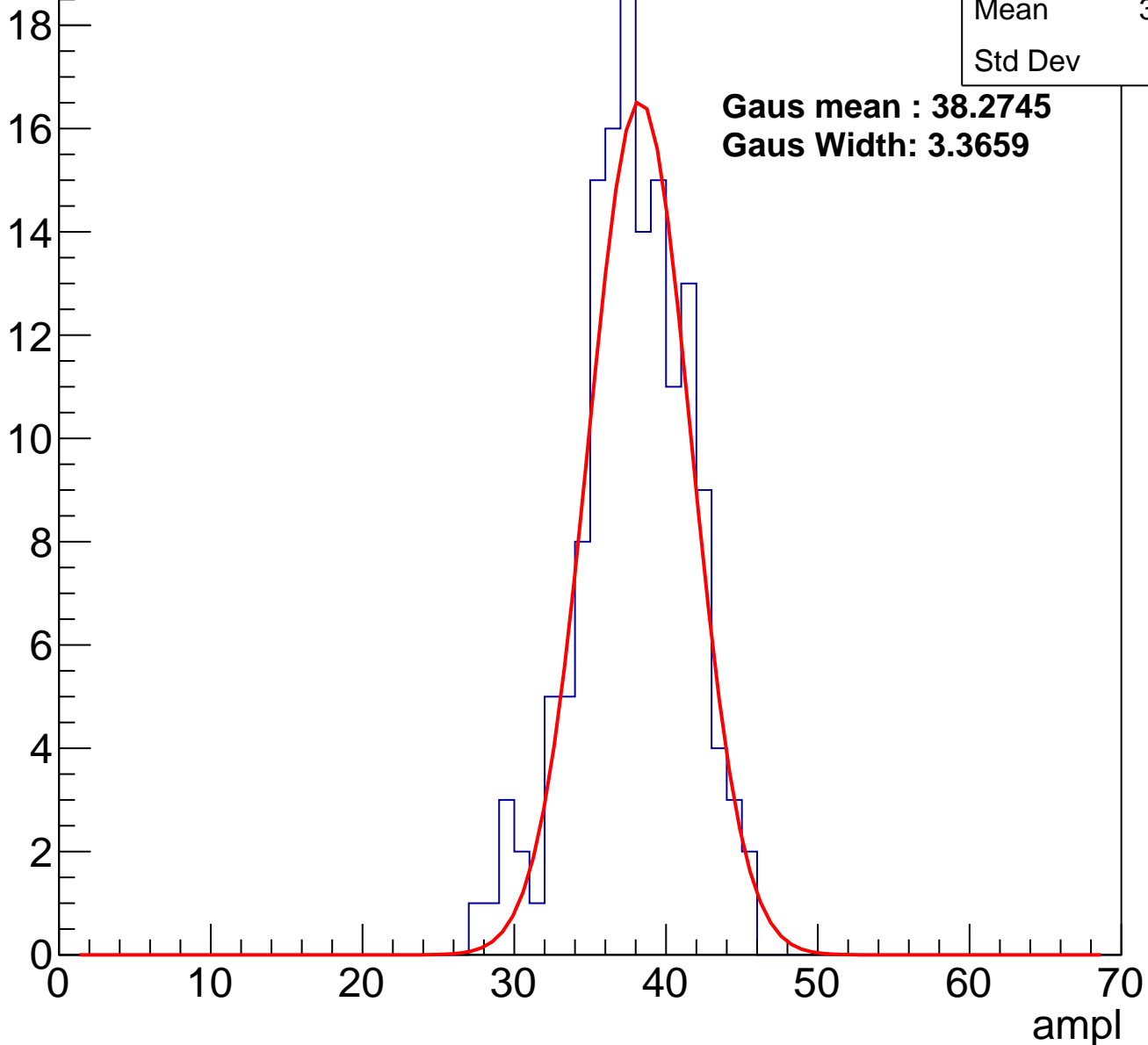
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 147 |
| Mean | 37.39 |
| Std Dev | 3.56 |

Gaus mean : 38.2745

Gaus Width: 3.3659

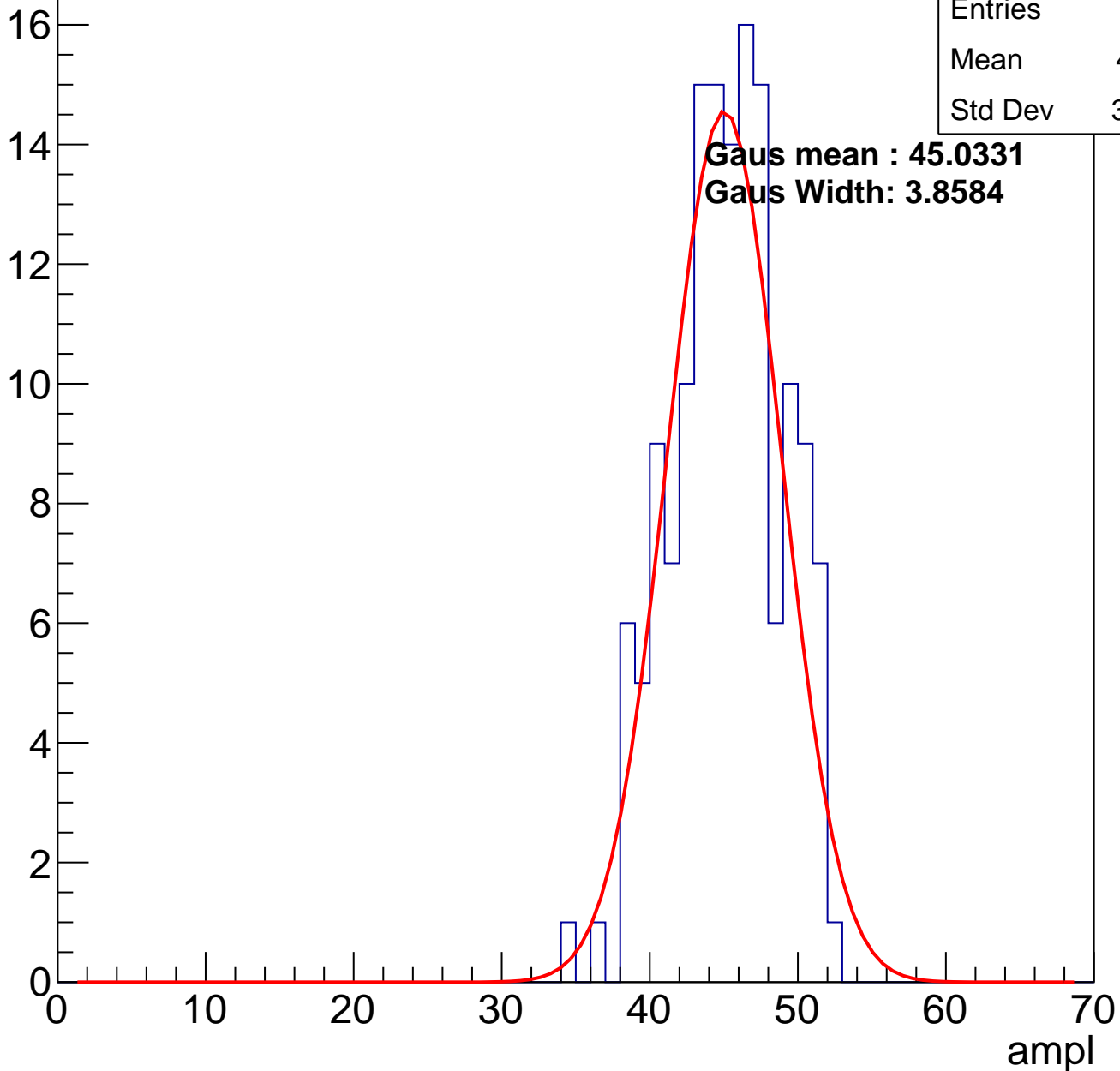
Entry



B0L101S, U22-ch28, adc2

calib_packv5_042523_0143.root, FC#1, port C1

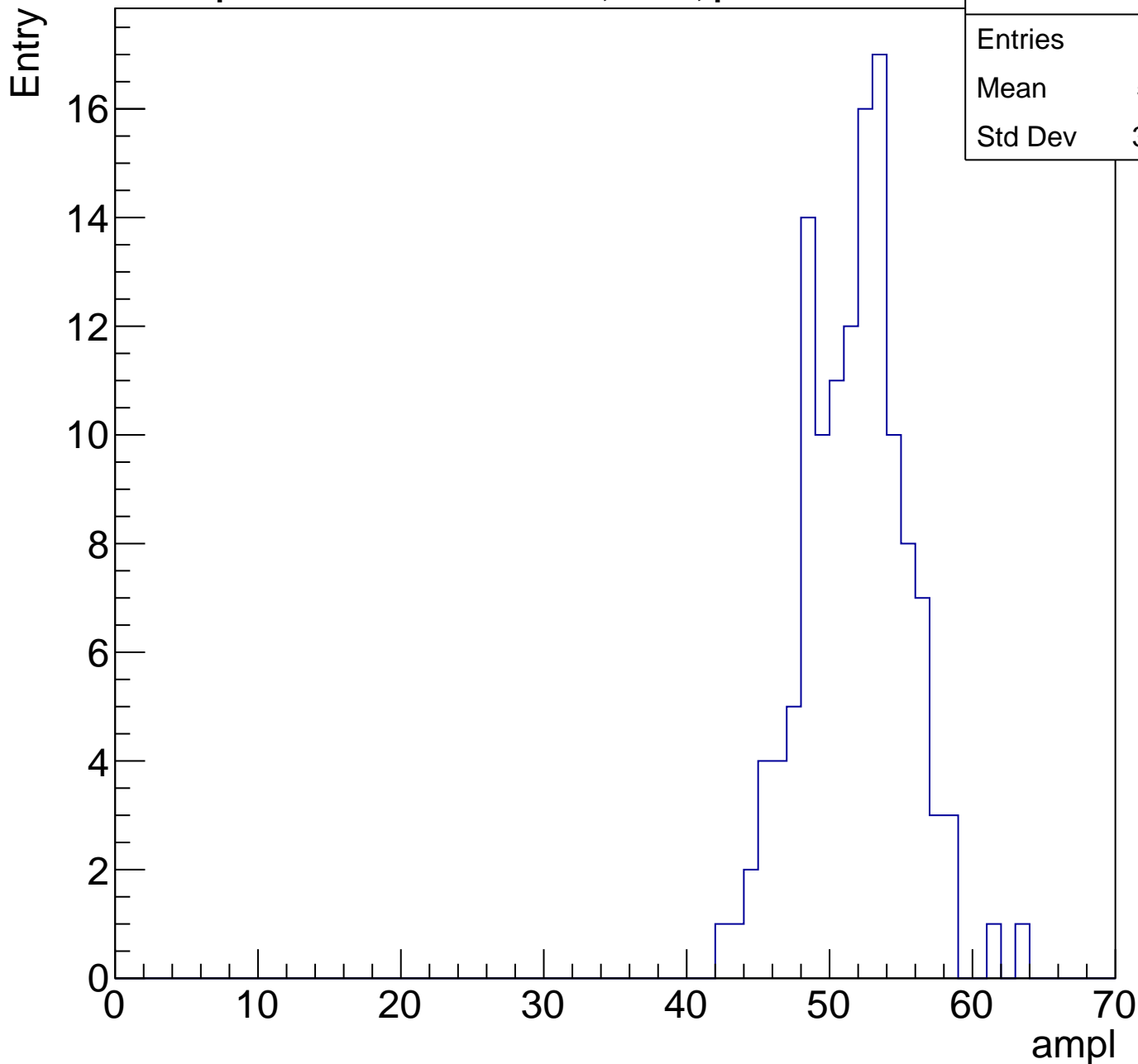
Entry



B0L101S, U22-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 51.31 |
| Std Dev | 3.624 |



B0L101S, U22-ch28, adc4

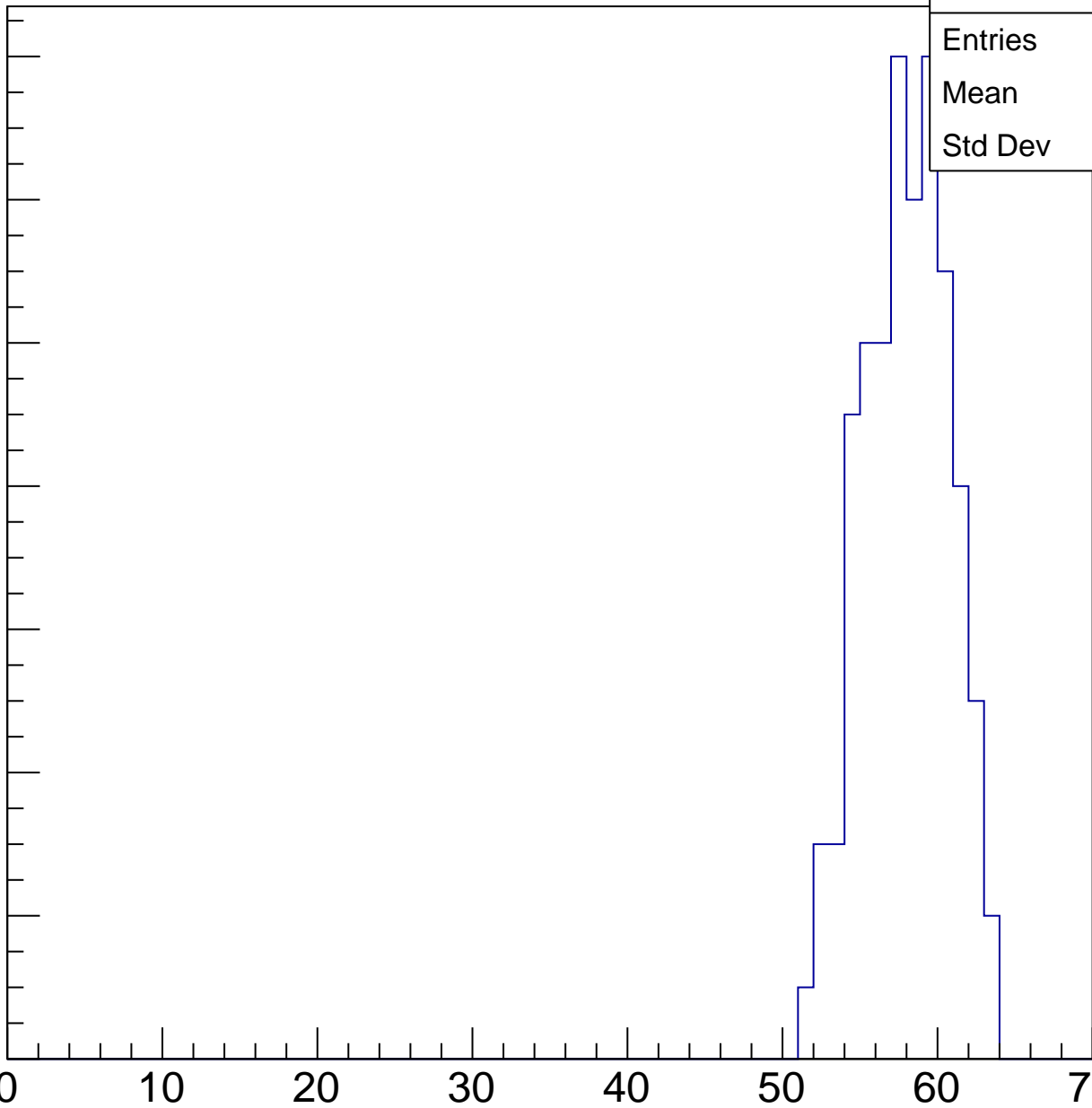
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 102 |
| Mean | 57.51 |
| Std Dev | 2.722 |

ampl



B0L101S, U22-ch28, adc5

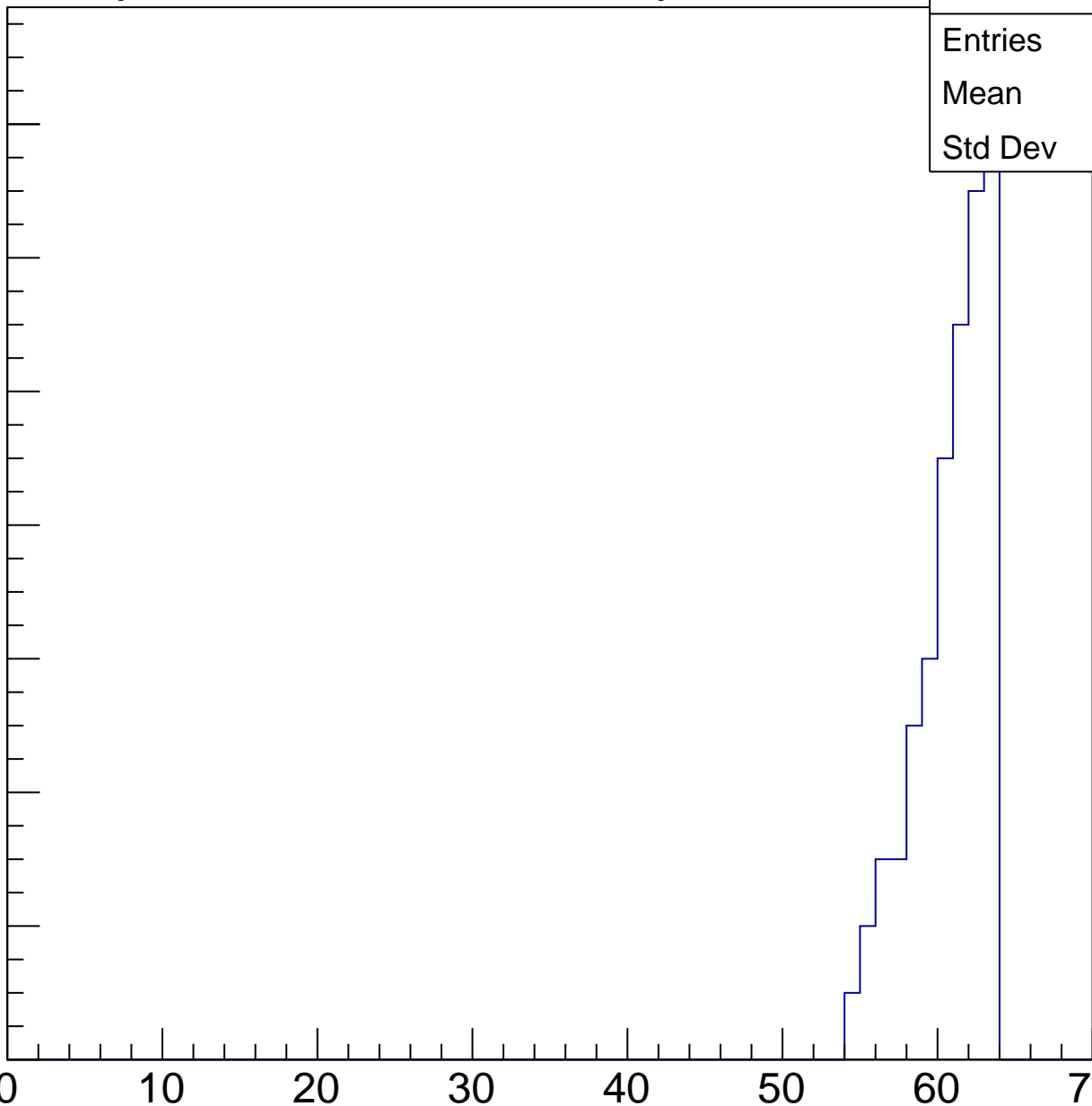
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 68 |
| Mean | 60.43 |
| Std Dev | 2.341 |

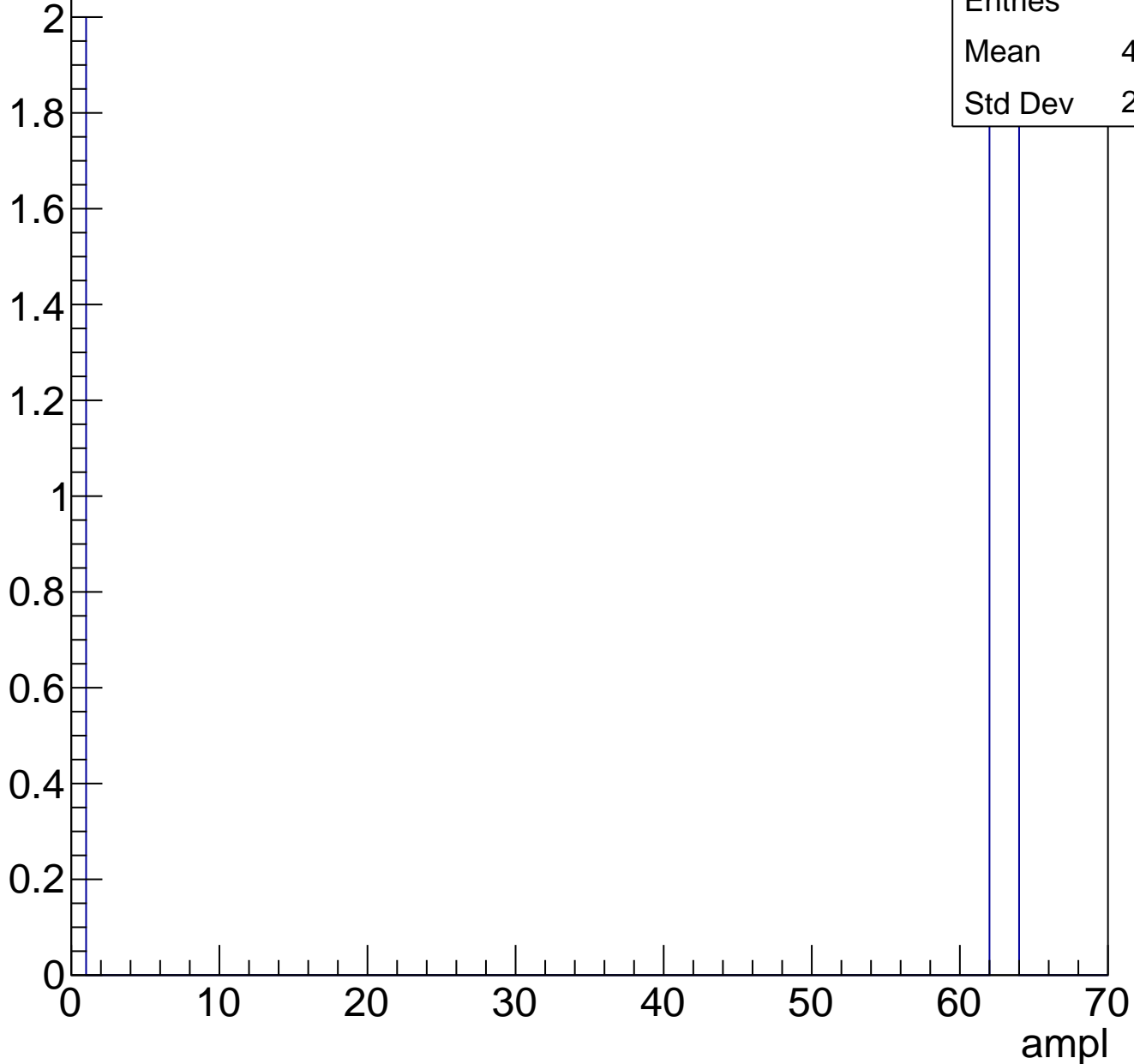
ampl



B0L101S, U22-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 118 |
| Mean | 29.56 |
| Std Dev | 3.499 |

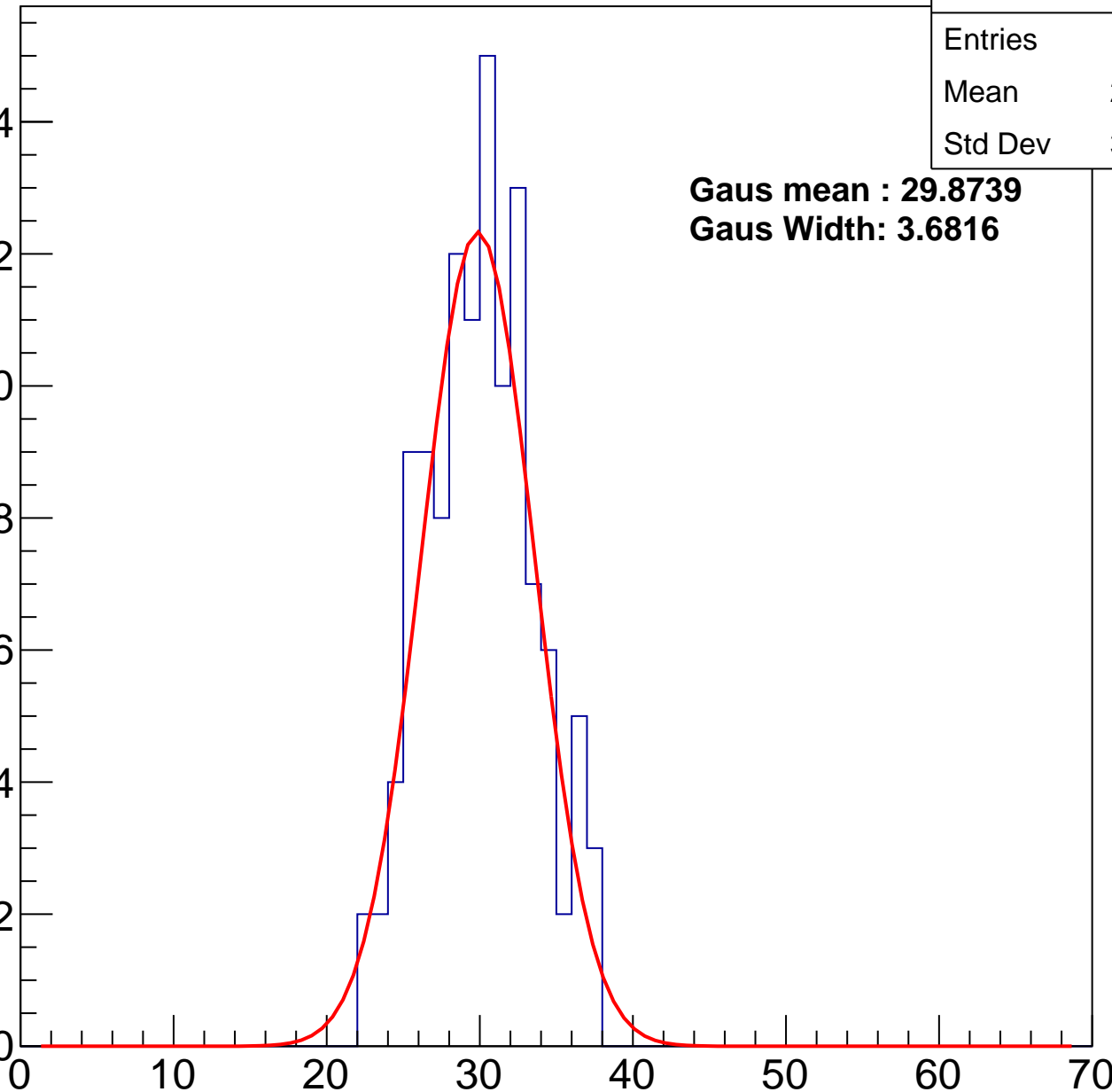
Gaus mean : 29.8739

Gaus Width: 3.6816

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch29, adc1

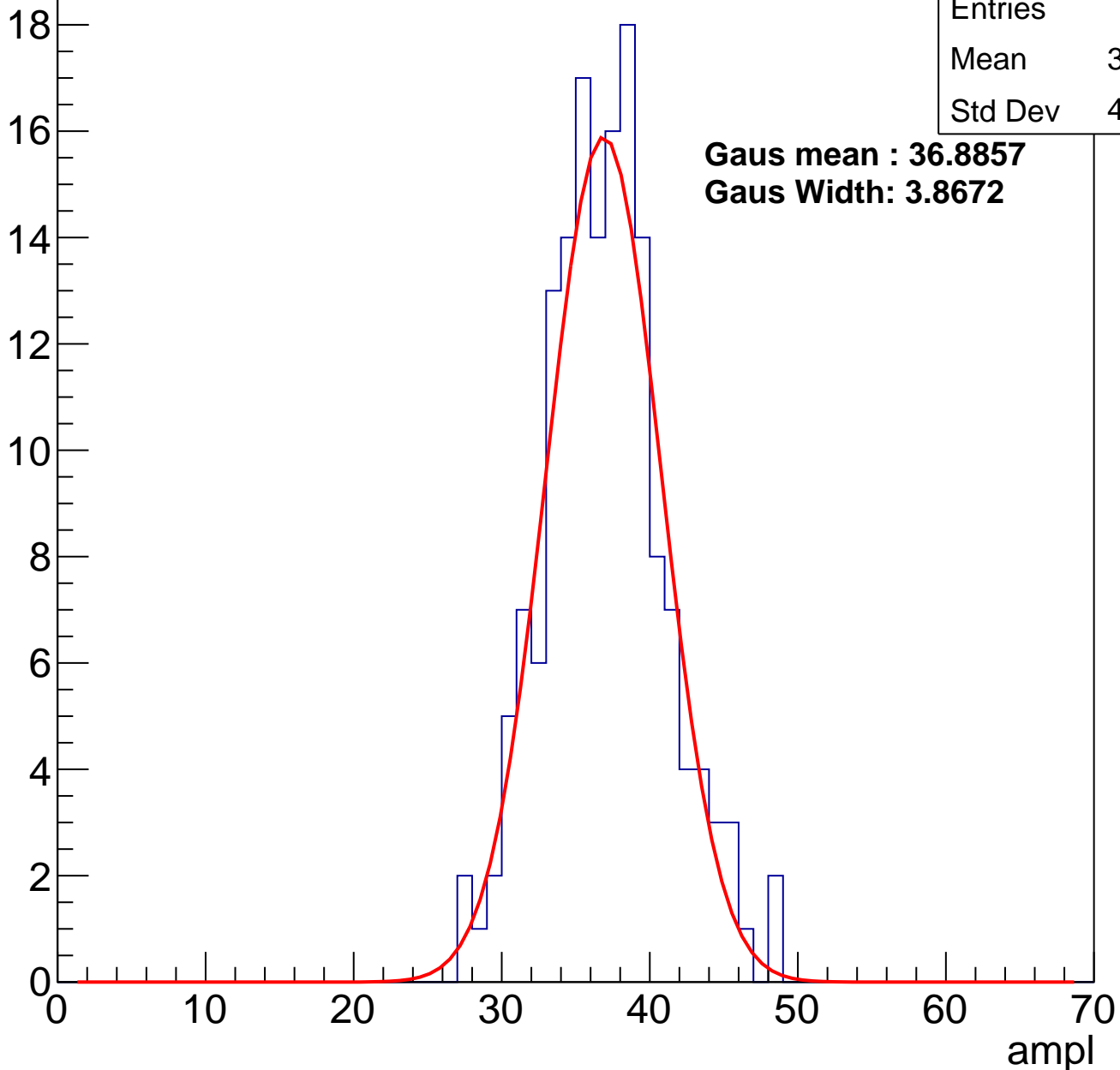
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 36.53 |
| Std Dev | 4.019 |

Gaus mean : 36.8857

Gaus Width: 3.8672

Entry



B0L101S, U22-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

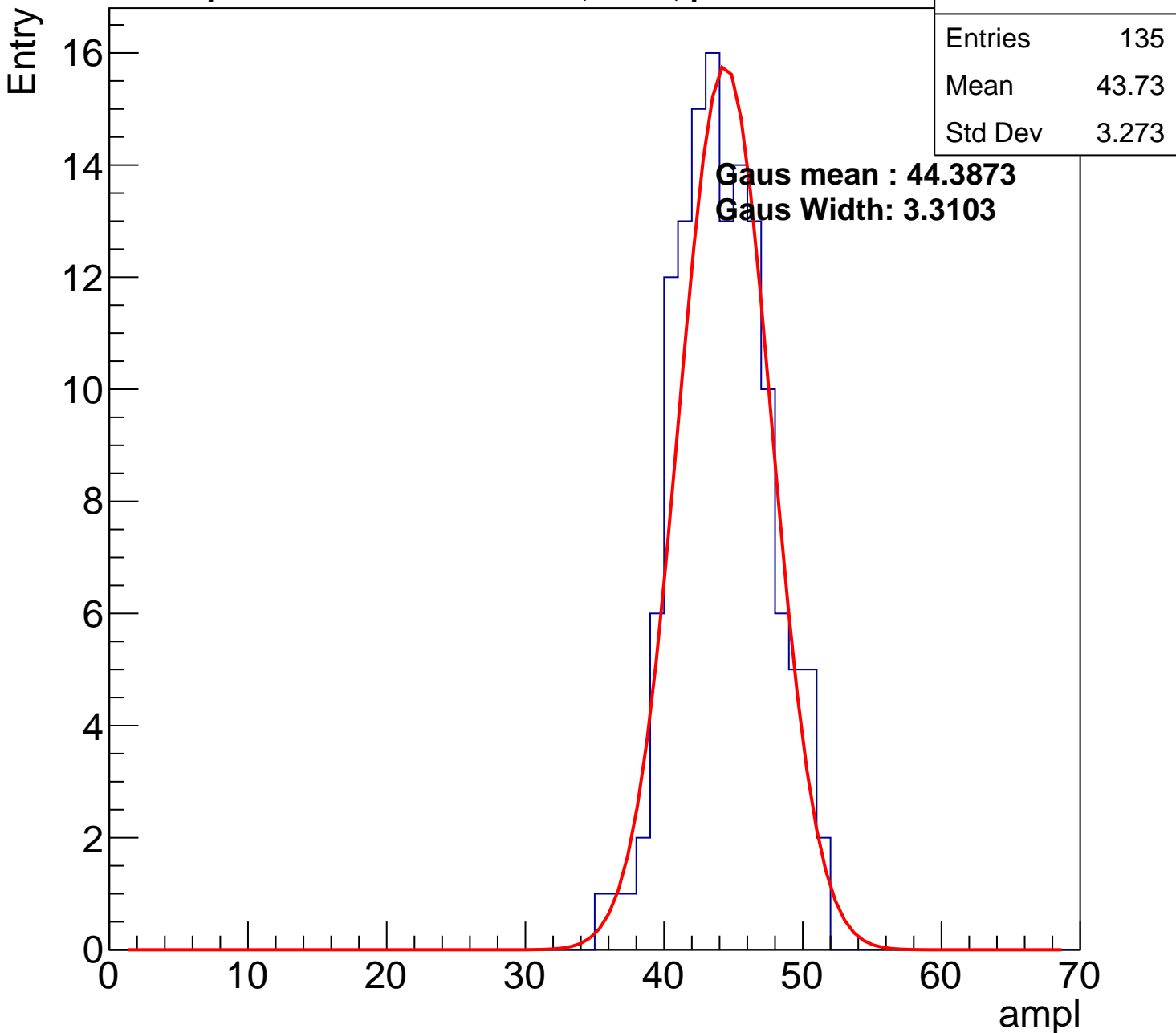
| | |
|---------|-------|
| Entries | 135 |
| Mean | 43.73 |
| Std Dev | 3.273 |

Gaus mean : 44.3873

Gaus Width: 3.3103

ampl

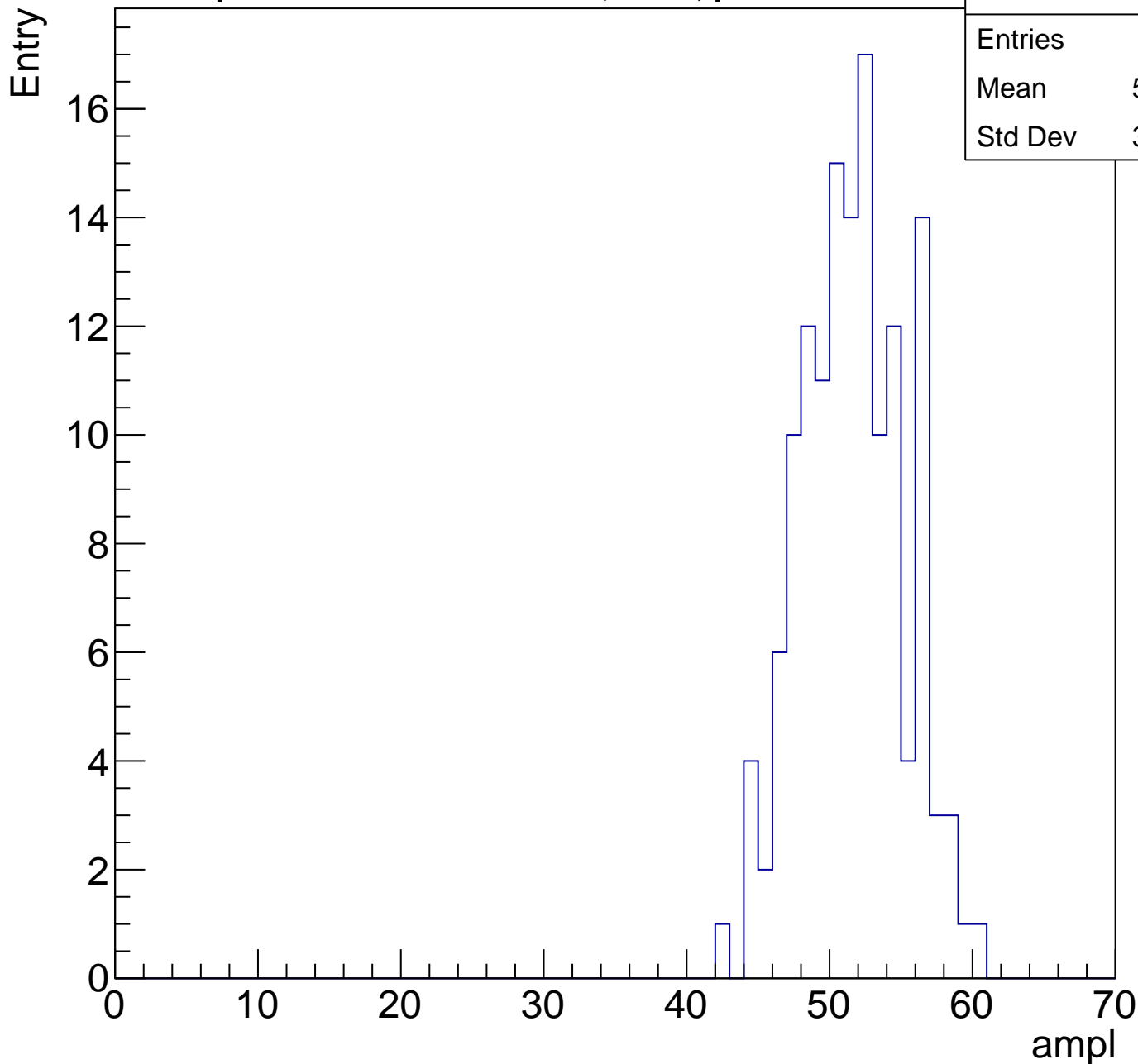
0 10 20 30 40 50 60 70



B0L101S, U22-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 140 |
| Mean | 51.16 |
| Std Dev | 3.587 |



B0L101S, U22-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 121 |
| Mean | 56.26 |
| Std Dev | 6.188 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

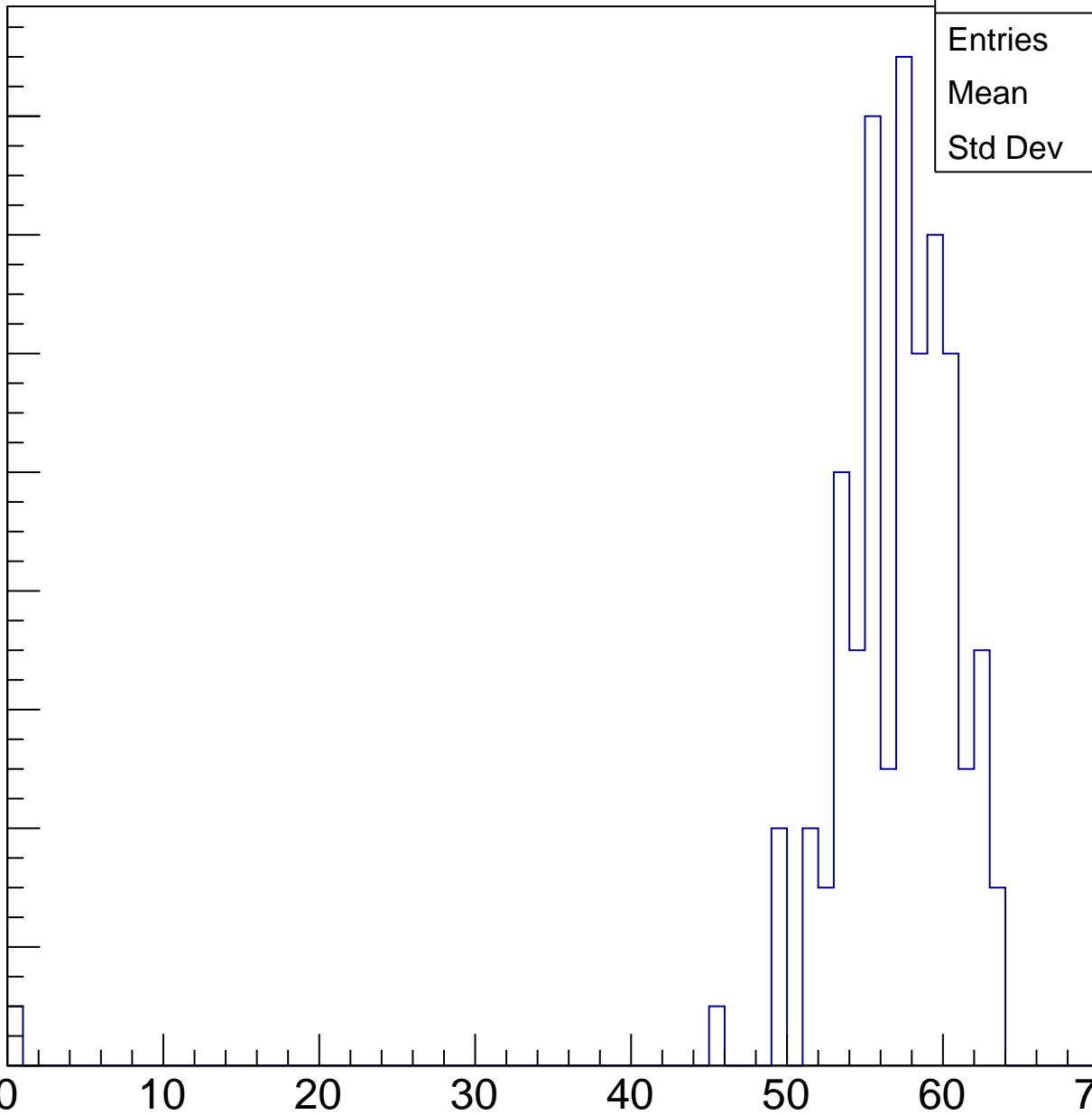
40

50

60

70

ampl



B0L101S, U22-ch29, adc5

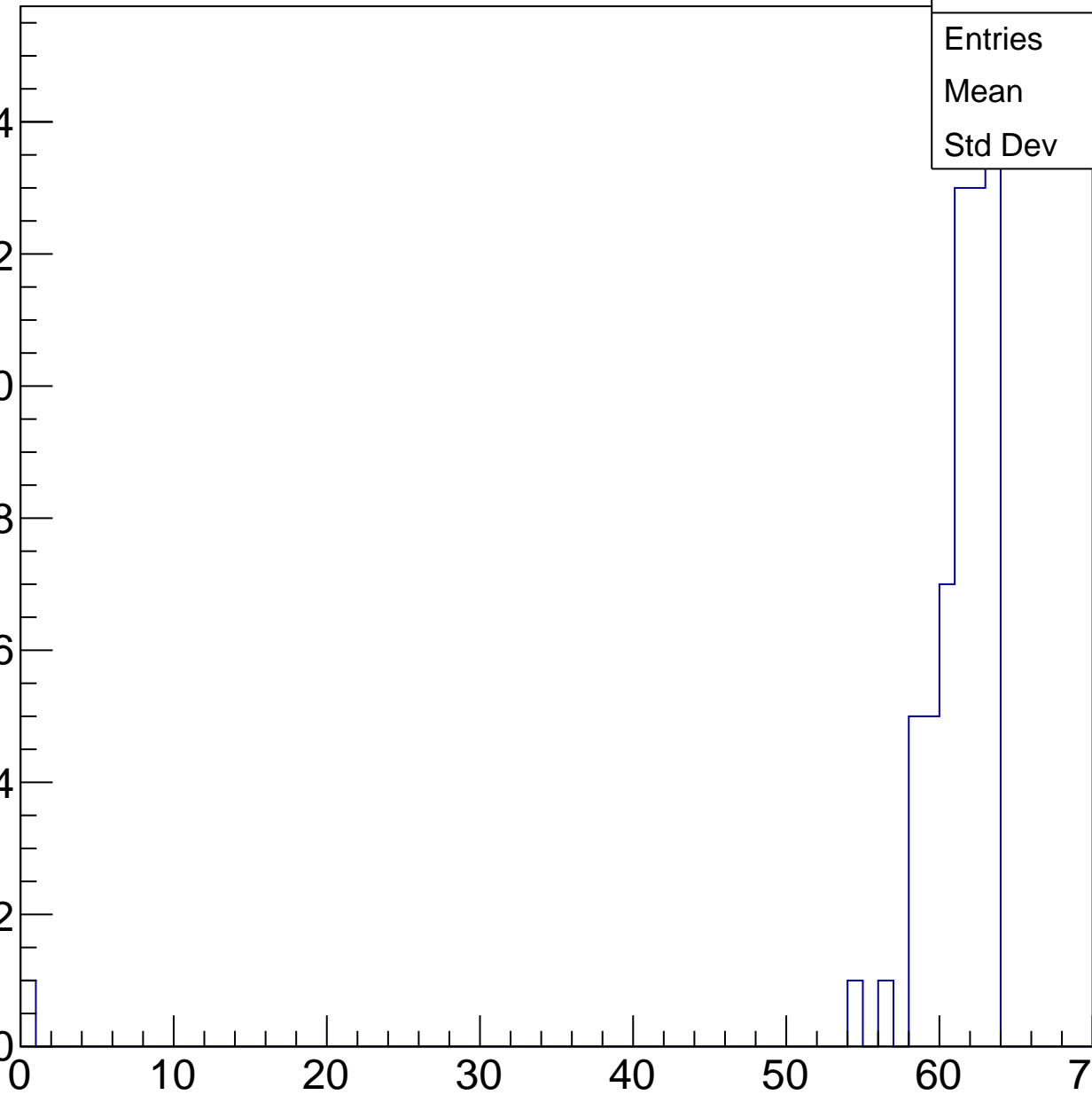
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 61 |
| Mean | 59.98 |
| Std Dev | 7.972 |

ampl



B0L101S, U22-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 4 |
| Mean | 62 |
| Std Dev | 1 |

ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch30, adc0

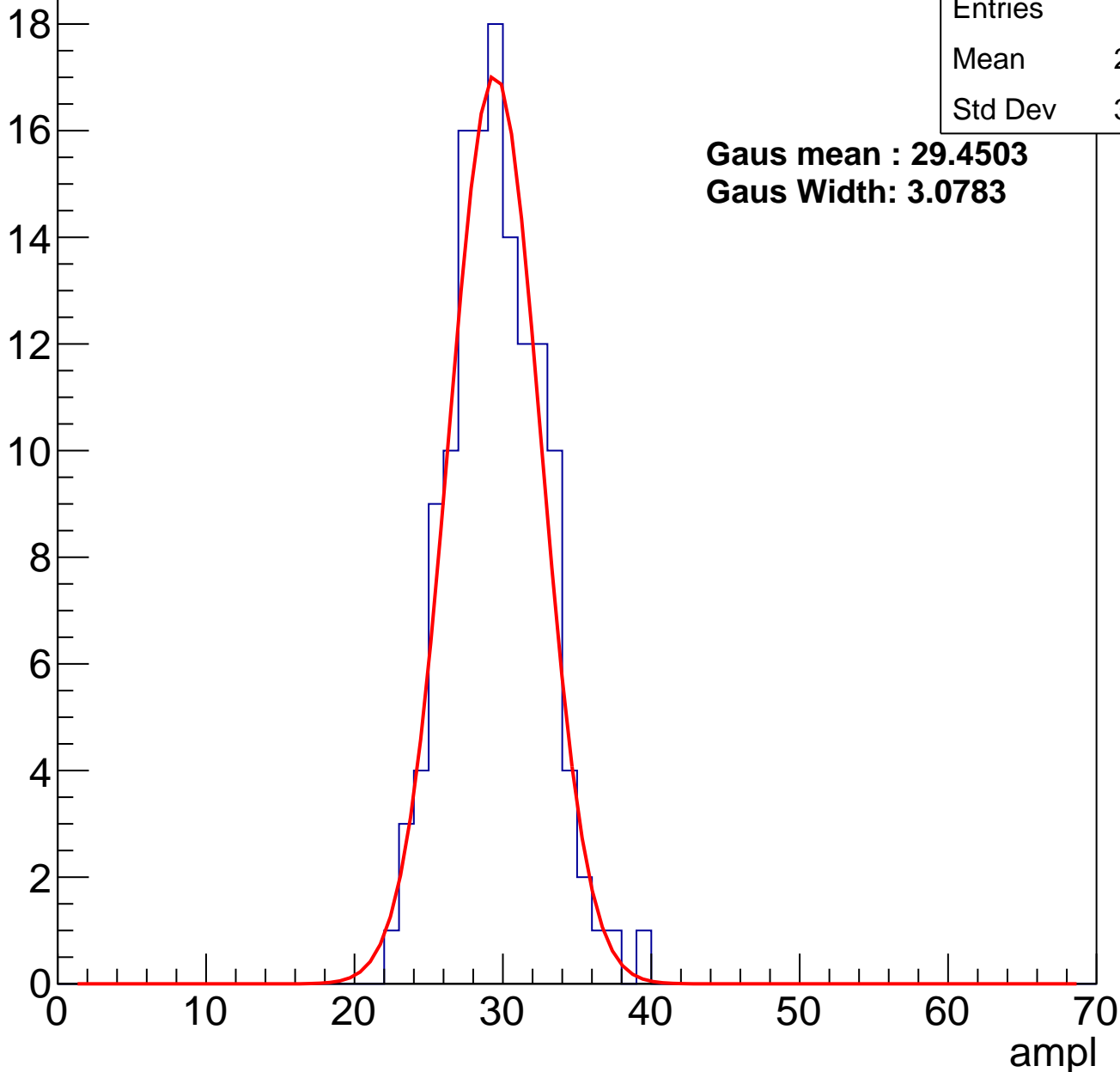
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 29.09 |
| Std Dev | 3.097 |

Gaus mean : 29.4503

Gaus Width: 3.0783

Entry



B0L101S, U22-ch30, adc1

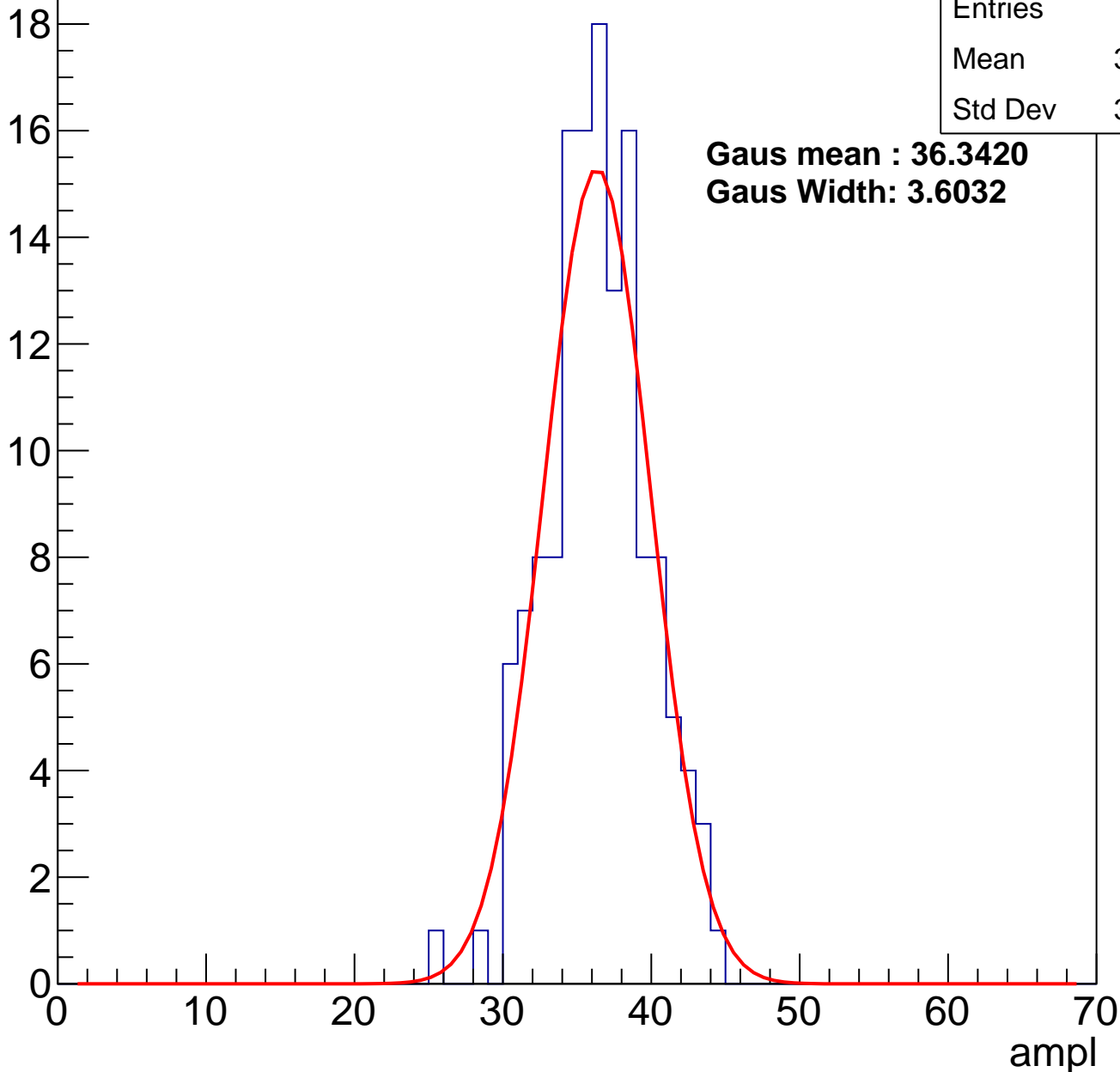
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 35.89 |
| Std Dev | 3.405 |

Gaus mean : 36.3420

Gaus Width: 3.6032

Entry



B0L101S, U22-ch30, adc2

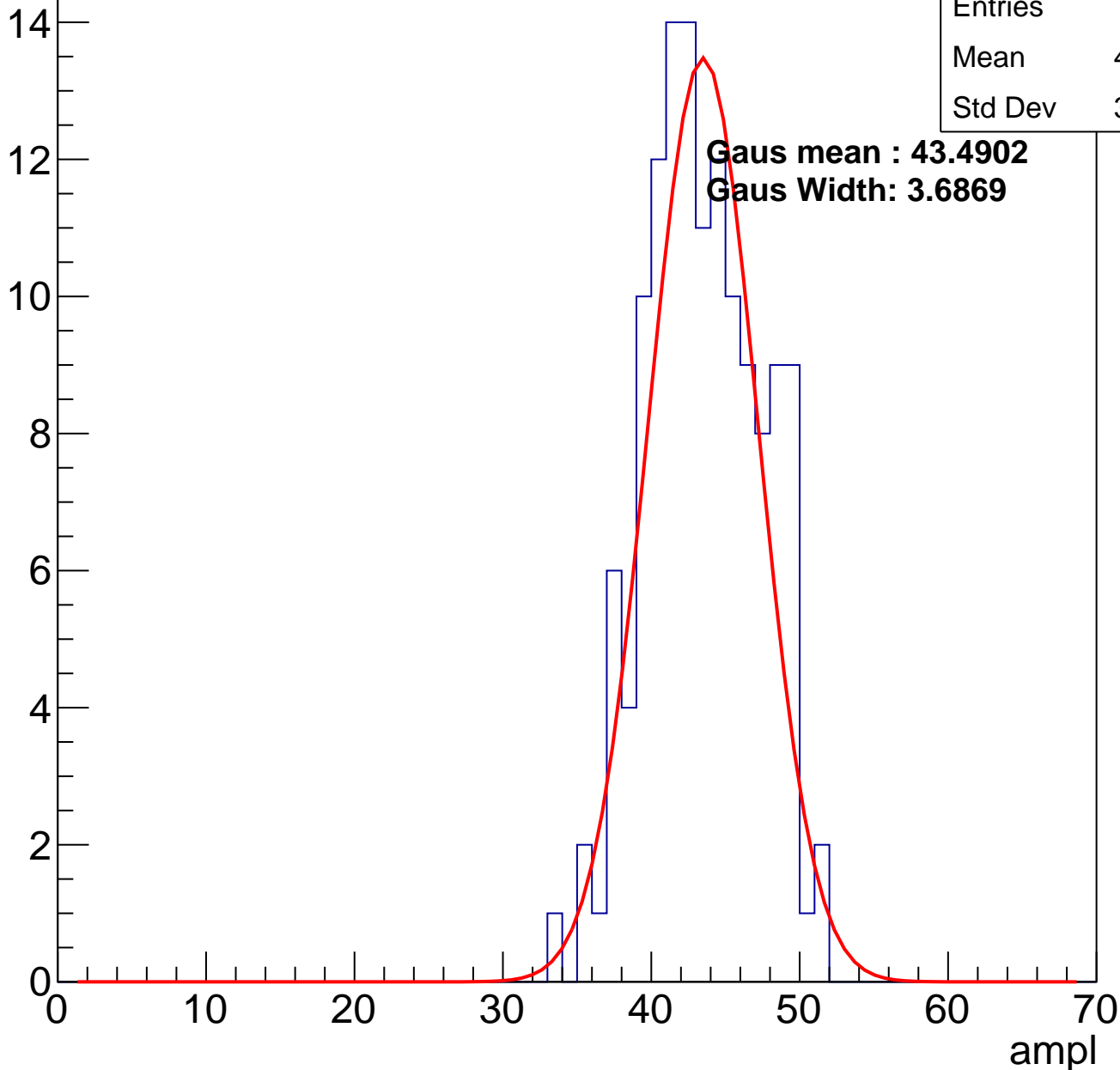
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 43.04 |
| Std Dev | 3.769 |

Gaus mean : 43.4902

Gaus Width: 3.6869

Entry



B0L101S, U22-ch30, adc3

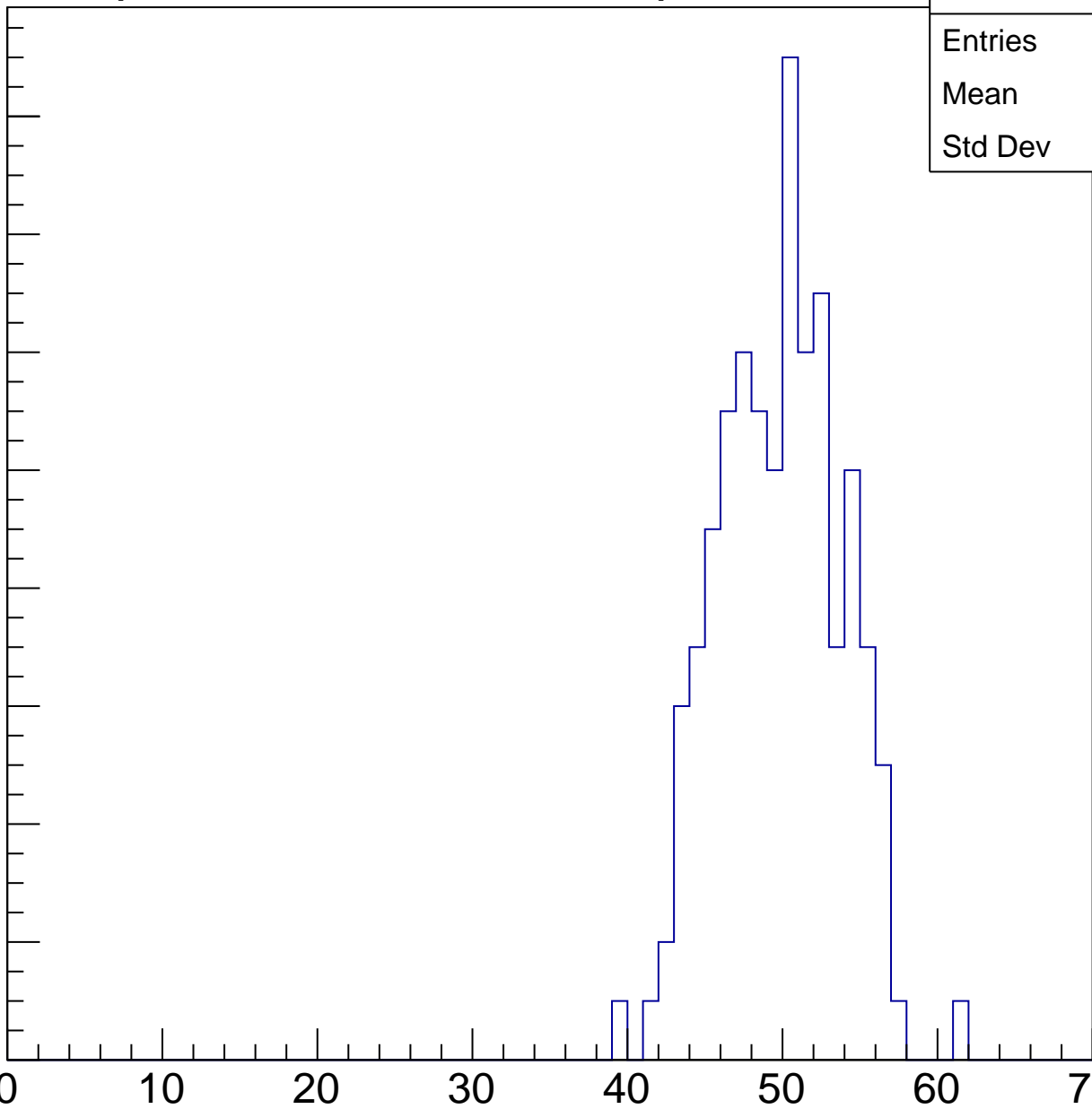
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 143 |
| Mean | 49.34 |
| Std Dev | 3.912 |

ampl



B0L101S, U22-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

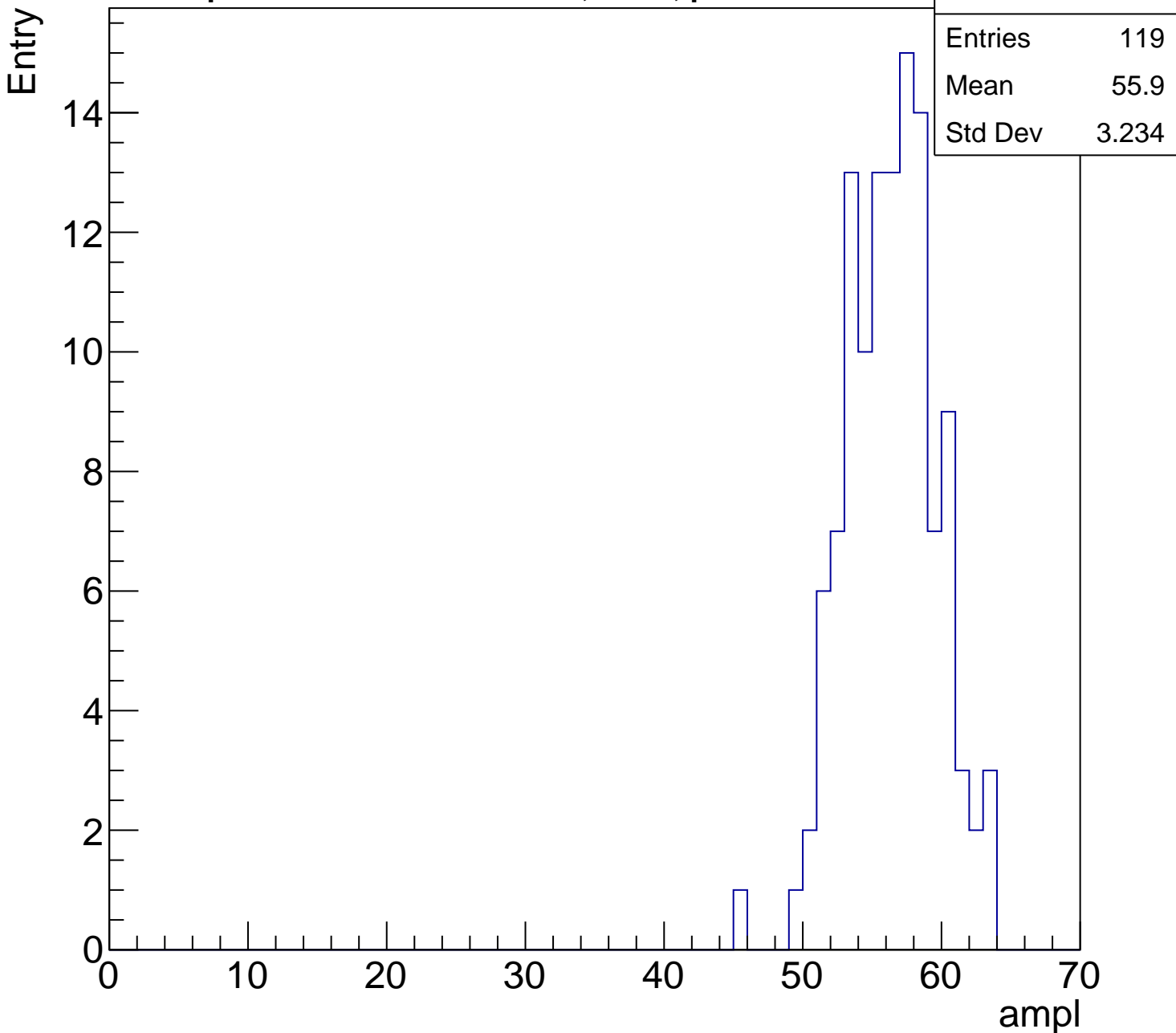
| | |
|---------|-------|
| Entries | 119 |
| Mean | 55.9 |
| Std Dev | 3.234 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch30, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 69 |
| Mean | 60.36 |
| Std Dev | 1.888 |

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

10

12

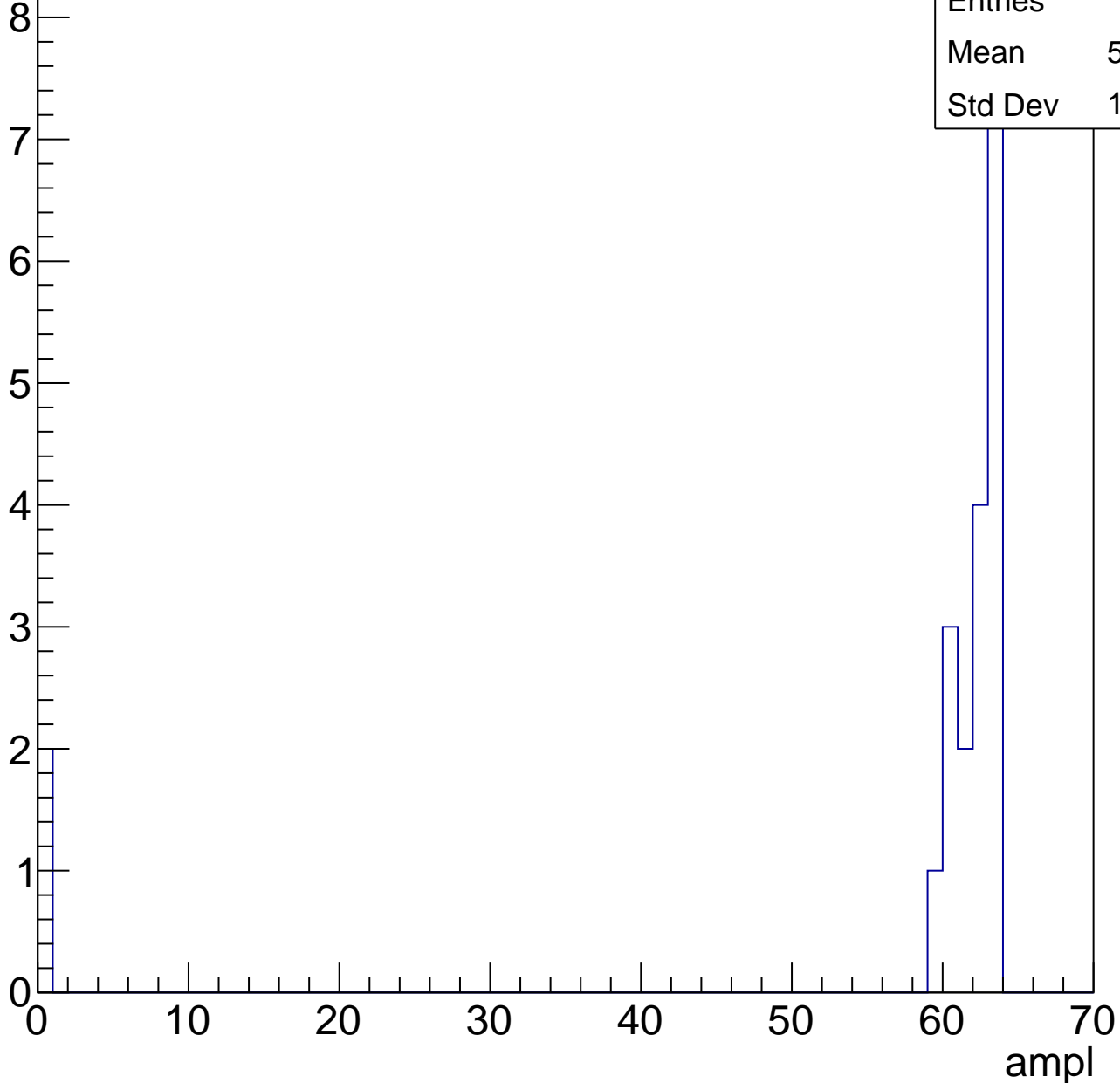
14

B0L101S, U22-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 55.65 |
| Std Dev | 18.59 |



B0L101S, U22-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

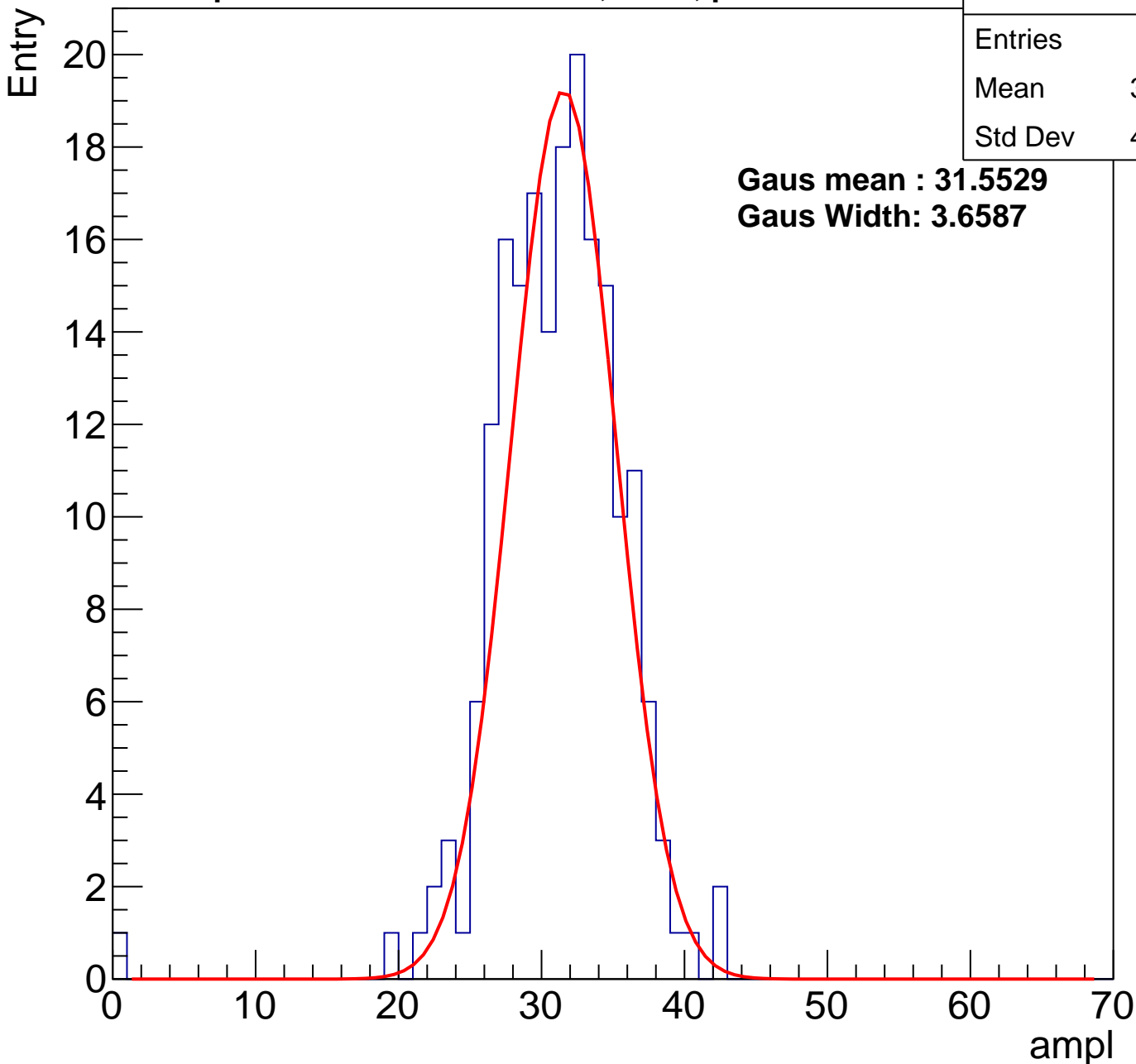
B0L101S, U22-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 192 |
| Mean | 30.65 |
| Std Dev | 4.552 |

Gaus mean : 31.5529

Gaus Width: 3.6587



B0L101S, U22-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

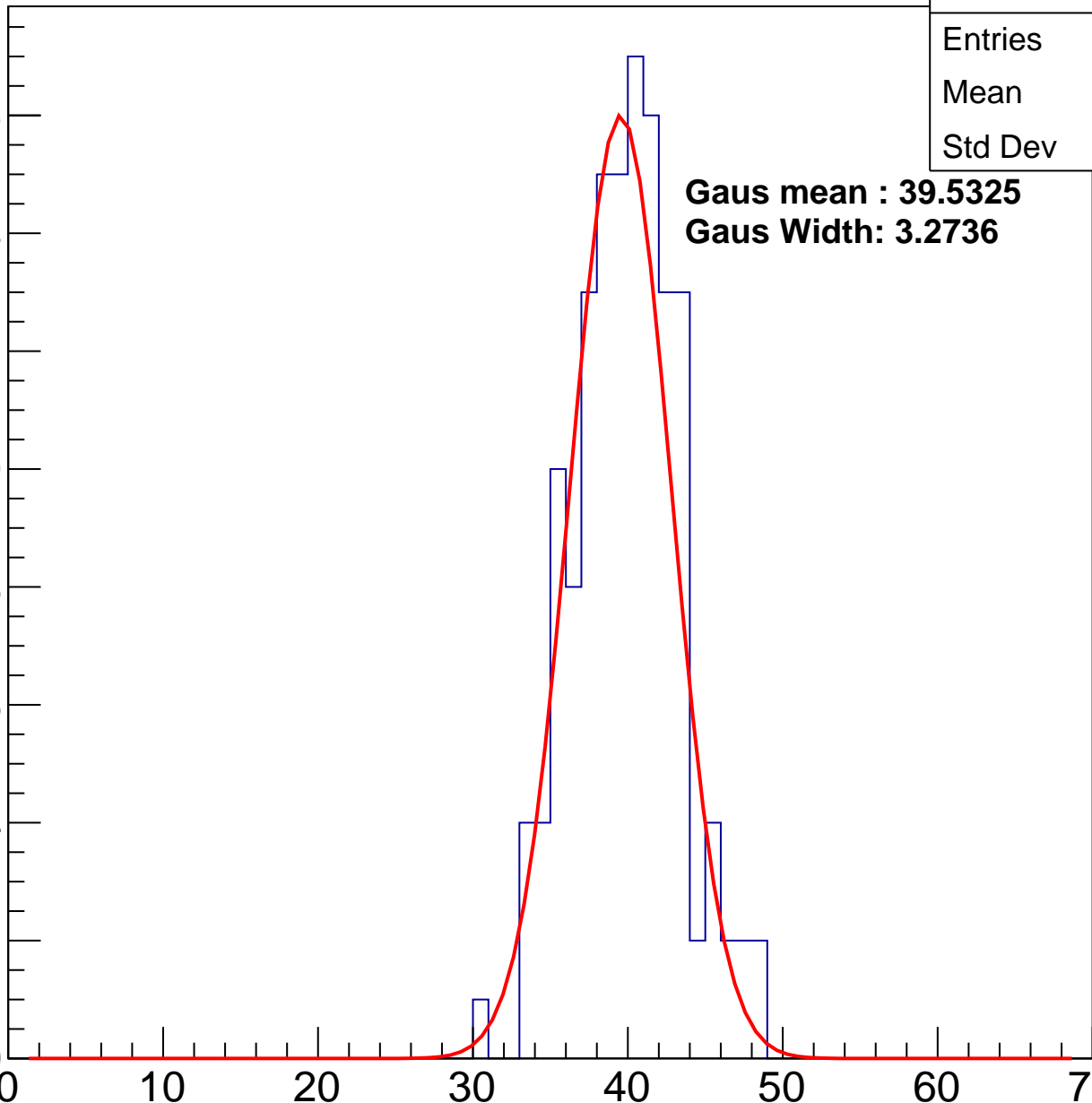
70

ampl

| | |
|---------|-------|
| Entries | 141 |
| Mean | 39.45 |
| Std Dev | 3.351 |

Gaus mean : 39.5325

Gaus Width: 3.2736



B0L101S, U22-ch31, adc2

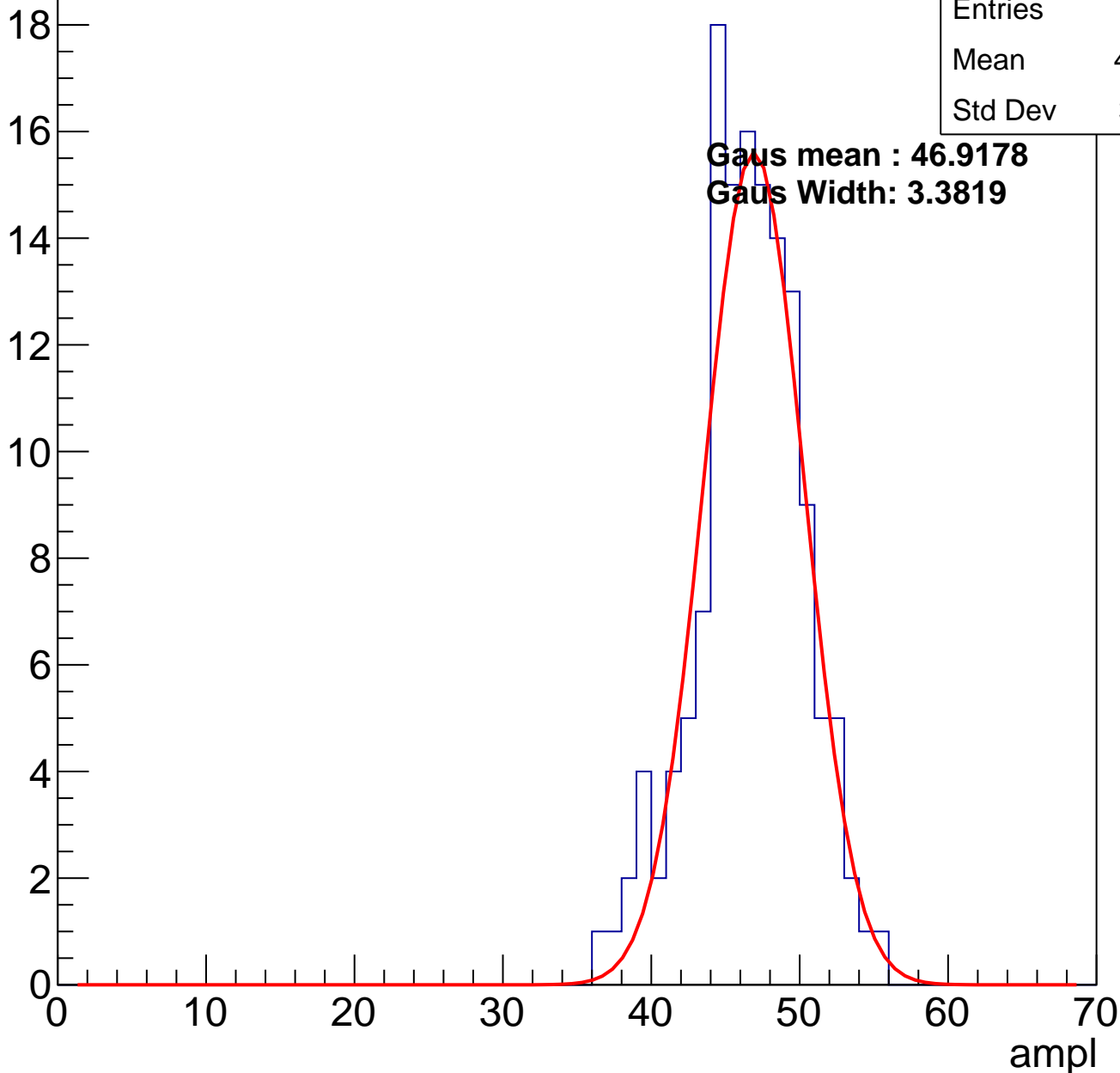
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 140 |
| Mean | 46.12 |
| Std Dev | 3.571 |

Gaus mean : 46.9178

Gaus Width: 3.3819

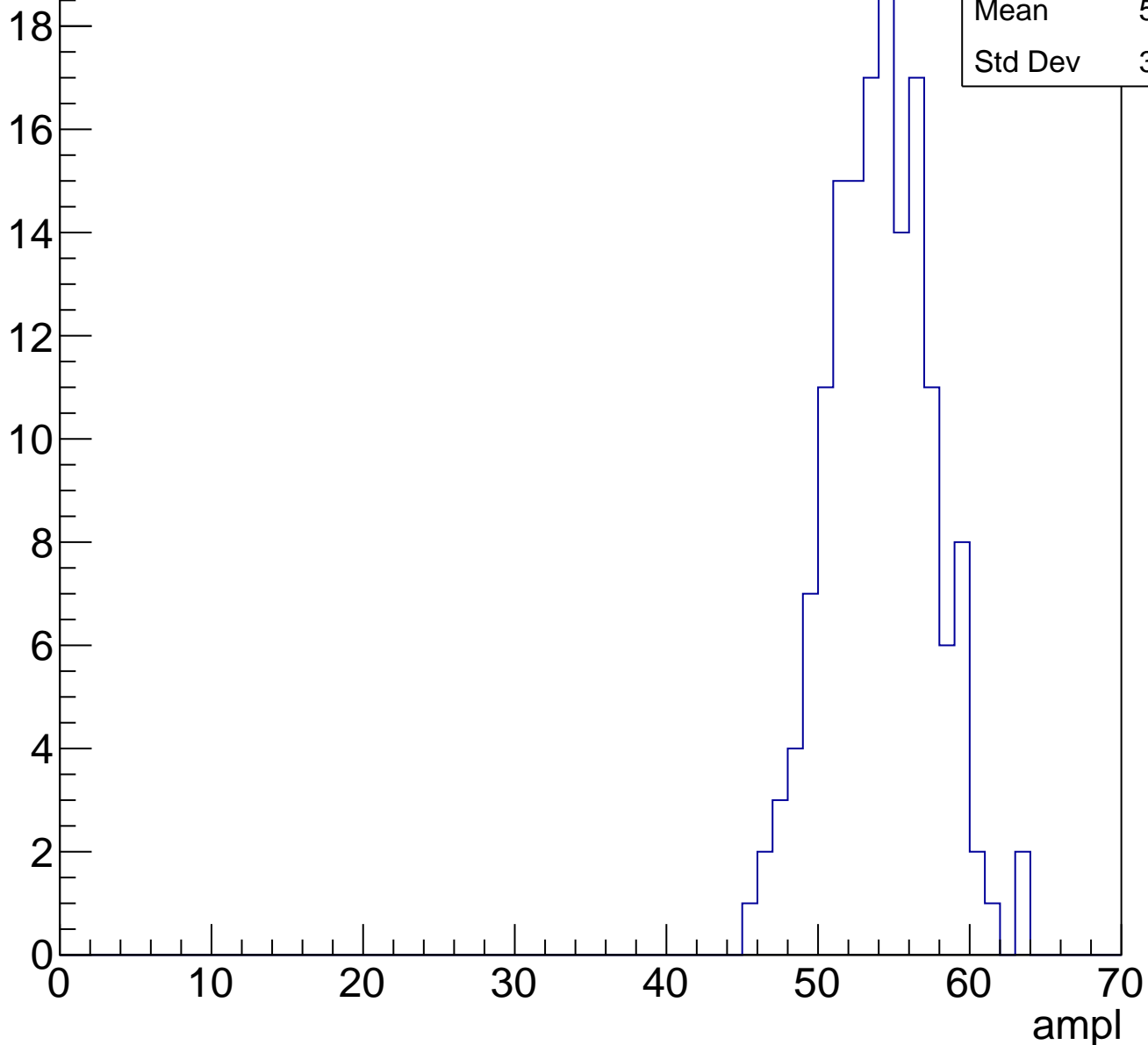
Entry



B0L101S, U22-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 155 |
| Mean | 53.62 |
| Std Dev | 3.408 |

B0L101S, U22-ch31, adc4

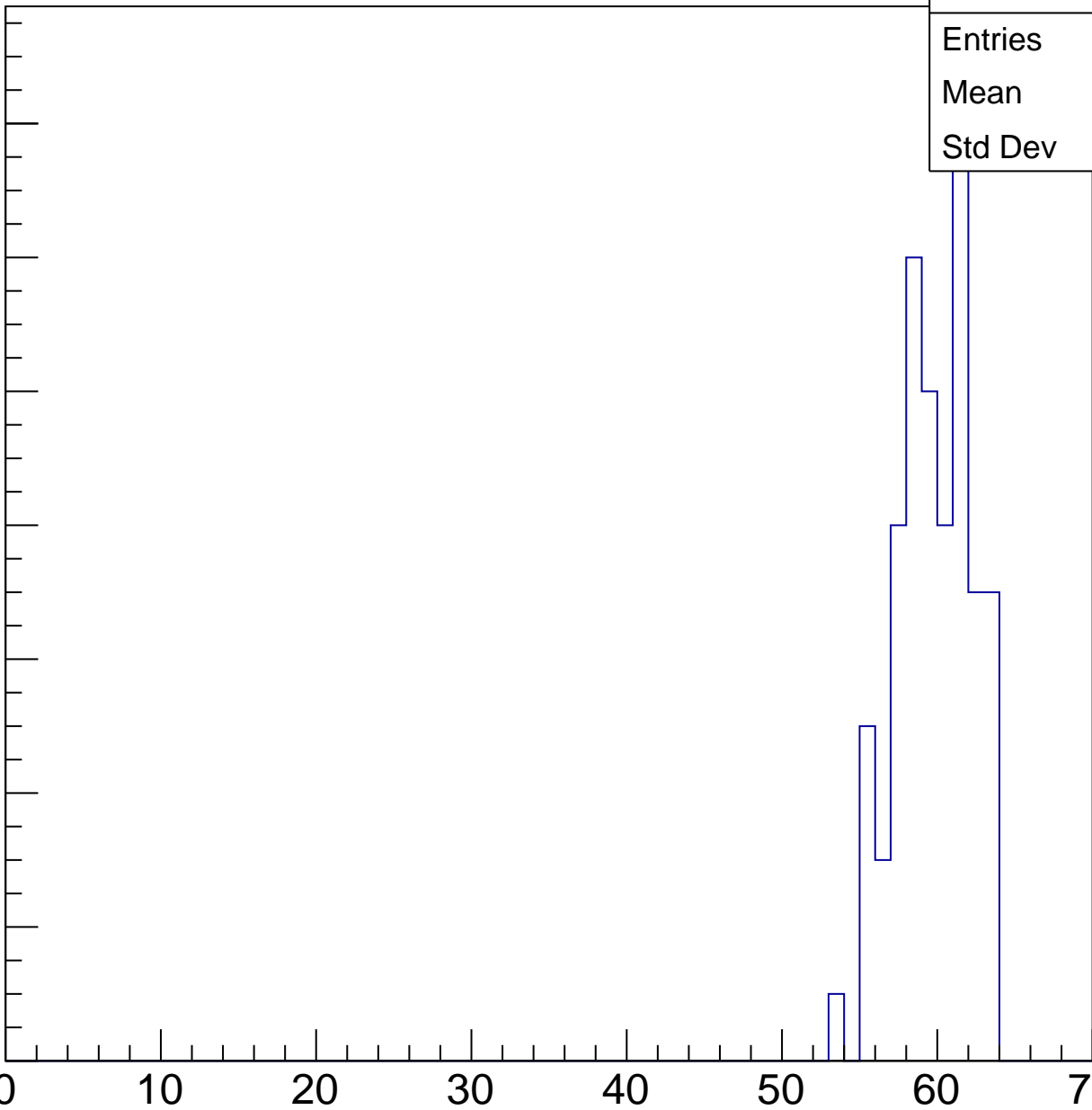
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 59.32 |
| Std Dev | 2.358 |

ampl



B0L101S, U22-ch31, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

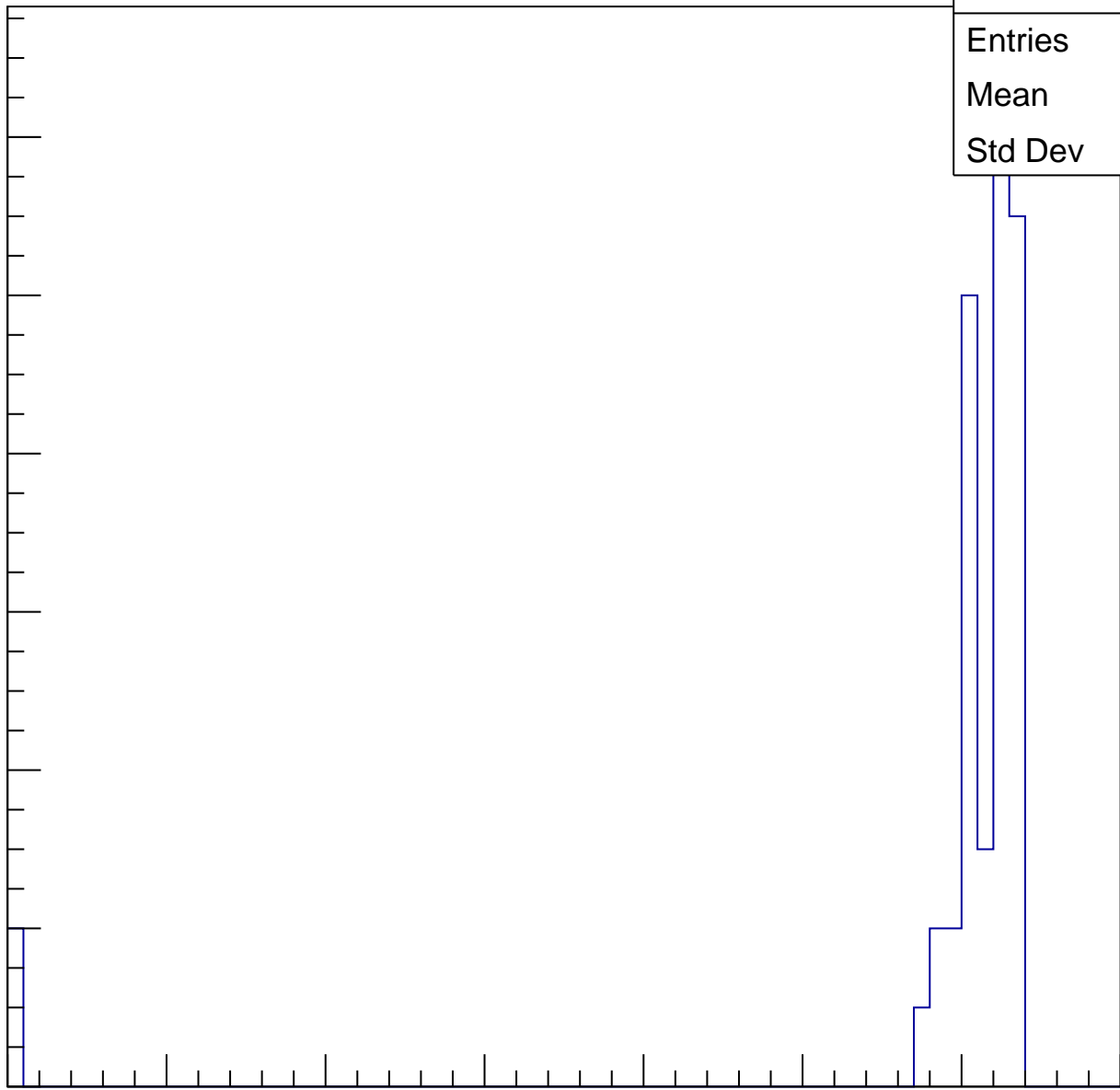
2

0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.48 |
| Std Dev | 12.85 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 31.11 |
| Std Dev | 3.54 |

Gaus mean : 31.2513

Gaus Width: 3.6831

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

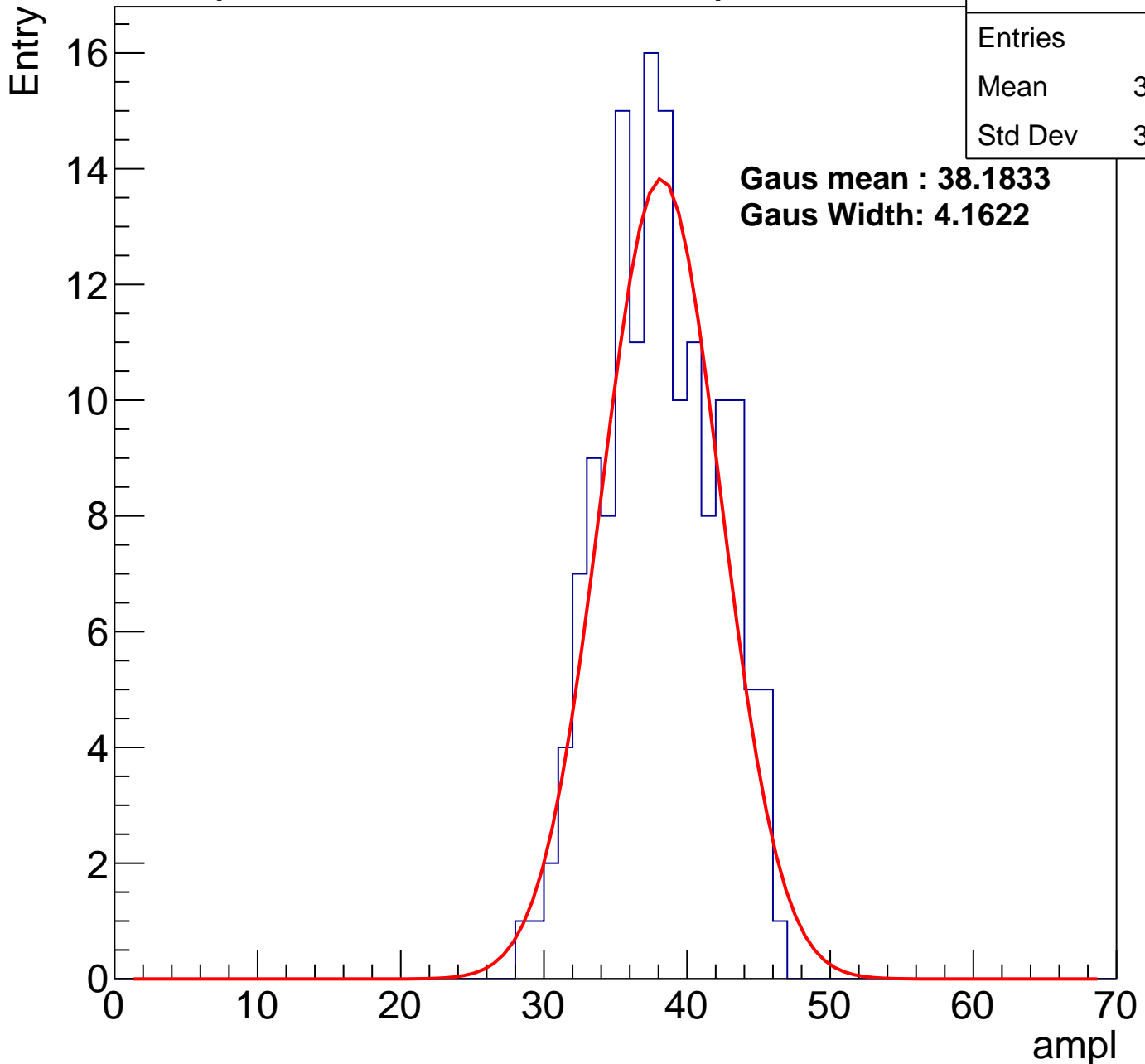
B0L101S, U22-ch32, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 37.69 |
| Std Dev | 3.942 |

Gaus mean : 38.1833

Gaus Width: 4.1622



B0L101S, U22-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 117 |
| Mean | 44.09 |
| Std Dev | 3.457 |

Gaus mean : 44.6734

Gaus Width: 3.5986

Entry

12

10

8

6

4

2

0

0

10

20

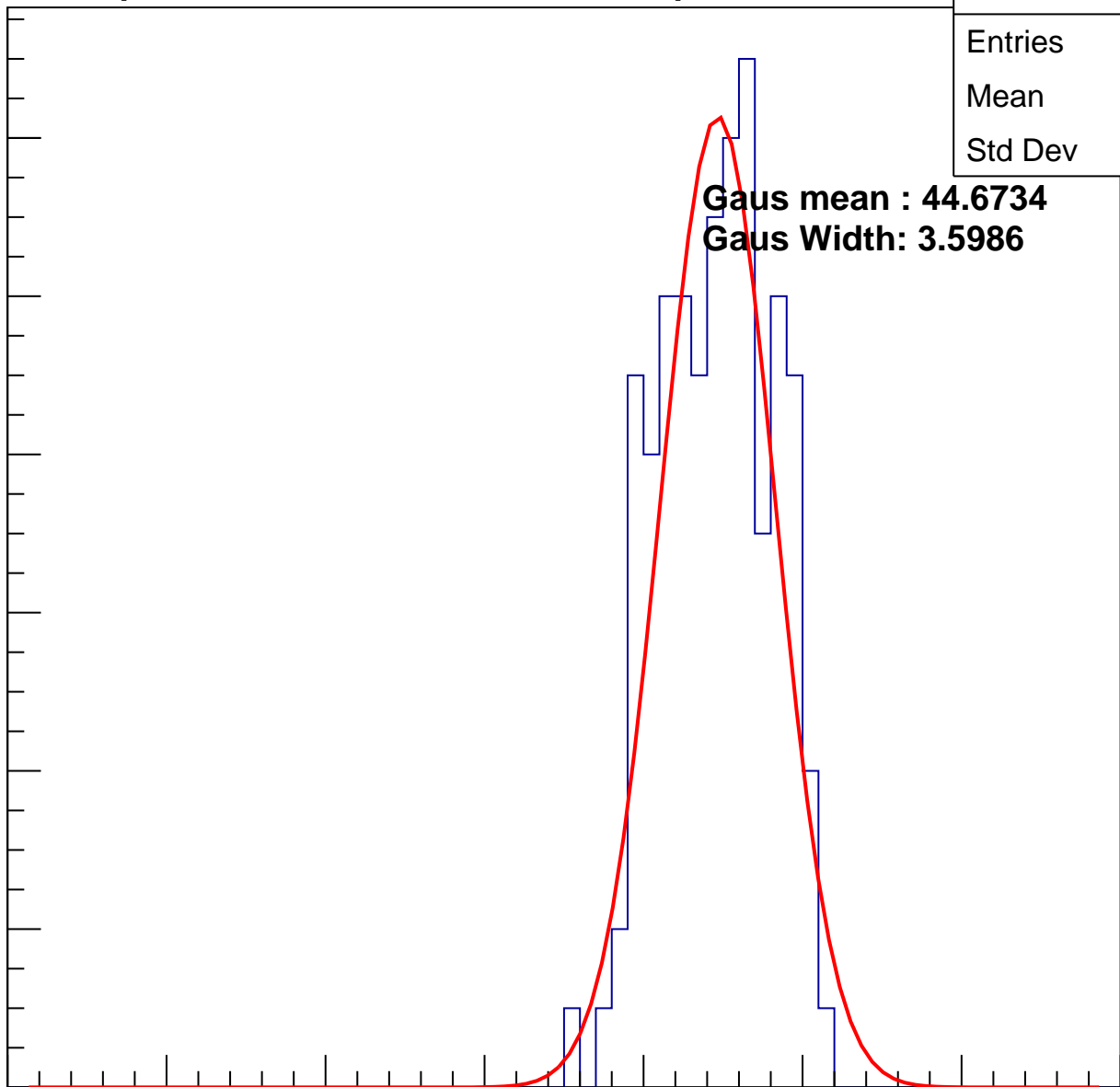
30

40

50

60

ampl



B0L101S, U22-ch32, adc3

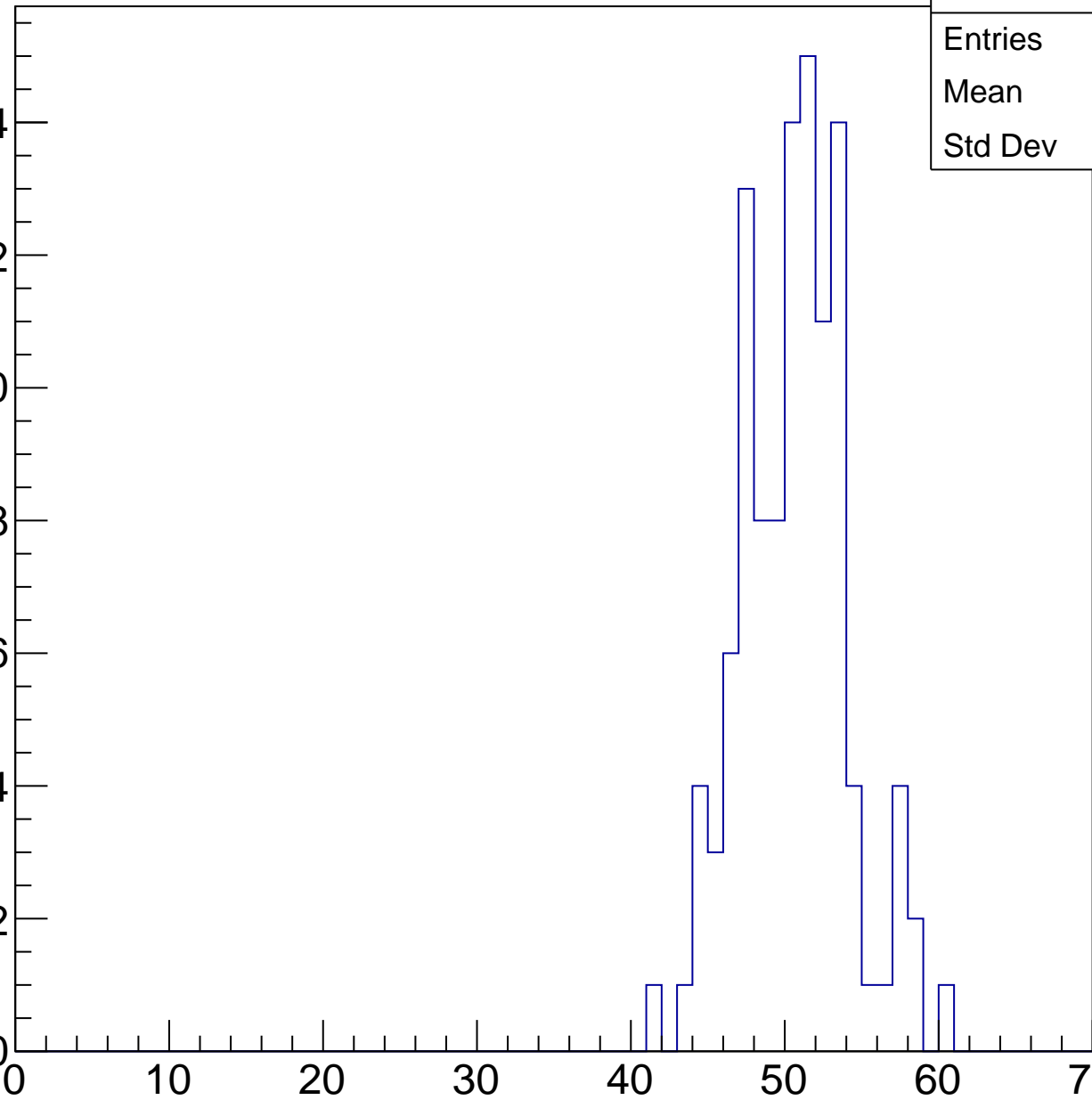
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 111 |
| Mean | 50.16 |
| Std Dev | 3.466 |

Entry

14
12
10
8
6
4
2
0

ampl

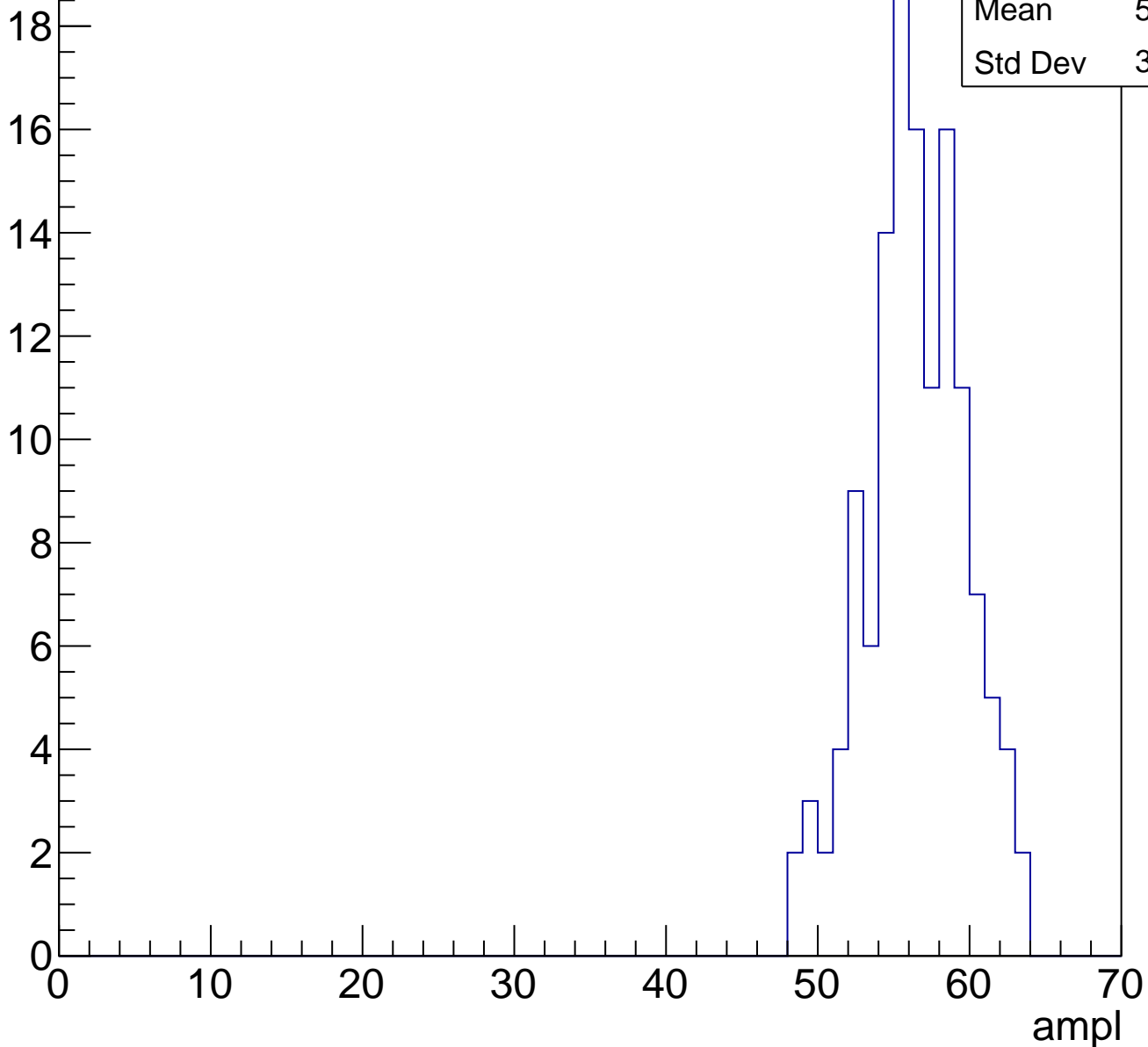


B0L101S, U22-ch32, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 55.98 |
| Std Dev | 3.237 |

Entry



B0L101S, U22-ch32, adc5

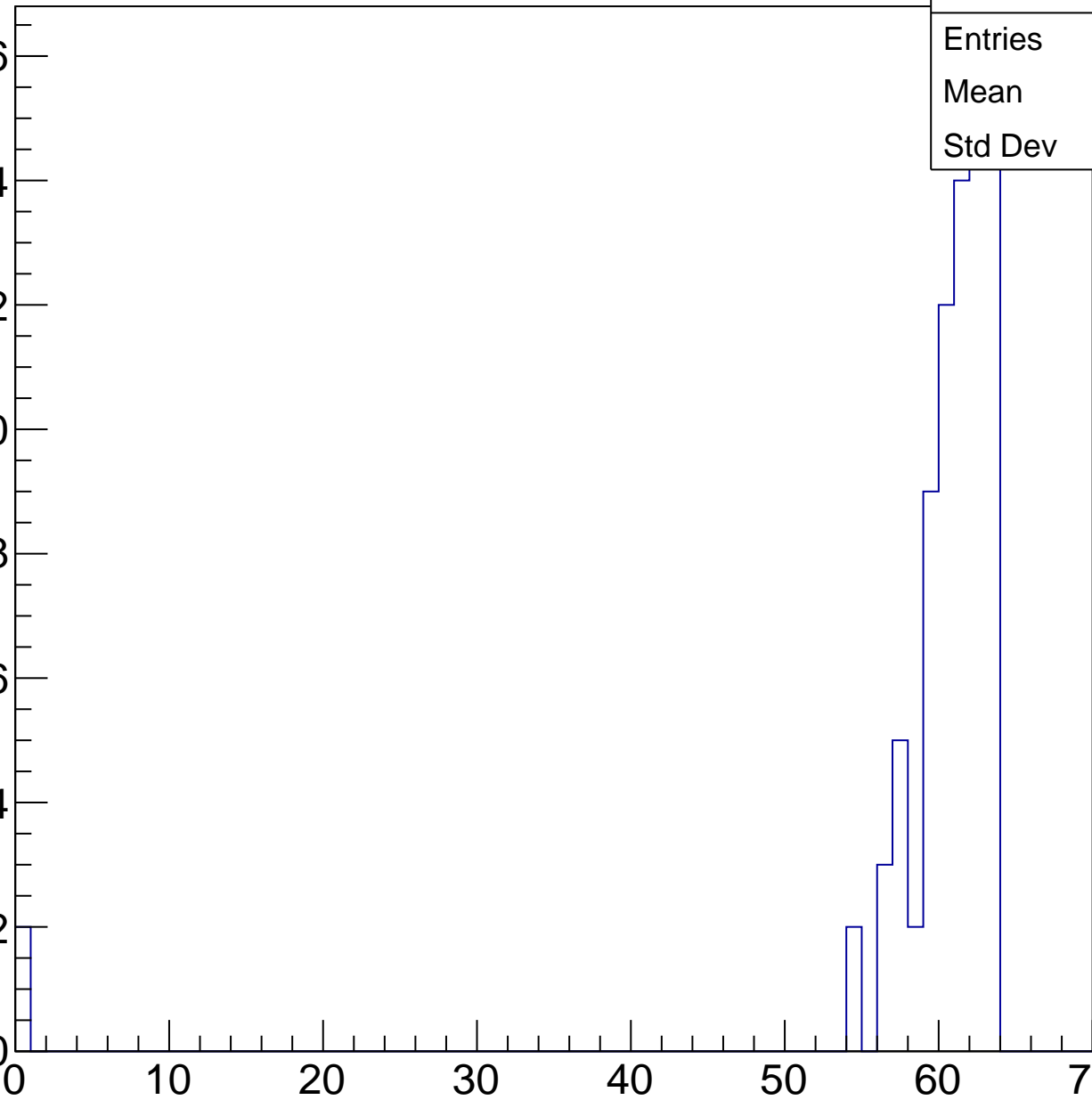
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 80 |
| Mean | 59 |
| Std Dev | 9.695 |

ampl



B0L101S, U22-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

128

Mean

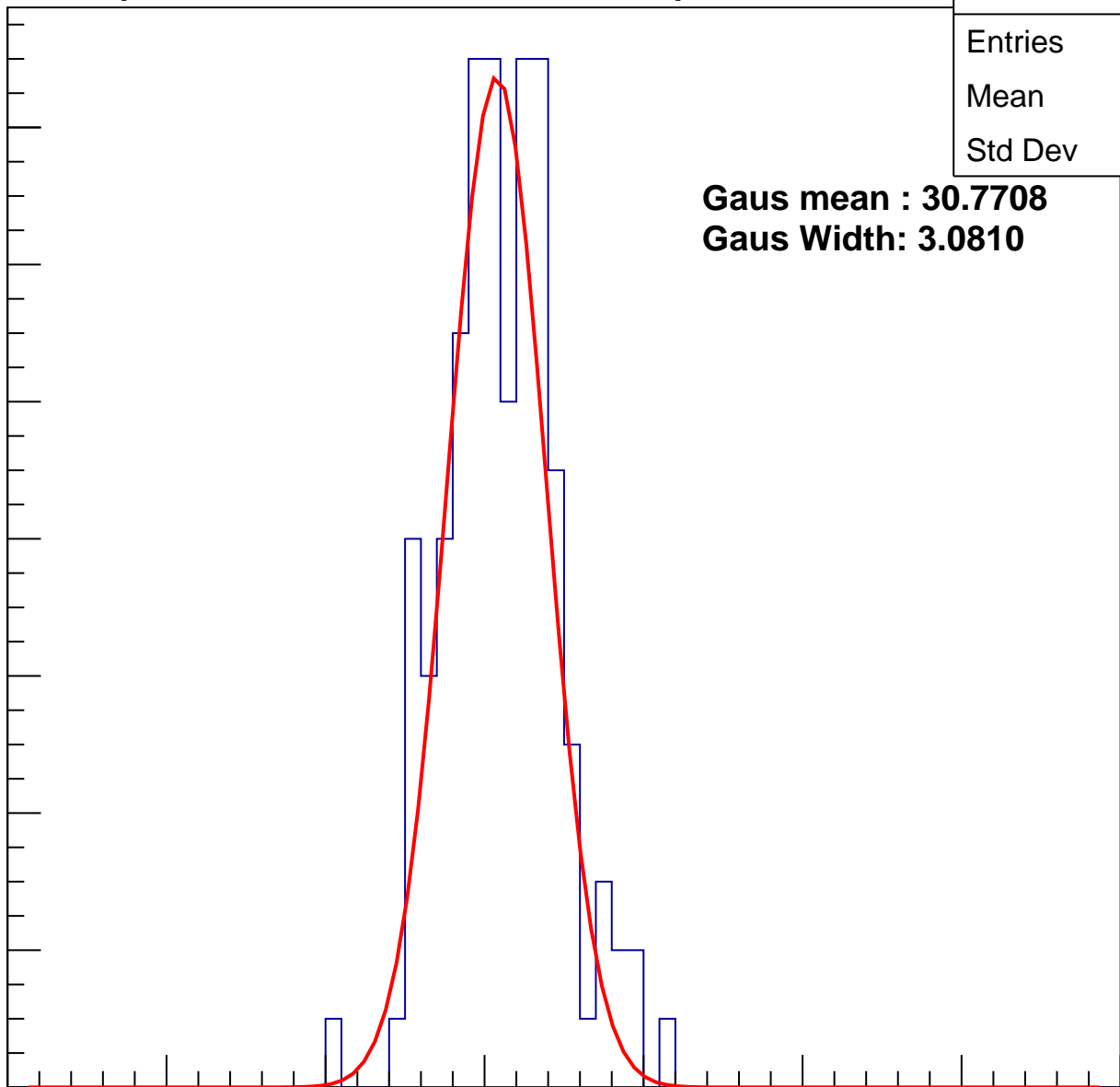
30.6

Std Dev

3.519

Gaus mean : 30.7708

Gaus Width: 3.0810

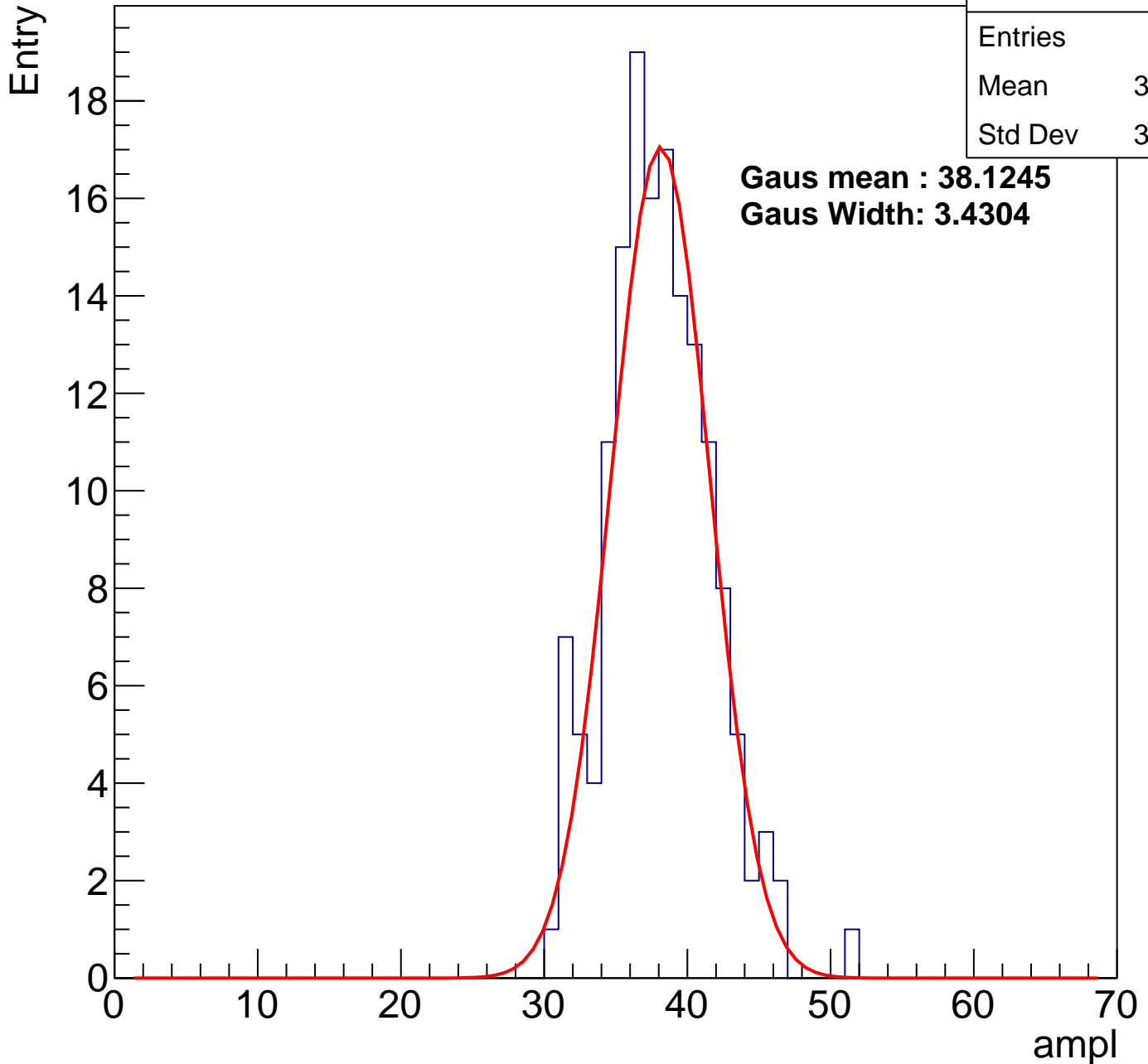


B0L101S, U22-ch33, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 154 |
| Mean | 37.62 |
| Std Dev | 3.606 |

Gaus mean : 38.1245
Gaus Width: 3.4304



B0L101S, U22-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 149 |
| Mean | 45.49 |
| Std Dev | 3.531 |

Gaus mean : 46.1469

Gaus Width: 3.7577

0

2

4

6

8

10

12

14

B0L101S, U22-ch33, adc3

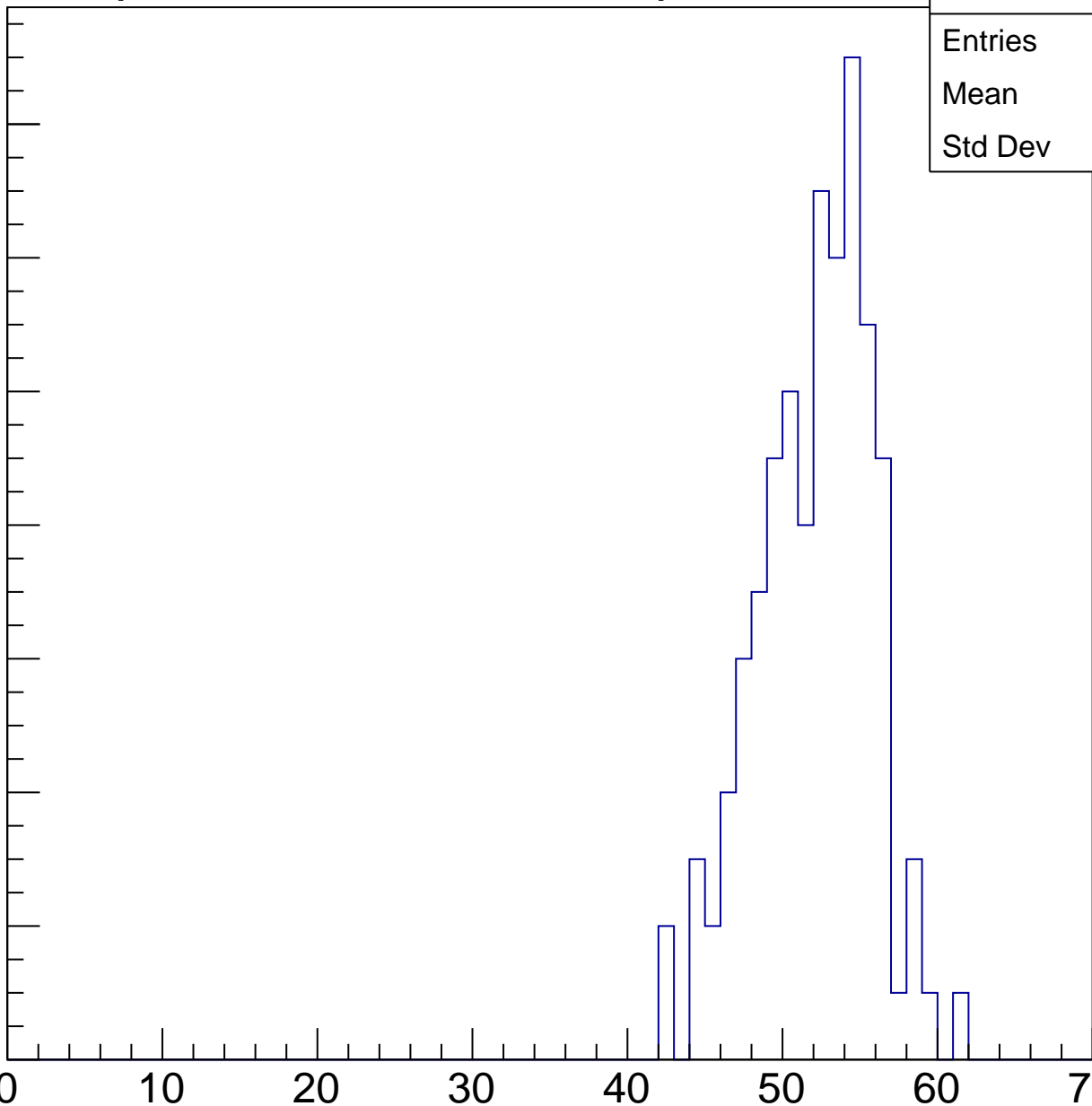
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 117 |
| Mean | 51.62 |
| Std Dev | 3.681 |

ampl



B0L101S, U22-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

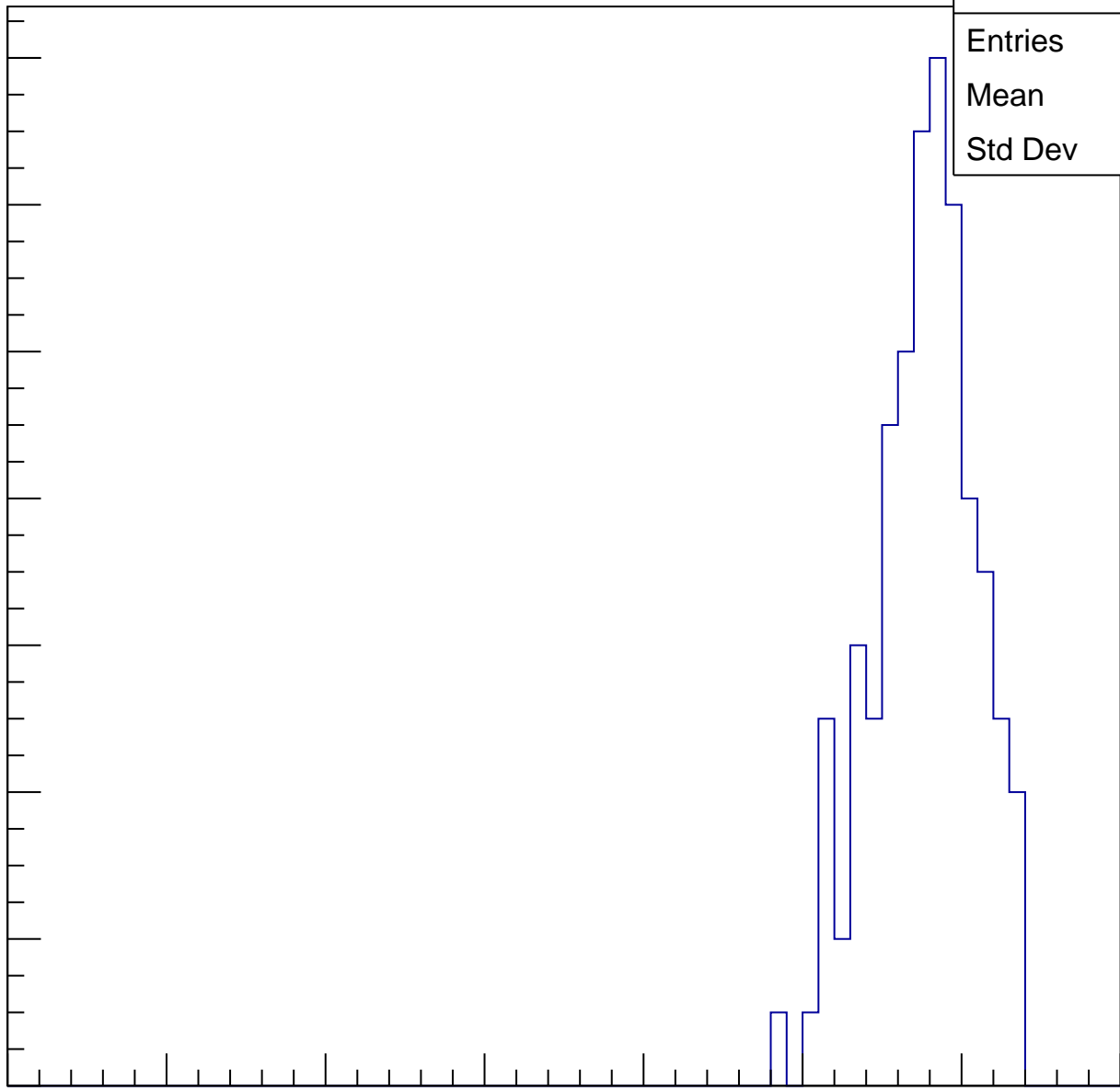
| | |
|---------|-------|
| Entries | 102 |
| Mean | 57.16 |
| Std Dev | 3.223 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch33, adc5

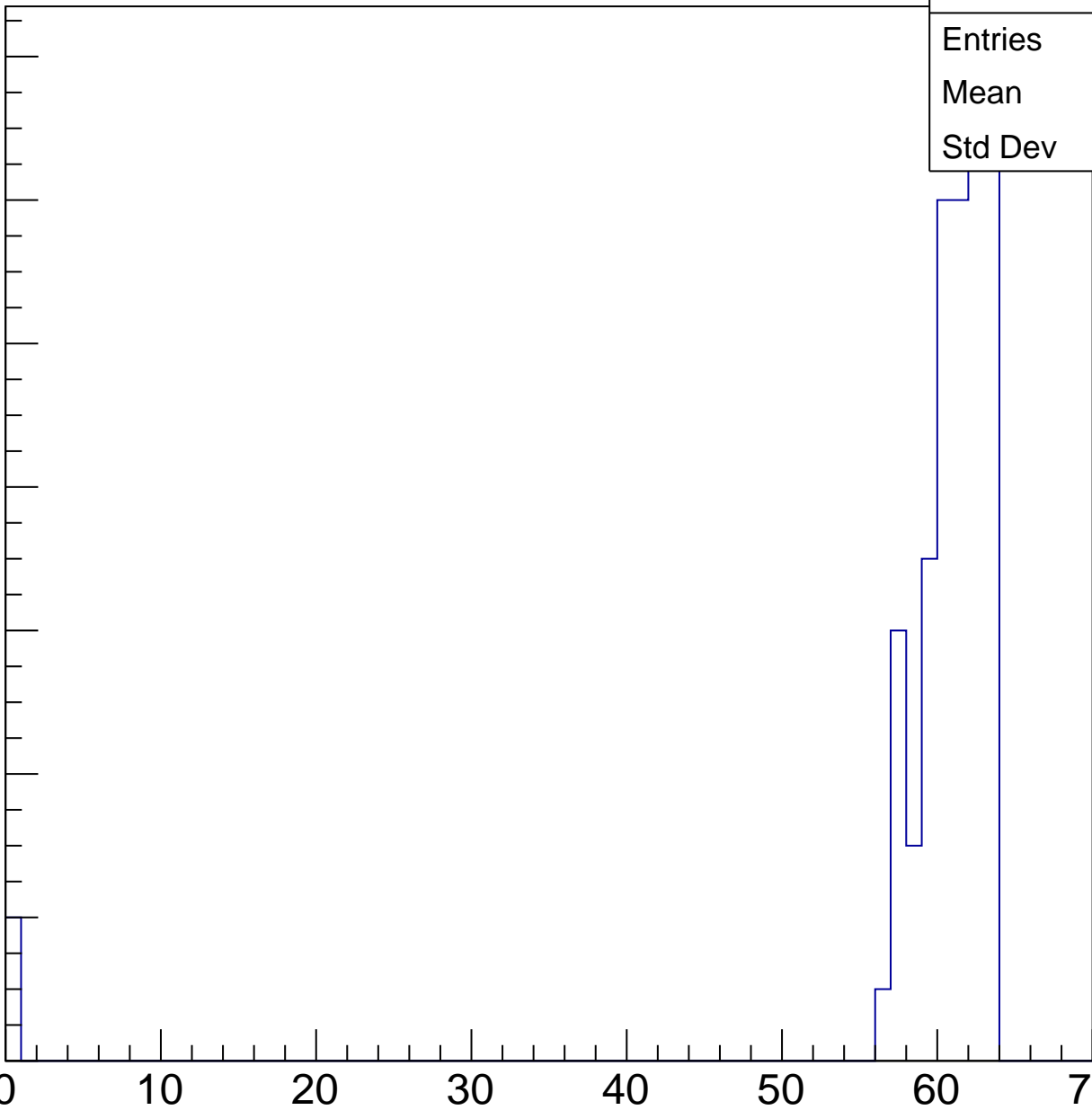
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 58.97 |
| Std Dev | 10.21 |

ampl



B0L101S, U22-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B0L101S, U22-ch34, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 29.18 |
| Std Dev | 4.393 |

Gaus mean : 29.8184

Gaus Width: 3.7116

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

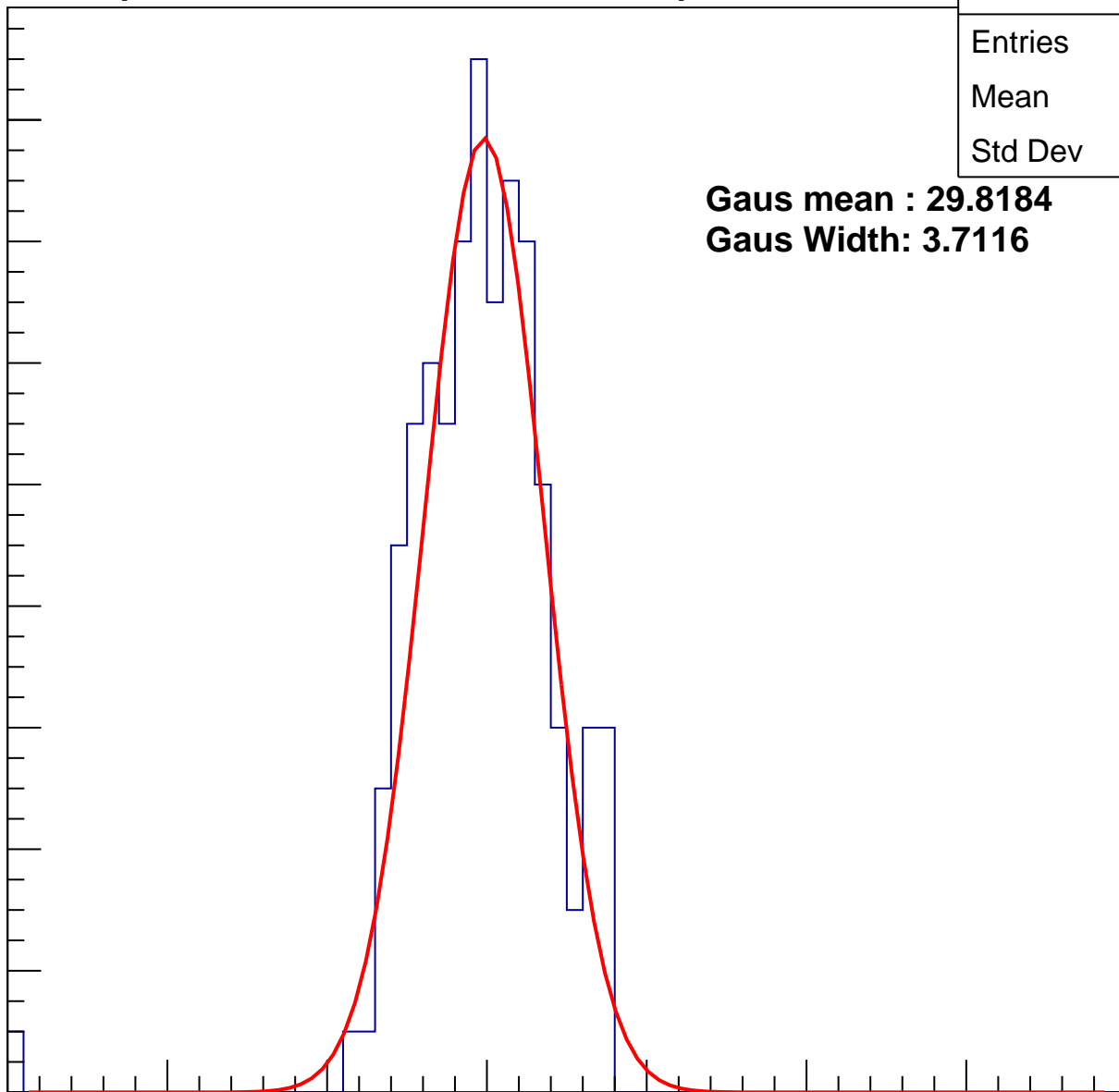
40

50

60

70

ampl



B0L101S, U22-ch34, adc1

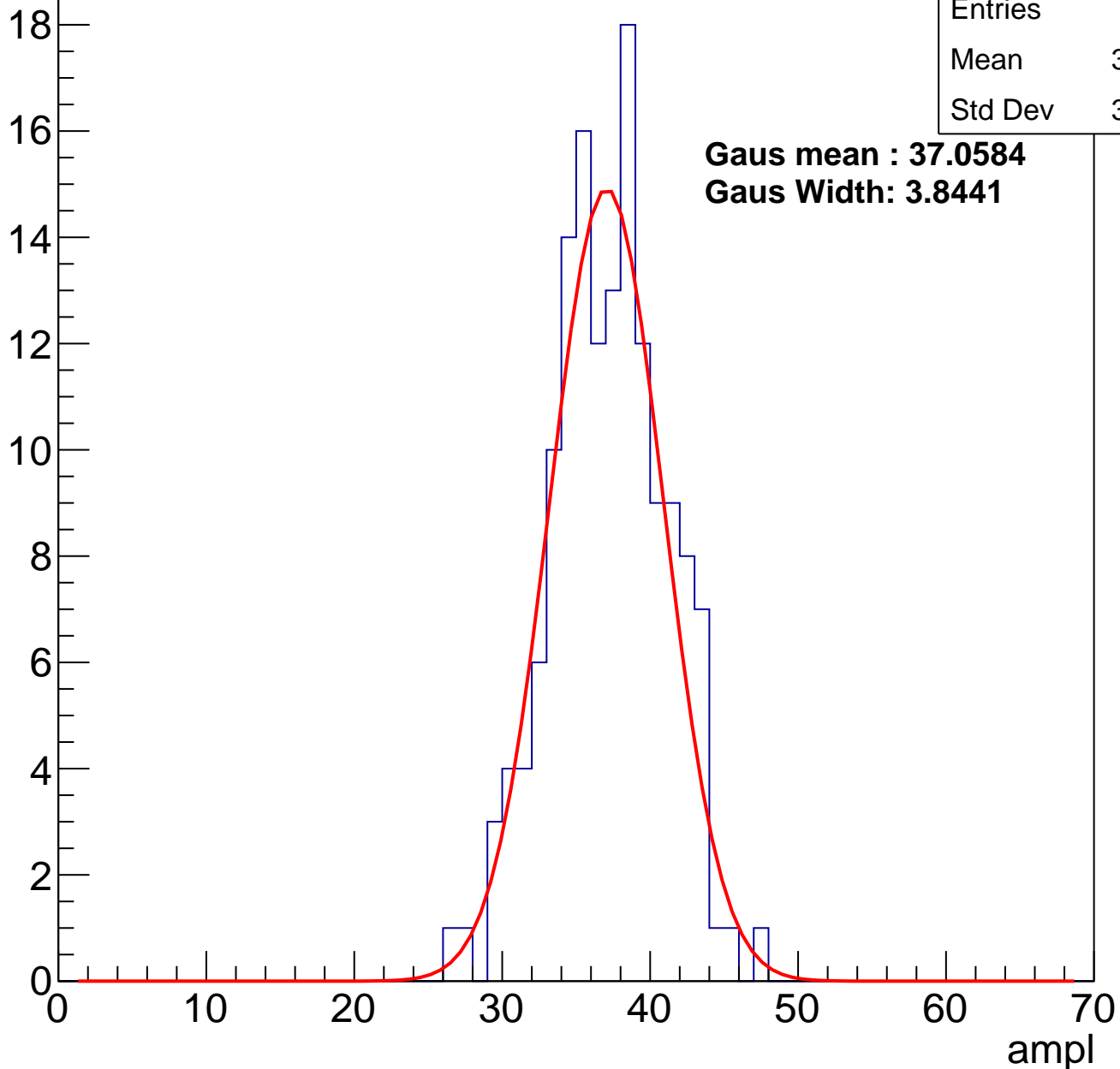
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 36.73 |
| Std Dev | 3.838 |

Gaus mean : 37.0584

Gaus Width: 3.8441

Entry



B0L101S, U22-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 112 |
| Mean | 42.93 |
| Std Dev | 5.25 |

Gaus mean : 44.0389

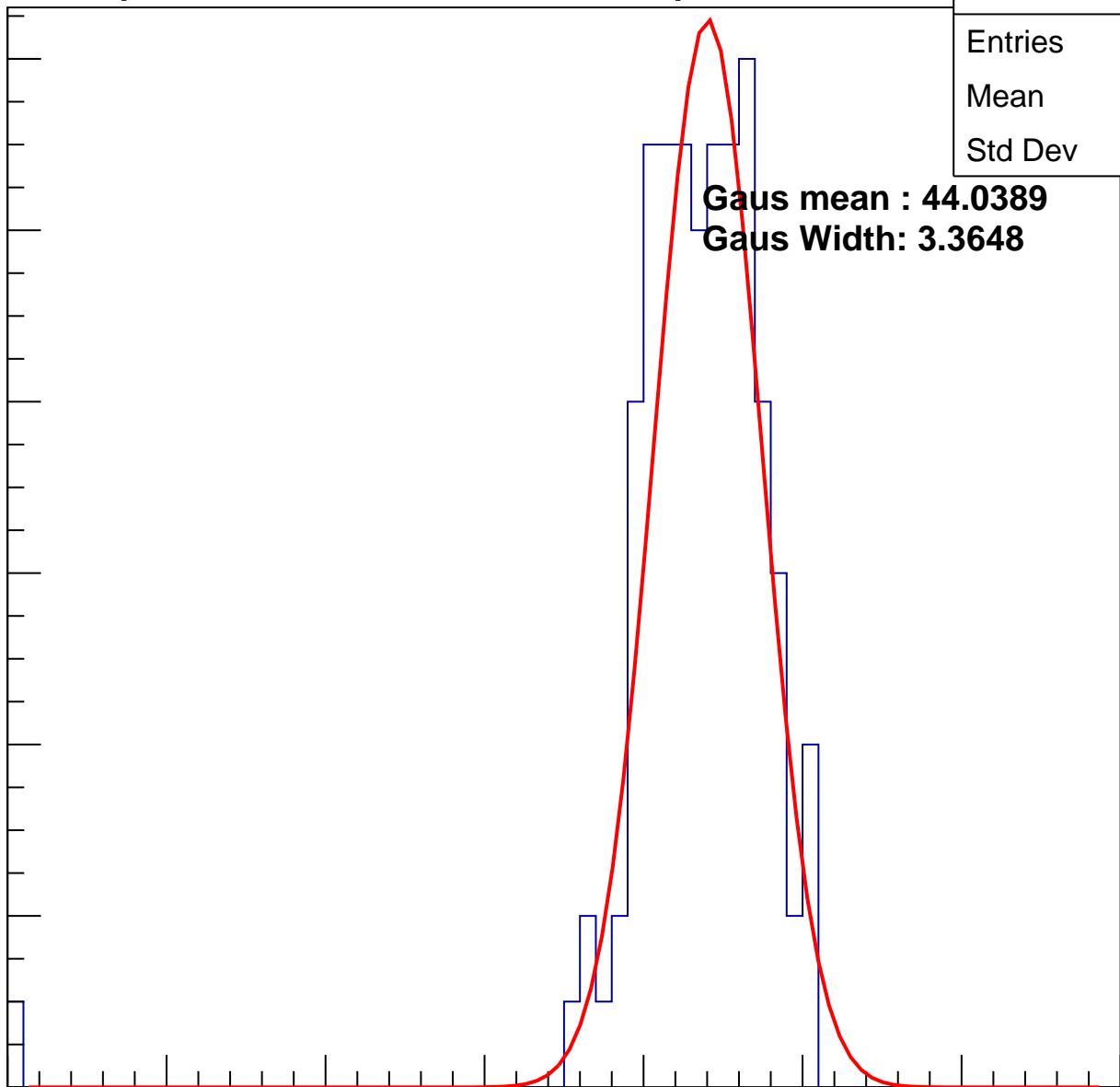
Gaus Width: 3.3648

Entry

12
10
8
6
4
2
0

ampl

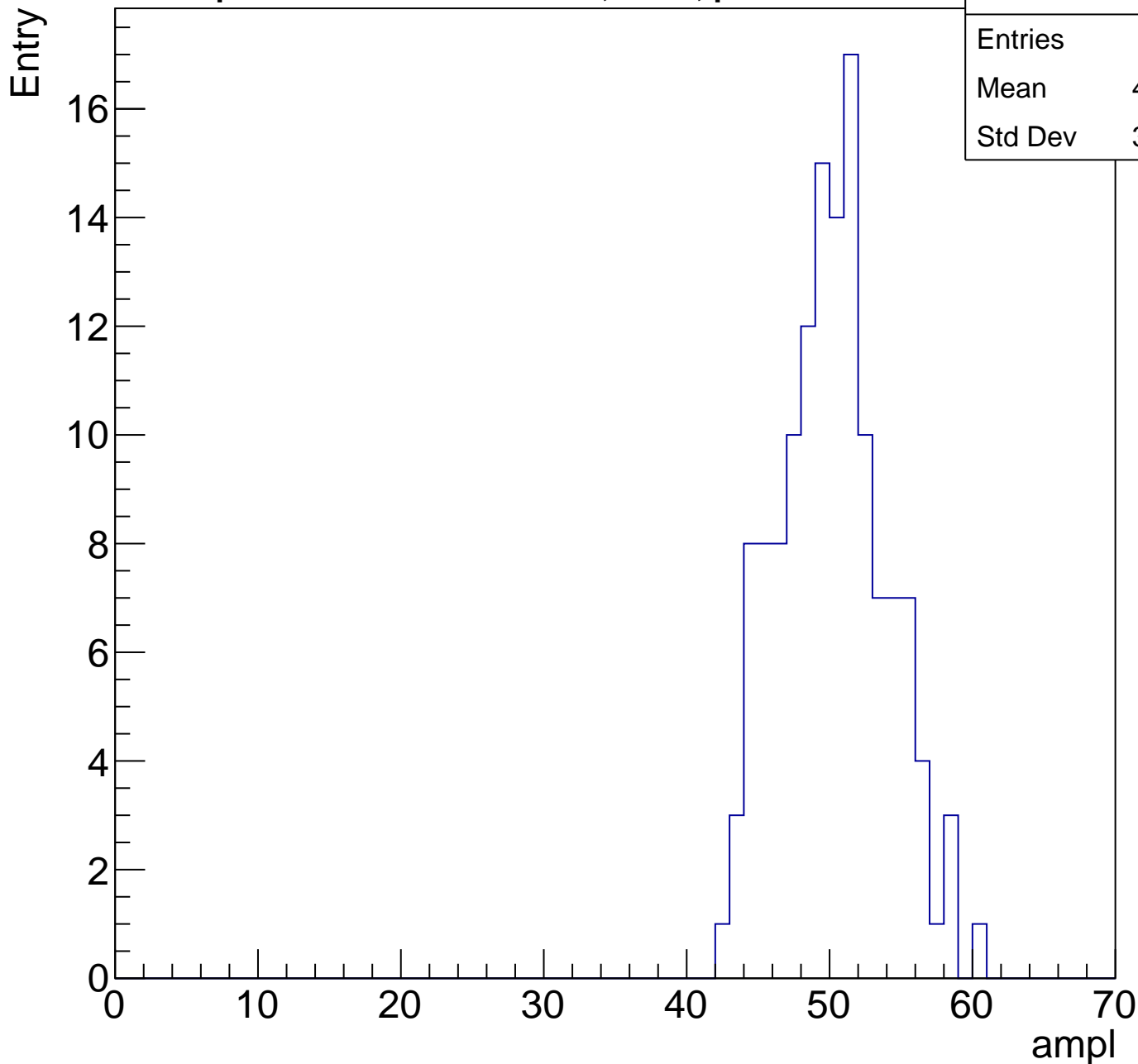
0 10 20 30 40 50 60 70



B0L101S, U22-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 49.76 |
| Std Dev | 3.703 |

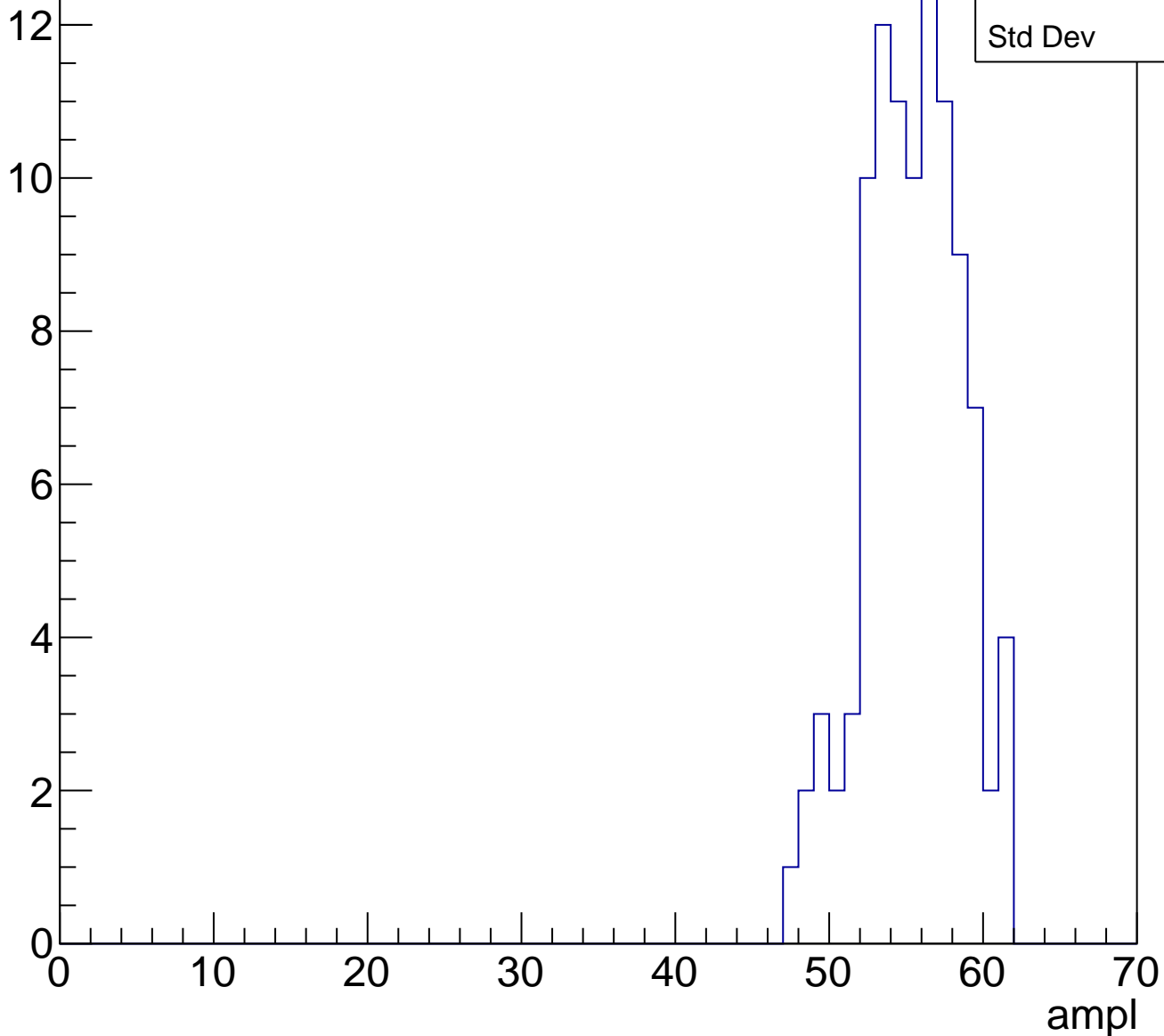


B0L101S, U22-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 100 |
| Mean | 54.97 |
| Std Dev | 3.1 |

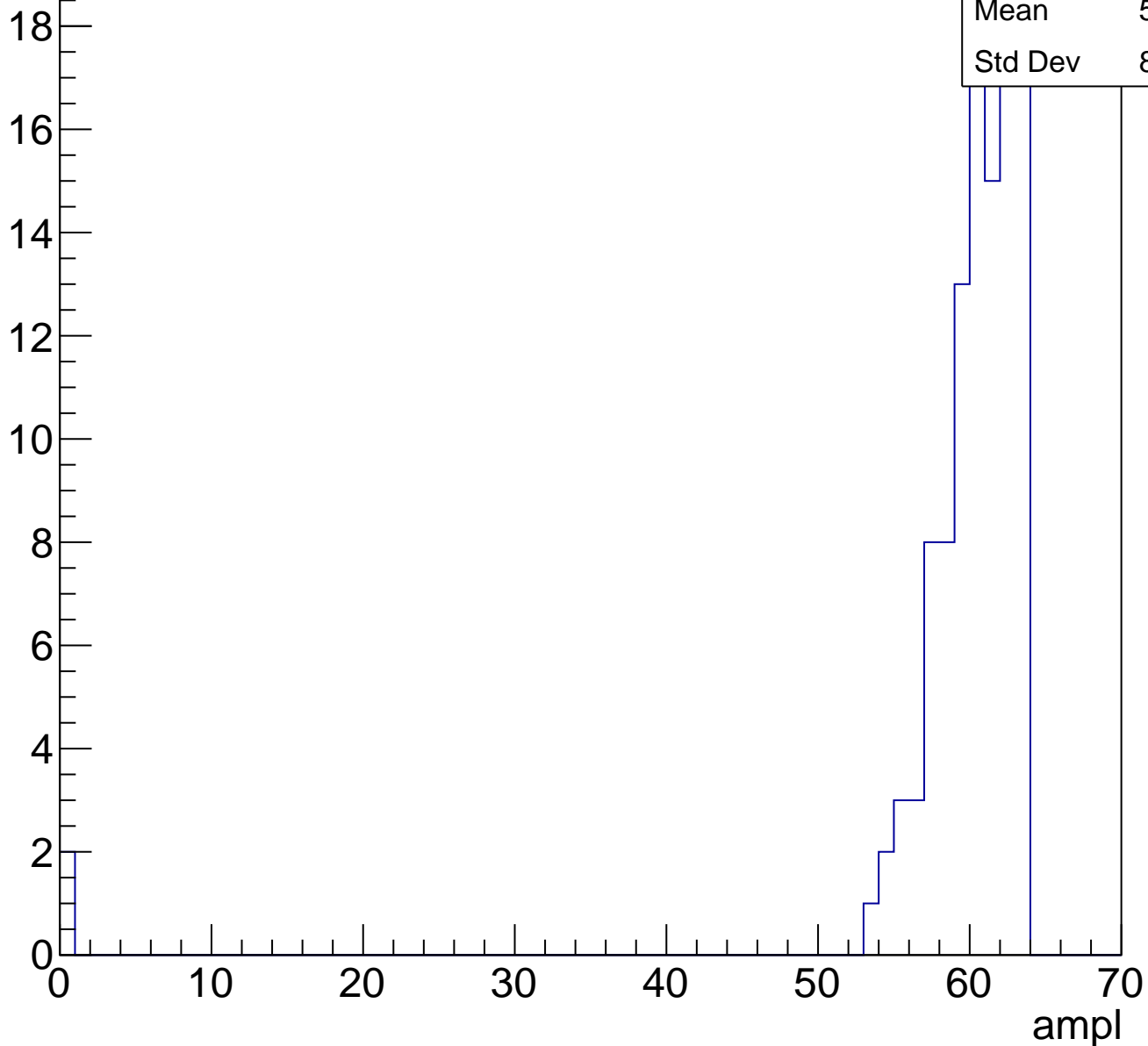
Entry



B0L101S, U22-ch34, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

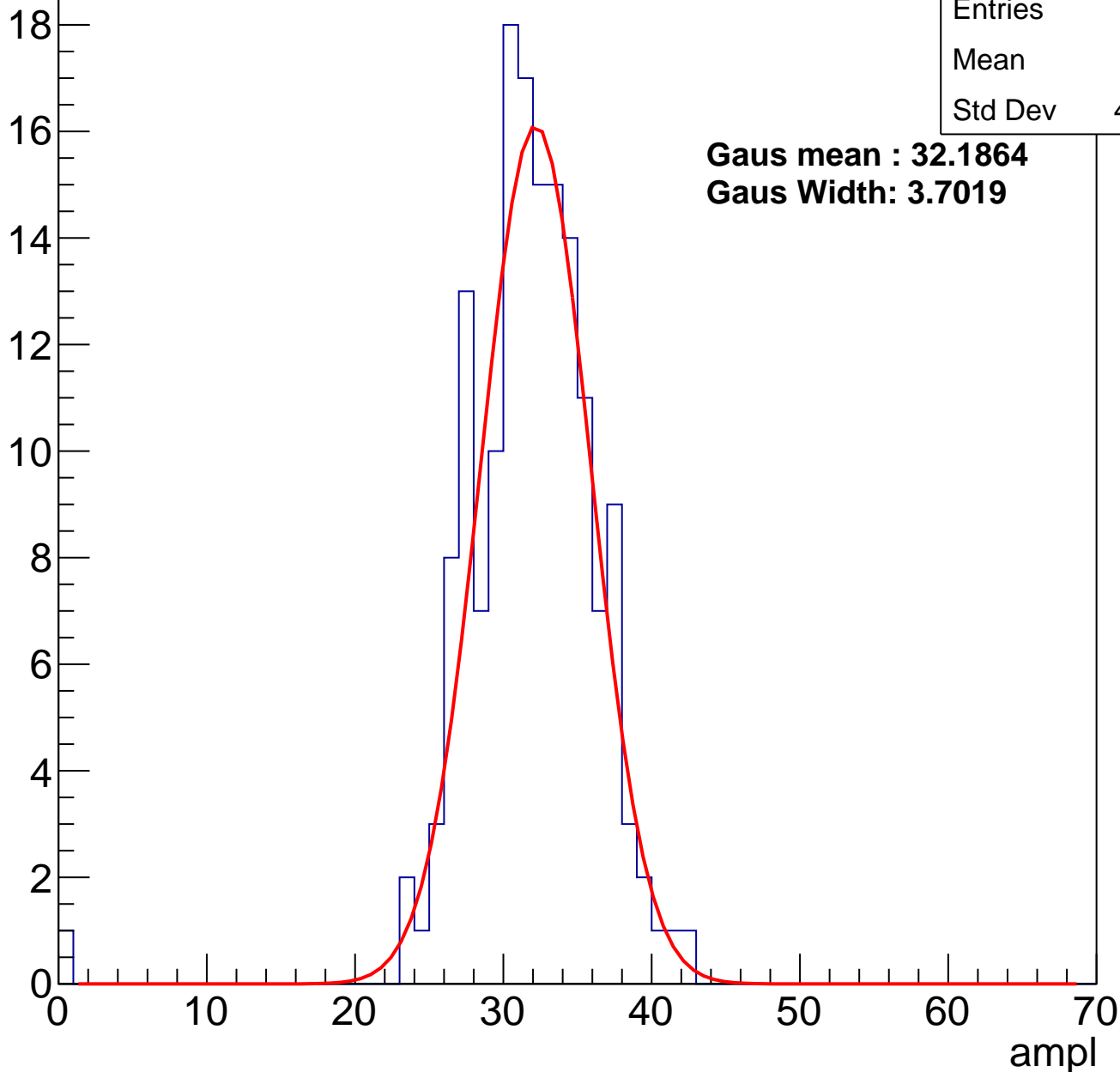
Entry



B0L101S, U22-ch35, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



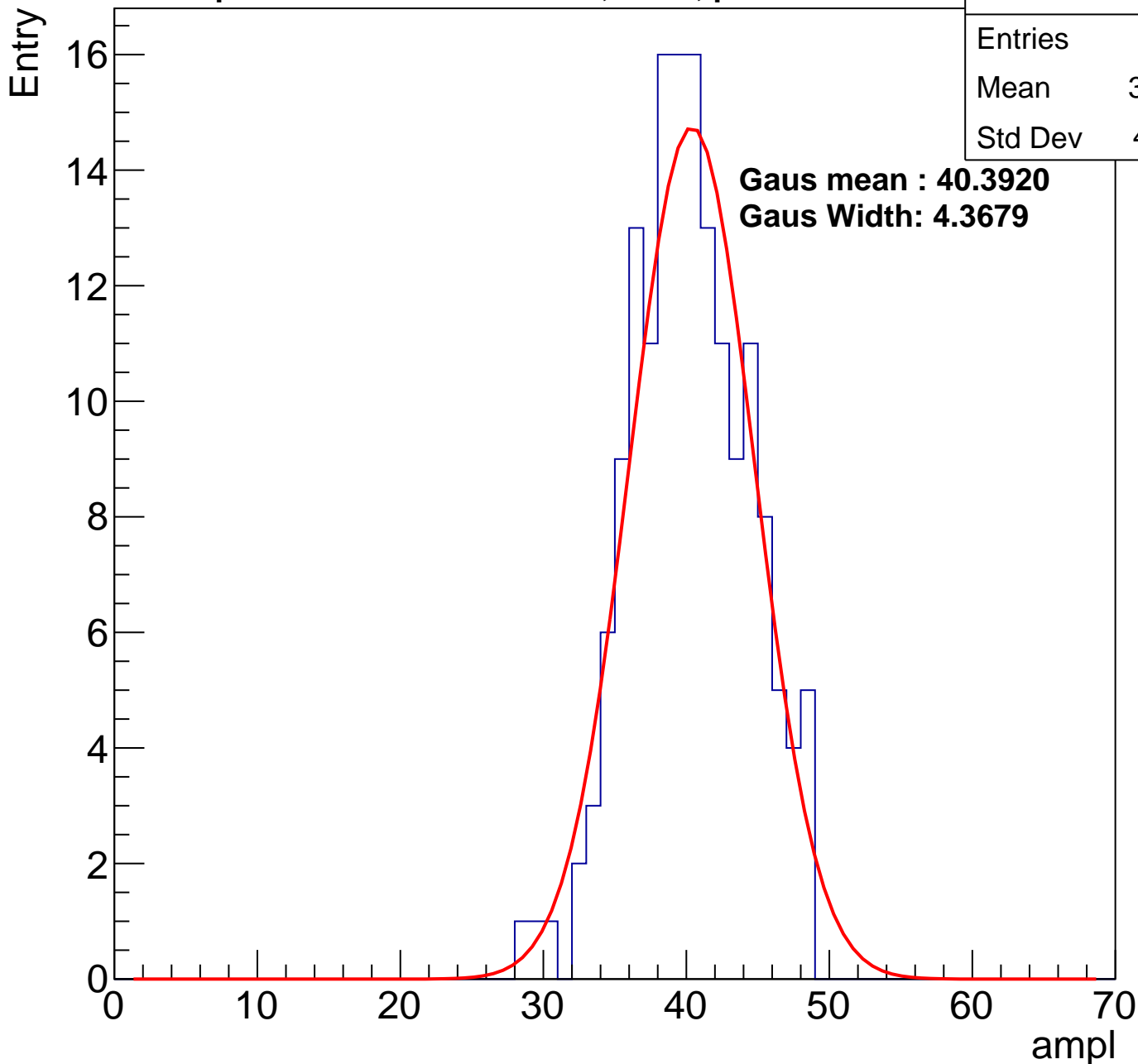
B0L101S, U22-ch35, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 39.75 |
| Std Dev | 4.071 |

Gaus mean : 40.3920

Gaus Width: 4.3679



B0L101S, U22-ch35, adc2

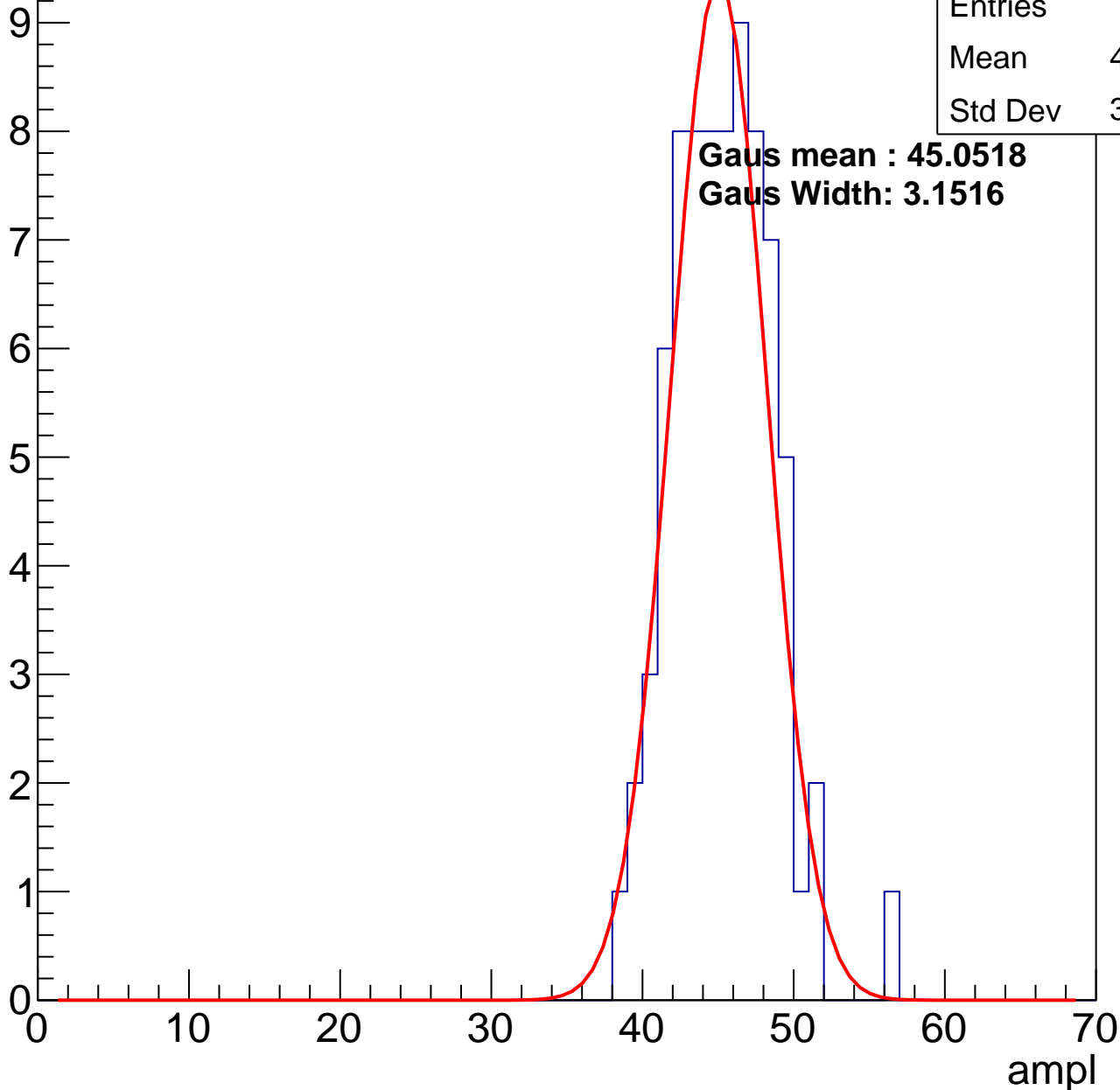
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 44.84 |
| Std Dev | 3.232 |

Gaus mean : 45.0518

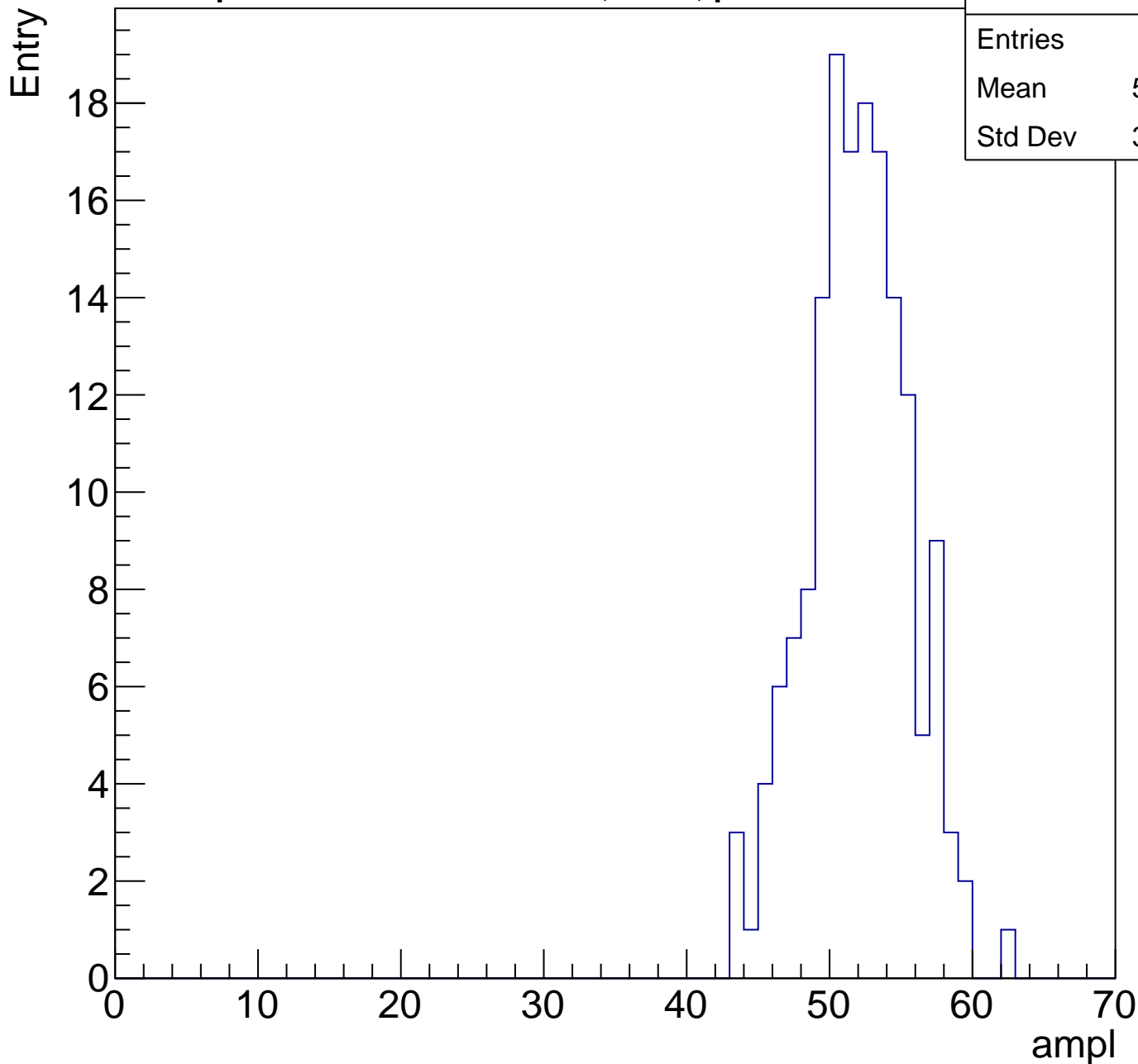
Gaus Width: 3.1516



B0L101S, U22-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 160 |
| Mean | 51.53 |
| Std Dev | 3.534 |



B0L101S, U22-ch35, adc4

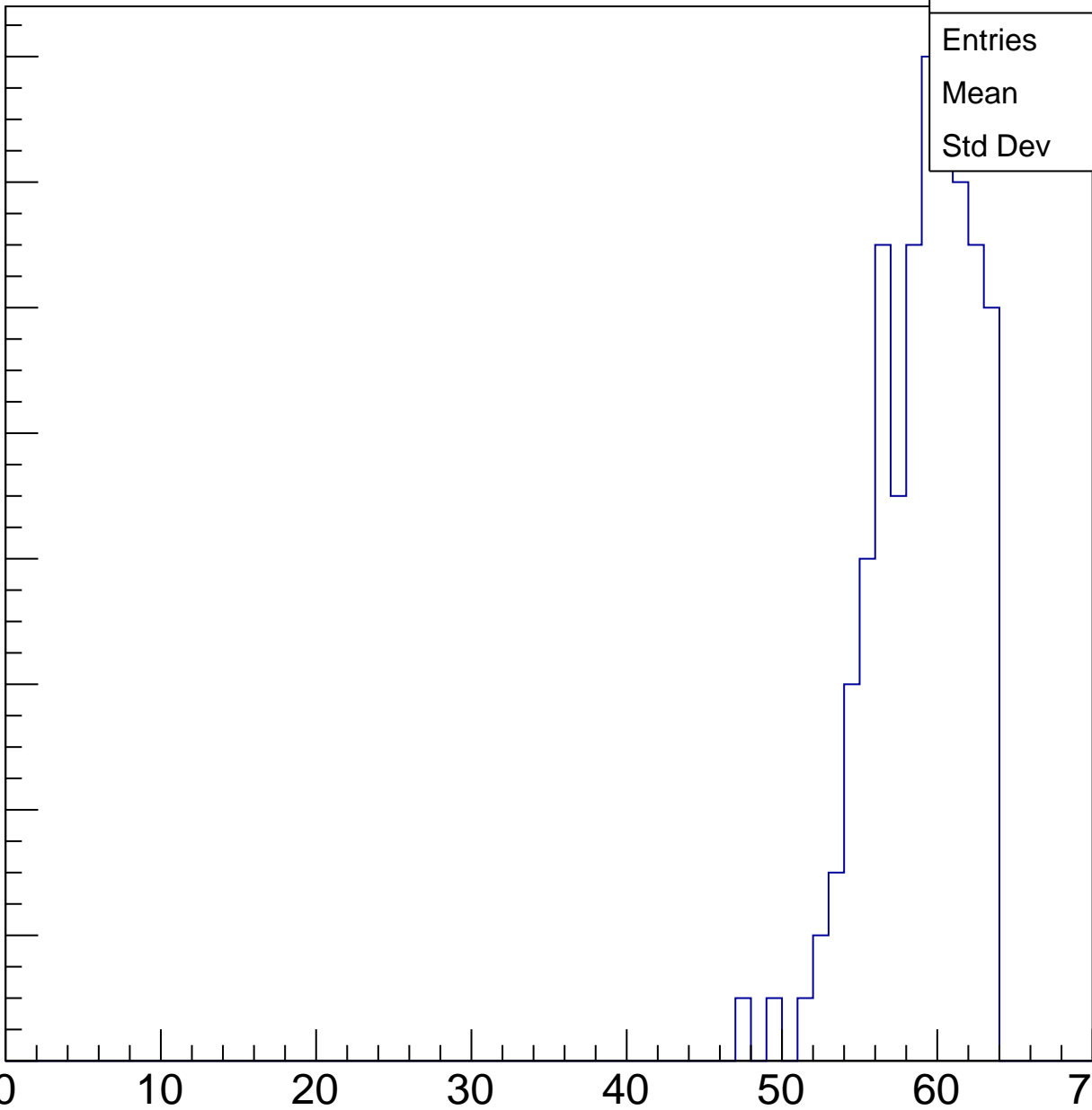
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 128 |
| Mean | 58.51 |
| Std Dev | 3.197 |

ampl

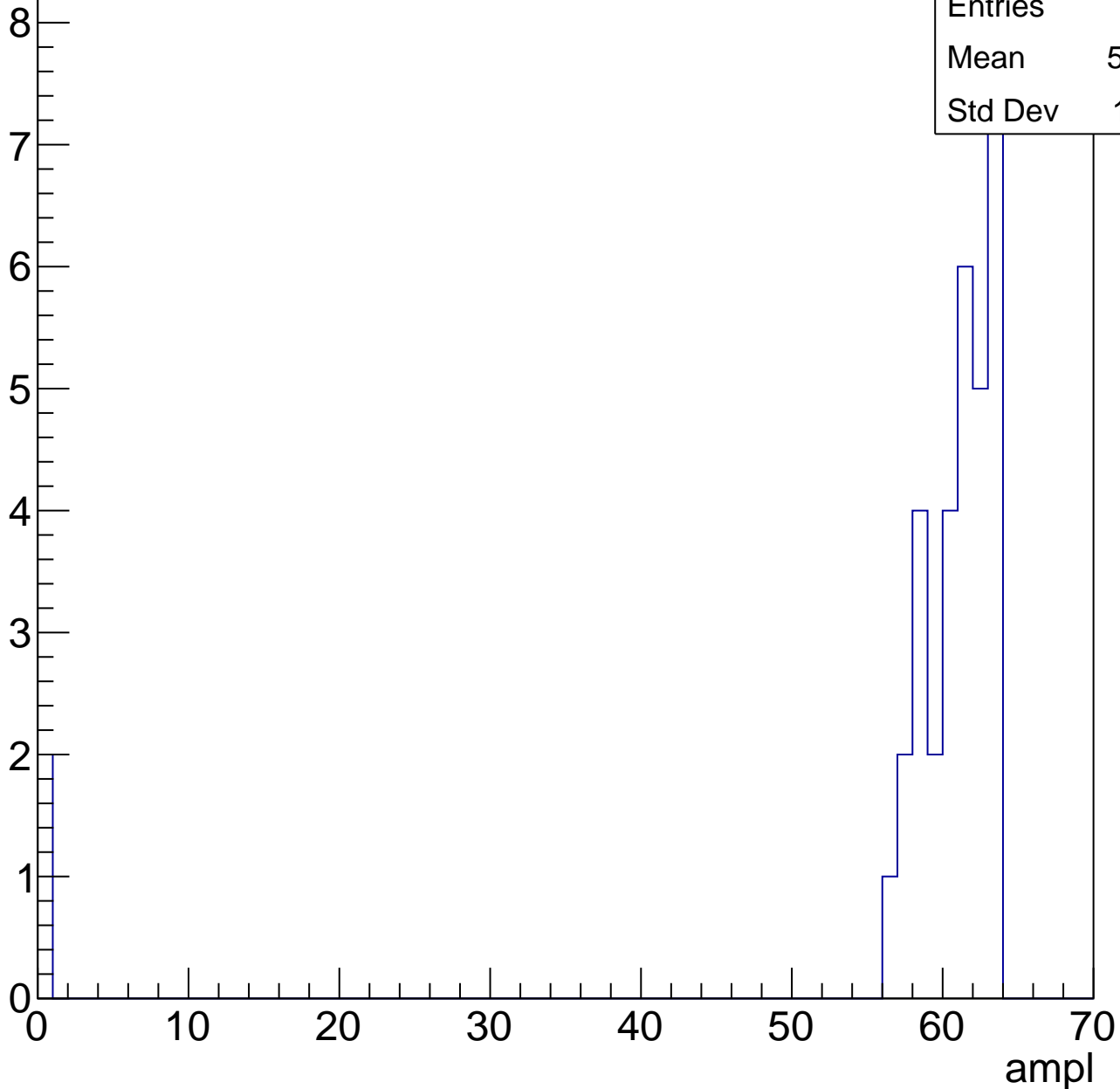


B0L101S, U22-ch35, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 57.06 |
| Std Dev | 14.41 |



B0L101S, U22-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

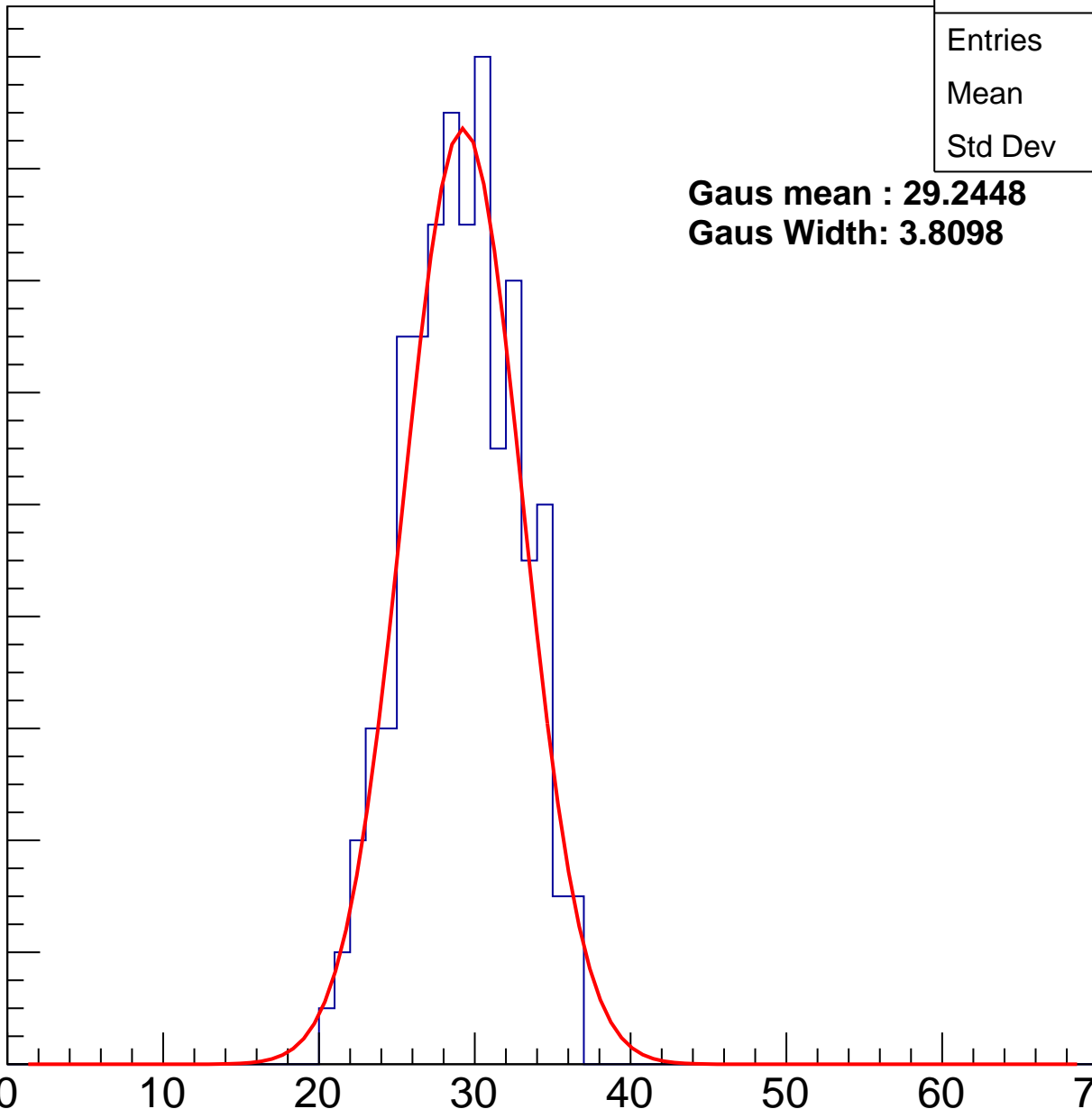
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 160 |
| Mean | 28.69 |
| Std Dev | 3.531 |

Gaus mean : 29.2448

Gaus Width: 3.8098

0 10 20 30 40 50 60 70
ampl



B0L101S, U22-ch36, adc1

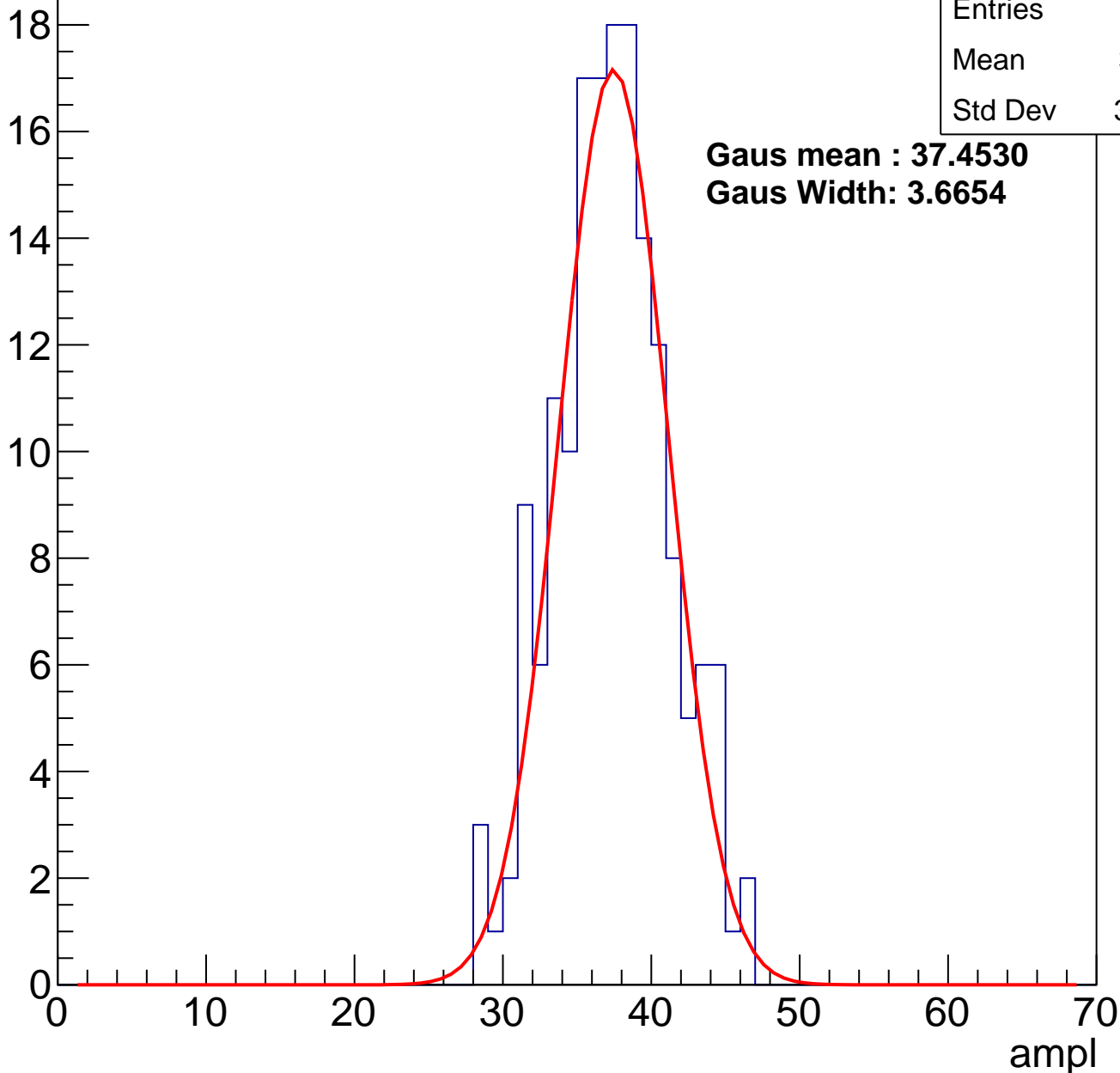
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 36.91 |
| Std Dev | 3.802 |

Gaus mean : 37.4530

Gaus Width: 3.6654

Entry



B0L101S, U22-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

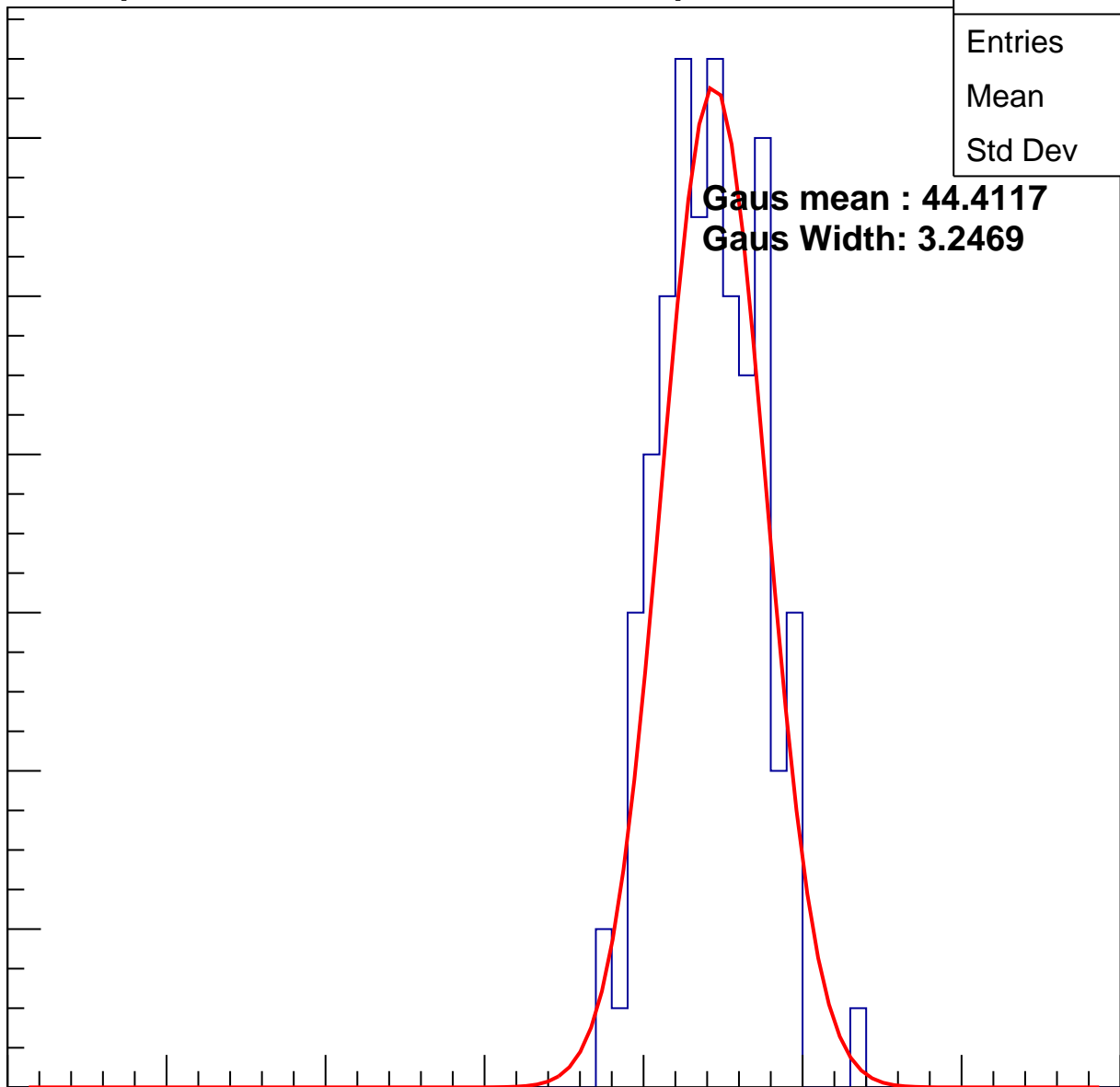
| | |
|---------|-------|
| Entries | 106 |
| Mean | 43.72 |
| Std Dev | 3.08 |

Gaus mean : 44.4117

Gaus Width: 3.2469

0 10 20 30 40 50 60 70

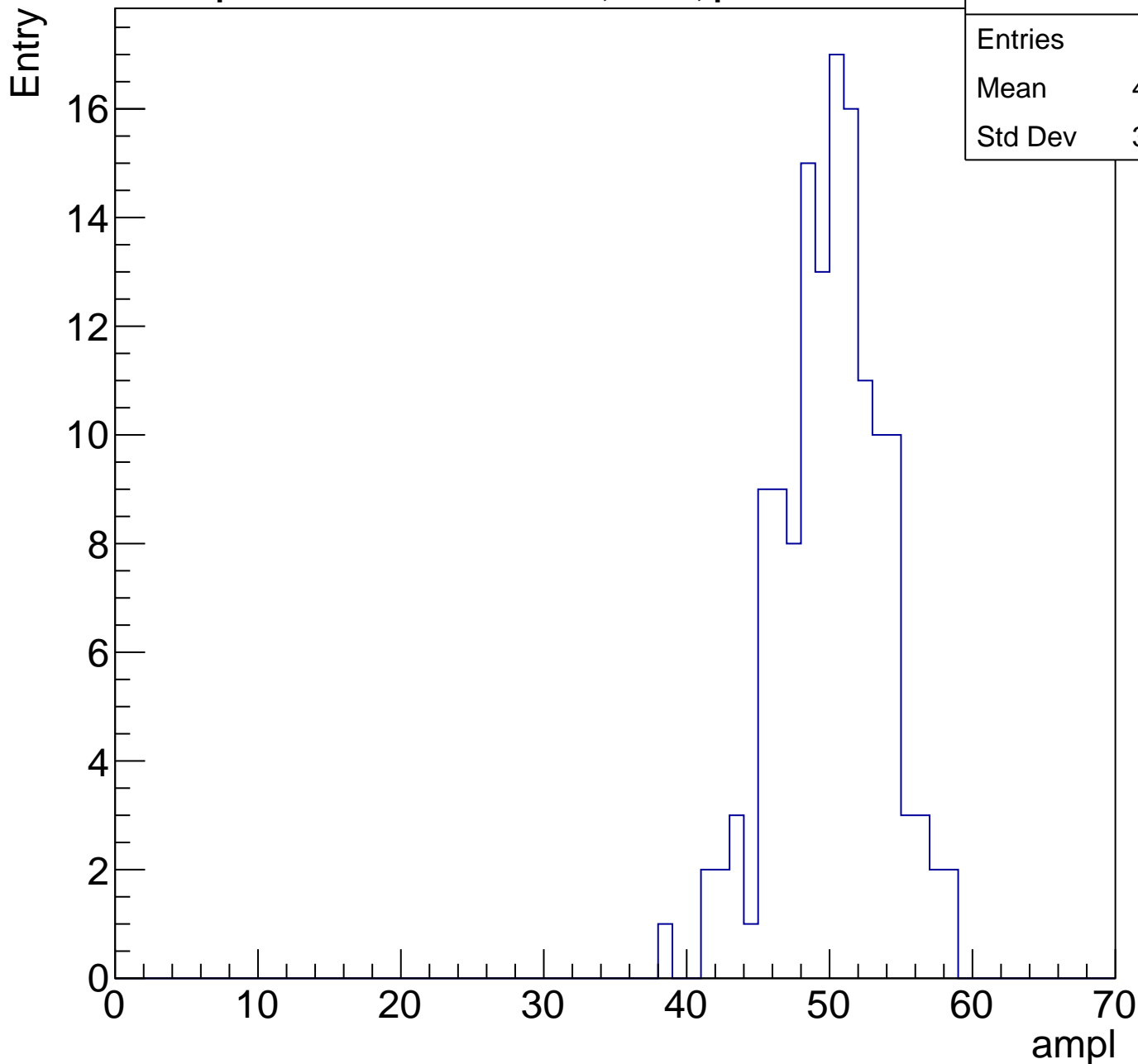
ampl



B0L101S, U22-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 49.64 |
| Std Dev | 3.657 |



B0L101S, U22-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 56.85 |
| Std Dev | 3.405 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

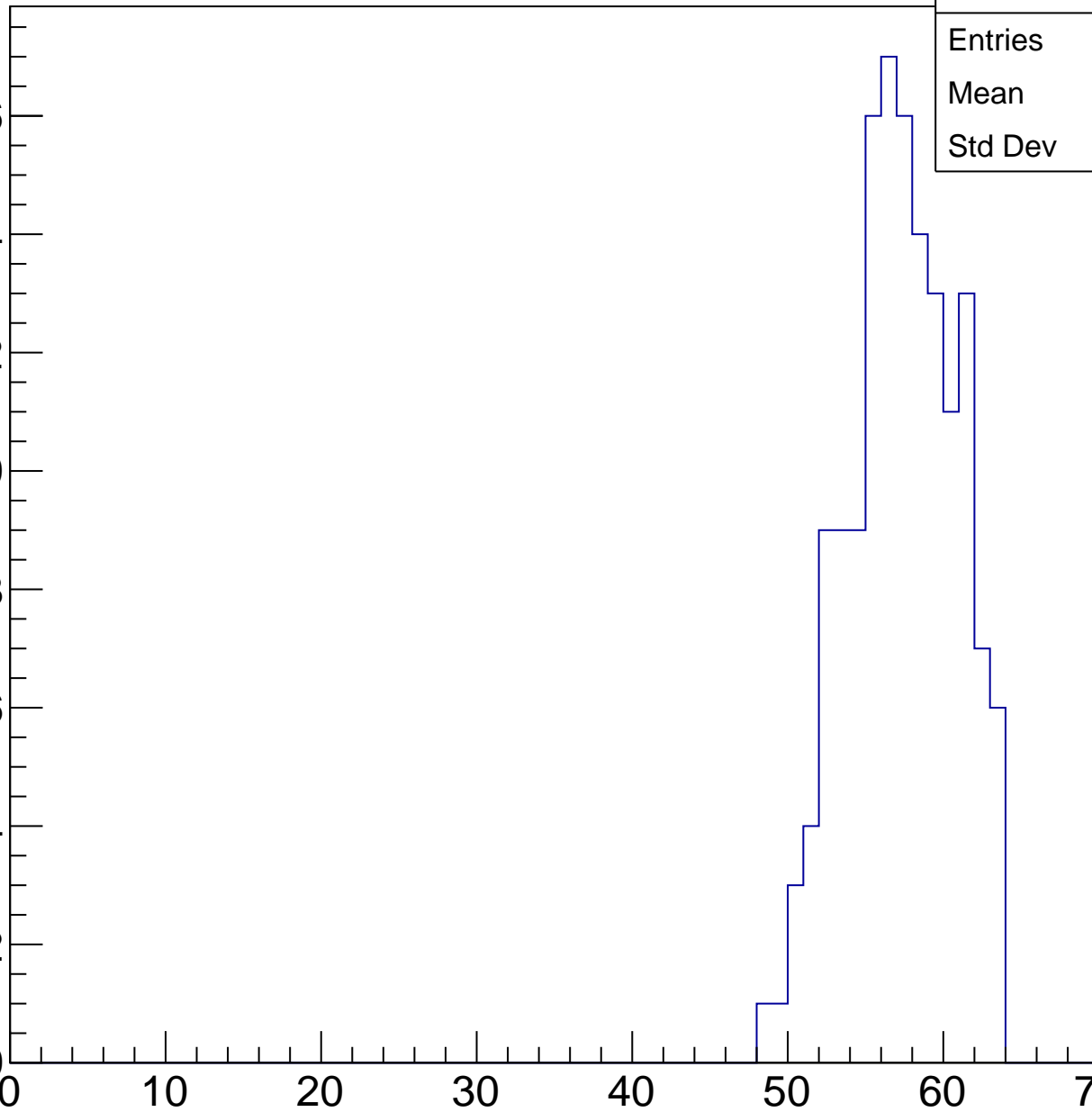
30

40

50

60

ampl



B0L101S, U22-ch36, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

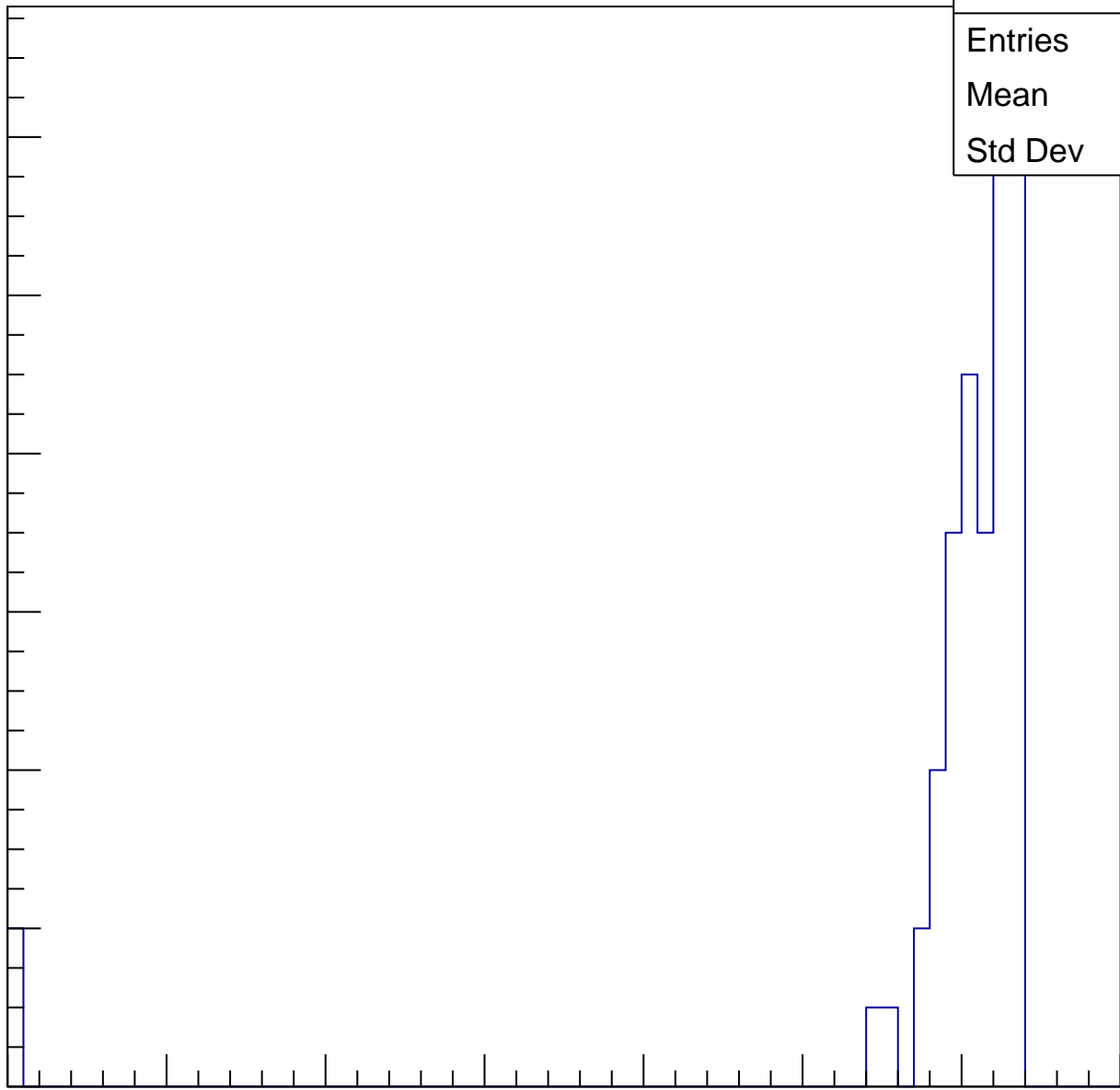
2

0

| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.64 |
| Std Dev | 11.18 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

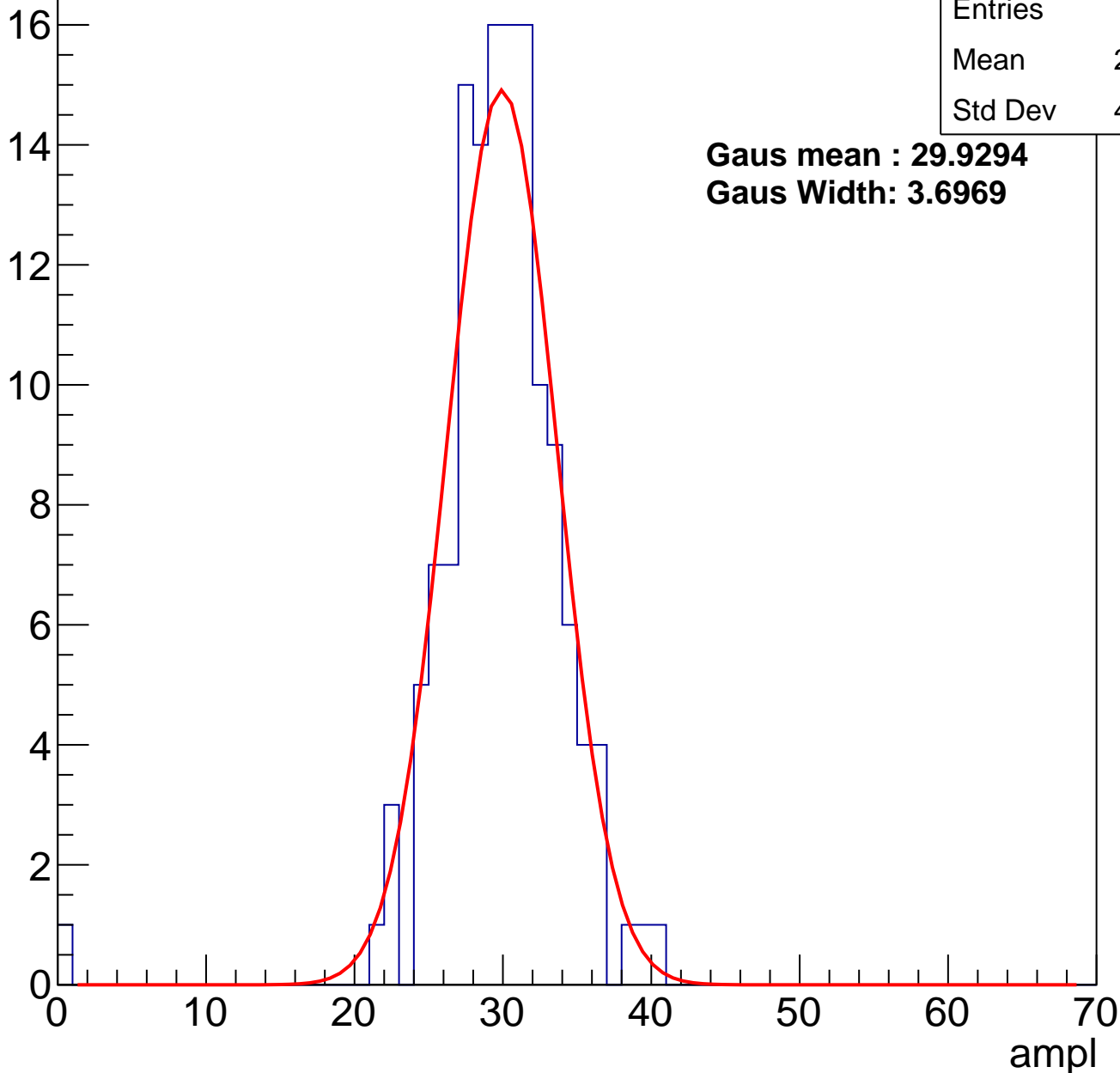
Entry



B0L101S, U22-ch37, adc0

calib_packv5_042523_0143.root, FC#1, port C1

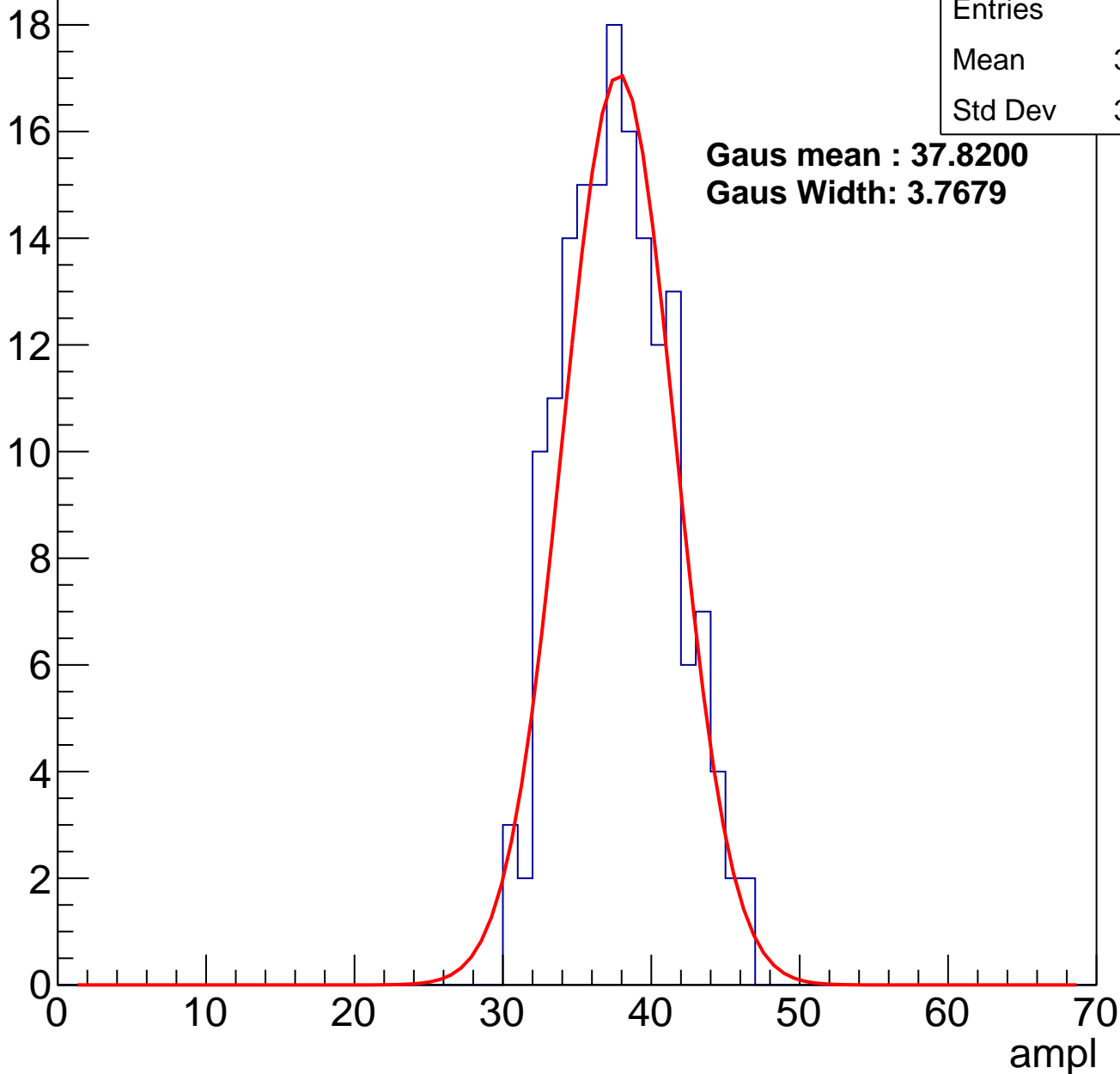
Entry



B0L101S, U22-ch37, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

44.13

Std Dev

3.495

Gaus mean : 44.5240

Gaus Width: 3.4881

0

10

20

30

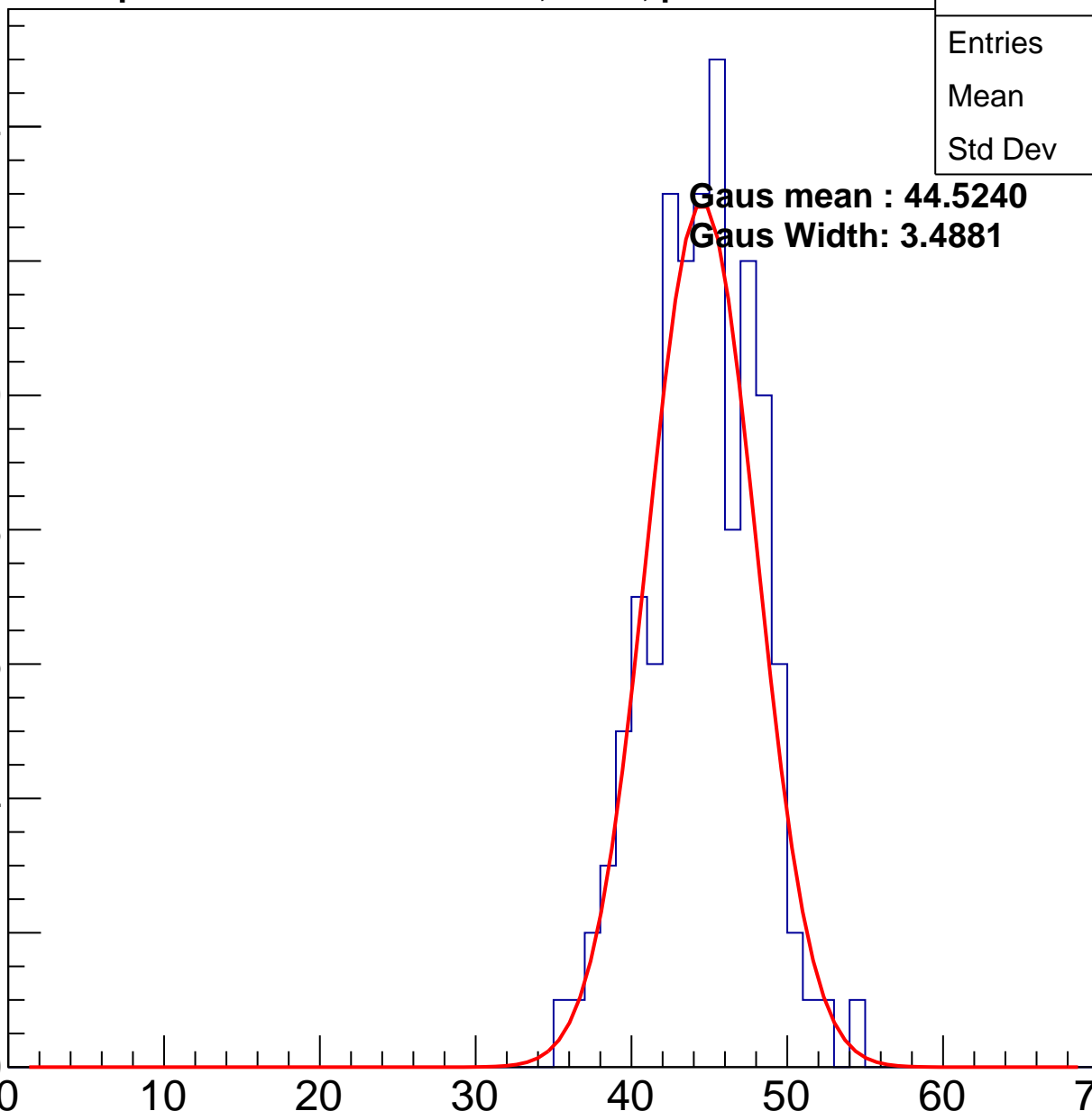
40

50

60

70

ampl



B0L101S, U22-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

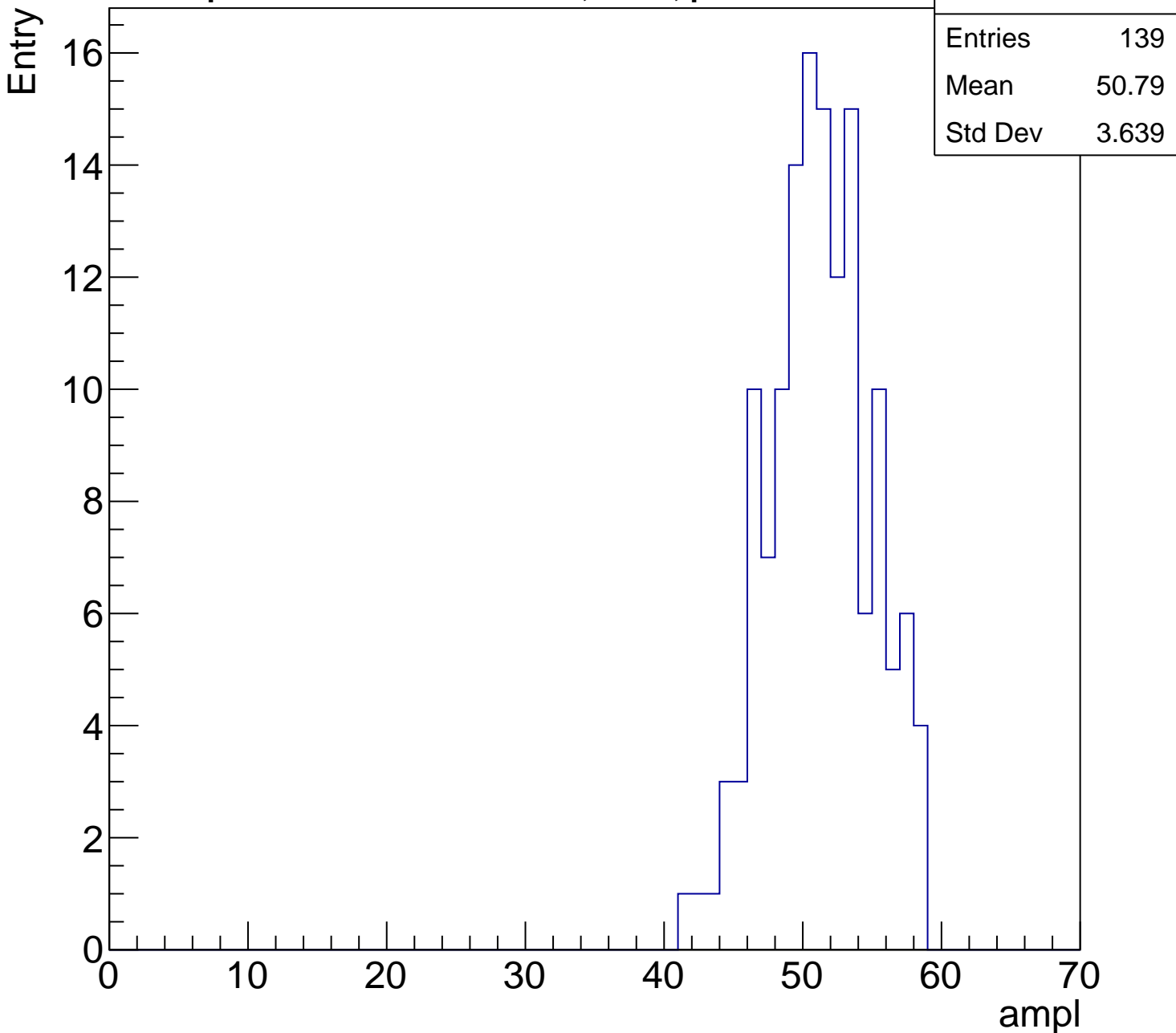
| | |
|---------|-------|
| Entries | 139 |
| Mean | 50.79 |
| Std Dev | 3.639 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch37, adc4

calib_packv5_042523_0143.root, FC#1, port C1

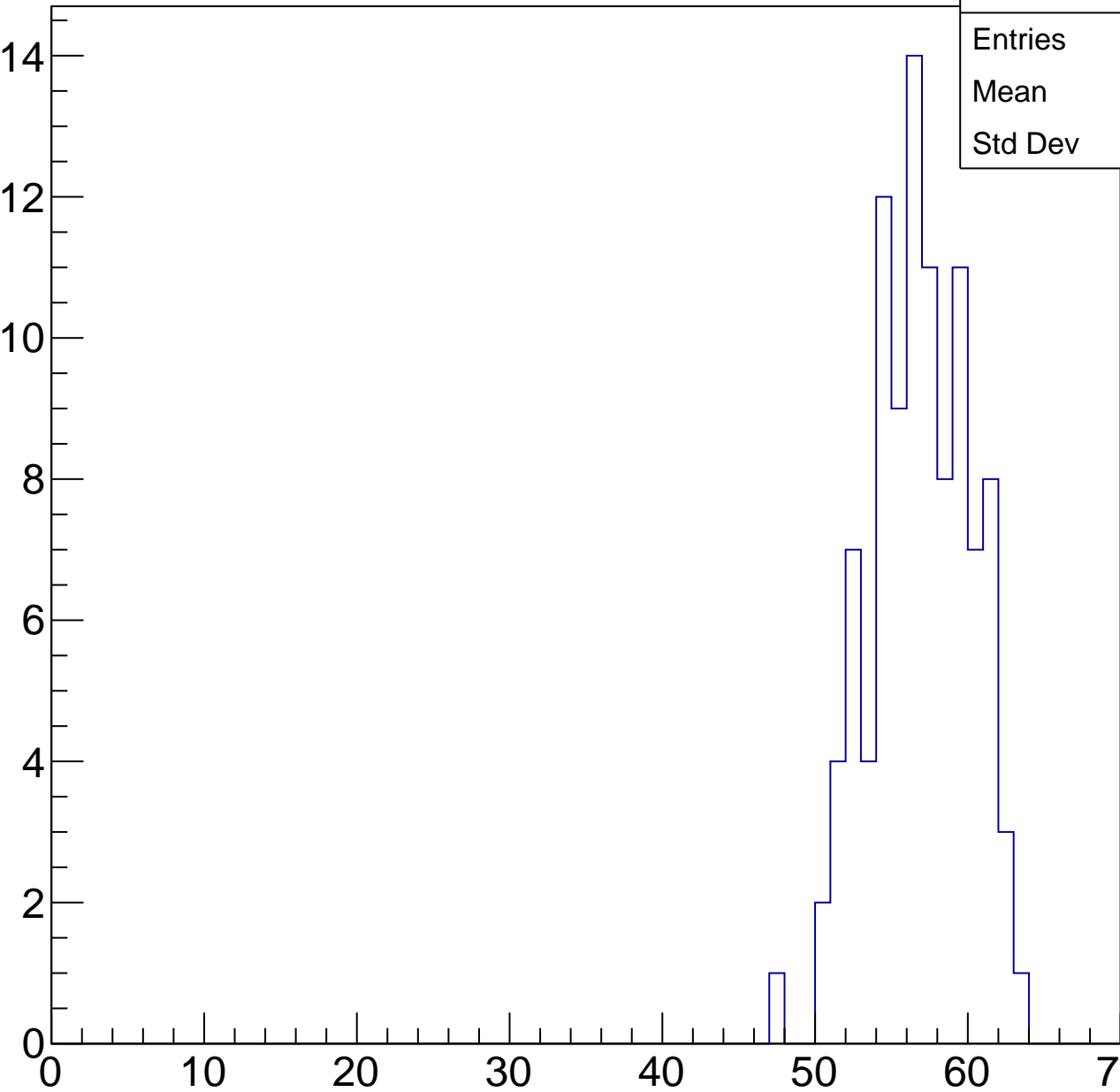
| | |
|---------|-------|
| Entries | 102 |
| Mean | 56.38 |
| Std Dev | 3.215 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch37, adc5

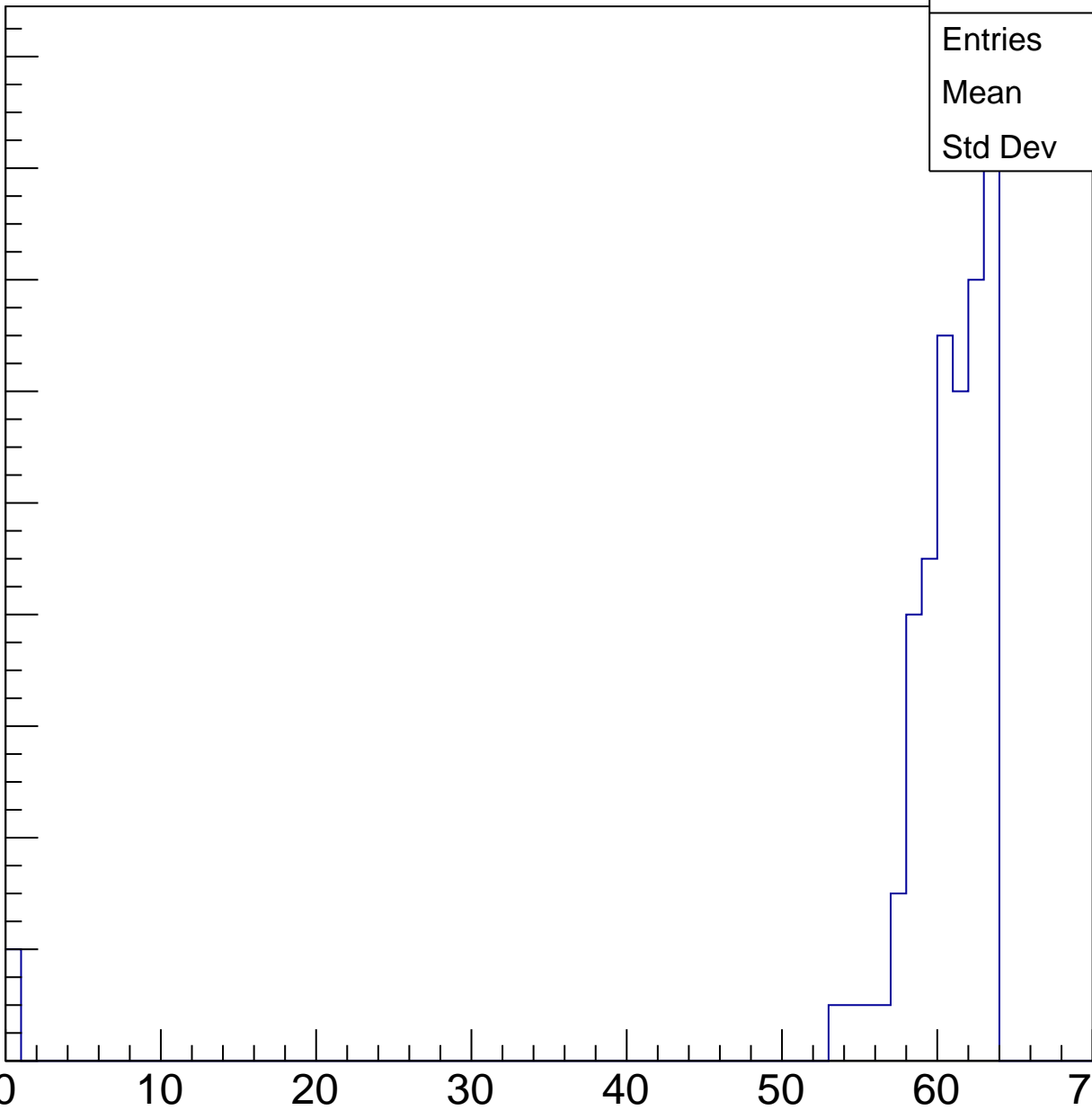
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 59.01 |
| Std Dev | 9.533 |

ampl



B0L101S, U22-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch38, adc0

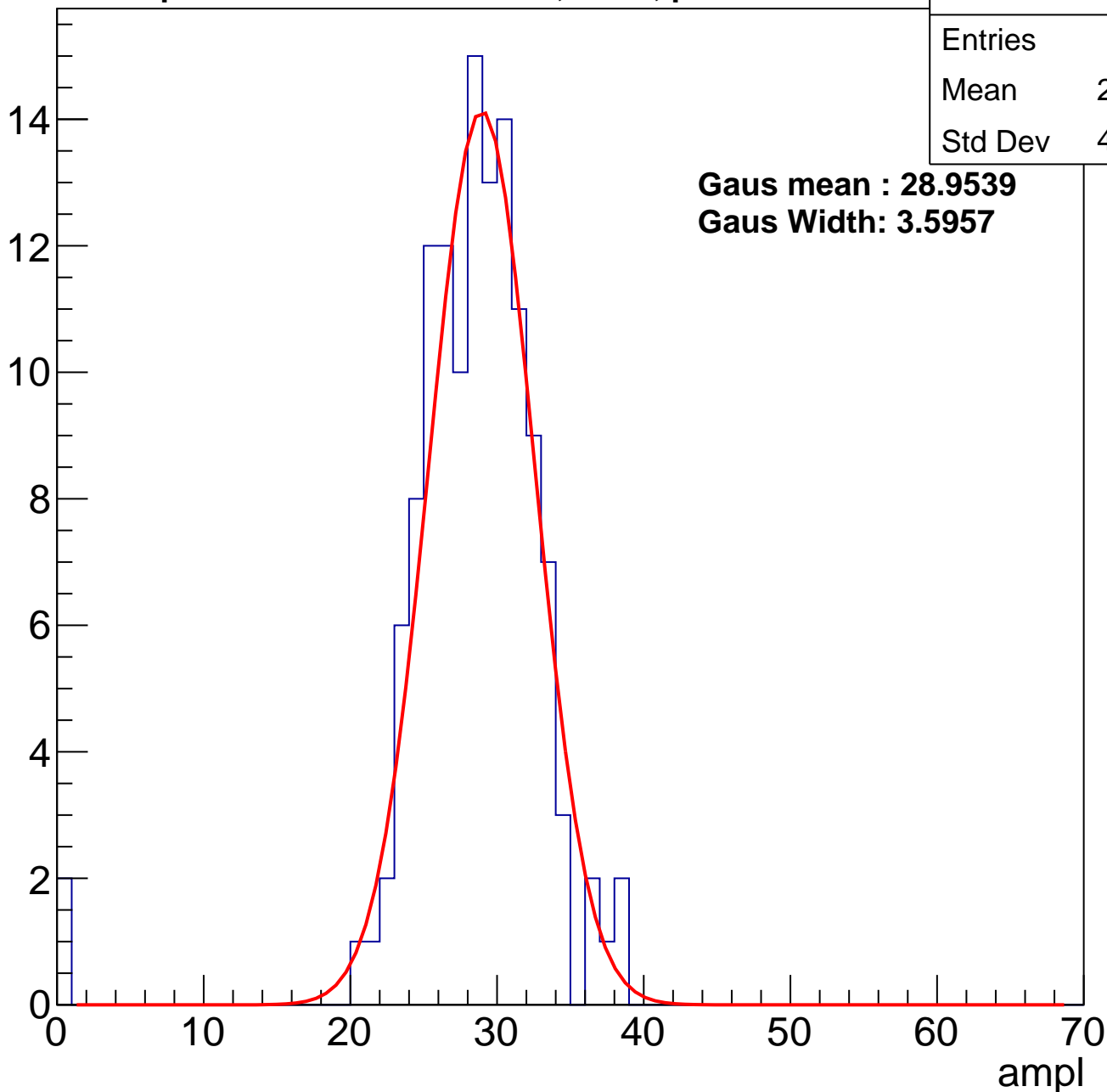
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 27.95 |
| Std Dev | 4.943 |

Gaus mean : 28.9539

Gaus Width: 3.5957

Entry



B0L101S, U22-ch38, adc1

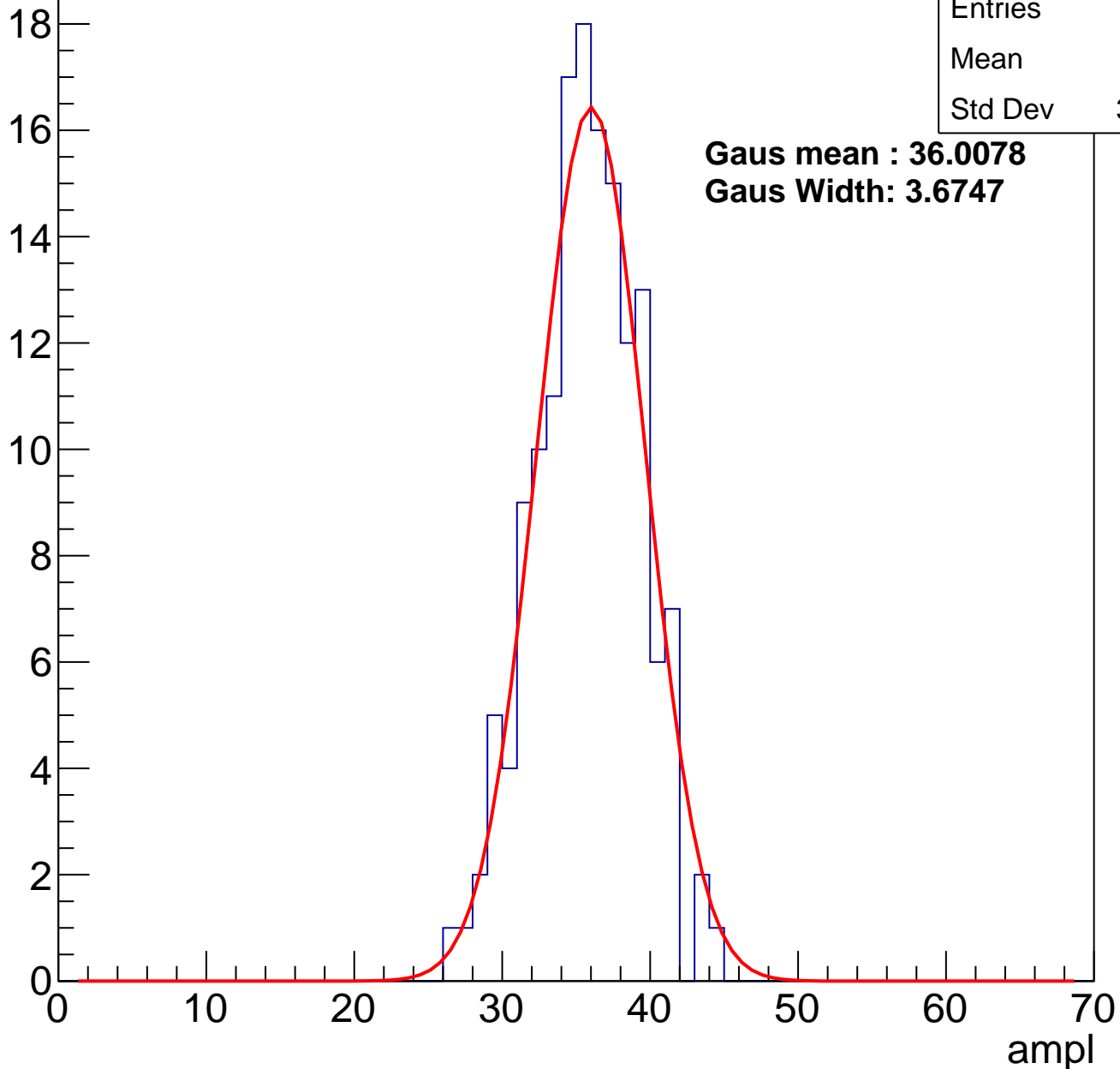
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 35.3 |
| Std Dev | 3.471 |

Gaus mean : 36.0078

Gaus Width: 3.6747

Entry



B0L101S, U22-ch38, adc2

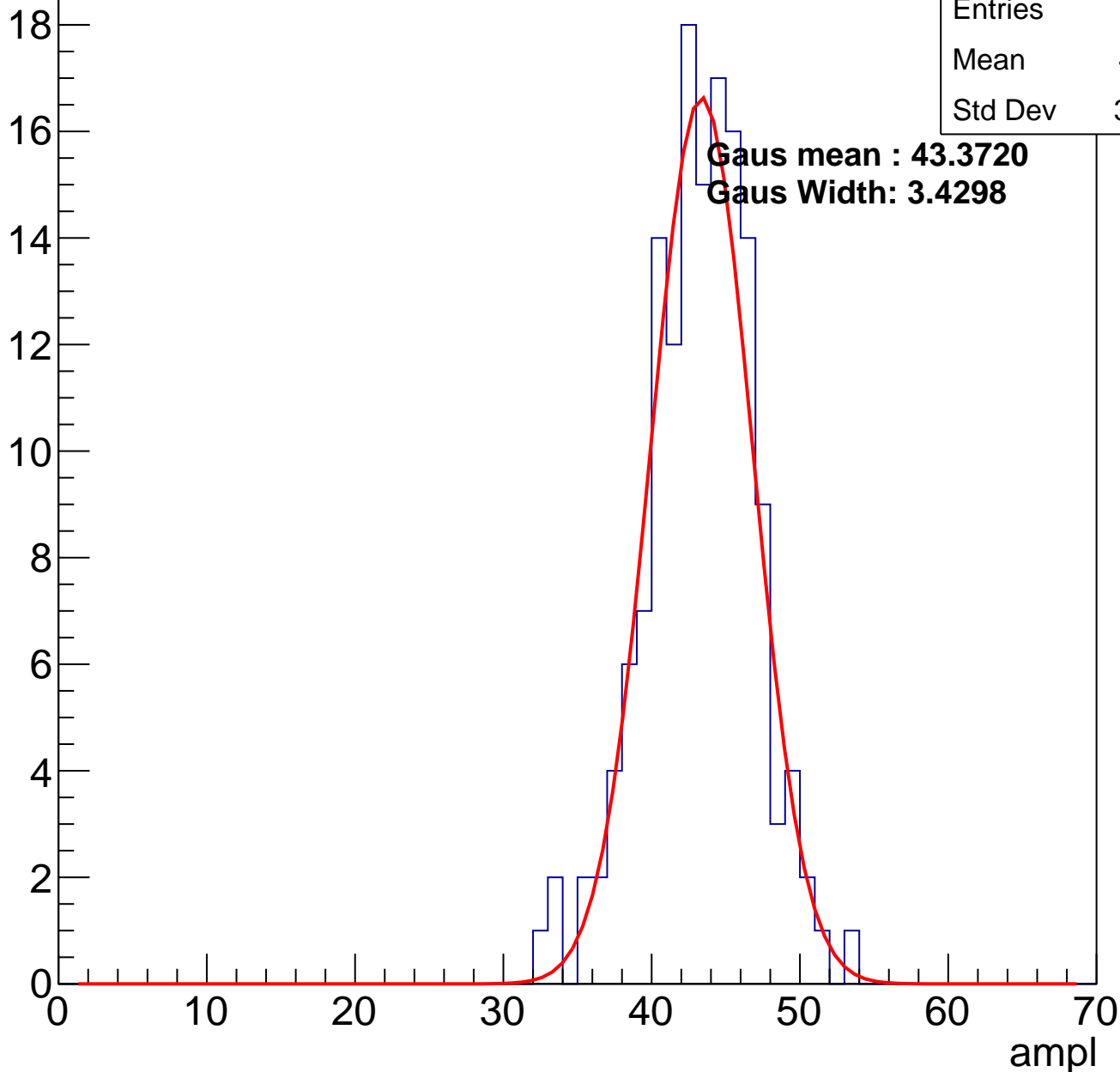
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 42.81 |
| Std Dev | 3.625 |

Gaus mean : 43.3720

Gaus Width: 3.4298

Entry



B0L101S, U22-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

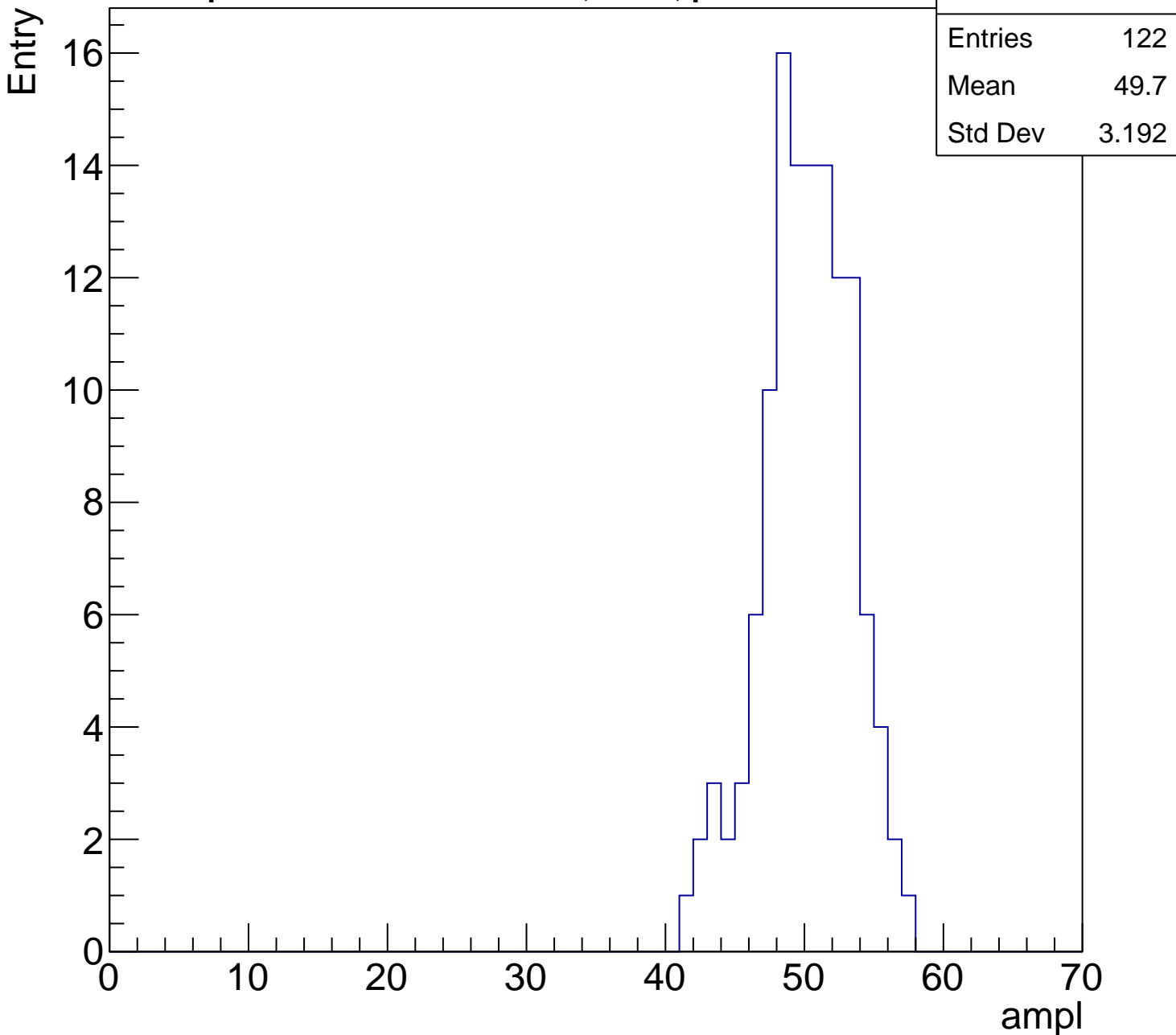
| | |
|---------|-------|
| Entries | 122 |
| Mean | 49.7 |
| Std Dev | 3.192 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

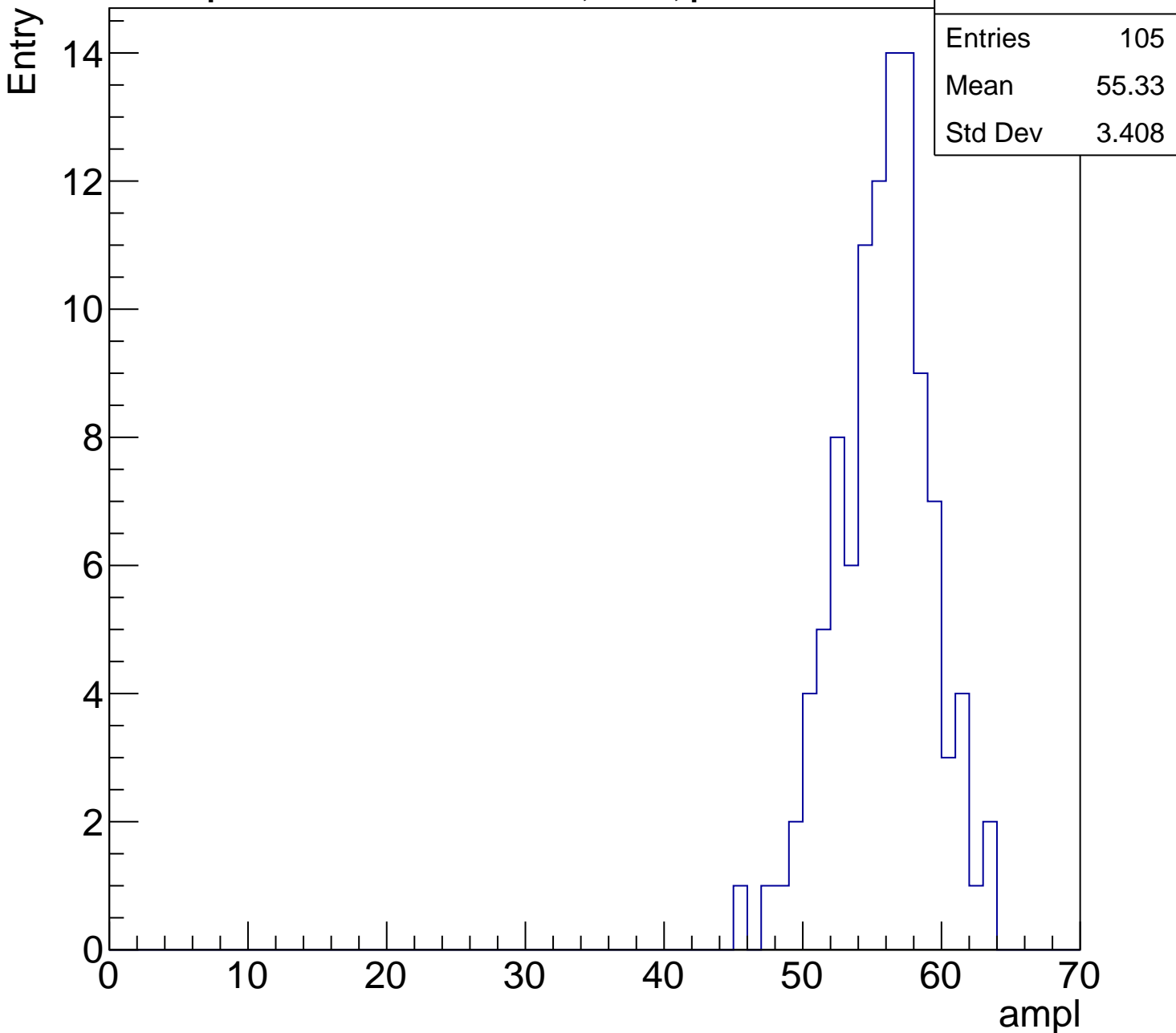
| | |
|---------|-------|
| Entries | 105 |
| Mean | 55.33 |
| Std Dev | 3.408 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch38, adc5

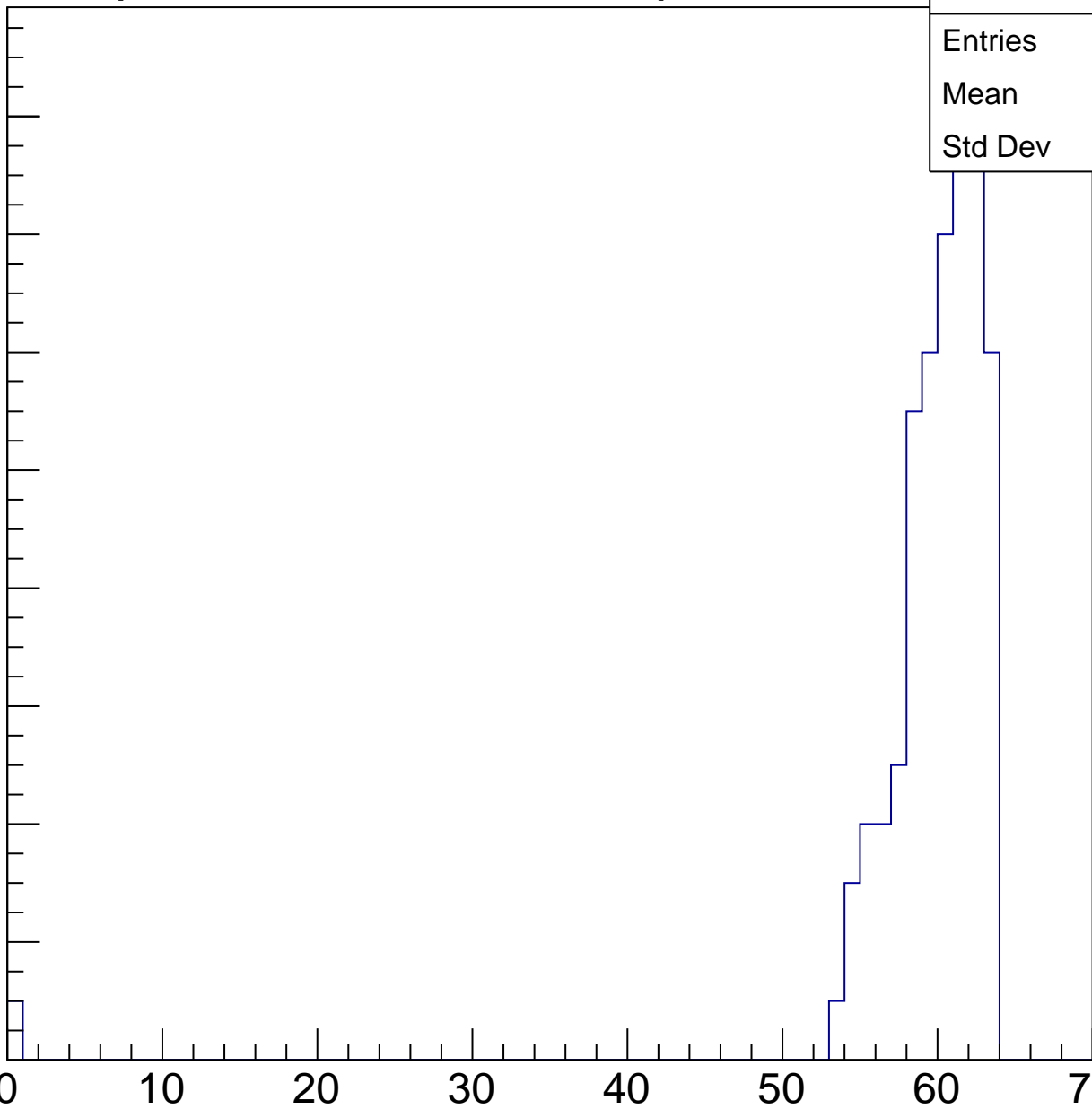
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 100 |
| Mean | 59.16 |
| Std Dev | 6.437 |

ampl

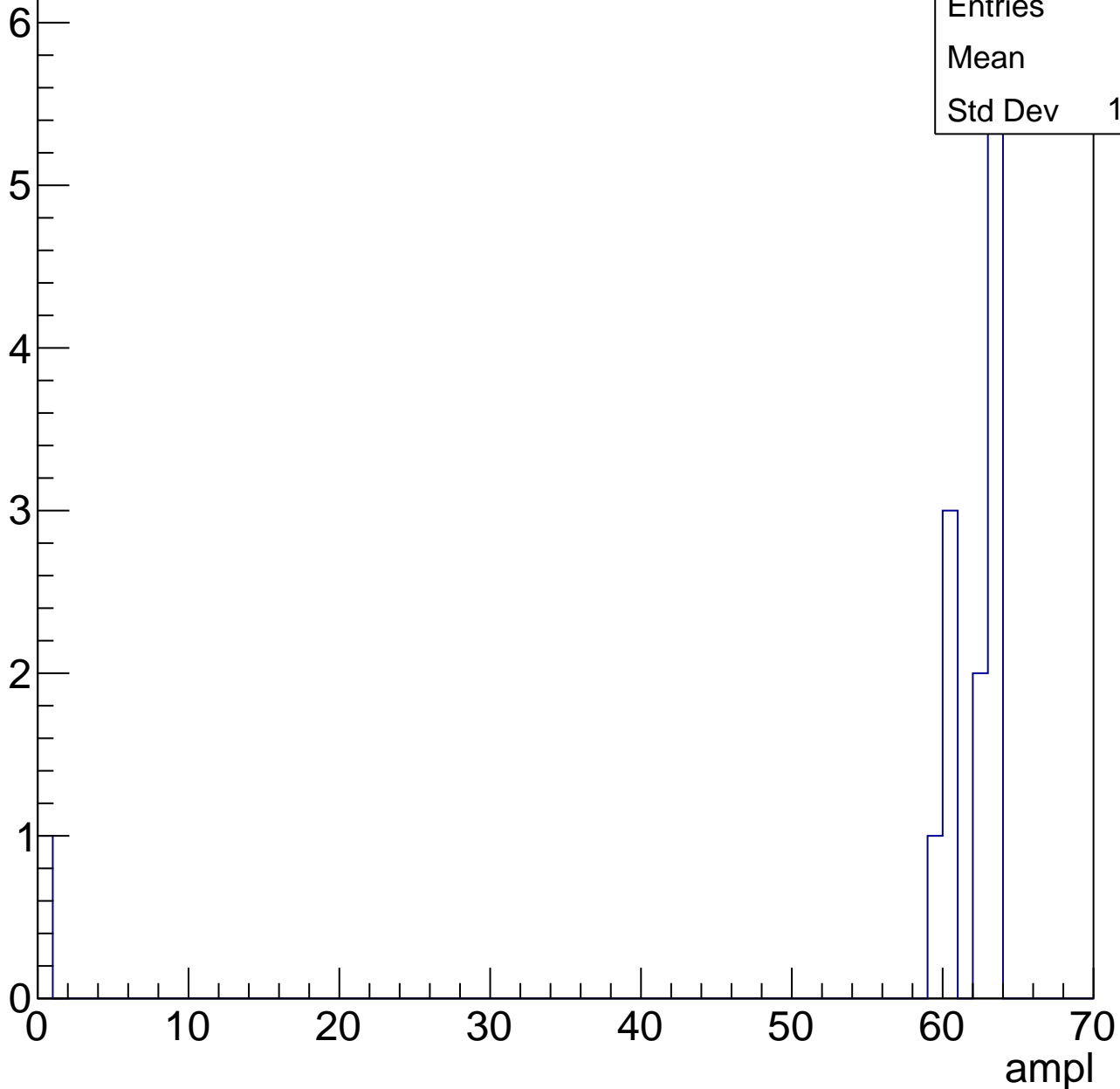


B0L101S, U22-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 57 |
| Std Dev | 16.52 |



B0L101S, U22-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

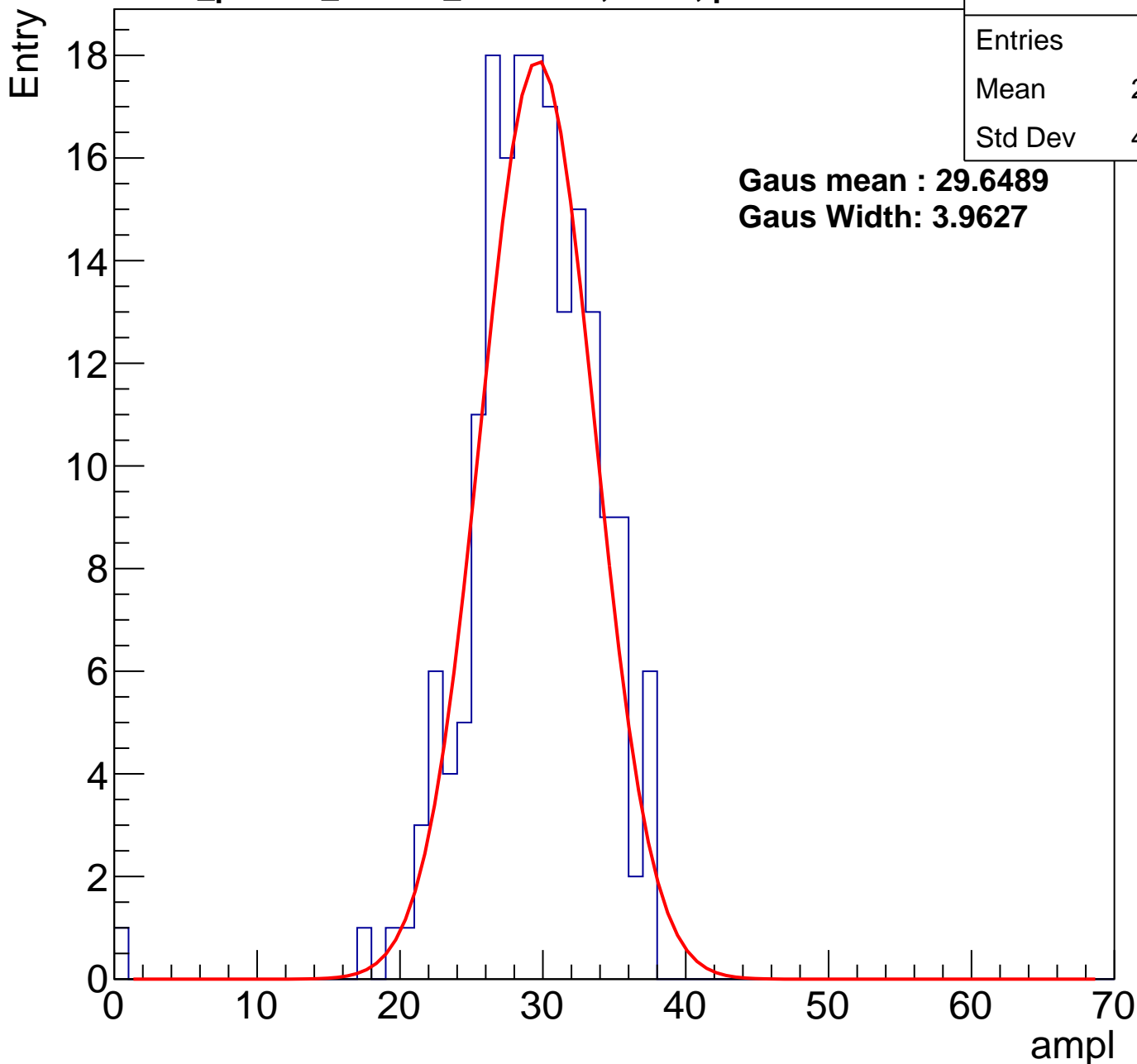
B0L101S, U22-ch39, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 187 |
| Mean | 28.88 |
| Std Dev | 4.497 |

Gaus mean : 29.6489

Gaus Width: 3.9627



B0L101S, U22-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.57 |
| Std Dev | 3.299 |

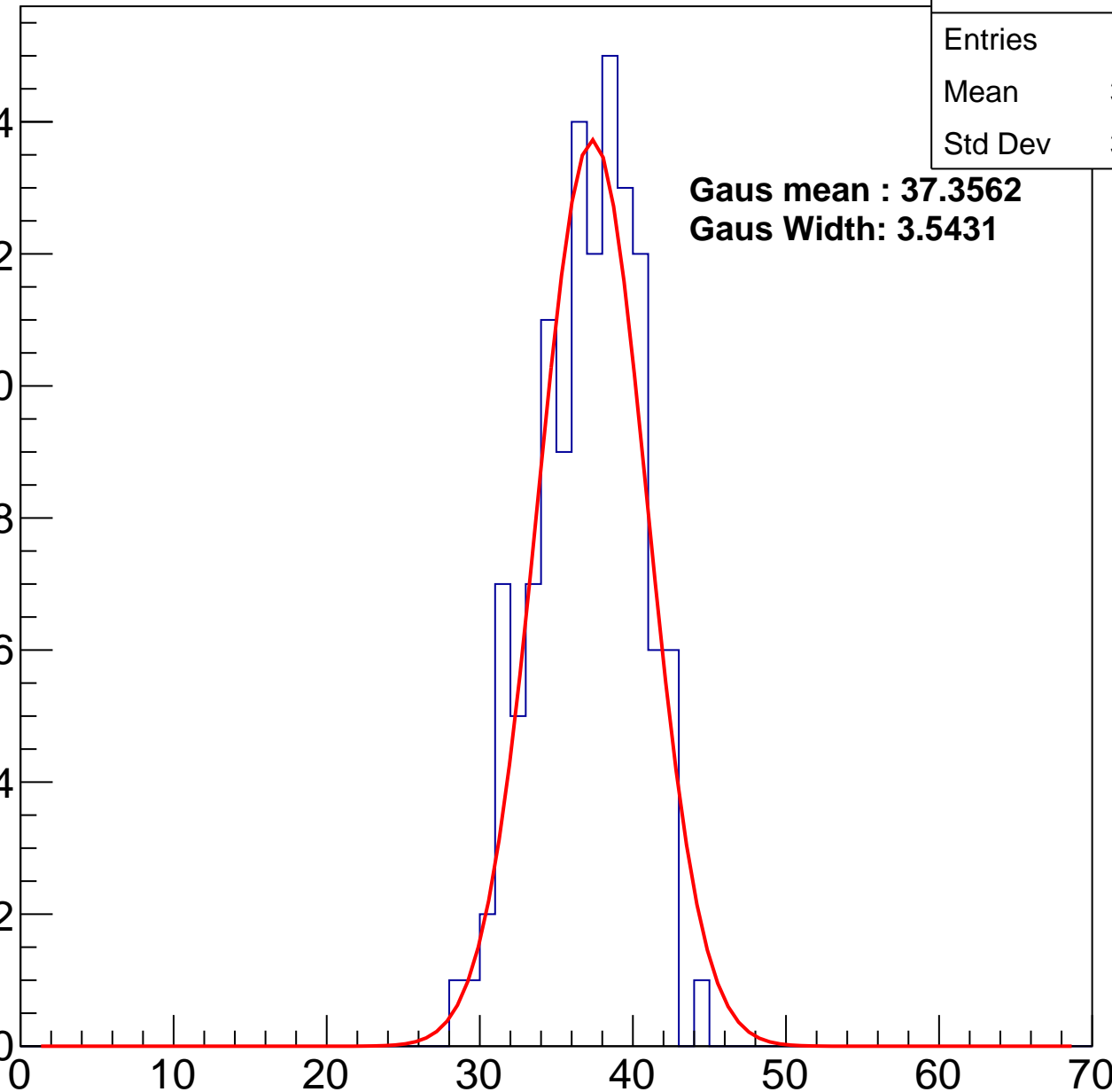
Gaus mean : 37.3562

Gaus Width: 3.5431

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch39, adc2

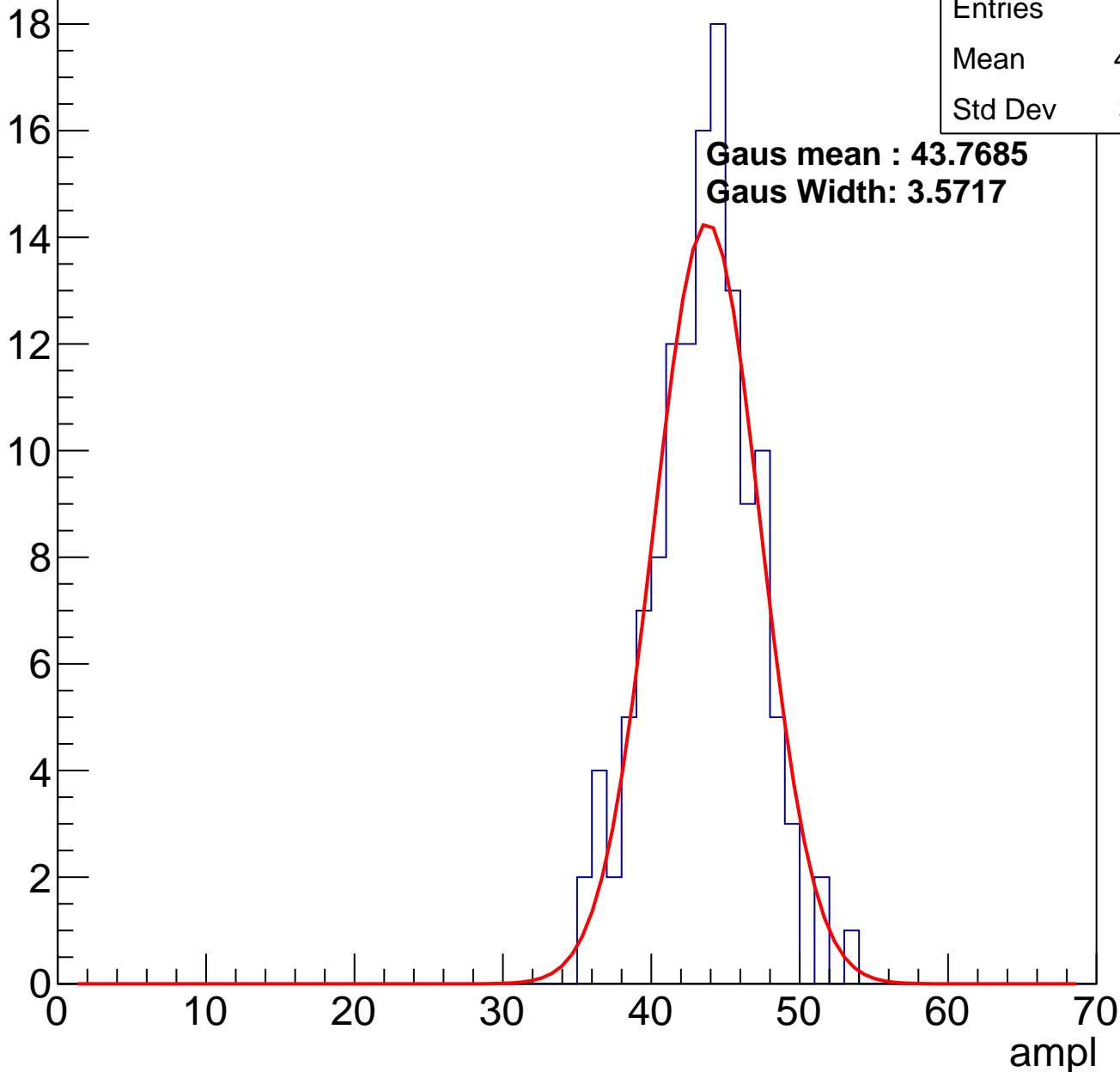
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 129 |
| Mean | 43.09 |
| Std Dev | 3.451 |

Gaus mean : 43.7685

Gaus Width: 3.5717

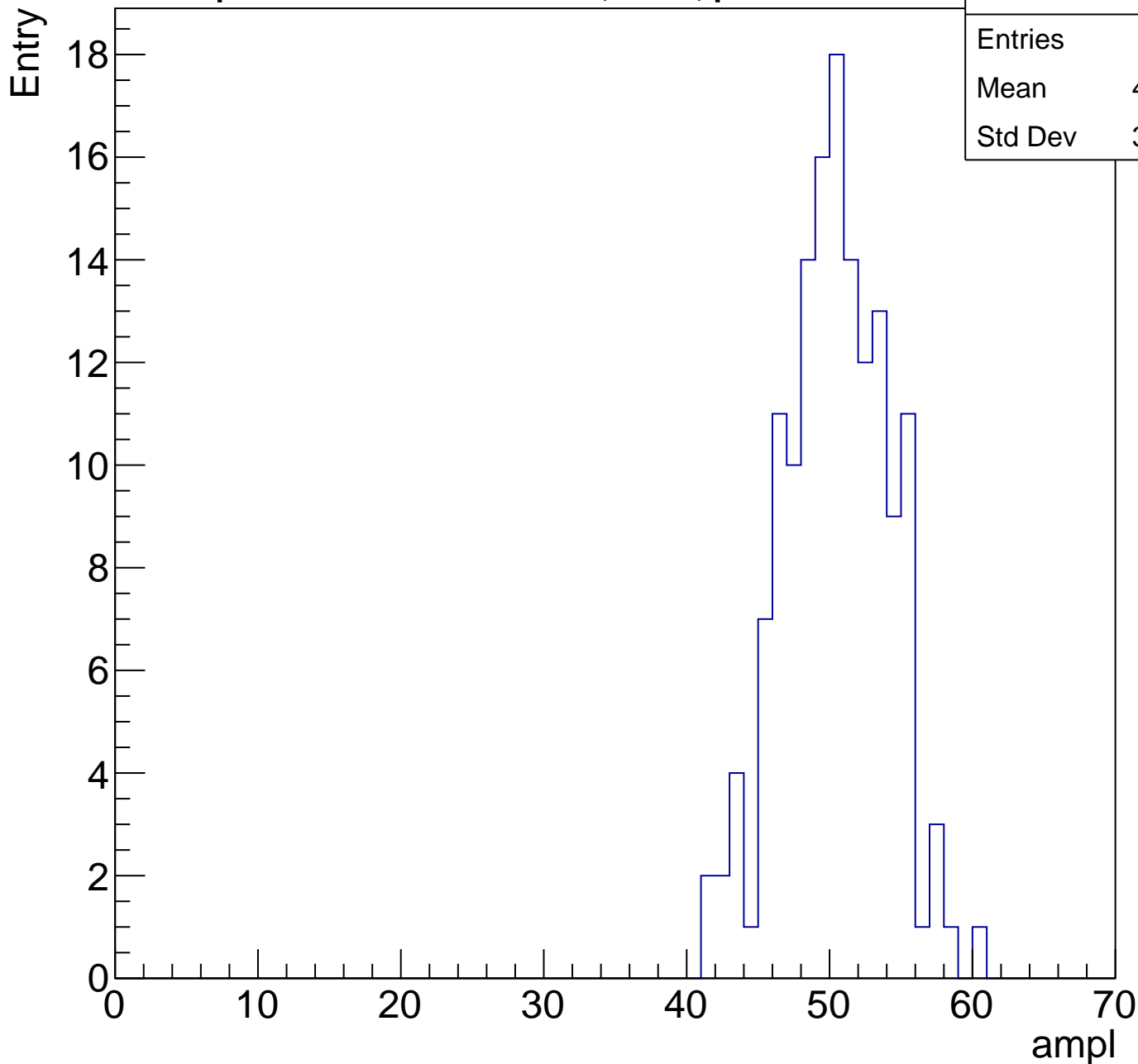
Entry



B0L101S, U22-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 49.95 |
| Std Dev | 3.633 |



B0L101S, U22-ch39, adc4

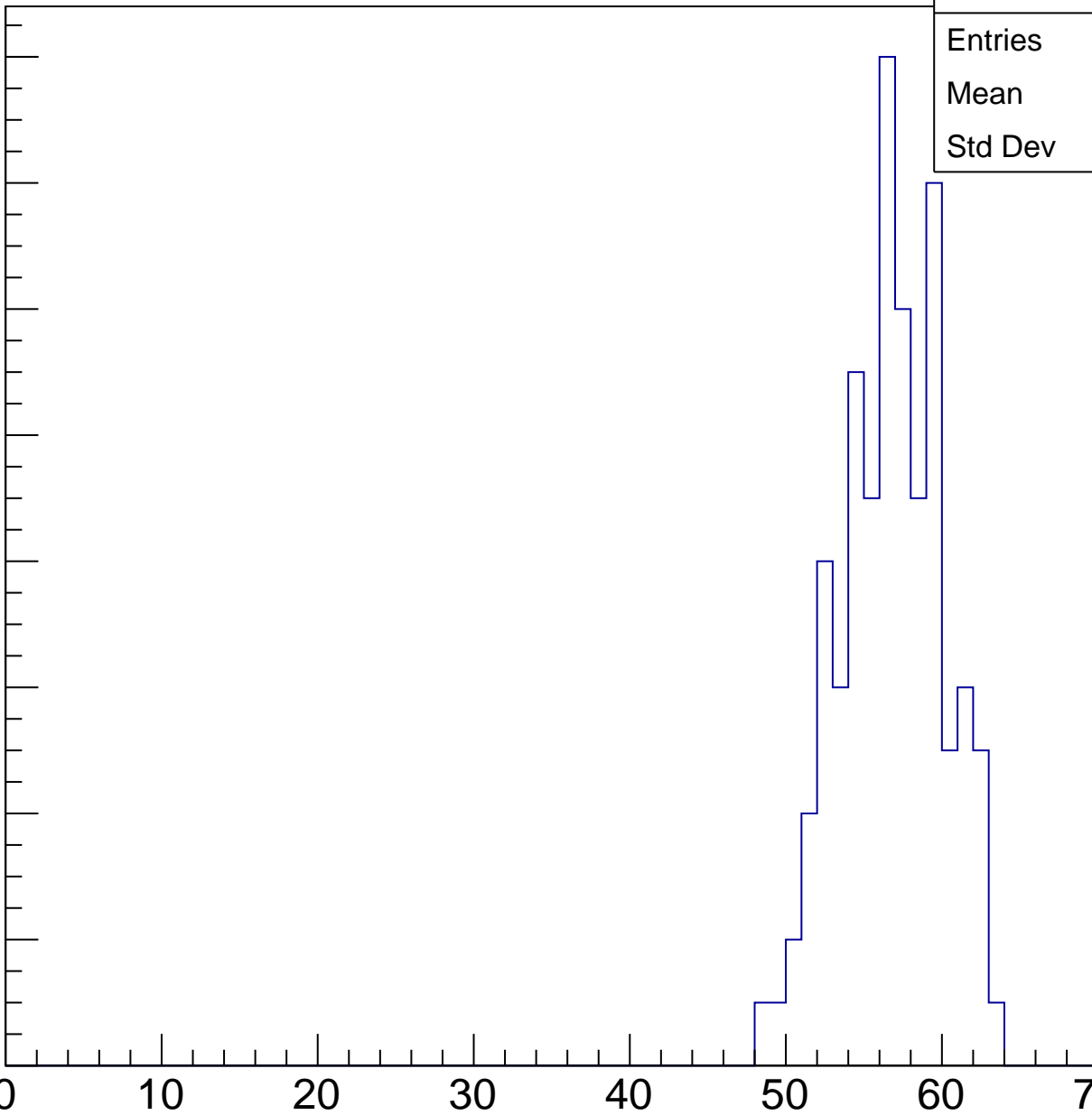
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 110 |
| Mean | 56.28 |
| Std Dev | 3.225 |

ampl



B0L101S, U22-ch39, adc5

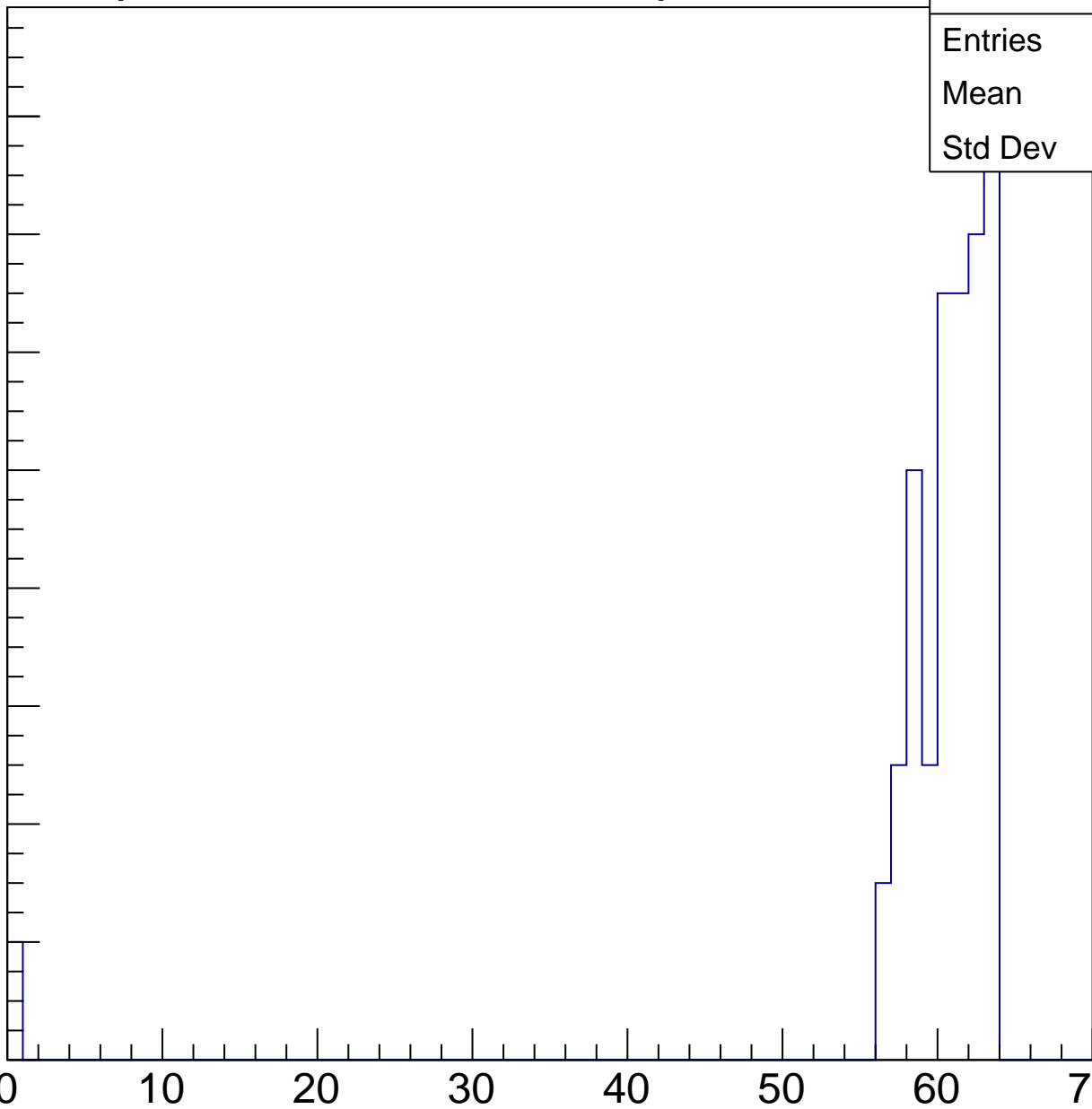
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 82 |
| Mean | 59.02 |
| Std Dev | 9.552 |

ampl



B0L101S, U22-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

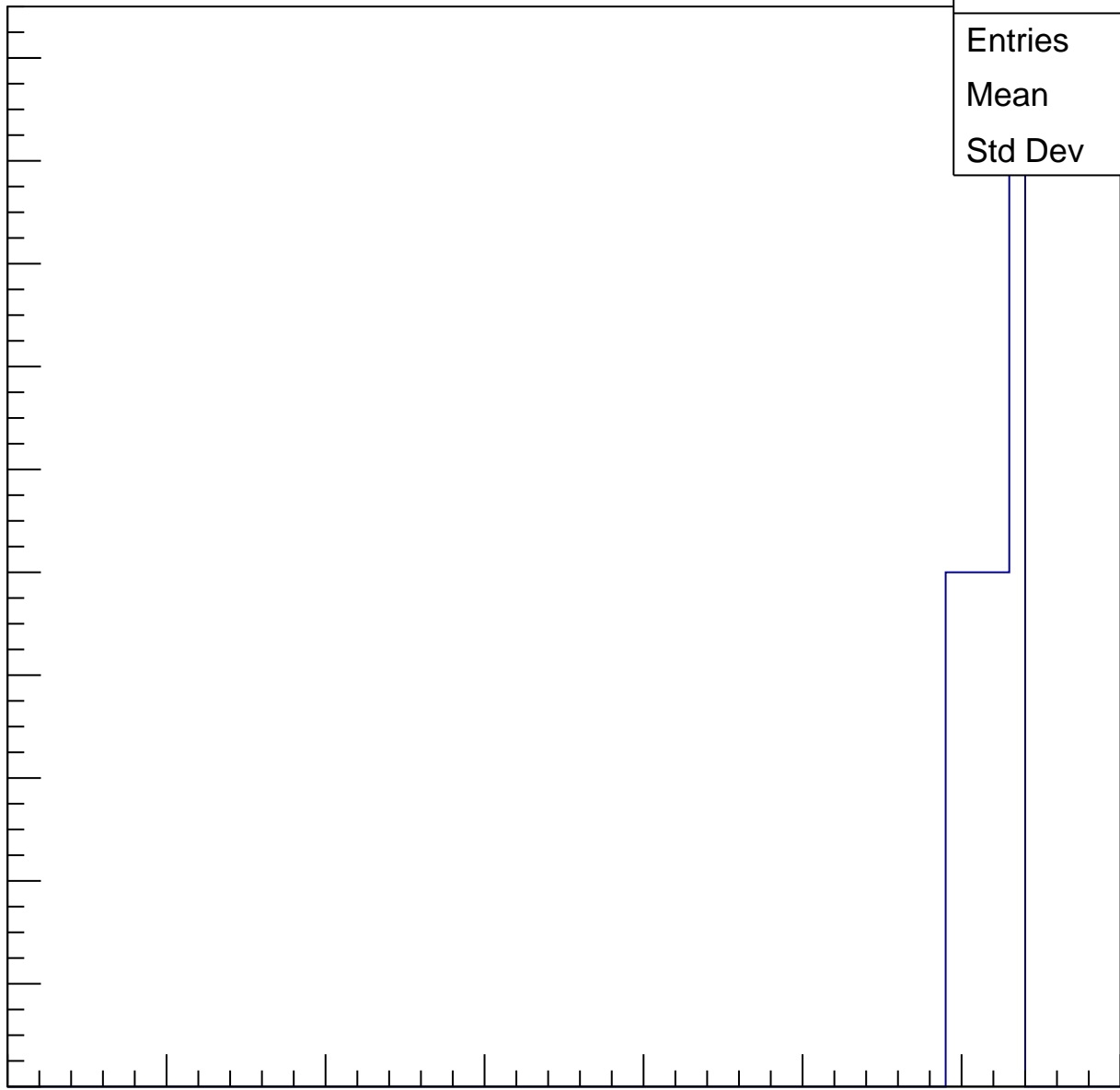
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.33 |
| Std Dev | 1.491 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch40, adc0

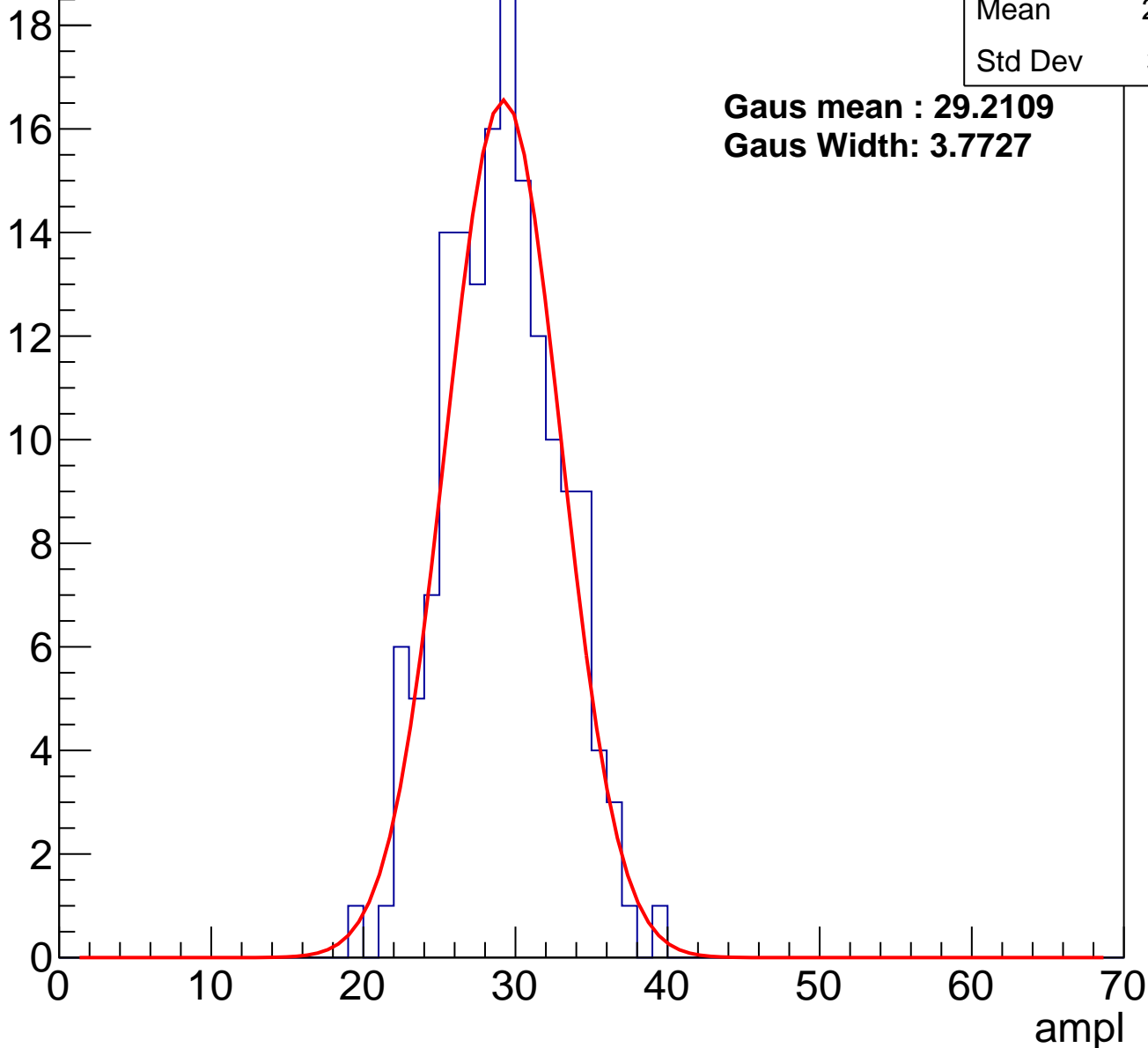
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 160 |
| Mean | 28.68 |
| Std Dev | 3.701 |

Gaus mean : 29.2109

Gaus Width: 3.7727

Entry



B0L101S, U22-ch40, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 147 |
| Mean | 36.27 |
| Std Dev | 3.407 |

Gaus mean : 36.7269

Gaus Width: 3.5173

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

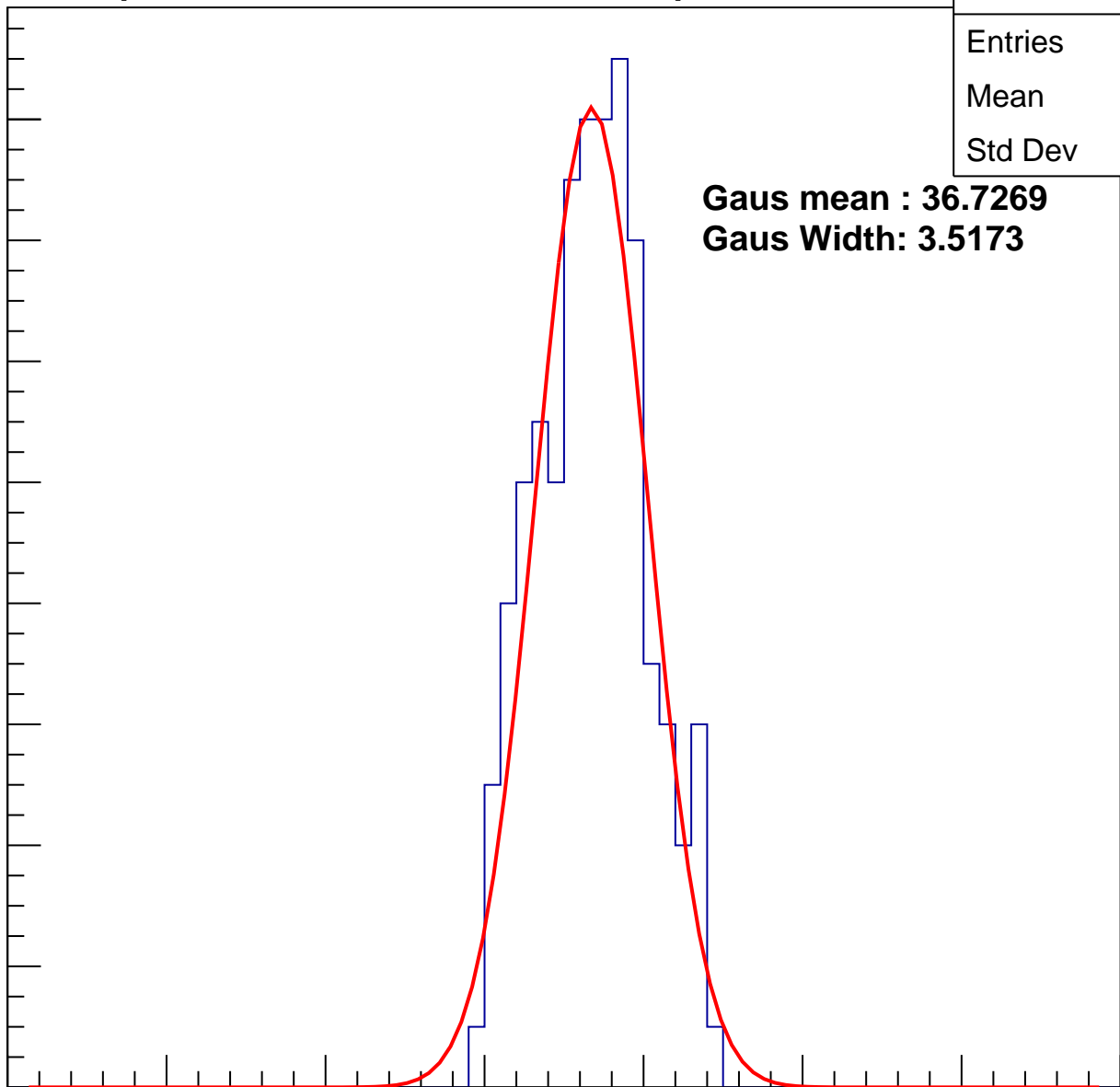
40

50

60

70

ampl



B0L101S, U22-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

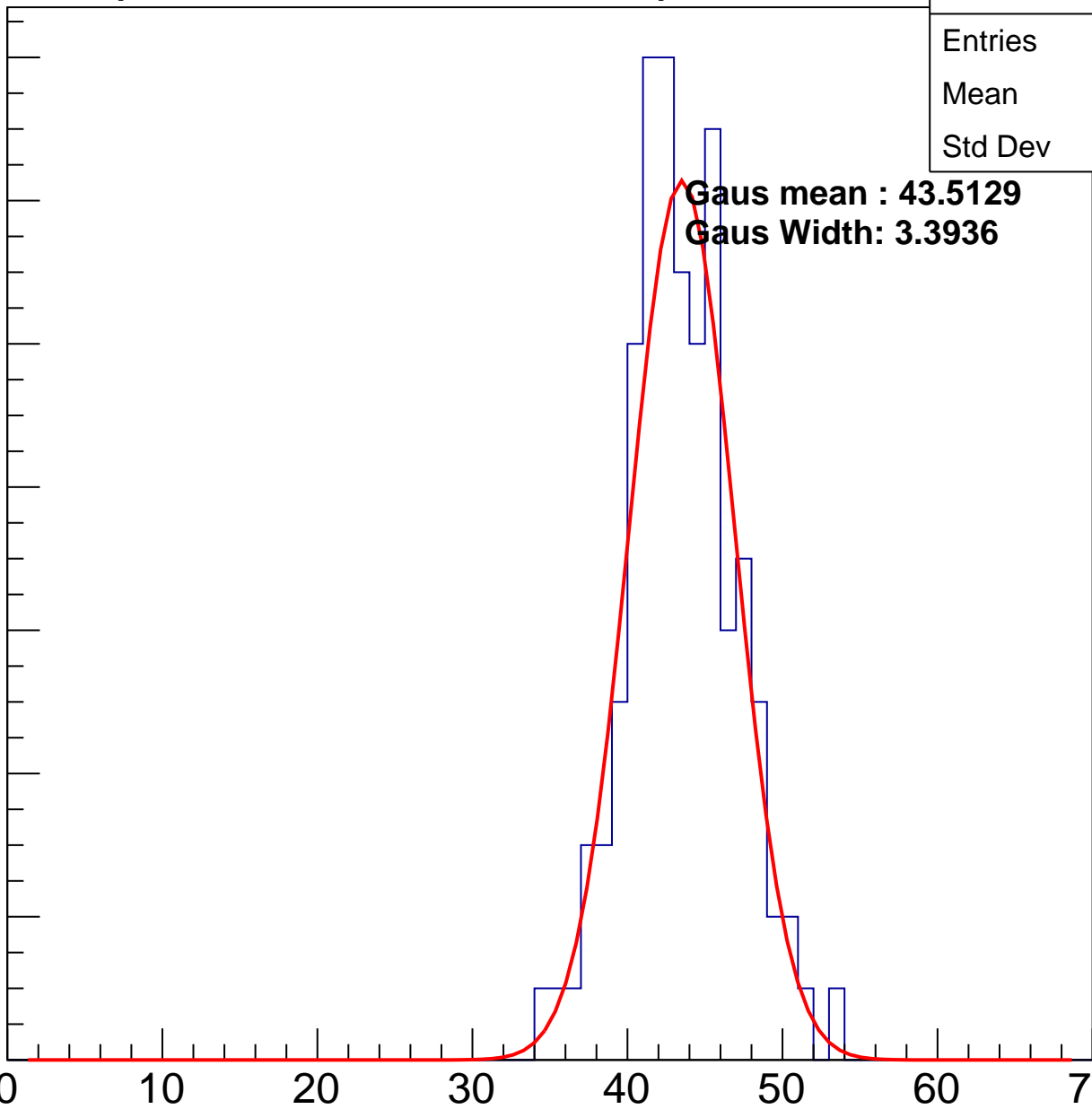
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 110 |
| Mean | 43.02 |
| Std Dev | 3.443 |

Gaus mean : 43.5129

Gaus Width: 3.3936

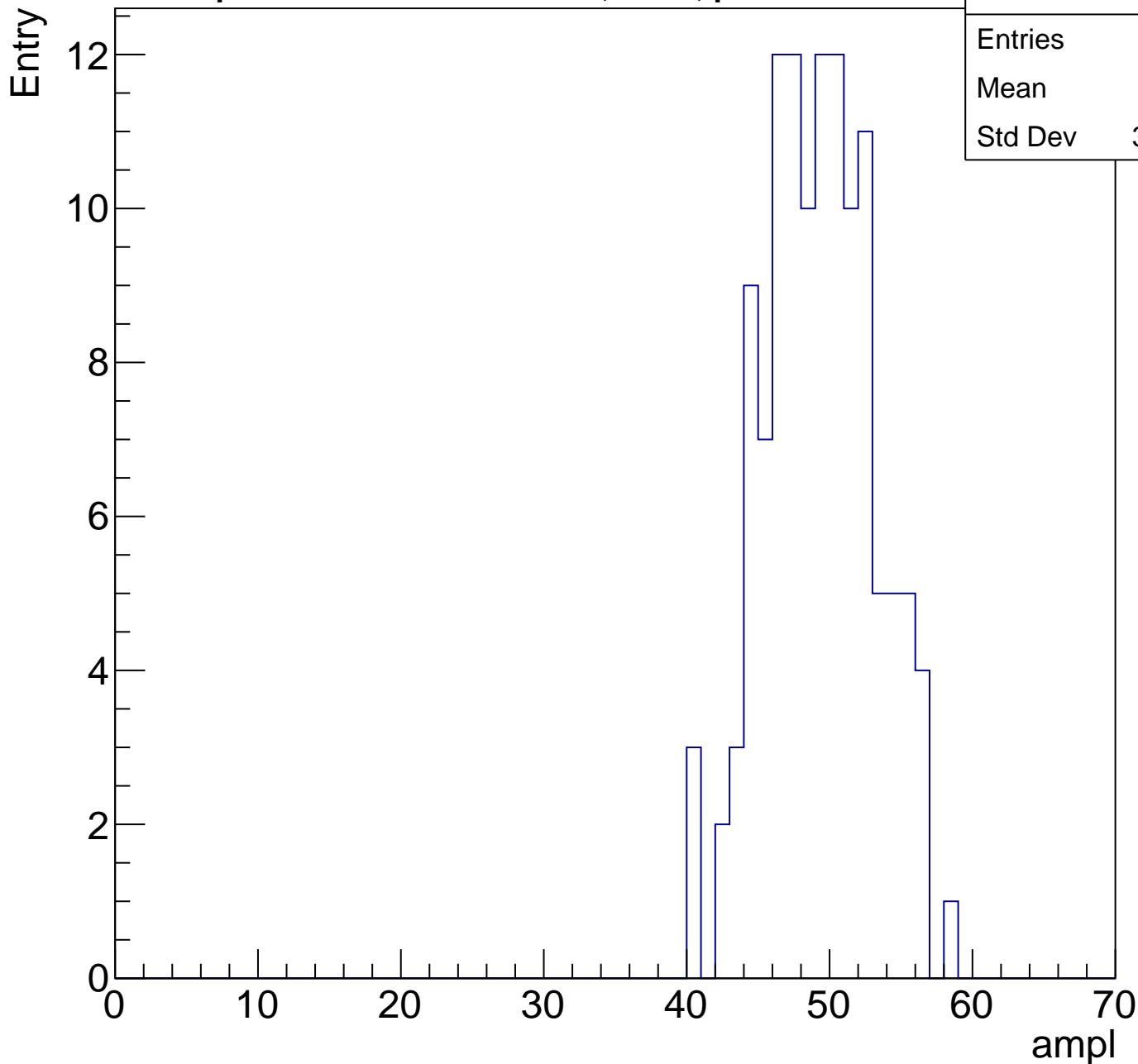
ampl



B0L101S, U22-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 48.8 |
| Std Dev | 3.783 |



B0L101S, U22-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 115 |
| Mean | 54.13 |
| Std Dev | 3.537 |

Entry

12

10

8

6

4

2

0

0

10

20

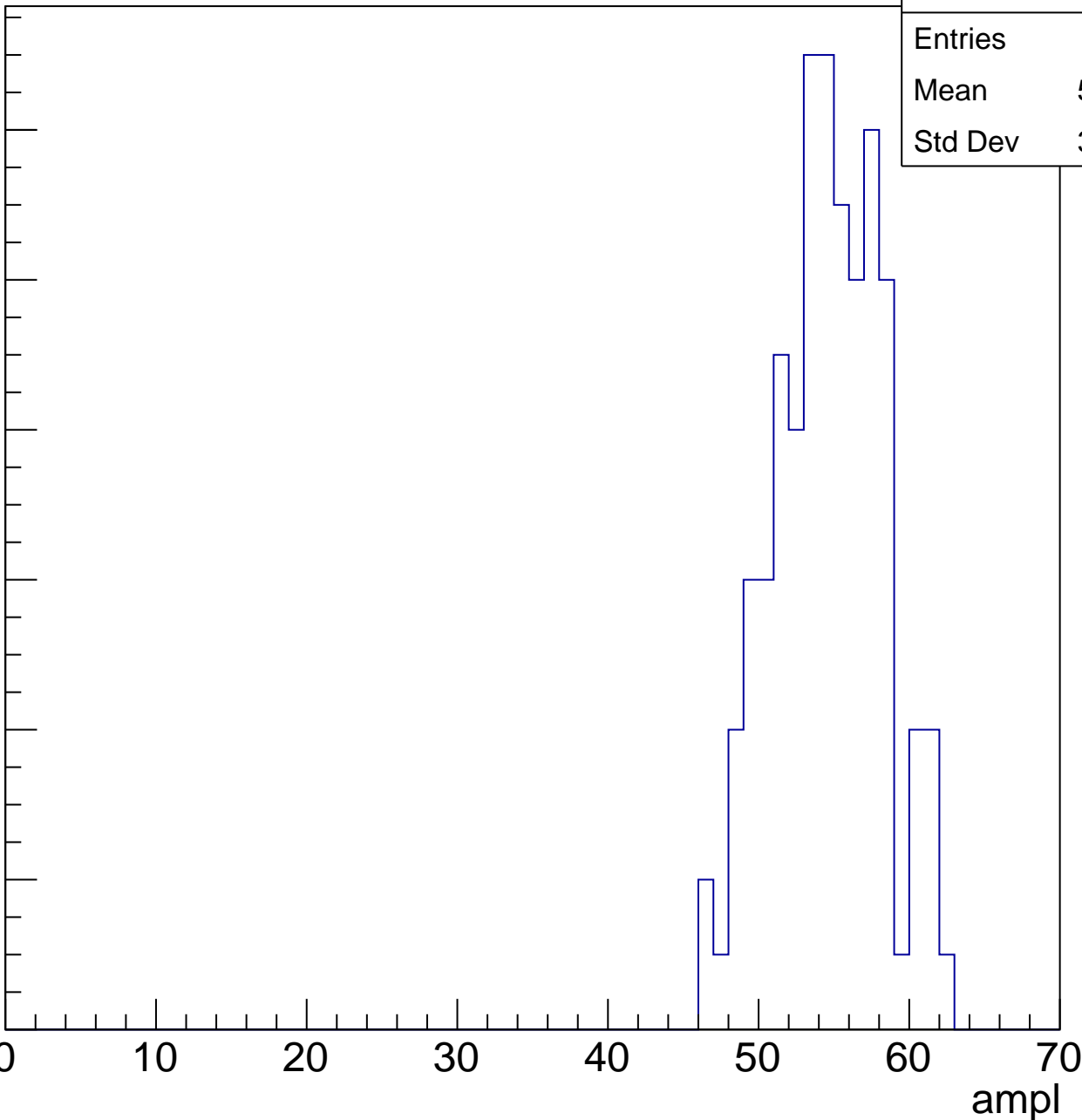
30

40

50

60

ampl



B0L101S, U22-ch40, adc5

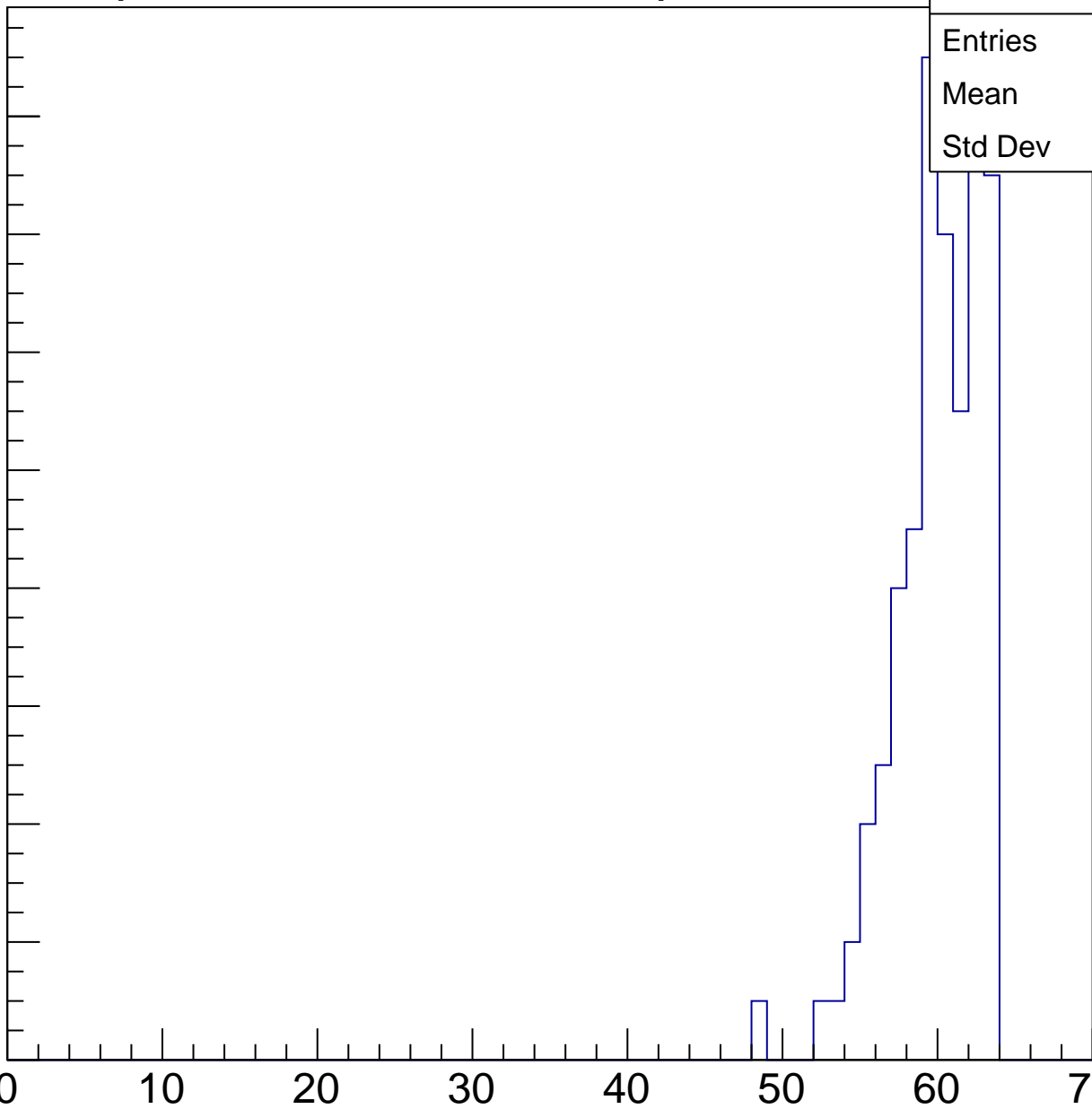
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 105 |
| Mean | 59.54 |
| Std Dev | 2.805 |

ampl

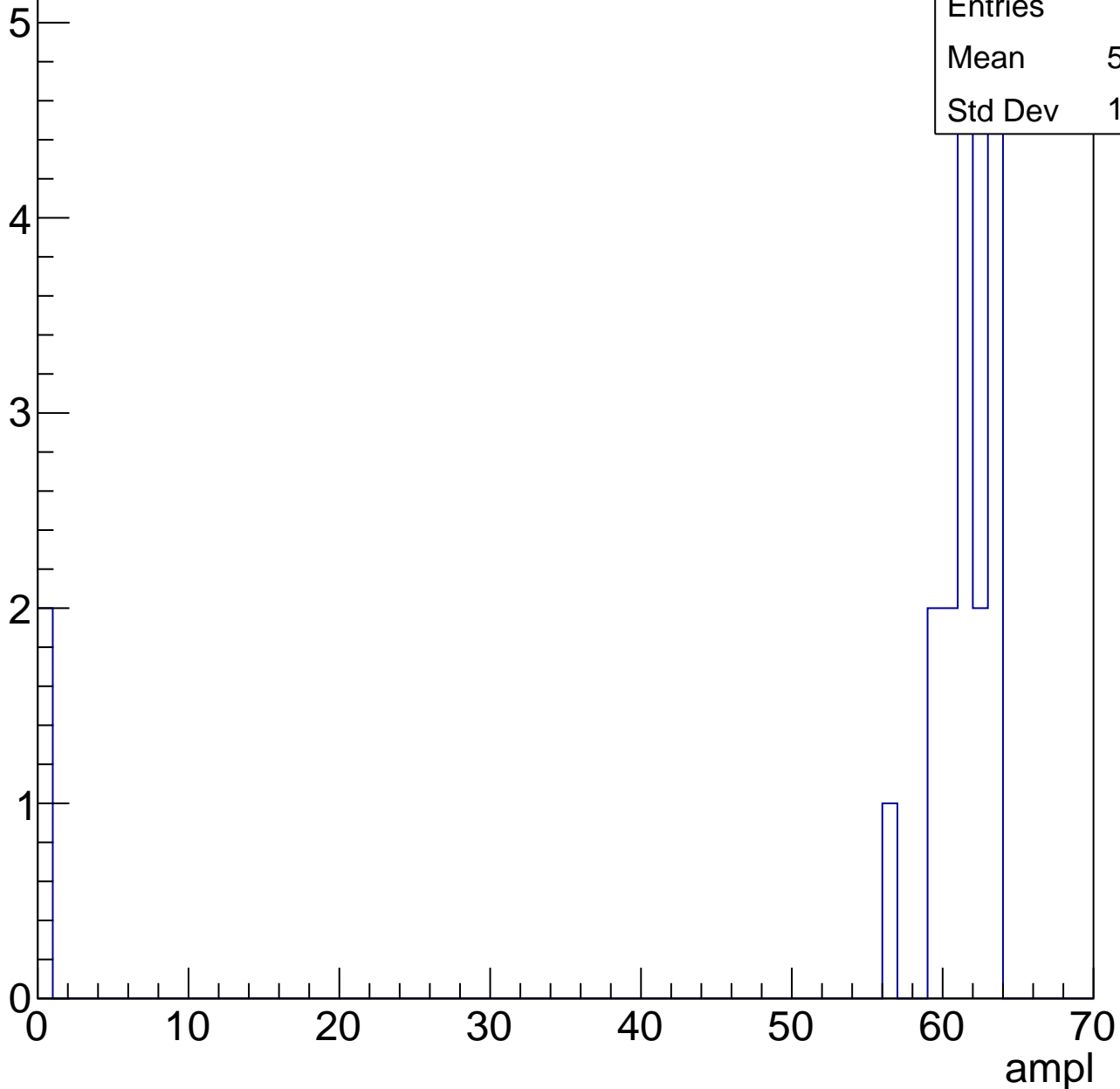


B0L101S, U22-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 19 |
| Mean | 54.63 |
| Std Dev | 18.82 |



B0L101S, U22-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch41, adc0

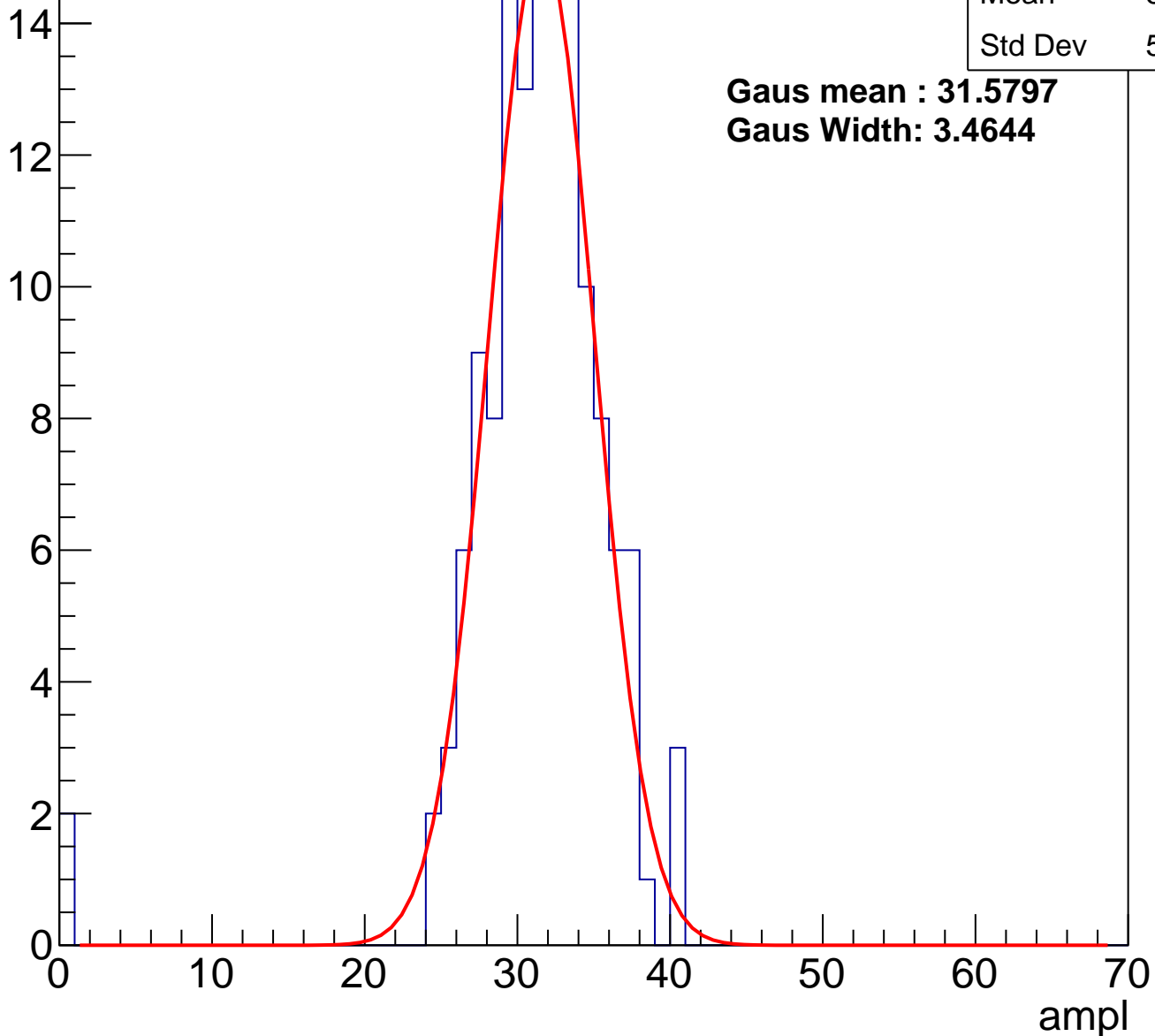
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 30.85 |
| Std Dev | 5.065 |

Gaus mean : 31.5797

Gaus Width: 3.4644

Entry



B0L101S, U22-ch41, adc1

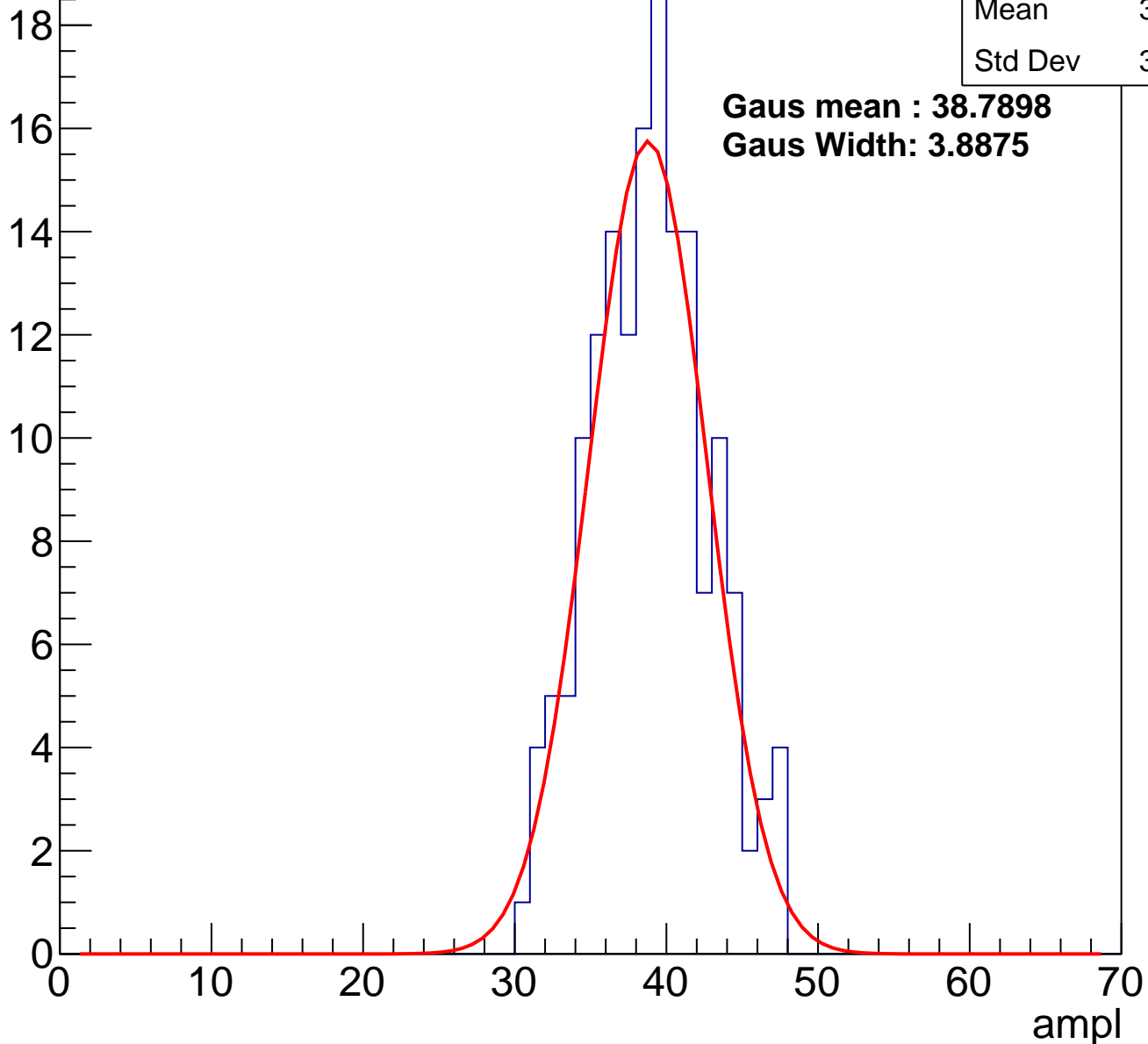
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 38.48 |
| Std Dev | 3.798 |

Gaus mean : 38.7898

Gaus Width: 3.8875

Entry



B0L101S, U22-ch41, adc2

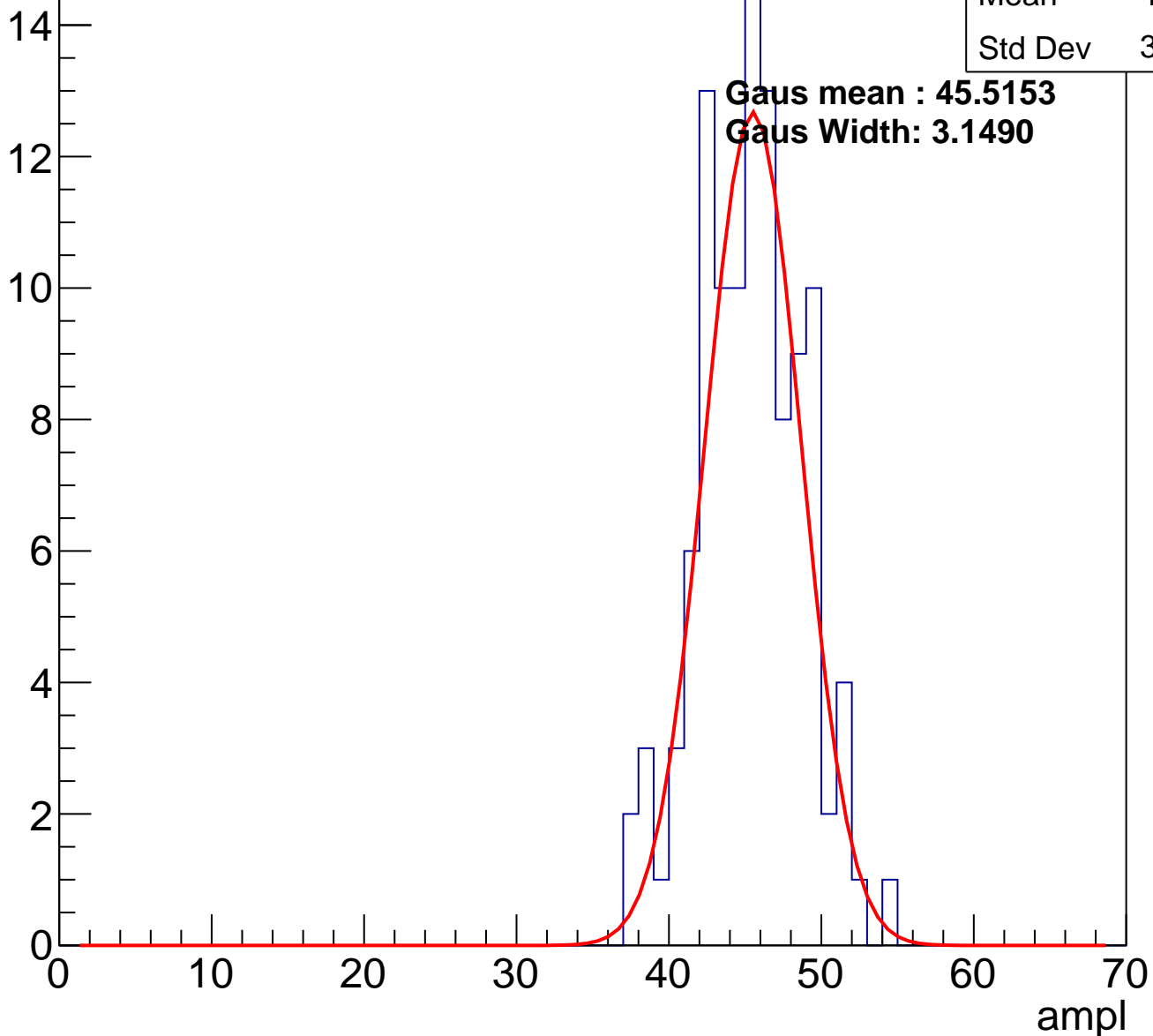
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 111 |
| Mean | 44.95 |
| Std Dev | 3.368 |

Gaus mean : 45.5153

Gaus Width: 3.1490

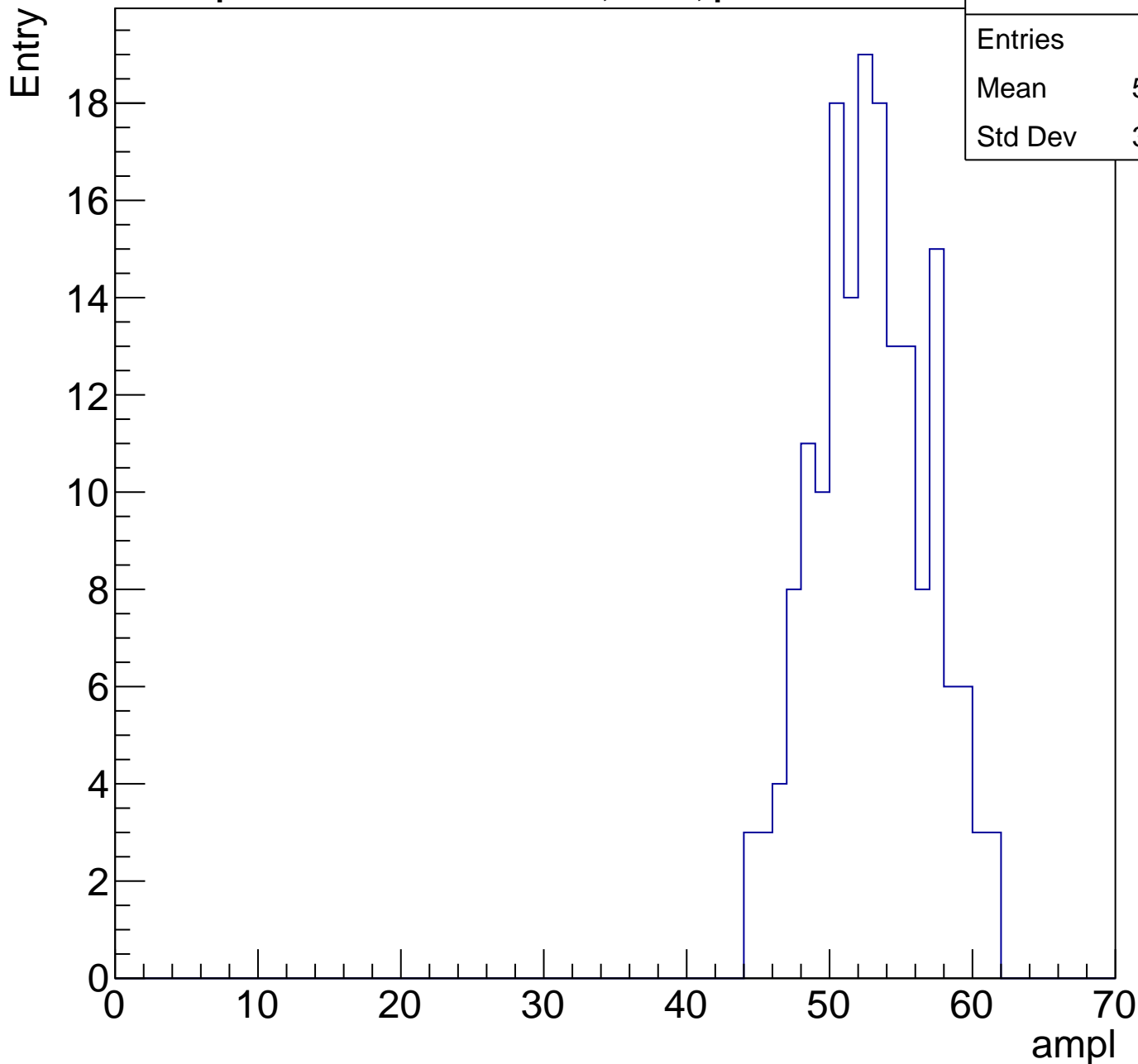
Entry



B0L101S, U22-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 175 |
| Mean | 52.49 |
| Std Dev | 3.875 |



B0L101S, U22-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 83 |
| Mean | 57.53 |
| Std Dev | 7.012 |

0

2

4

6

8

10

B0L101S, U22-ch41, adc5

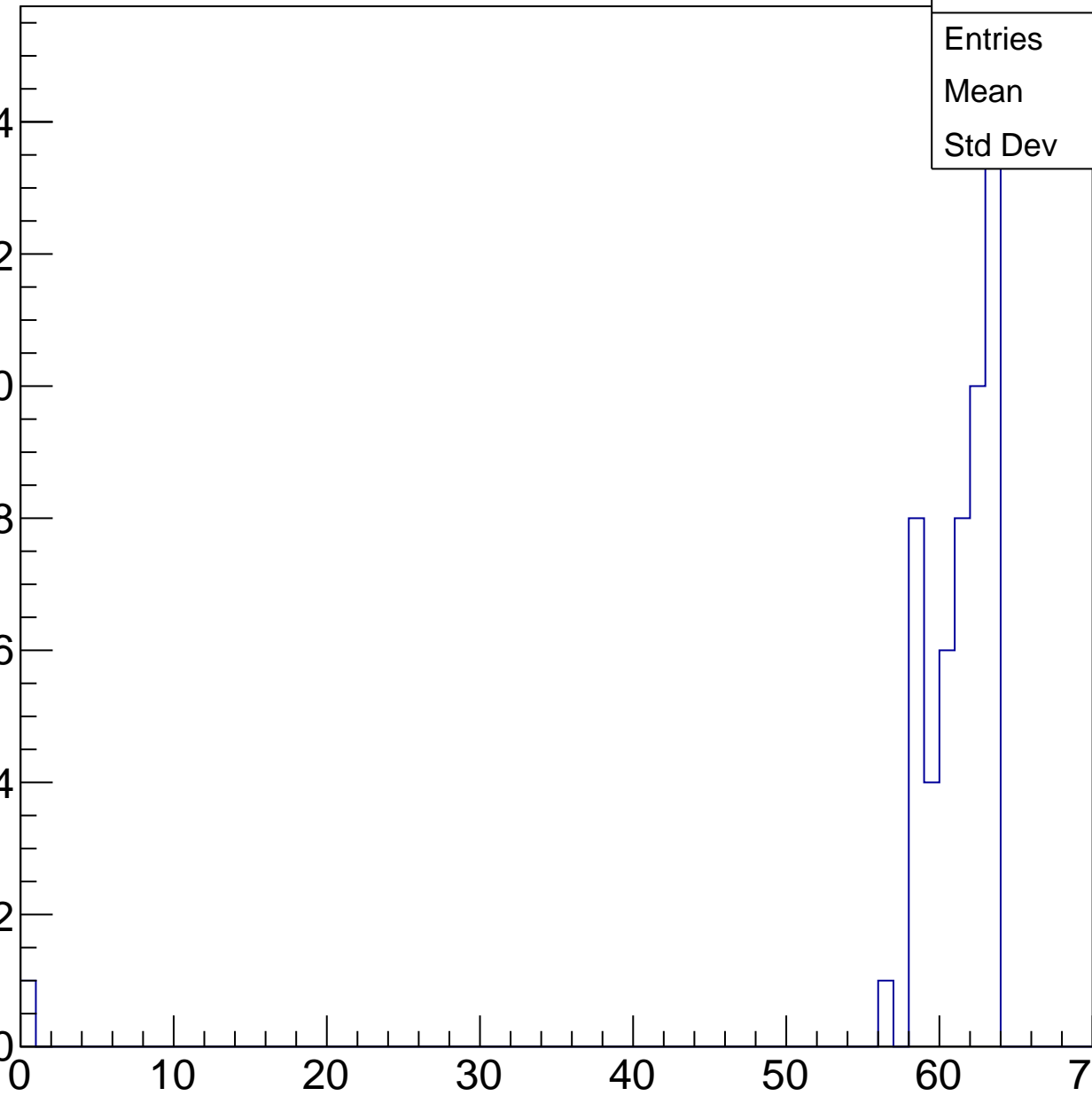
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.79 |
| Std Dev | 8.504 |

ampl



B0L101S, U22-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 23 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl

2

B0L101S, U22-ch42, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 30.37 |
| Std Dev | 3.664 |

Gaus mean : 30.6528

Gaus Width: 3.8455

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

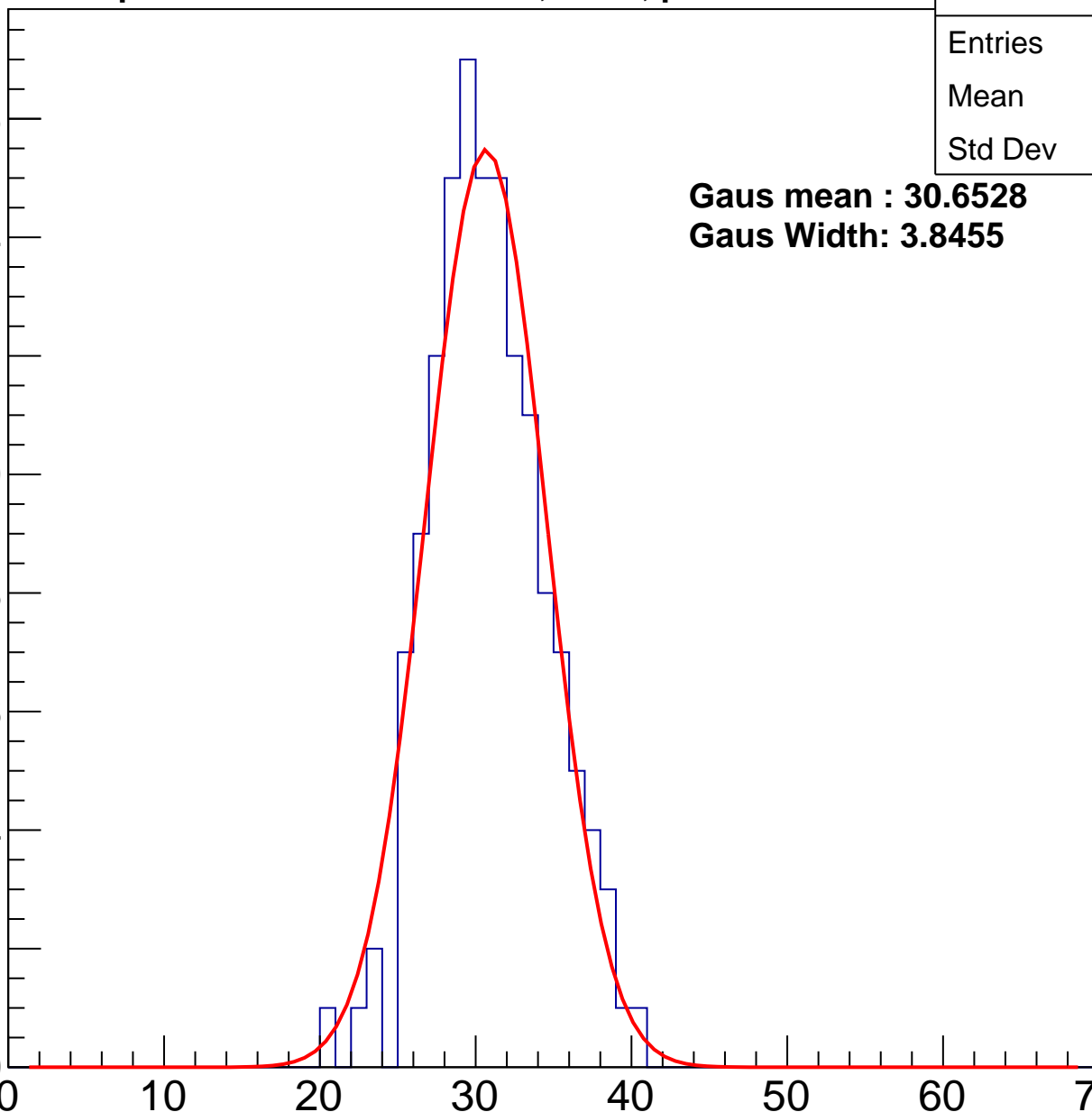
40

50

60

70

ampl



B0L101S, U22-ch42, adc1

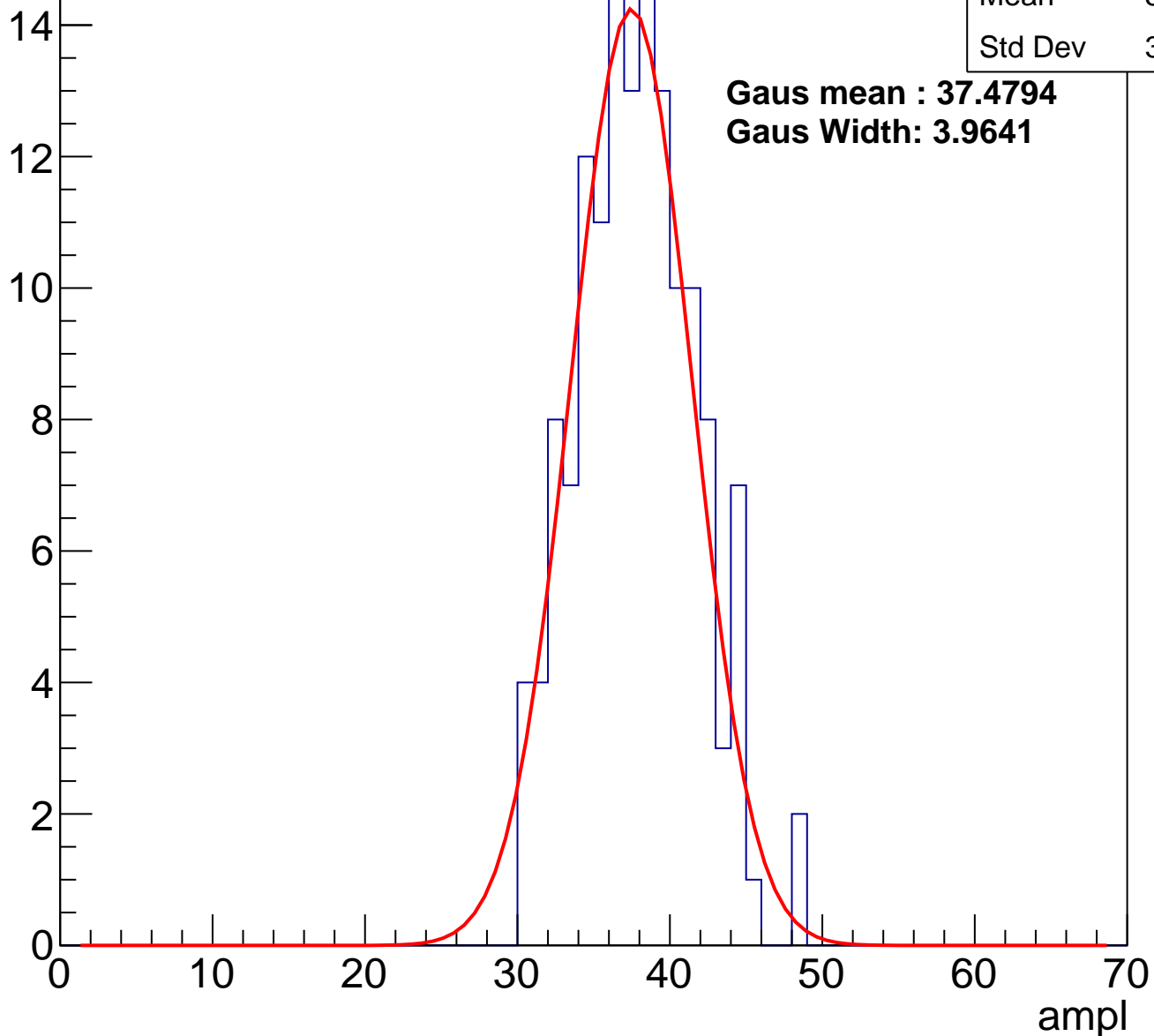
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 37.38 |
| Std Dev | 3.806 |

Gaus mean : 37.4794

Gaus Width: 3.9641

Entry



B0L101S, U22-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

44.41

Std Dev

3.602

Gaus mean : 44.9437

Gaus Width: 3.7030

0

10

20

30

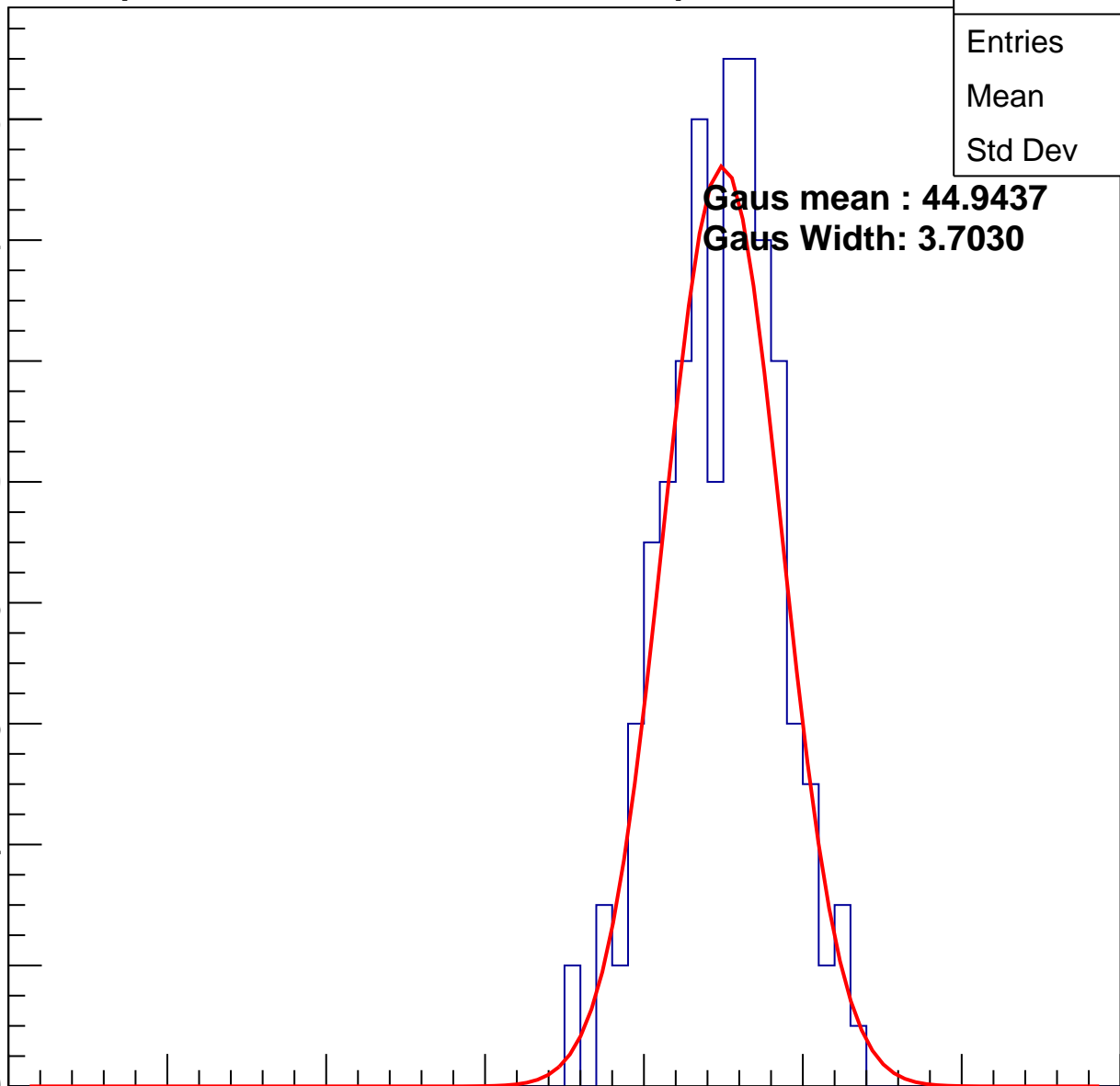
40

50

60

70

ampl



B0L101S, U22-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

100

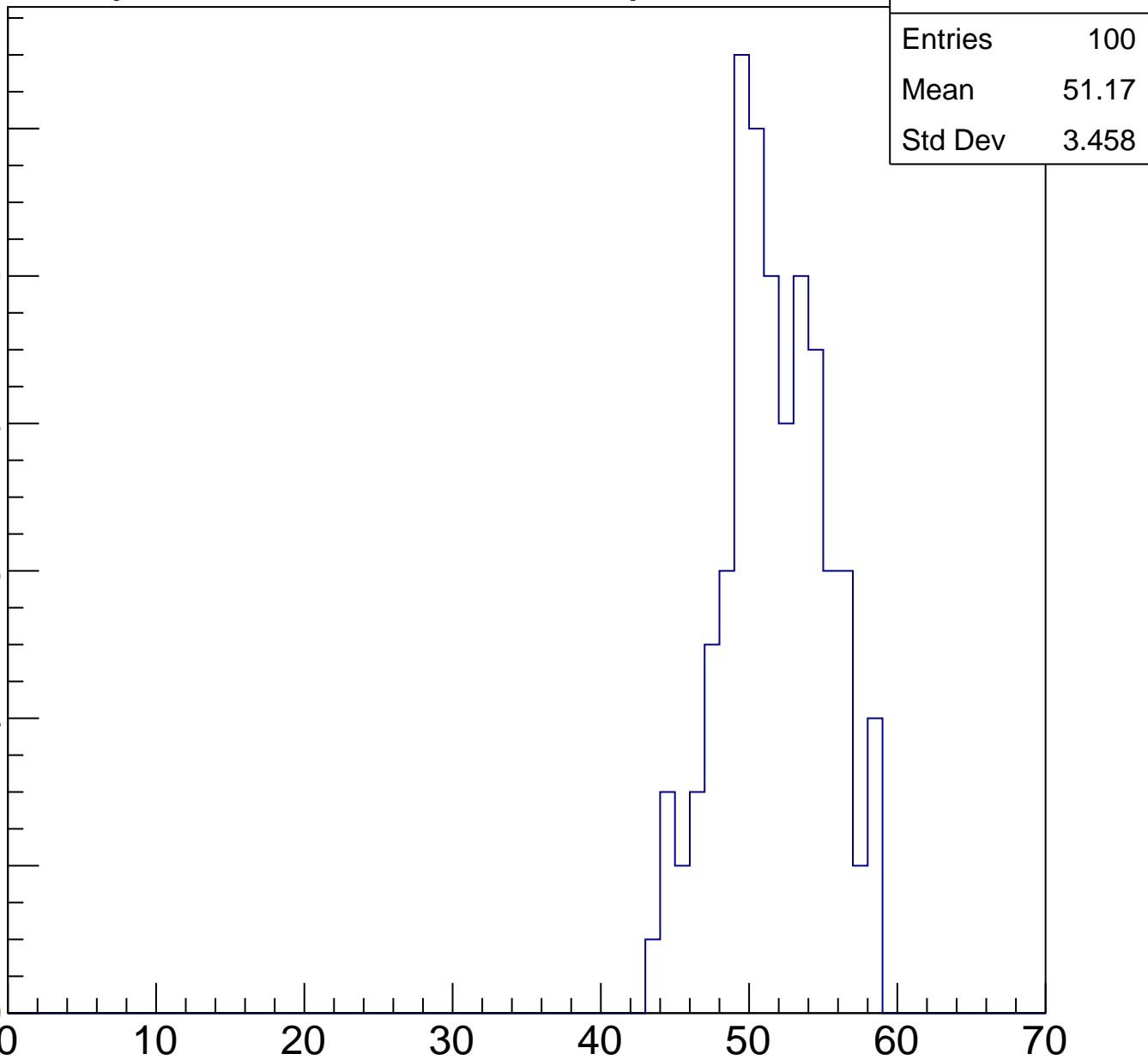
Mean

51.17

Std Dev

3.458

ampl



B0L101S, U22-ch42, adc4

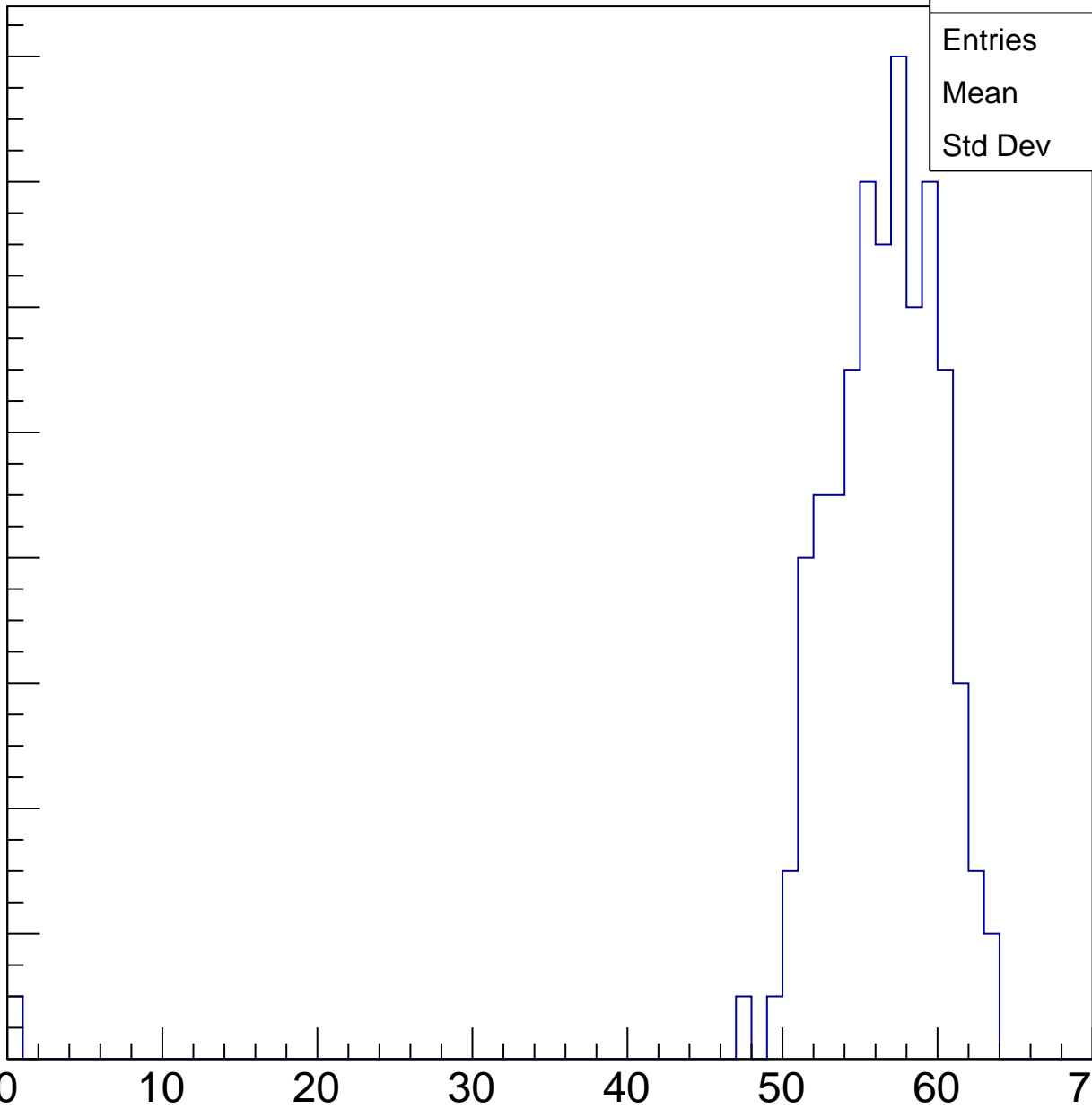
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 55.69 |
| Std Dev | 5.838 |

ampl



B0L101S, U22-ch42, adc5

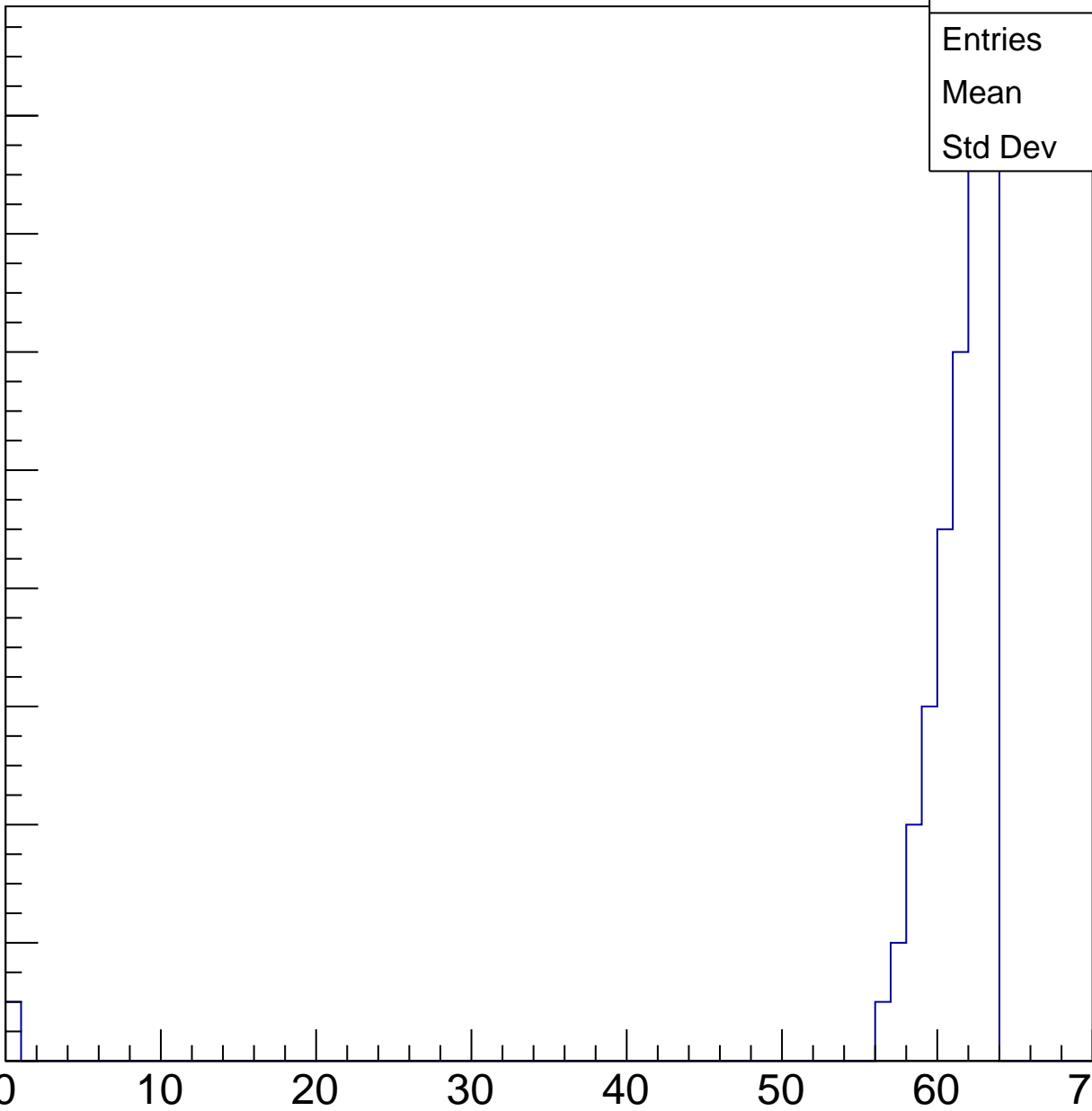
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 68 |
| Mean | 60.15 |
| Std Dev | 7.552 |

ampl



B0L101S, U22-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 30.3 |
| Std Dev | 3.488 |

Gaus mean : 30.5533

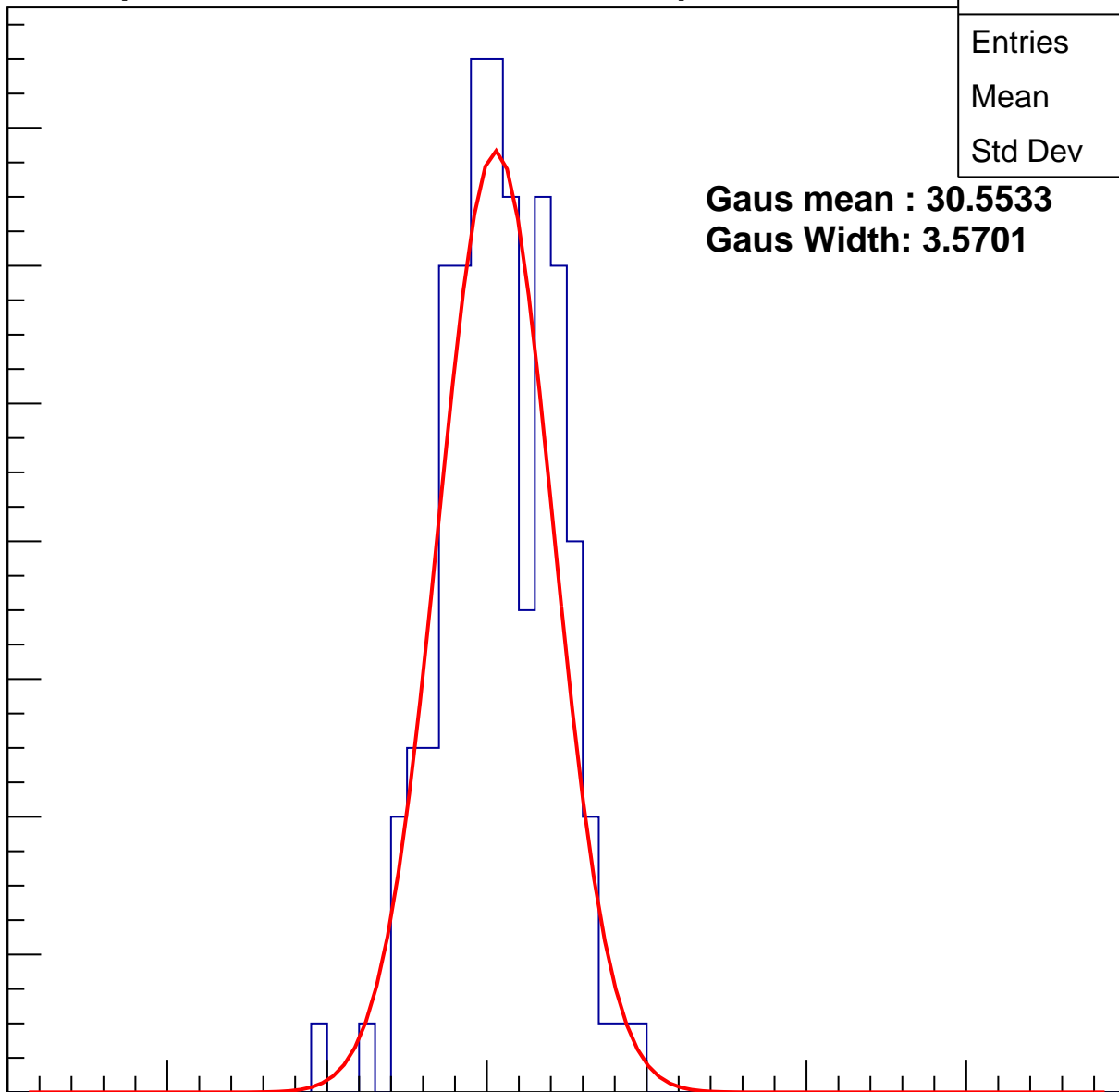
Gaus Width: 3.5701

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

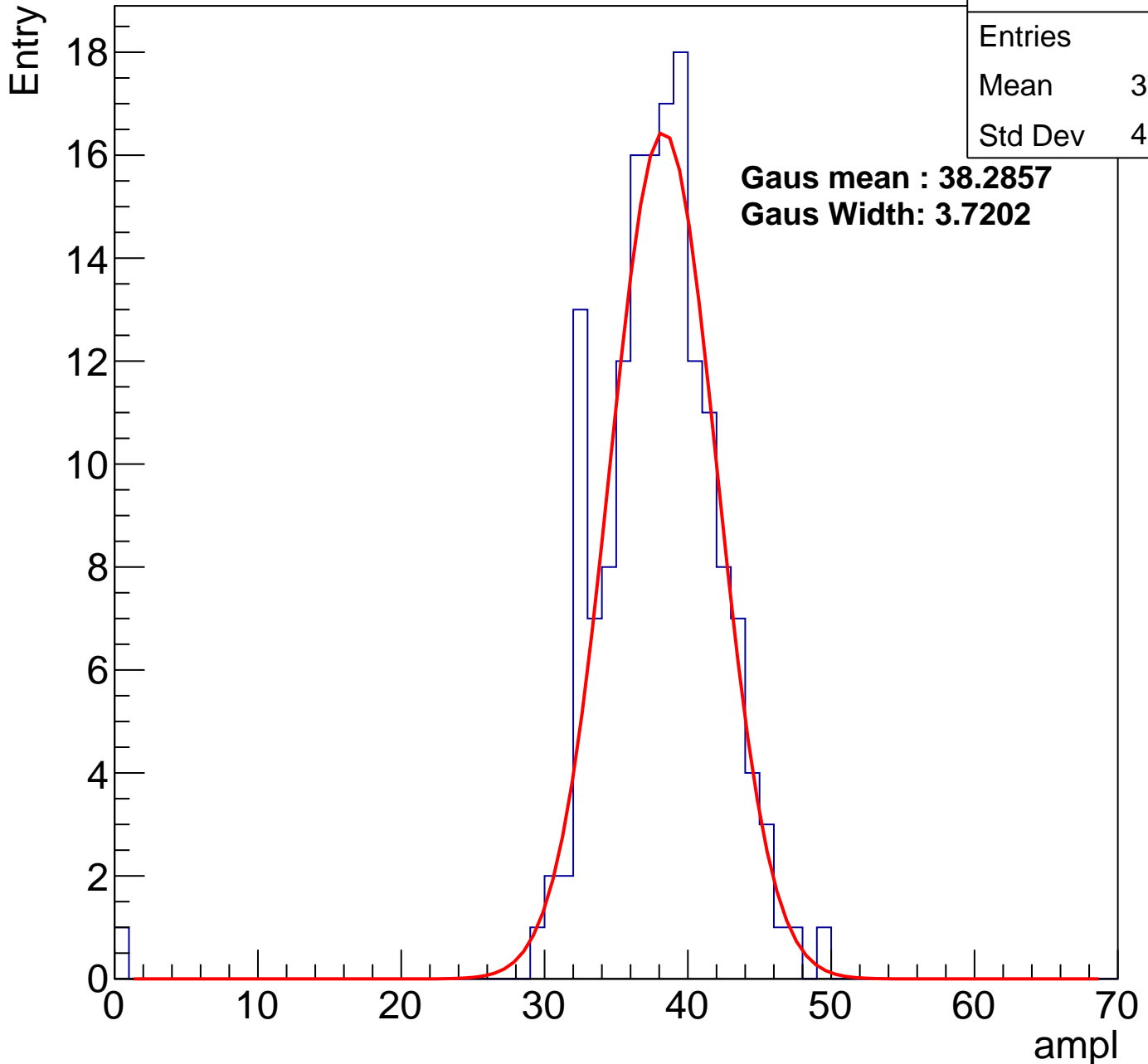


B0L101S, U22-ch43, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 37.43 |
| Std Dev | 4.783 |

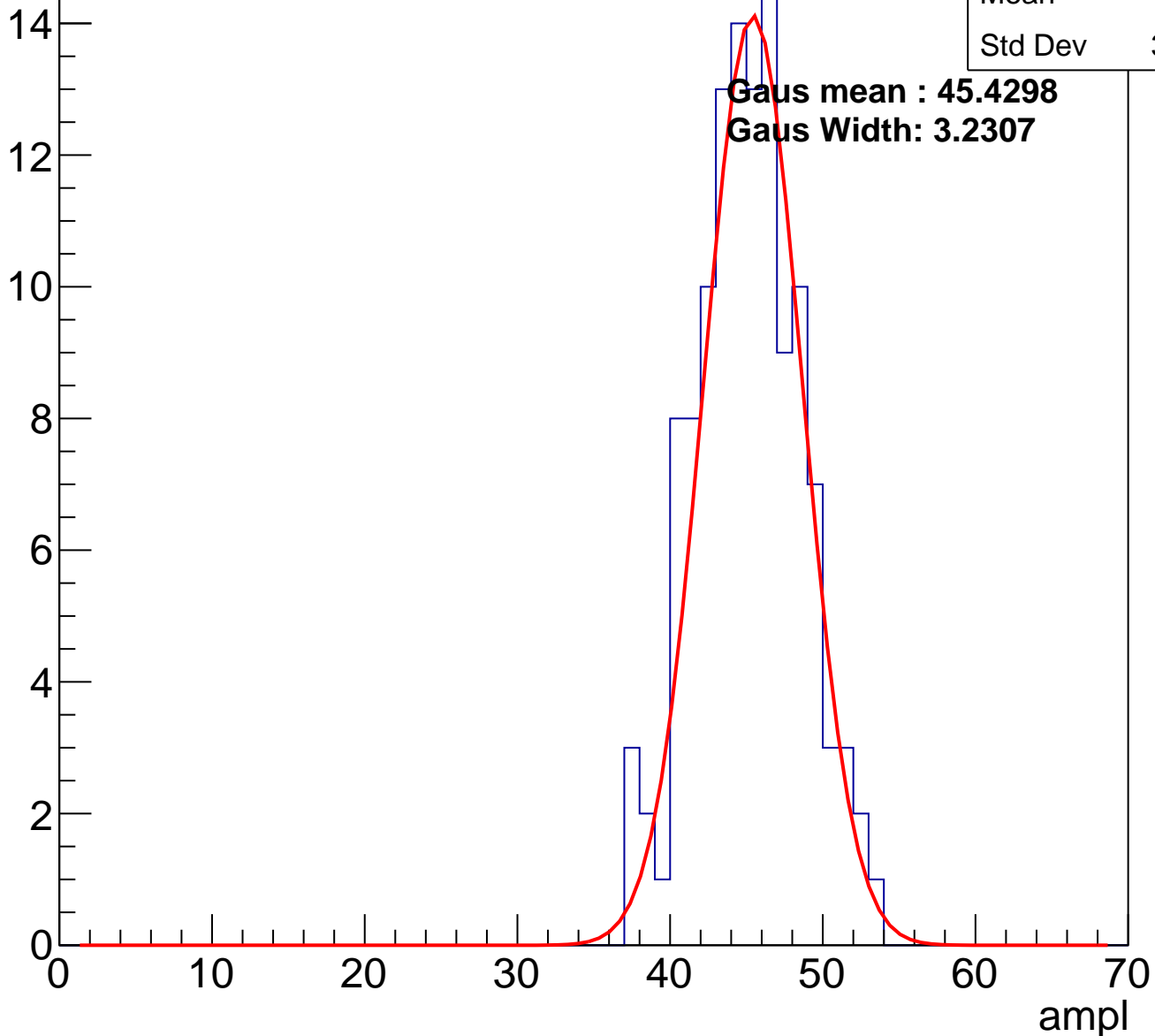
Gaus mean : 38.2857
Gaus Width: 3.7202



B0L101S, U22-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

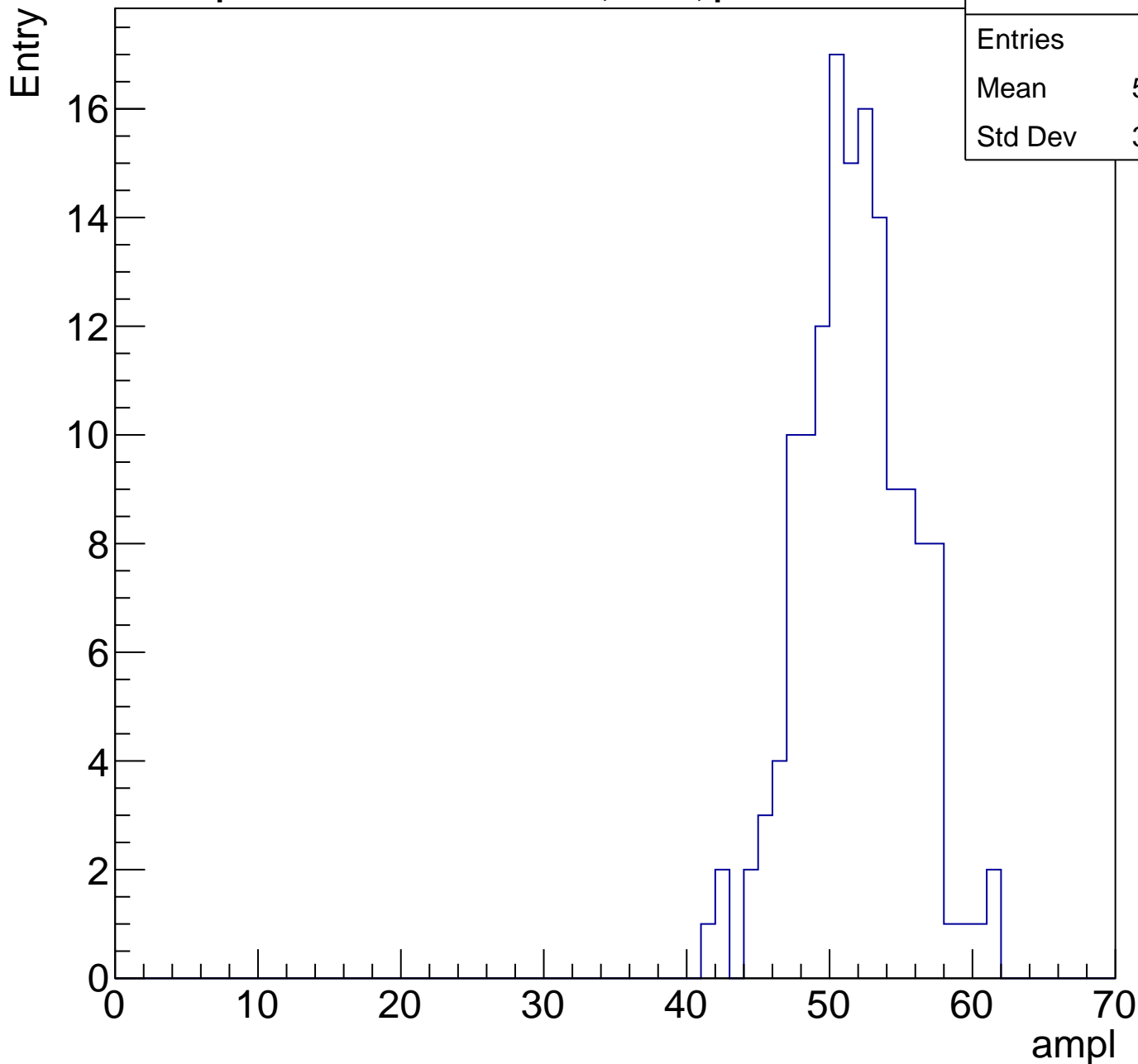
Entry



B0L101S, U22-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 51.33 |
| Std Dev | 3.756 |



B0L101S, U22-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

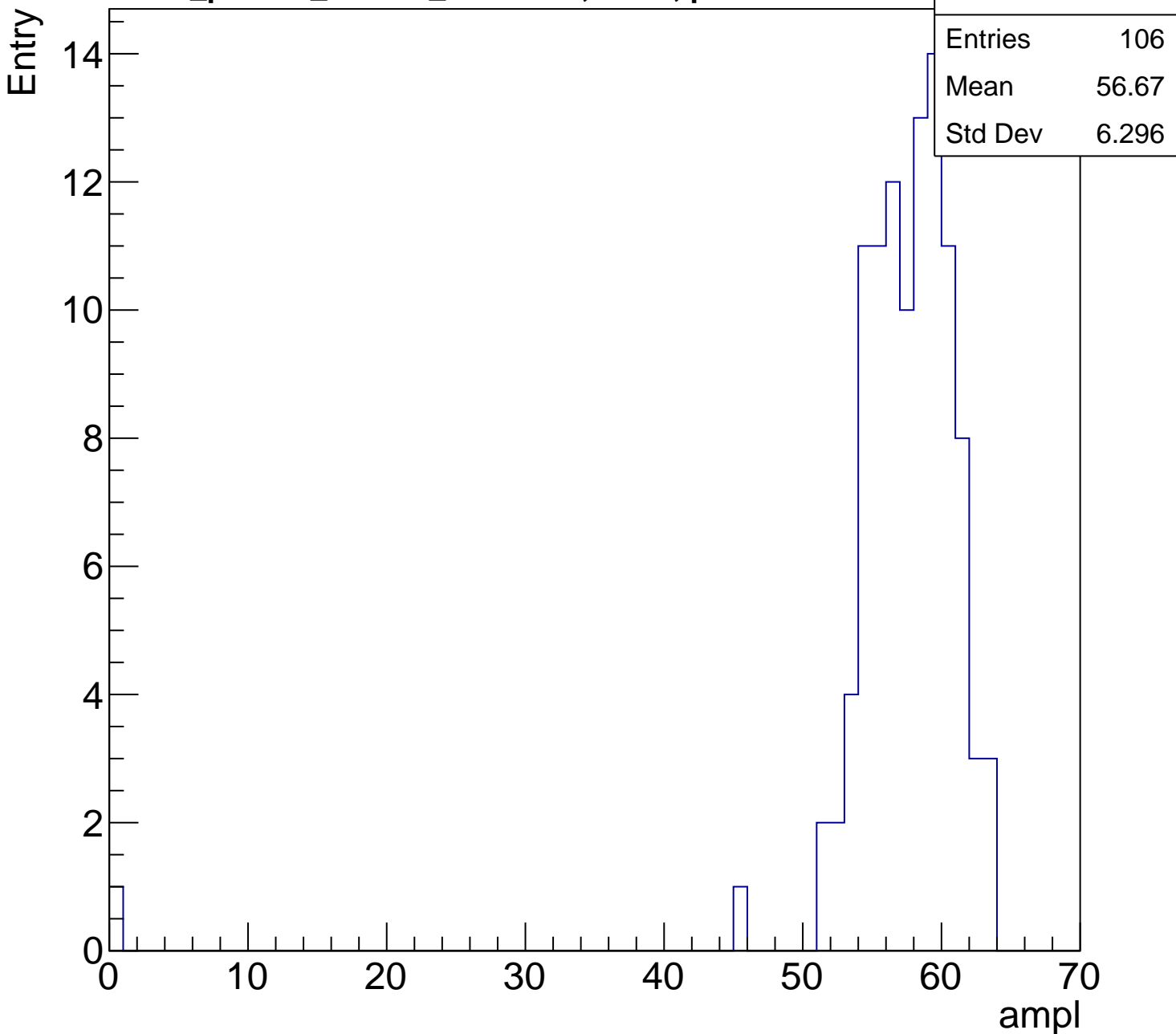
| | |
|---------|-------|
| Entries | 106 |
| Mean | 56.67 |
| Std Dev | 6.296 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch43, adc5

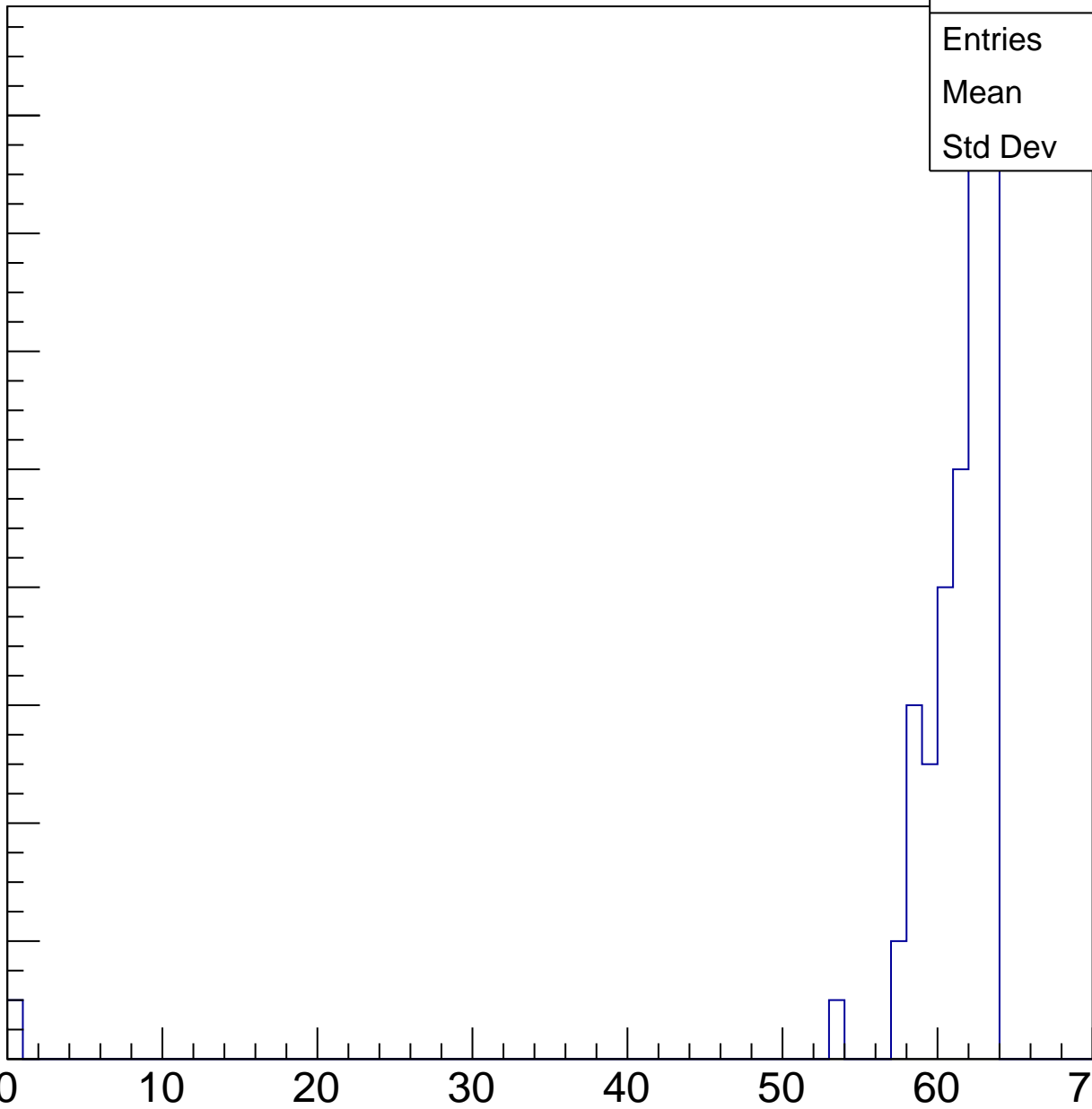
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 66 |
| Mean | 60.05 |
| Std Dev | 7.71 |

ampl



B0L101S, U22-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

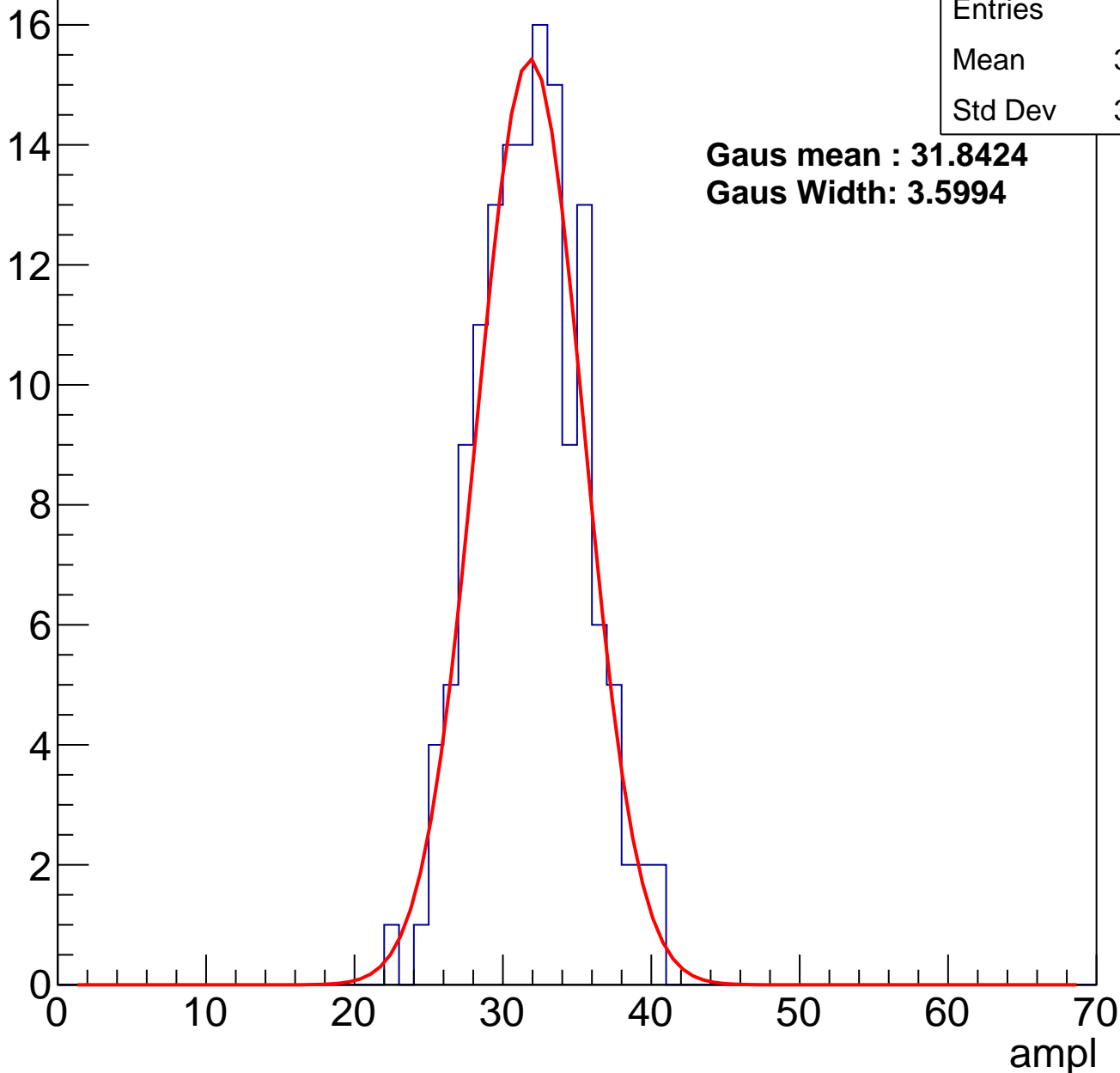
Entry



B0L101S, U22-ch44, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 38.68 |
| Std Dev | 3.639 |

Gaus mean : 39.2265

Gaus Width: 3.7645

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

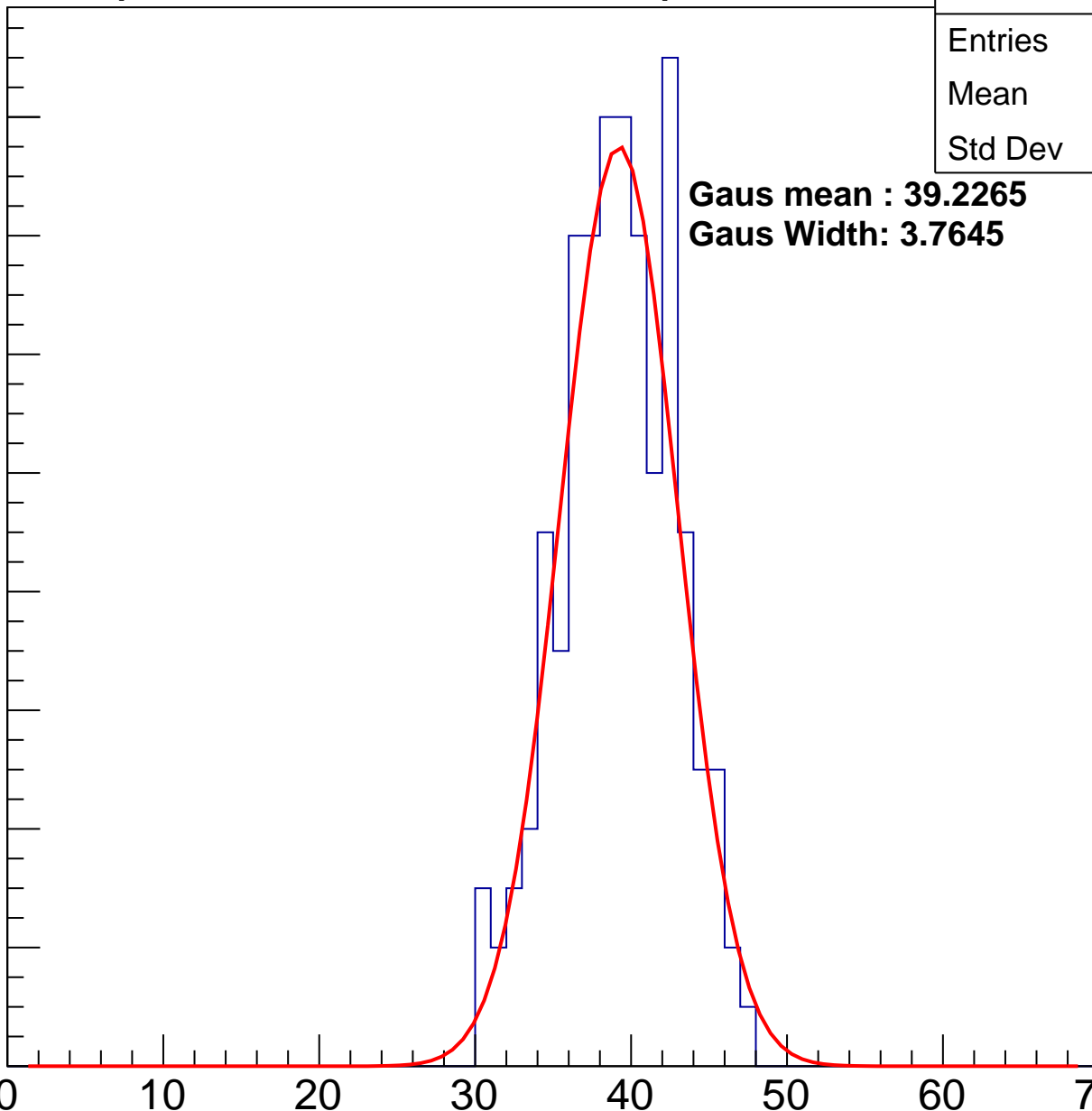
40

50

60

70

ampl



B0L101S, U22-ch44, adc2

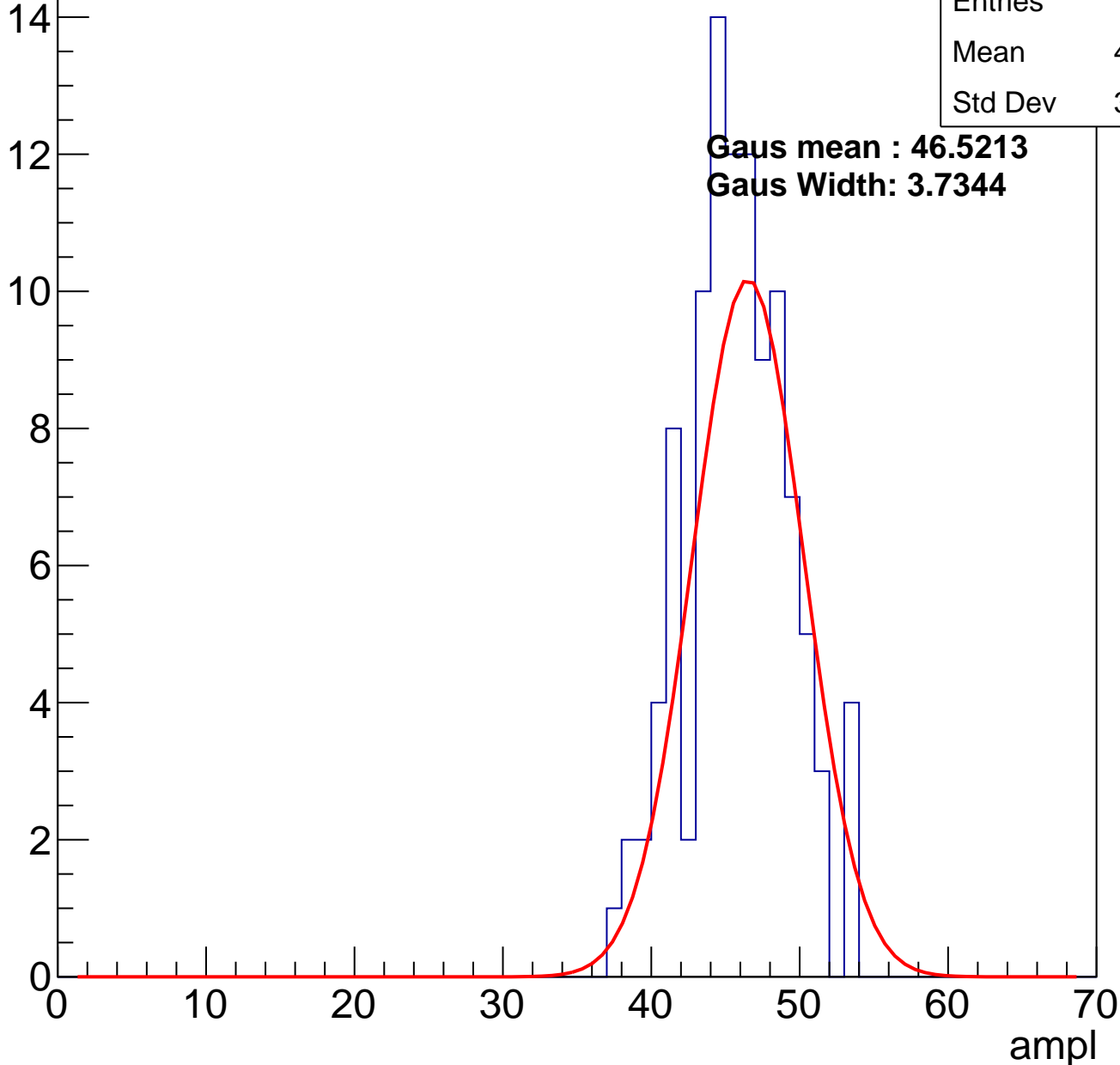
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 105 |
| Mean | 45.35 |
| Std Dev | 3.464 |

Gaus mean : 46.5213

Gaus Width: 3.7344

Entry

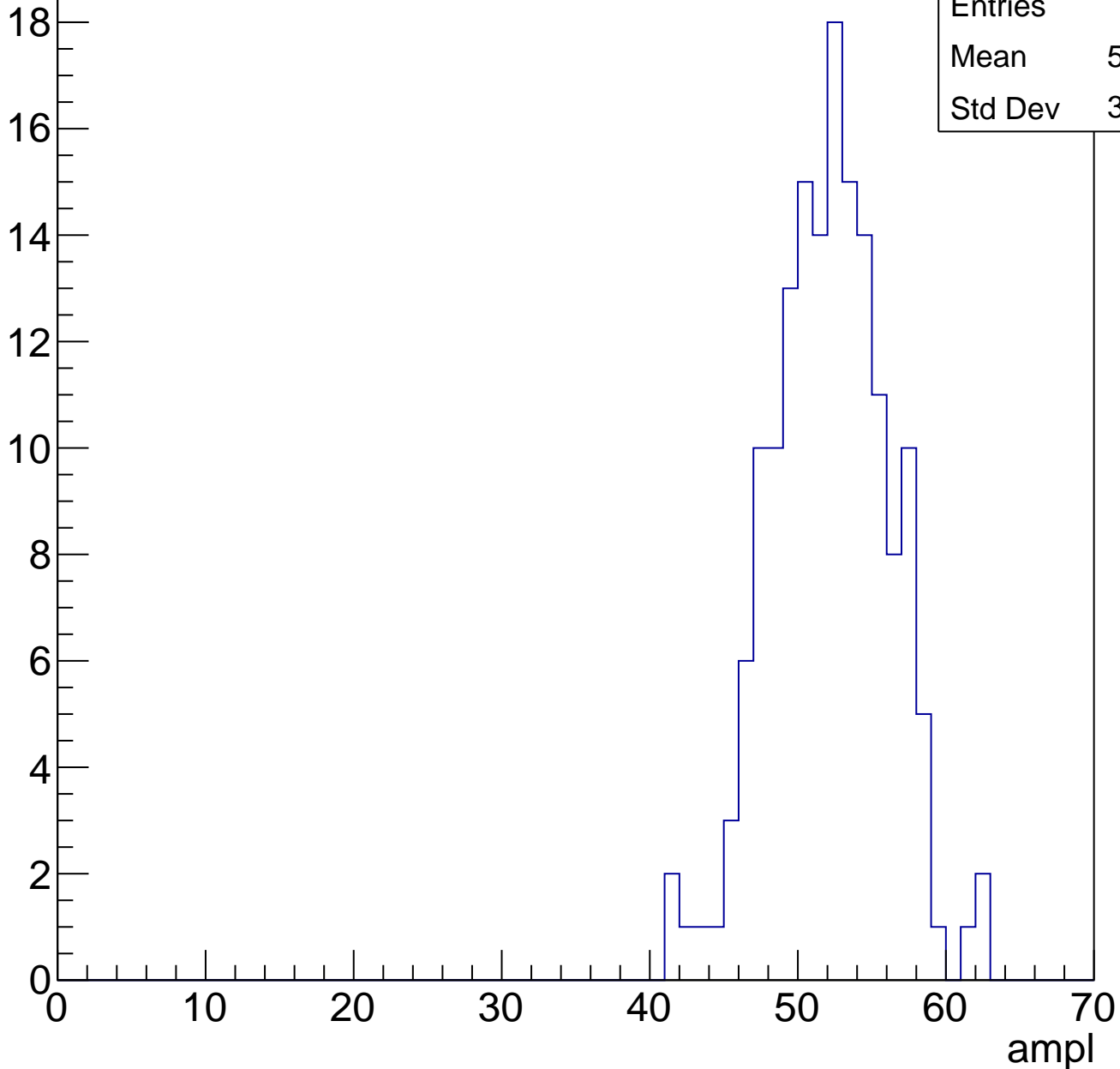


B0L101S, U22-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 51.66 |
| Std Dev | 3.933 |

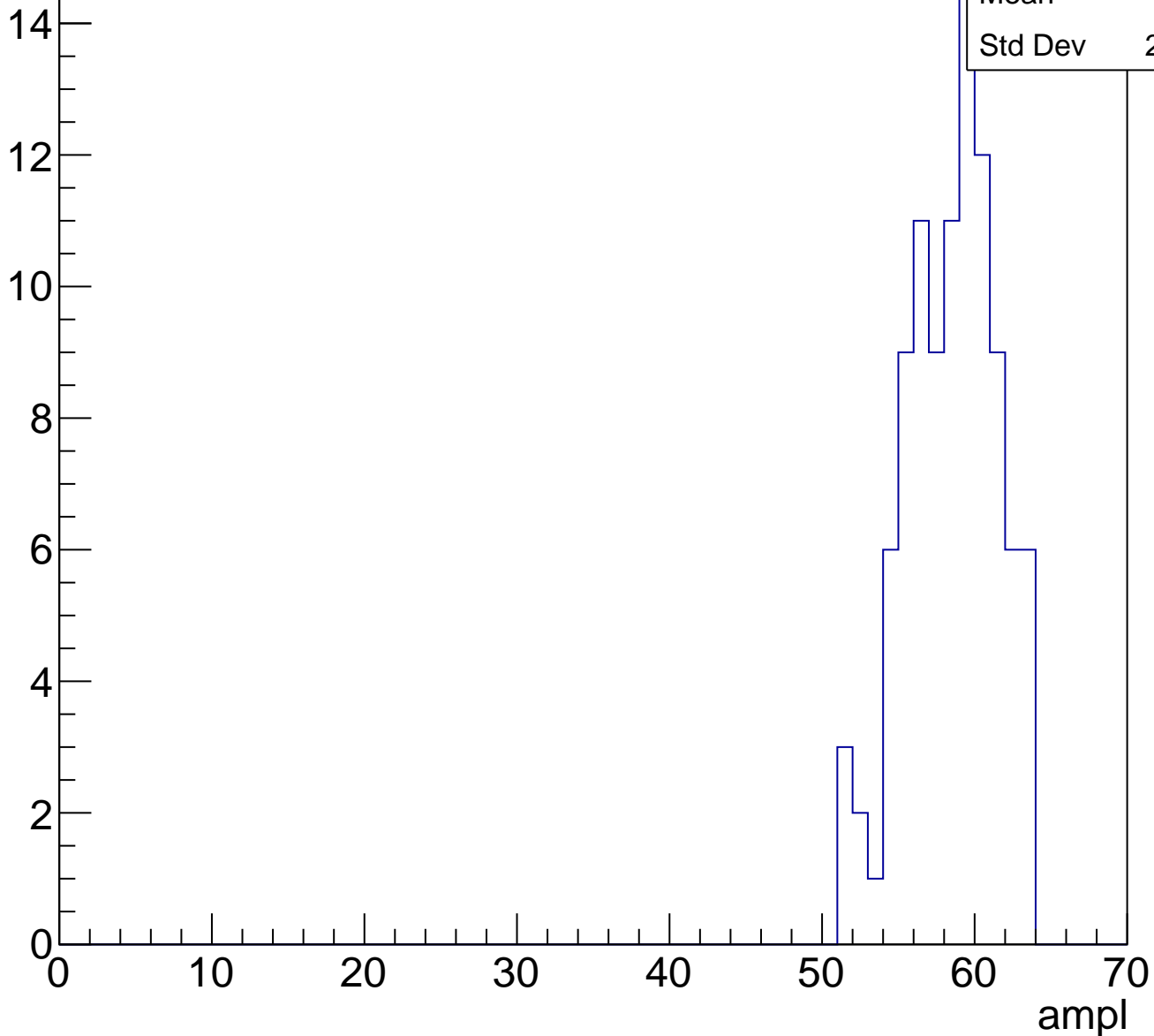
Entry



B0L101S, U22-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch44, adc5

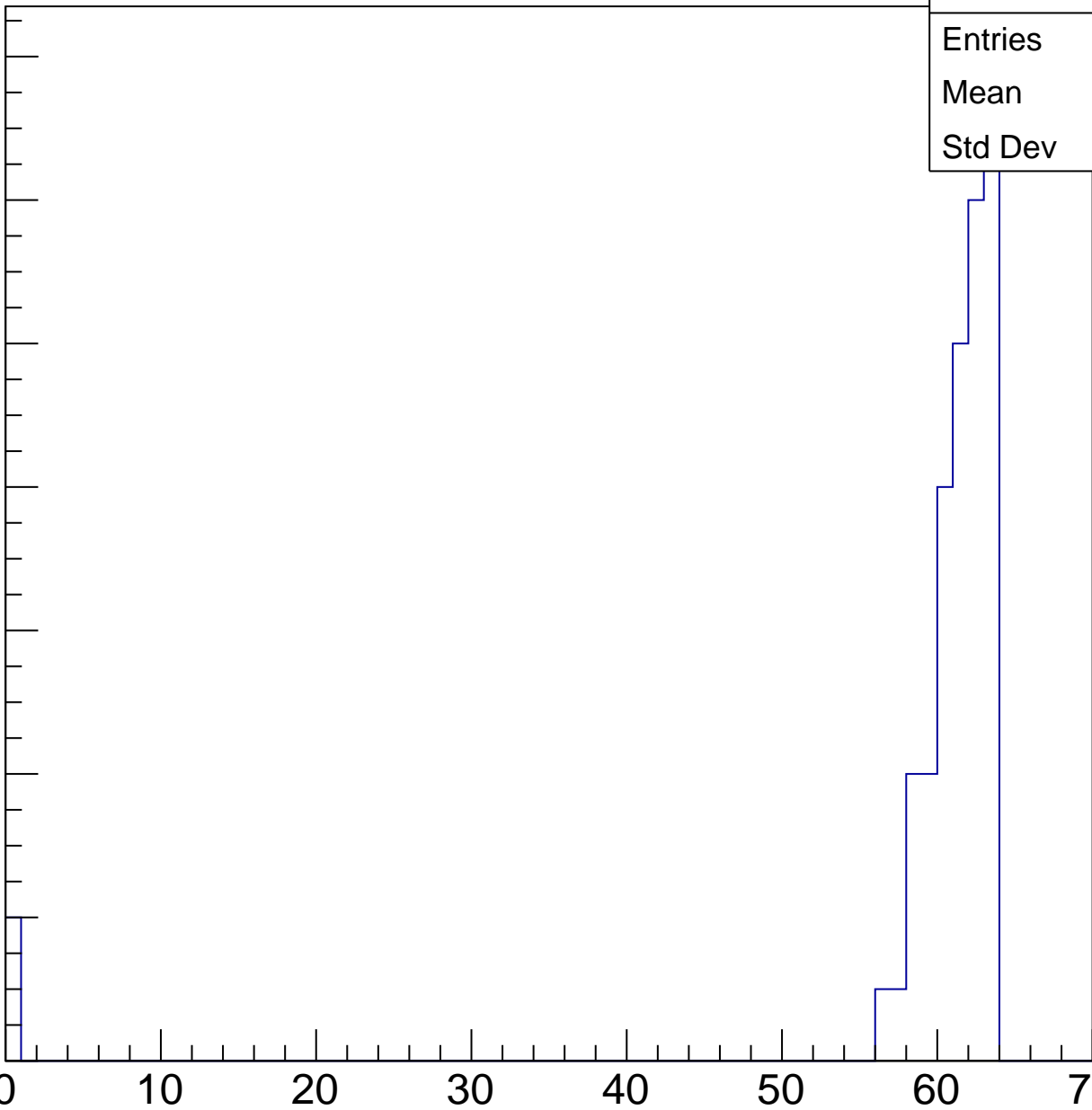
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.88 |
| Std Dev | 11.46 |

ampl



B0L101S, U22-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch45, adc0

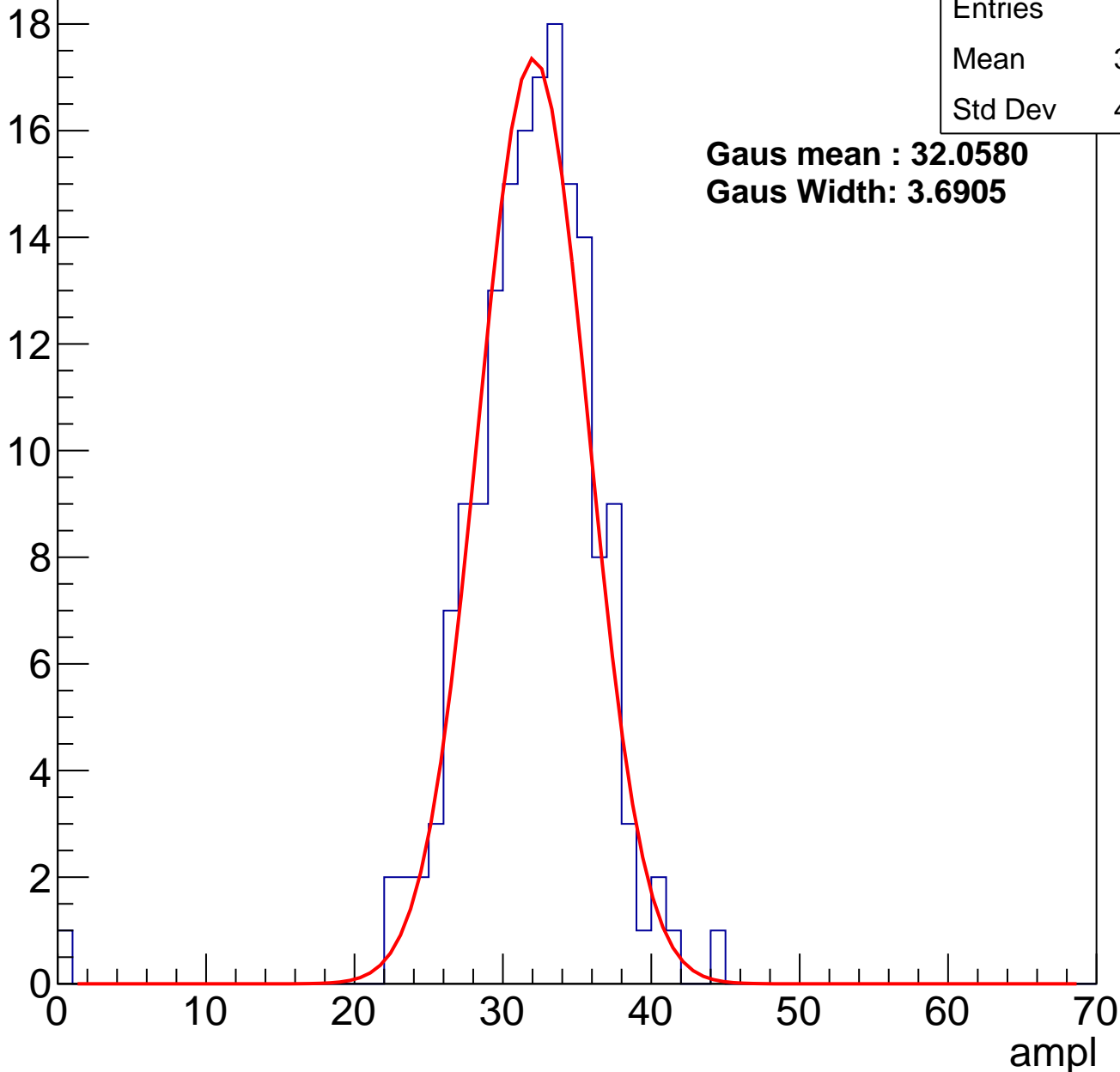
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 168 |
| Mean | 31.49 |
| Std Dev | 4.563 |

Gaus mean : 32.0580

Gaus Width: 3.6905

Entry



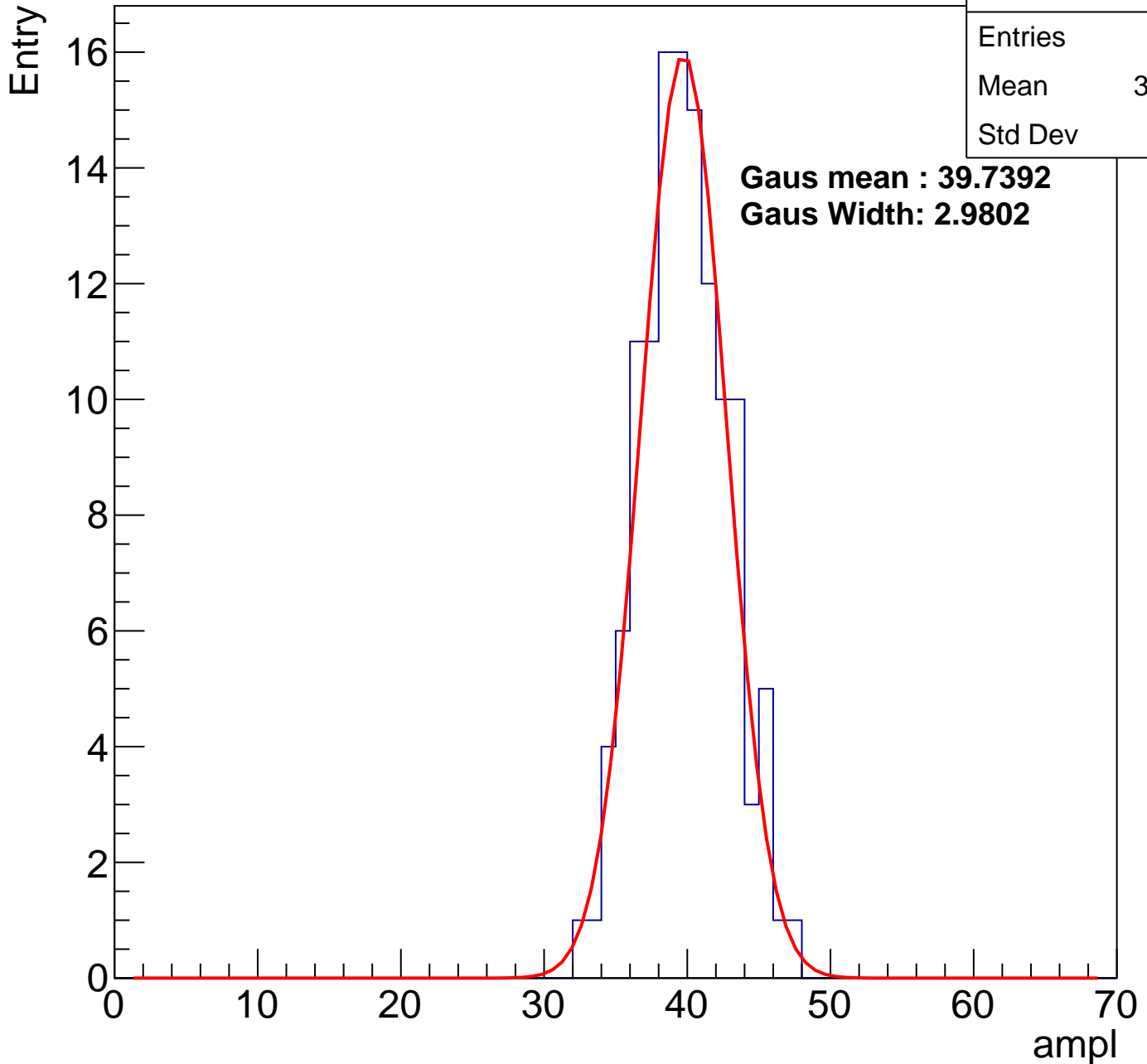
B0L101S, U22-ch45, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 39.33 |
| Std Dev | 3 |

Gaus mean : 39.7392

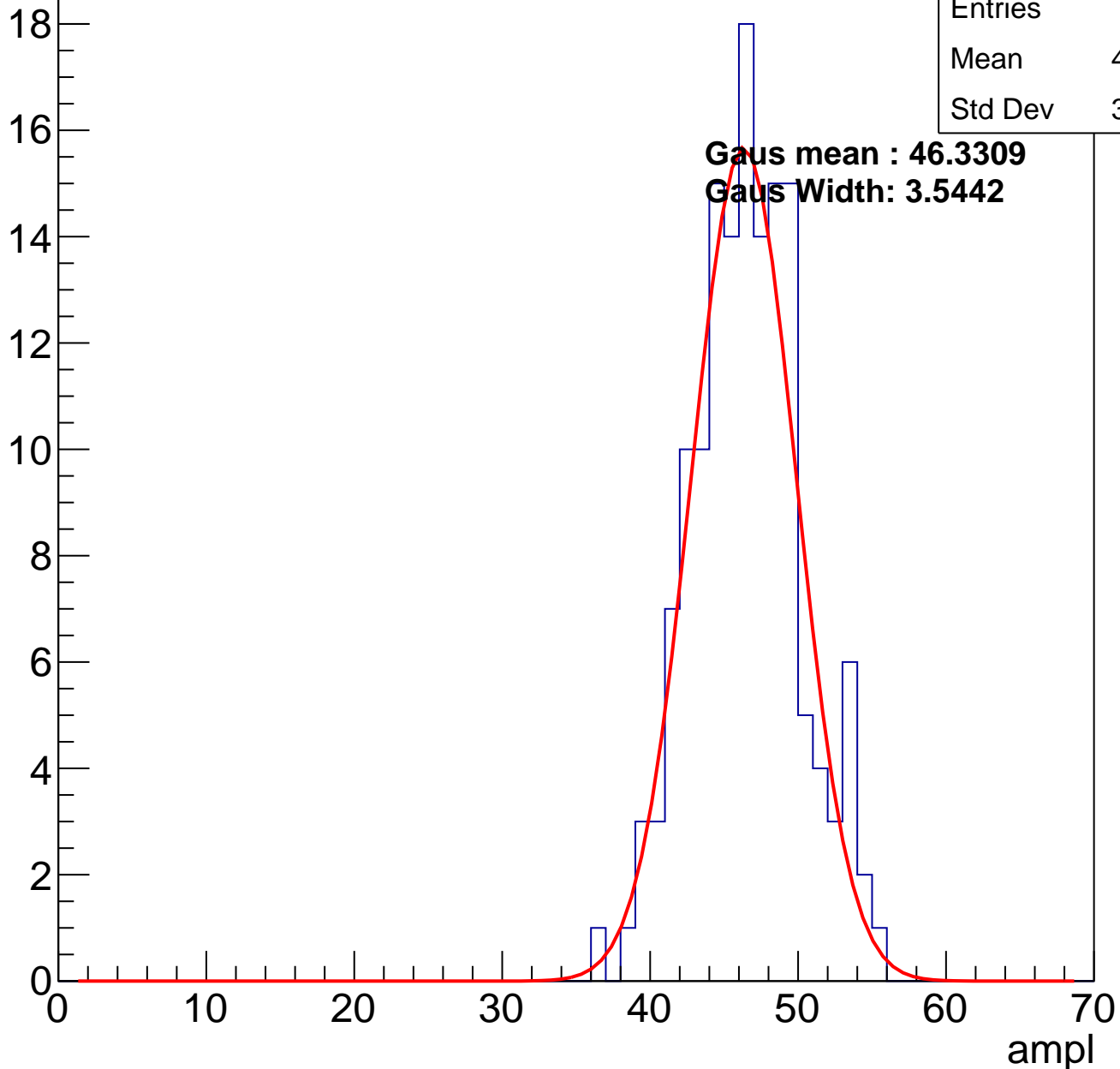
Gaus Width: 2.9802



B0L101S, U22-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 147 |
| Mean | 46.05 |
| Std Dev | 3.612 |

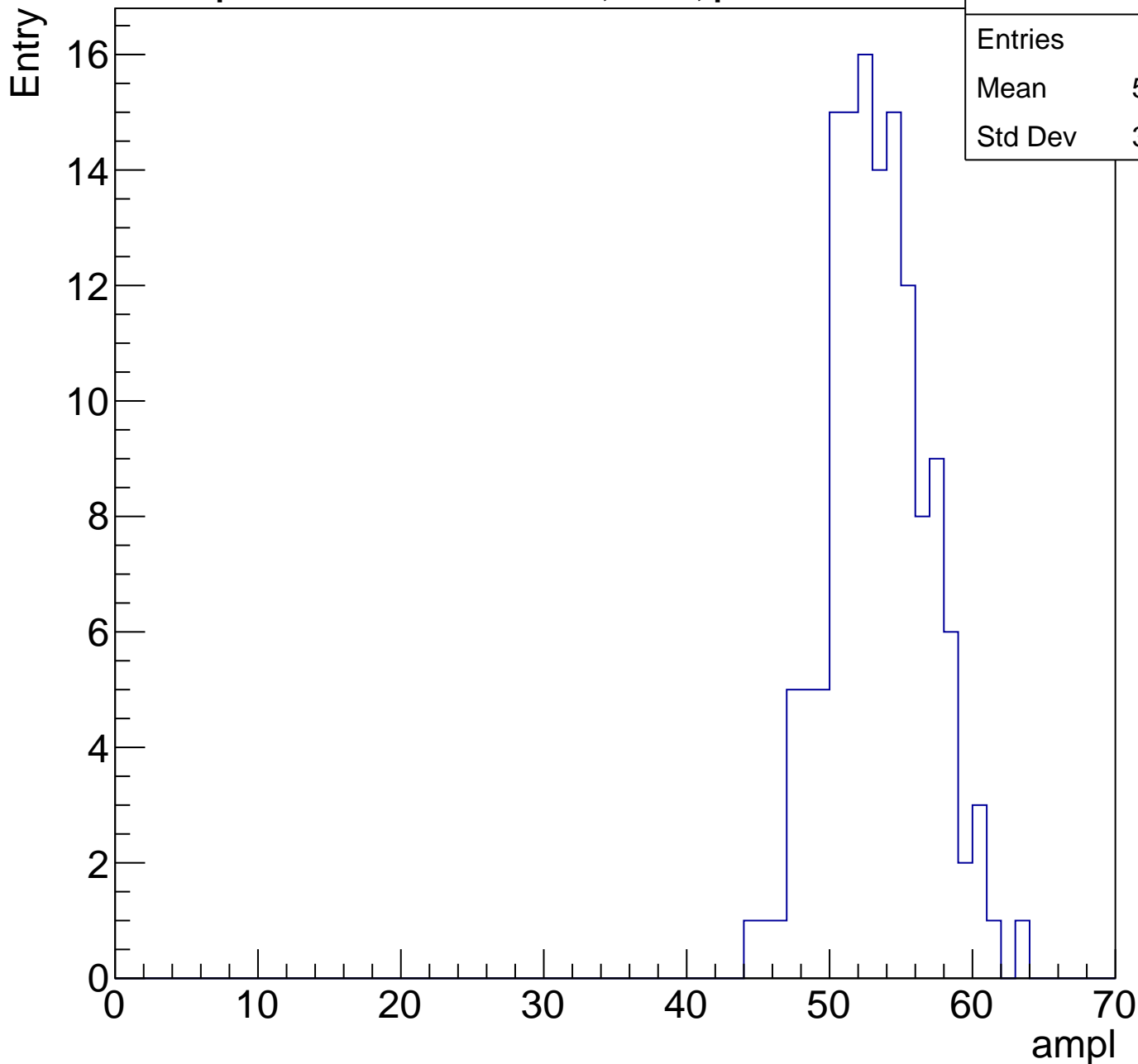
Gaus mean : 46.3309

Gaus Width: 3.5442

B0L101S, U22-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 52.93 |
| Std Dev | 3.444 |



B0L101S, U22-ch45, adc4

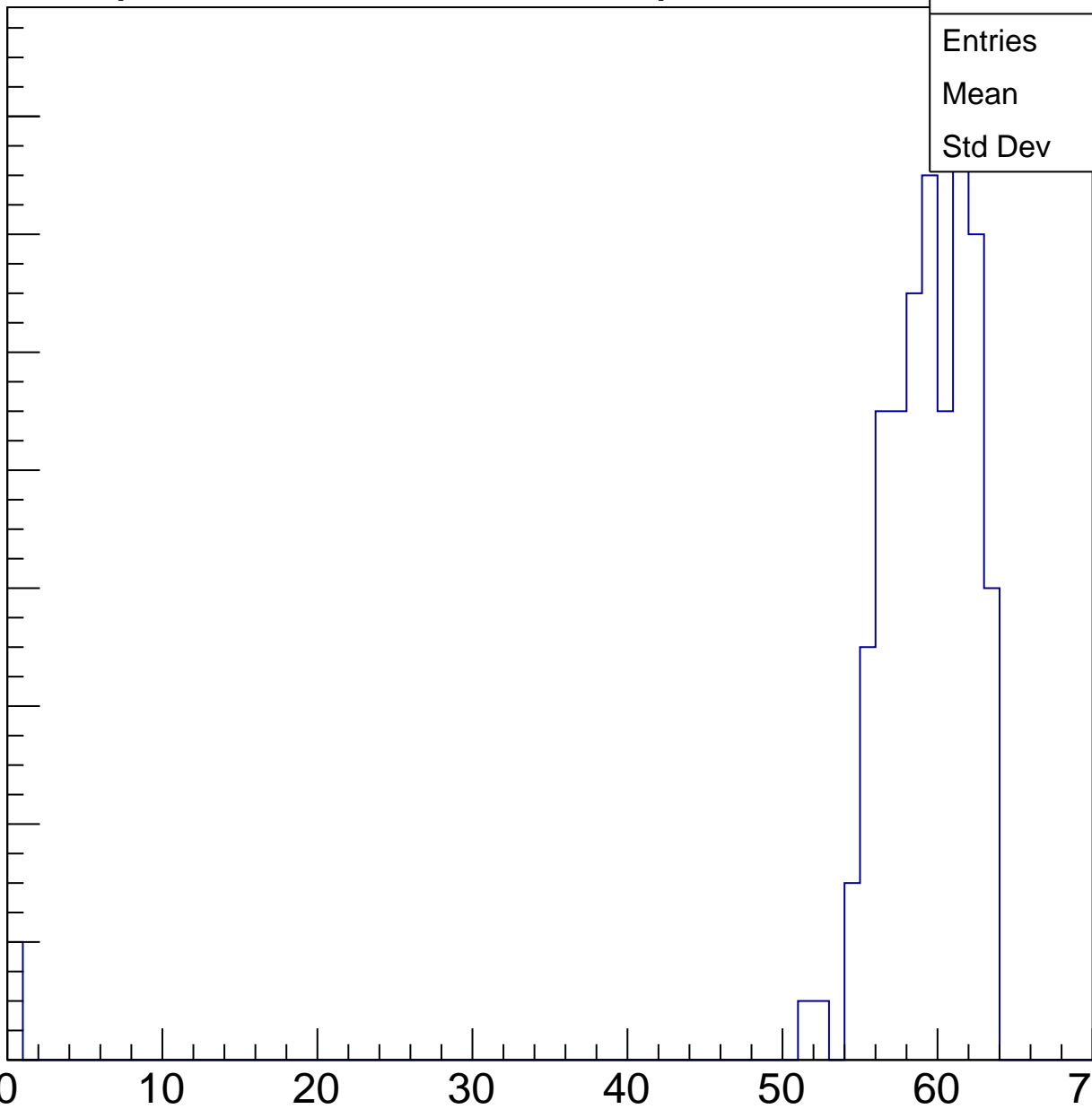
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 114 |
| Mean | 57.9 |
| Std Dev | 8.172 |

ampl



B0L101S, U22-ch45, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 29 |
| Mean | 61.31 |
| Std Dev | 1.84 |

B0L101S, U22-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch46, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 30.25 |
| Std Dev | 3.521 |

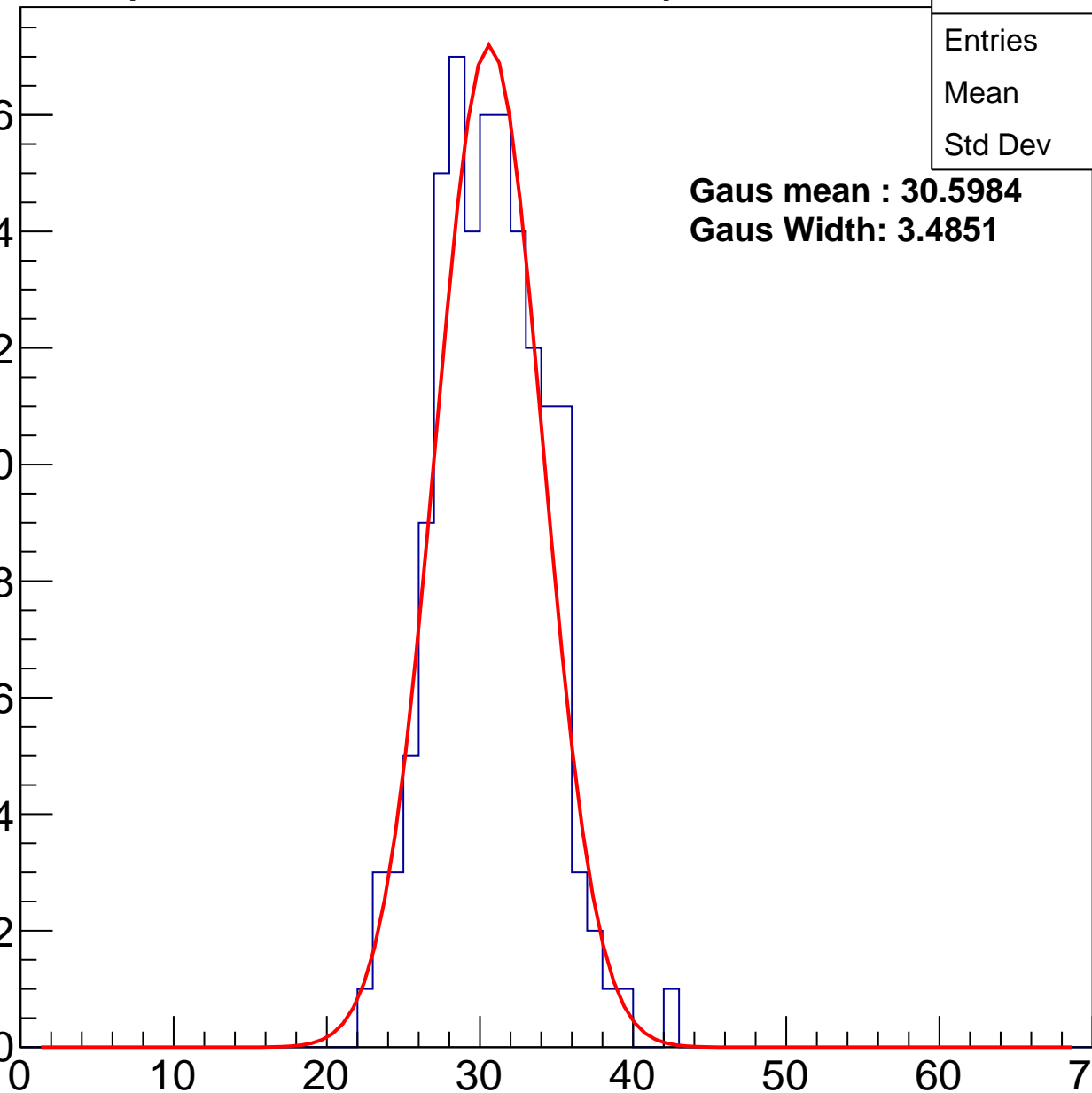
Gaus mean : 30.5984

Gaus Width: 3.4851

Entry

16
14
12
10
8
6
4
2
0

ampl

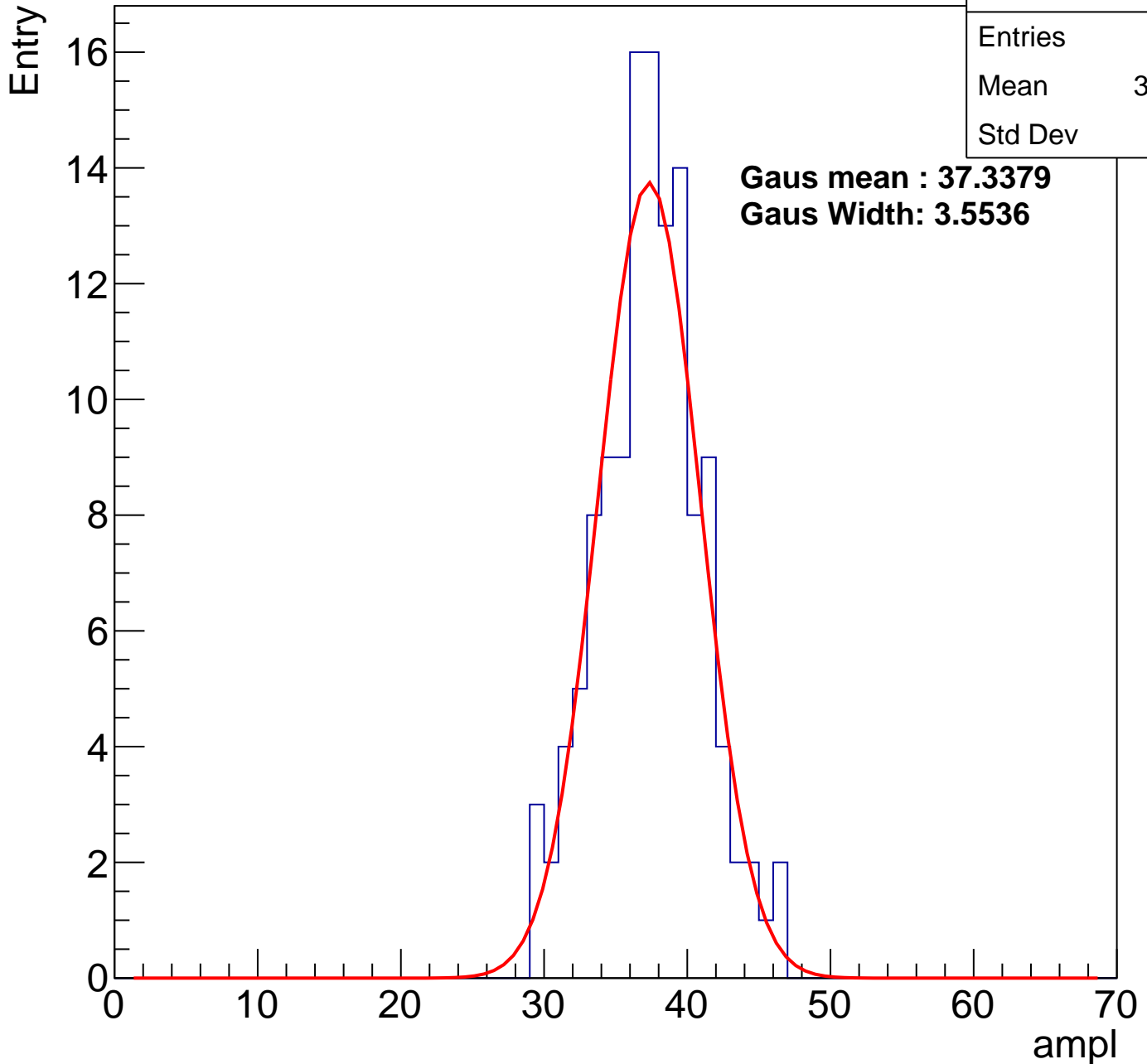


B0L101S, U22-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 127 |
| Mean | 36.94 |
| Std Dev | 3.55 |

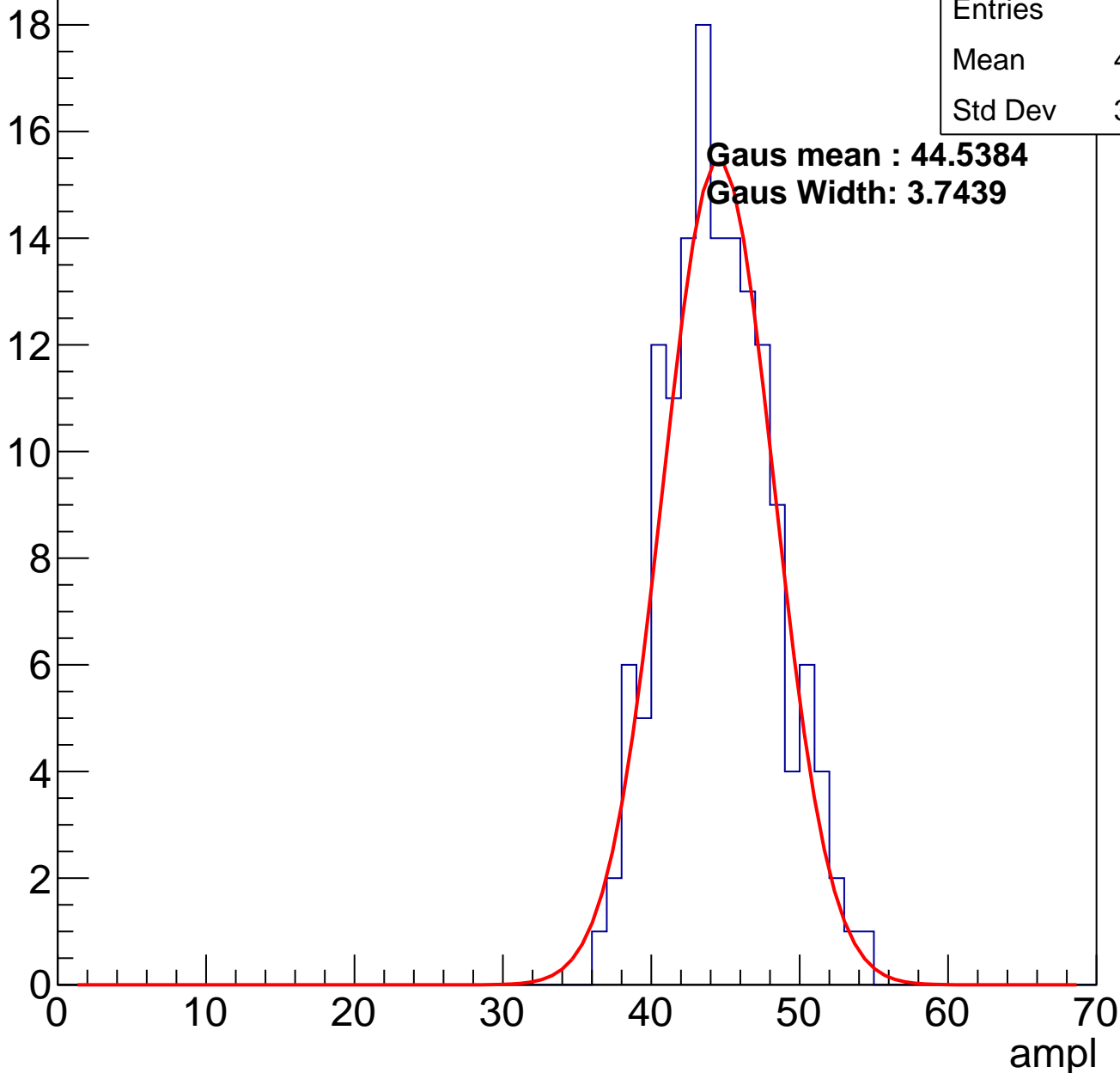
Gaus mean : 37.3379
Gaus Width: 3.5536



B0L101S, U22-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

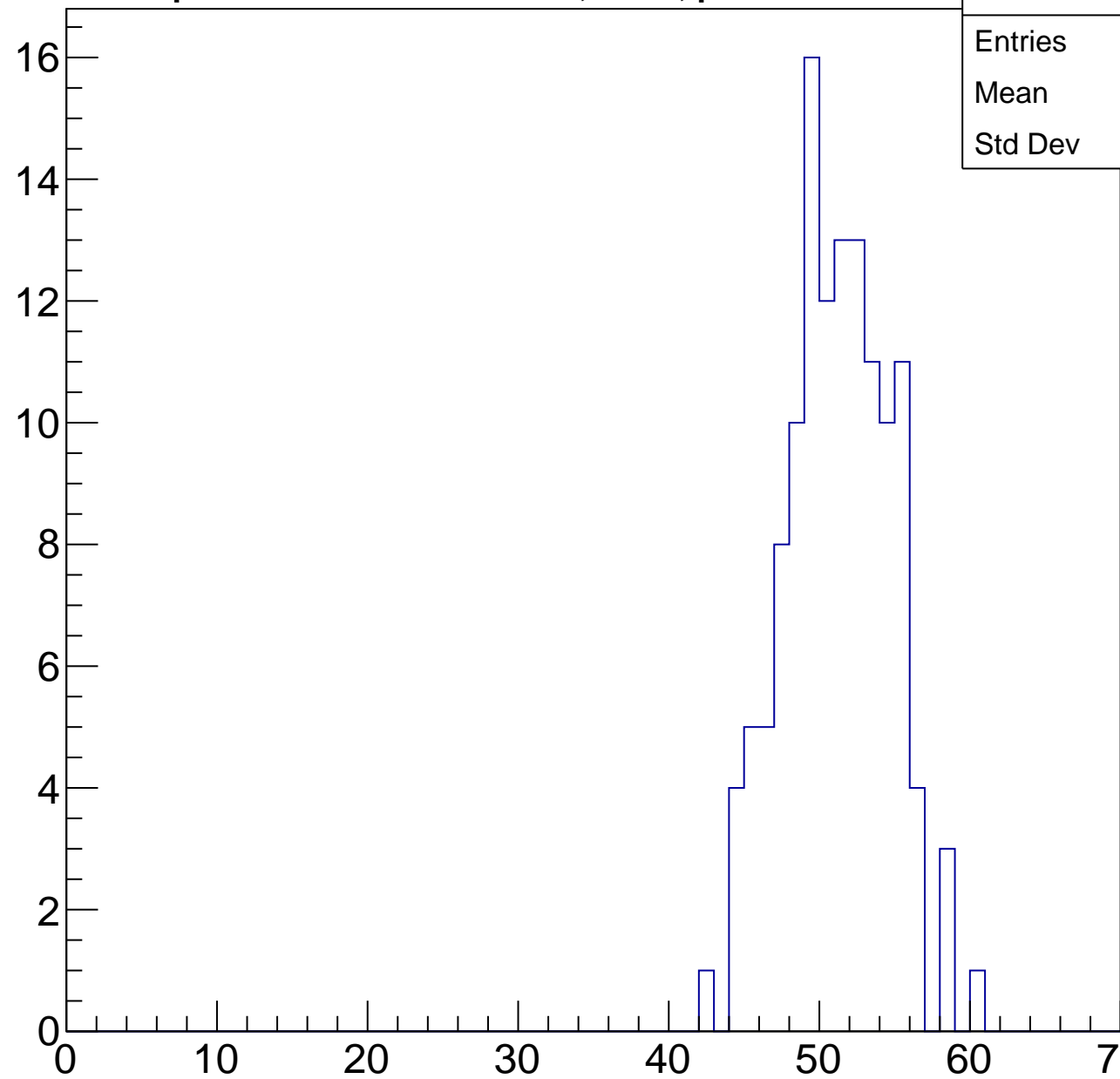
| | |
|---------|-------|
| Entries | 127 |
| Mean | 50.69 |
| Std Dev | 3.454 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

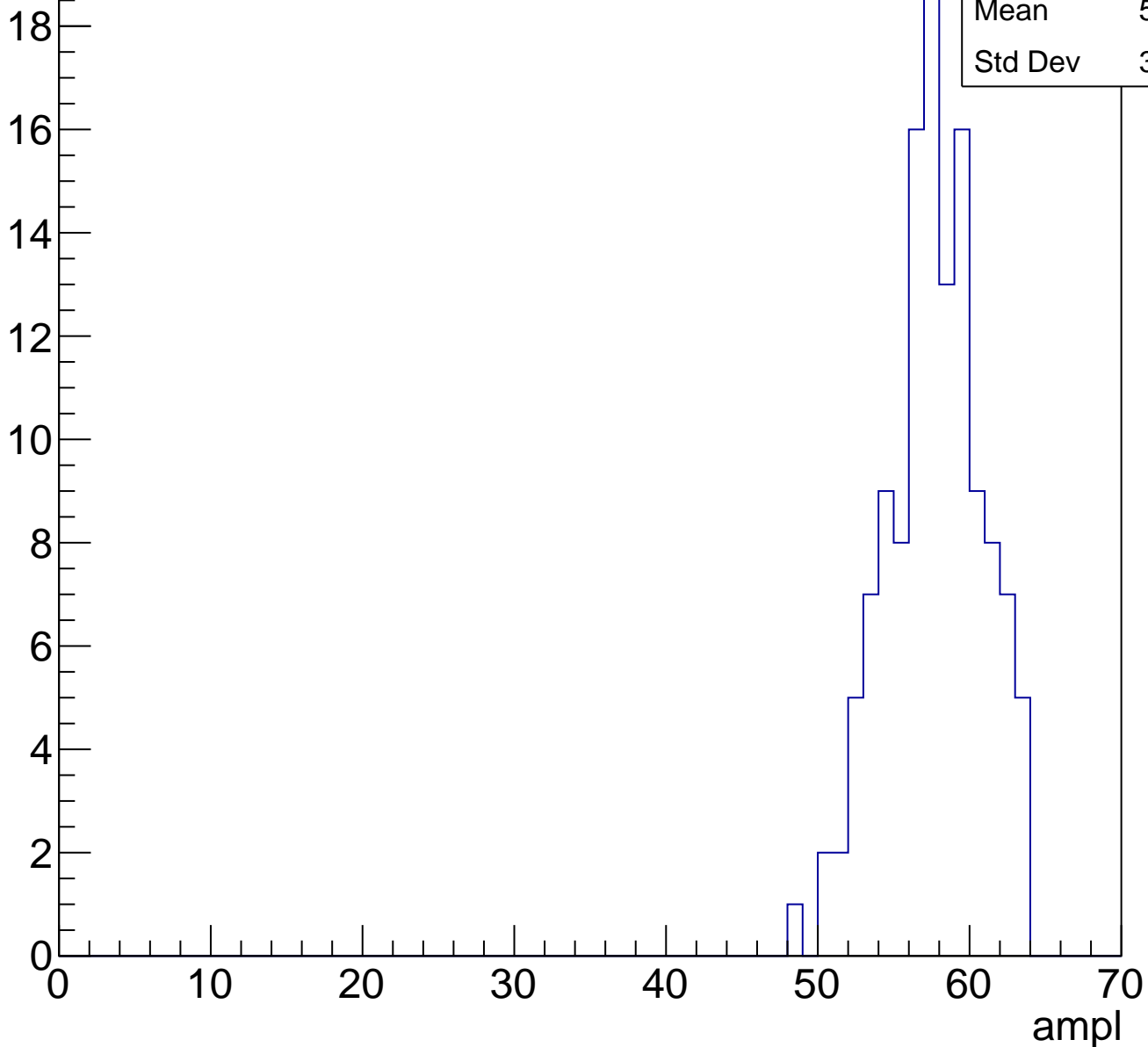


B0L101S, U22-ch46, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 127 |
| Mean | 57.17 |
| Std Dev | 3.143 |

Entry



B0L101S, U22-ch46, adc5

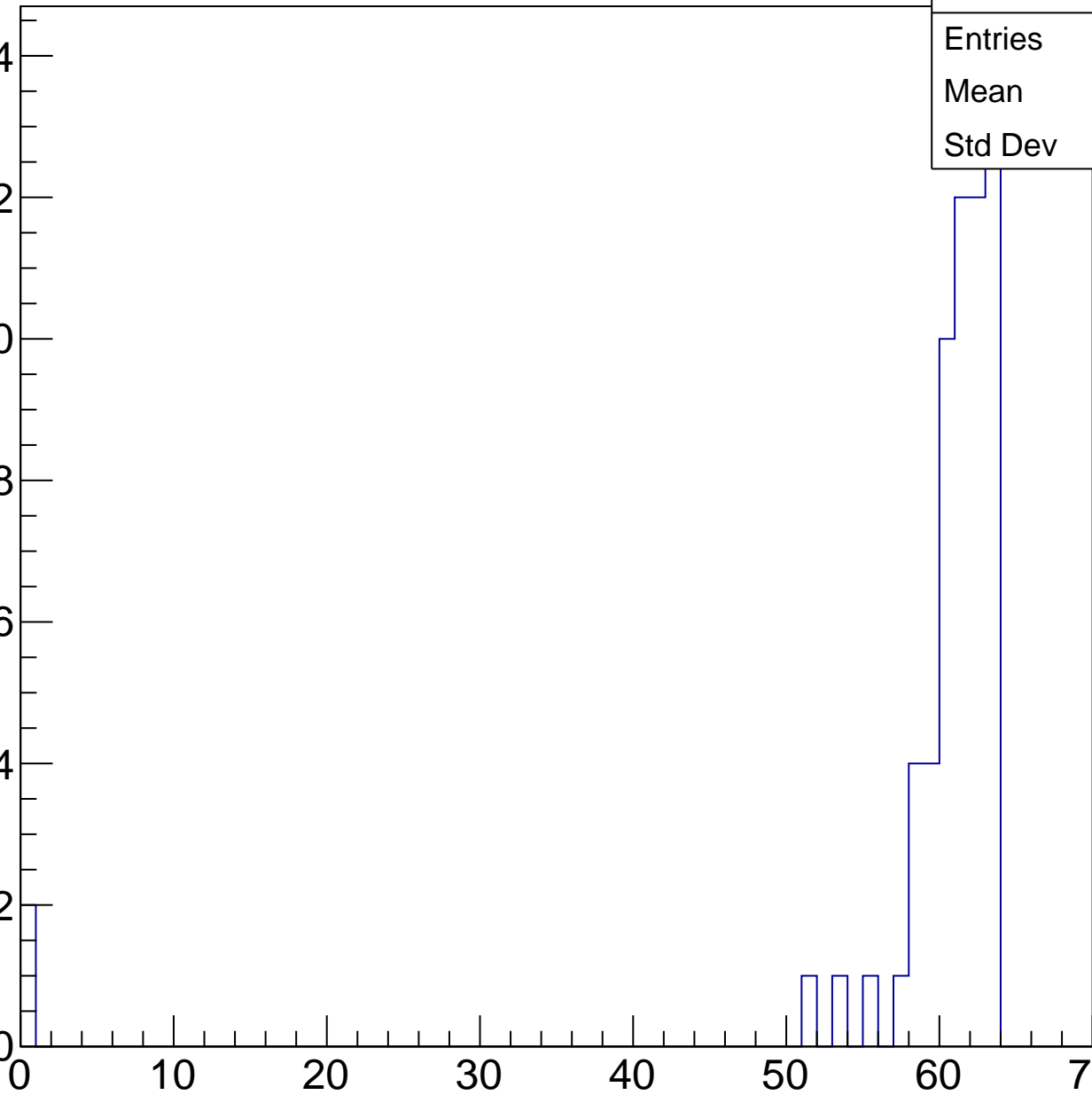
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 58.74 |
| Std Dev | 10.98 |

ampl



B0L101S, U22-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch47, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

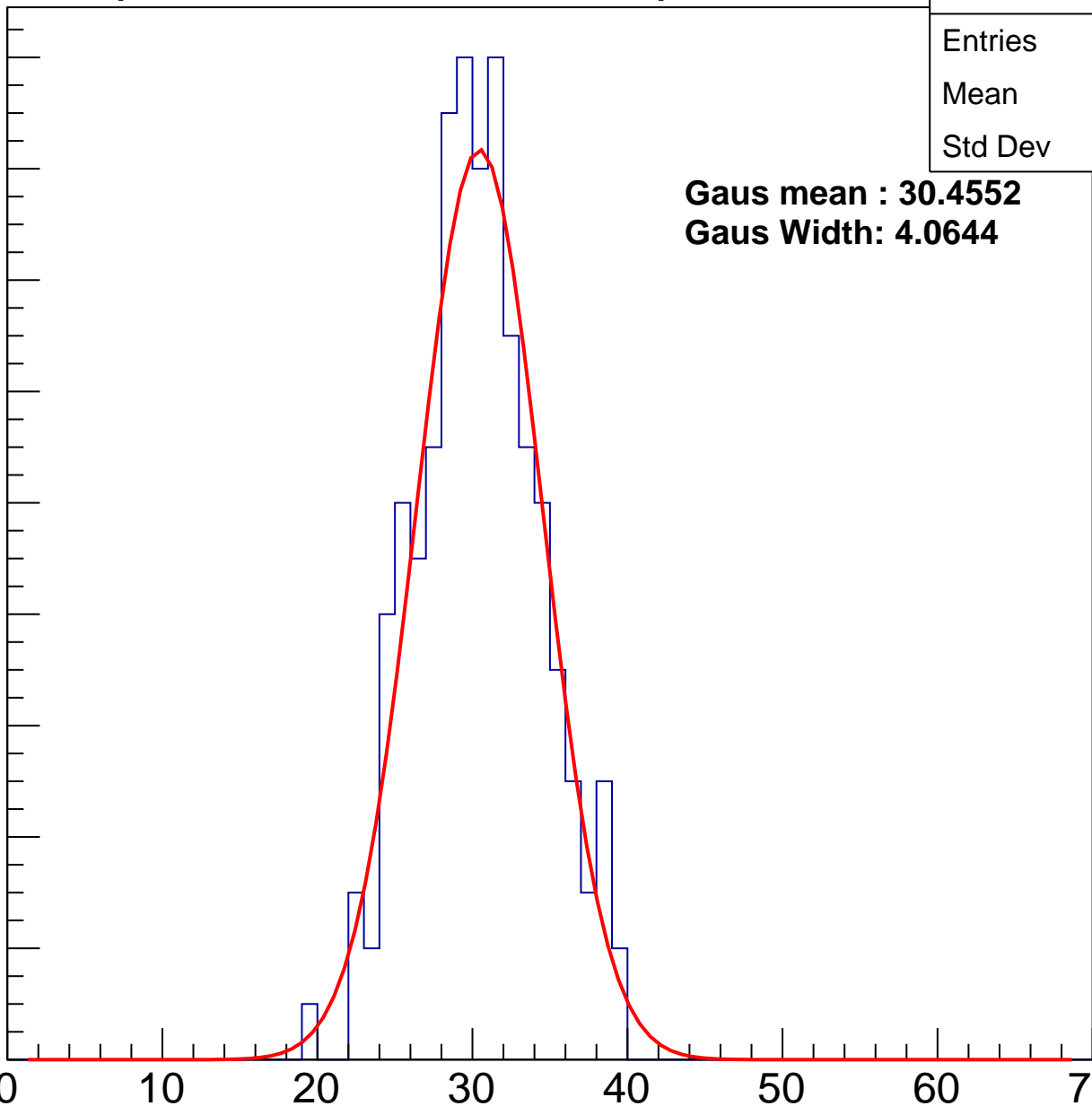
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 169 |
| Mean | 29.96 |
| Std Dev | 3.915 |

Gaus mean : 30.4552

Gaus Width: 4.0644

0 10 20 30 40 50 60 70
ampl



B0L101S, U22-ch47, adc1

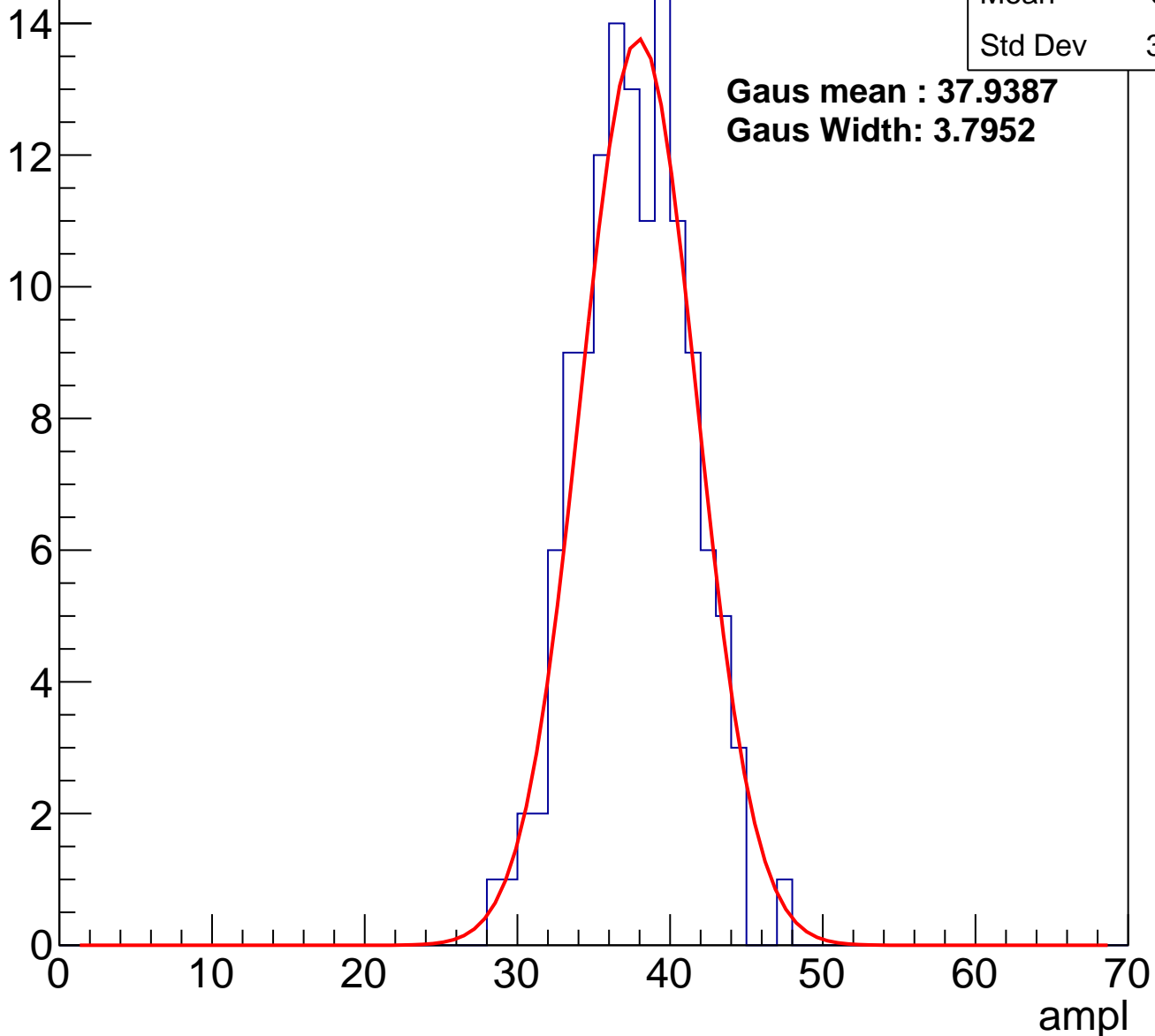
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 37.21 |
| Std Dev | 3.549 |

Gaus mean : 37.9387

Gaus Width: 3.7952

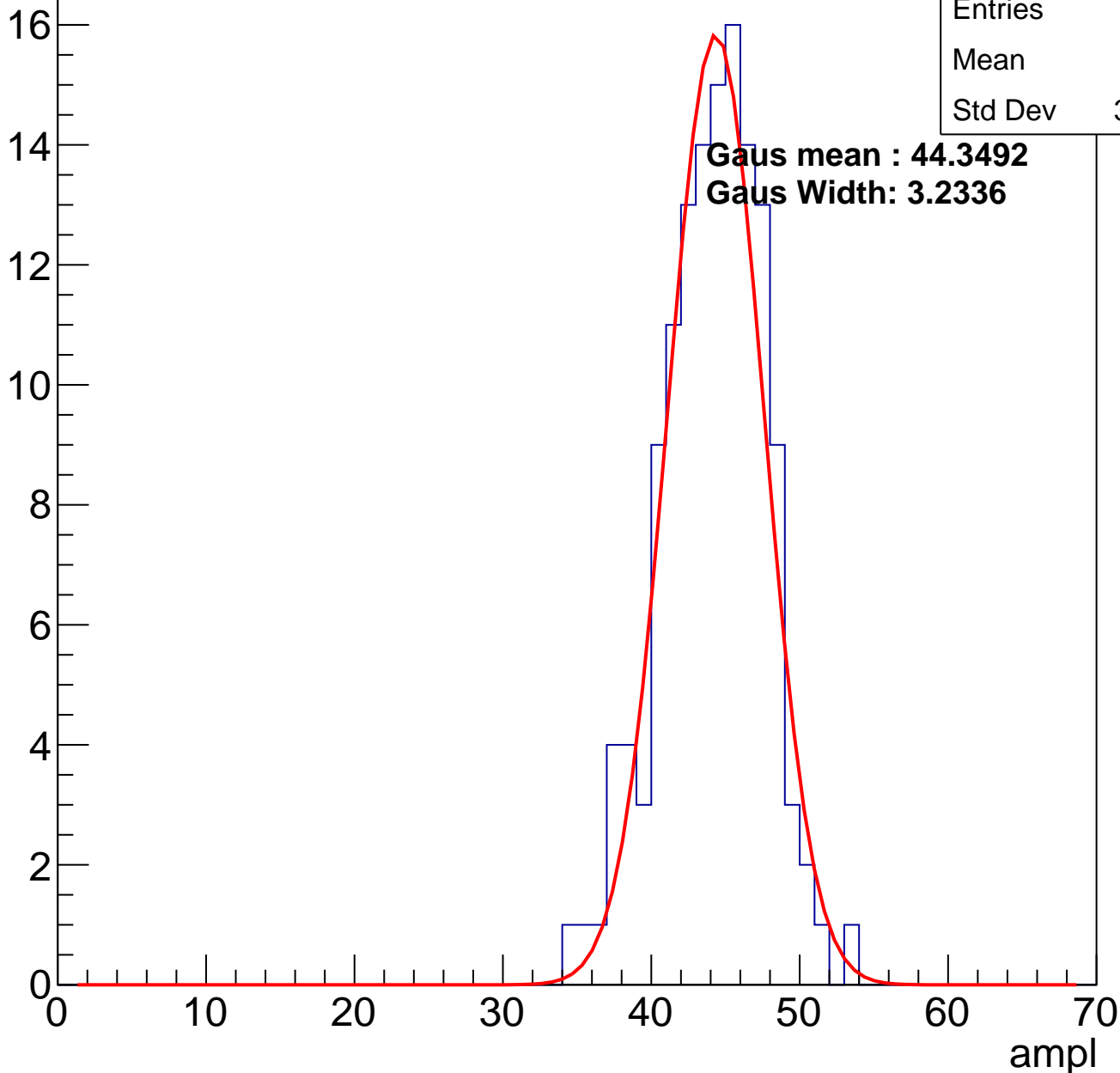
Entry



B0L101S, U22-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

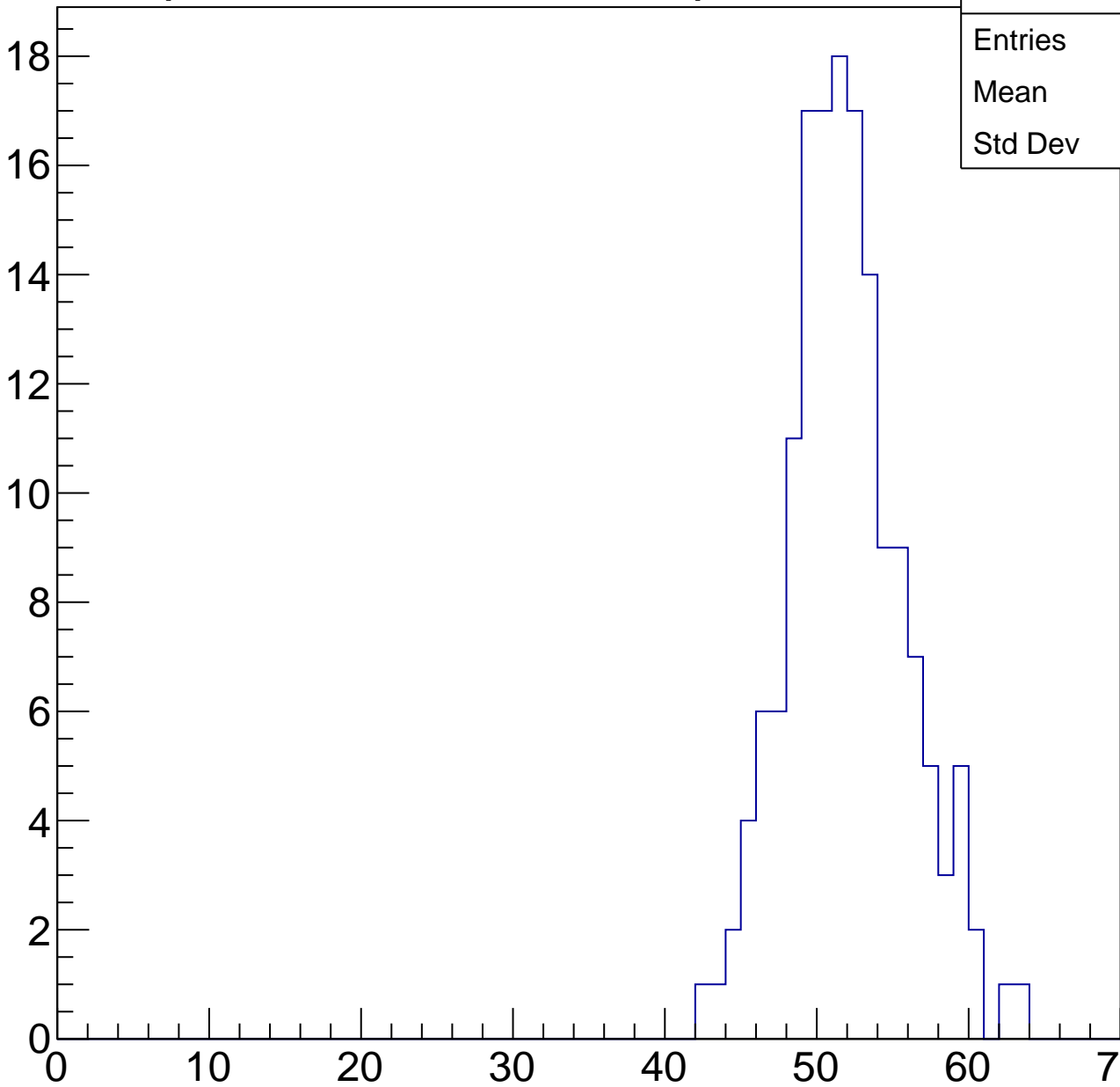
| | |
|---------|-------|
| Entries | 156 |
| Mean | 51.53 |
| Std Dev | 3.857 |

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

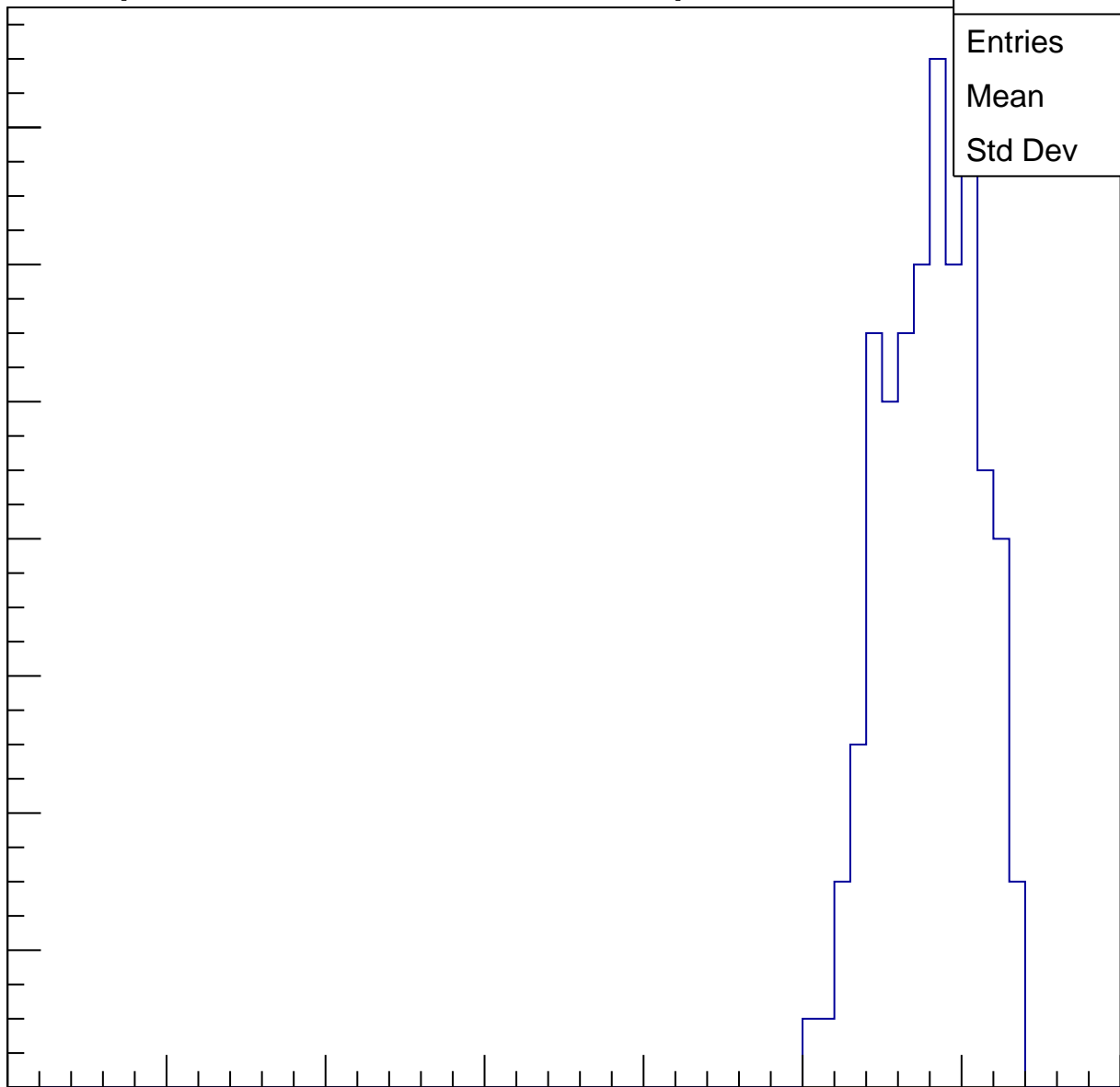
| | |
|---------|-------|
| Entries | 115 |
| Mean | 57.55 |
| Std Dev | 2.944 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch47, adc5

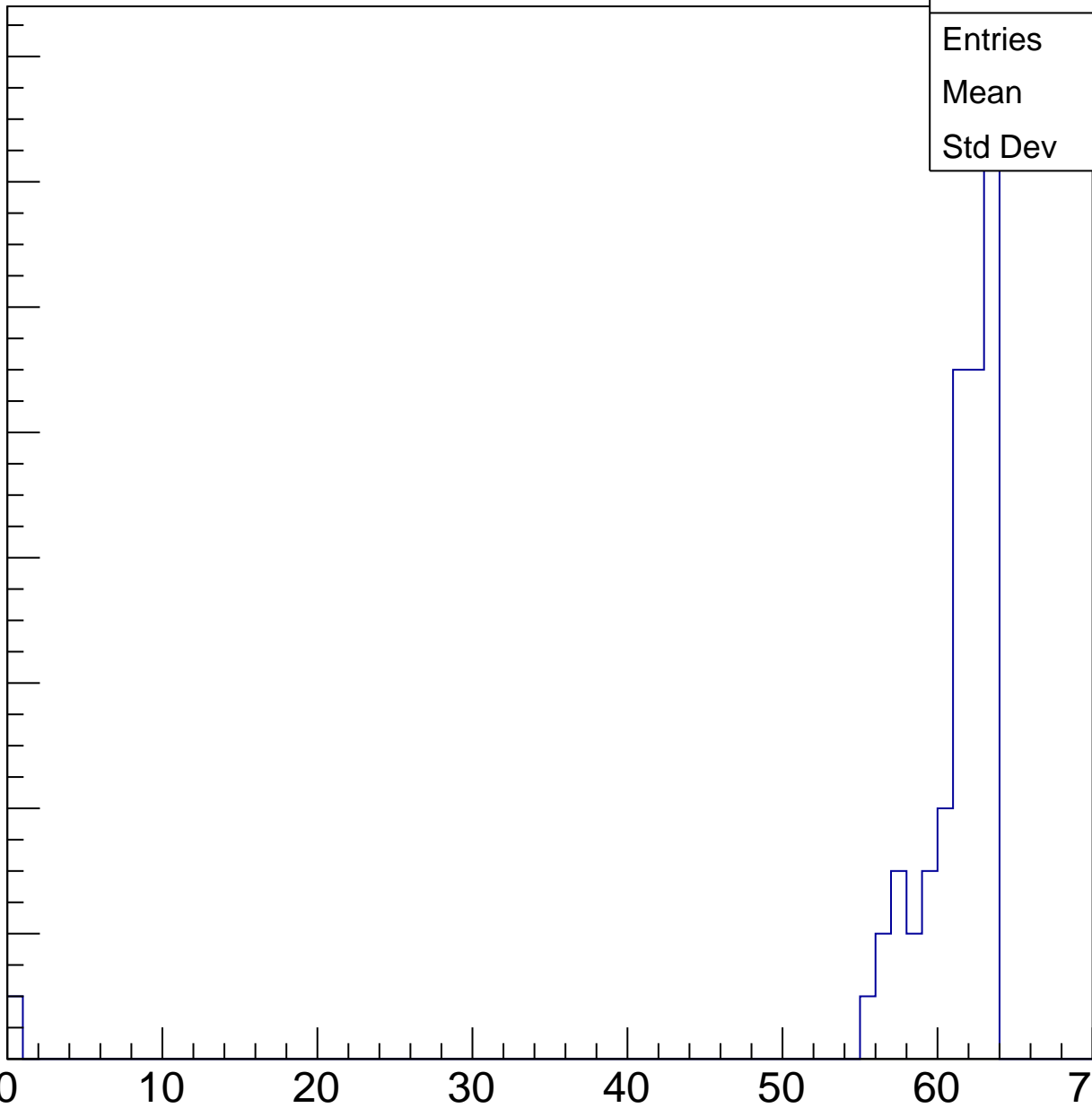
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 54 |
| Mean | 59.85 |
| Std Dev | 8.491 |

ampl



B0L101S, U22-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 28.42 |
| Std Dev | 3.642 |

Gaus mean : 28.7893

Gaus Width: 3.7869

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

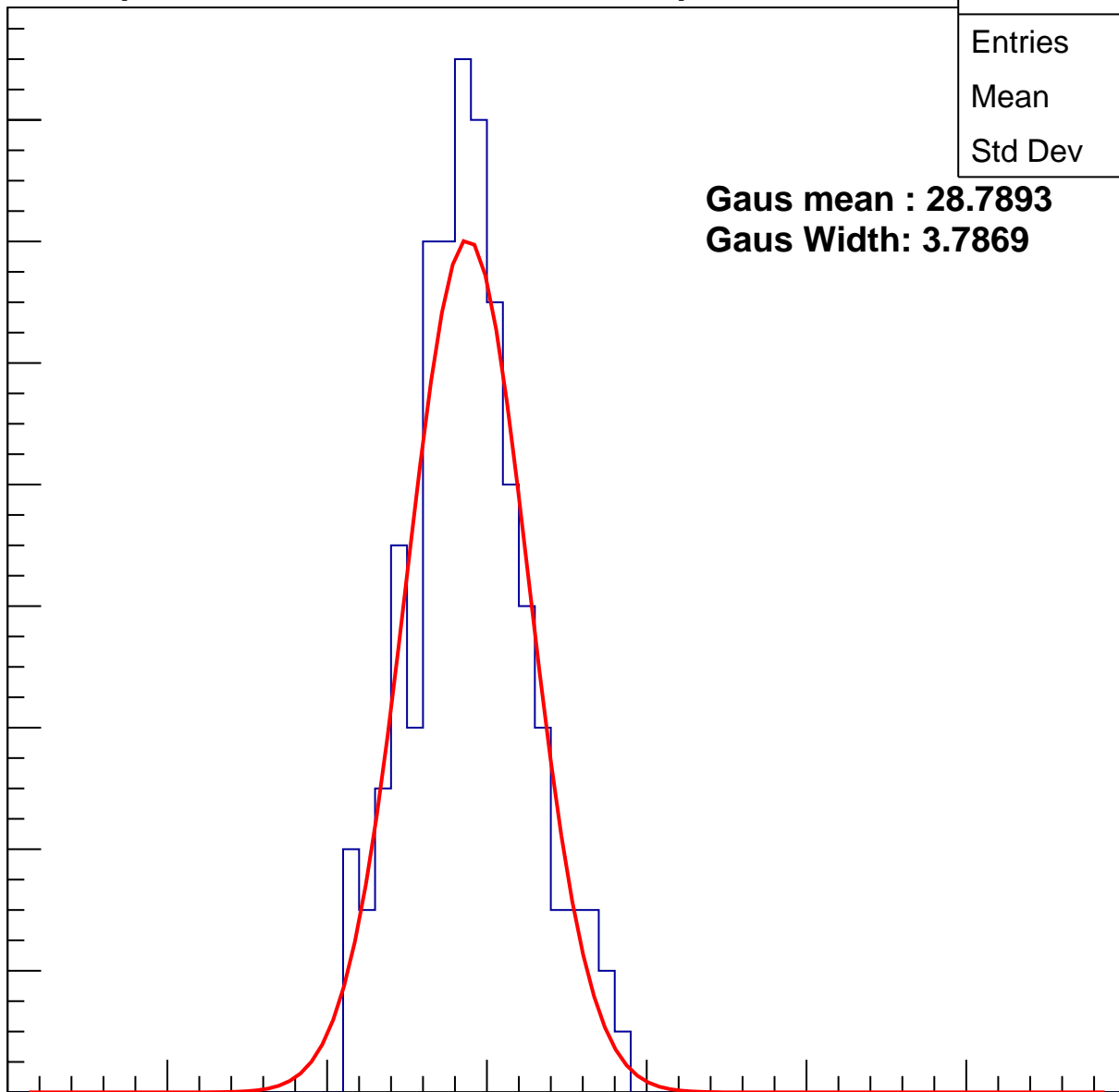
40

50

60

70

ampl



B0L101S, U22-ch48, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 34.62 |
| Std Dev | 3.254 |

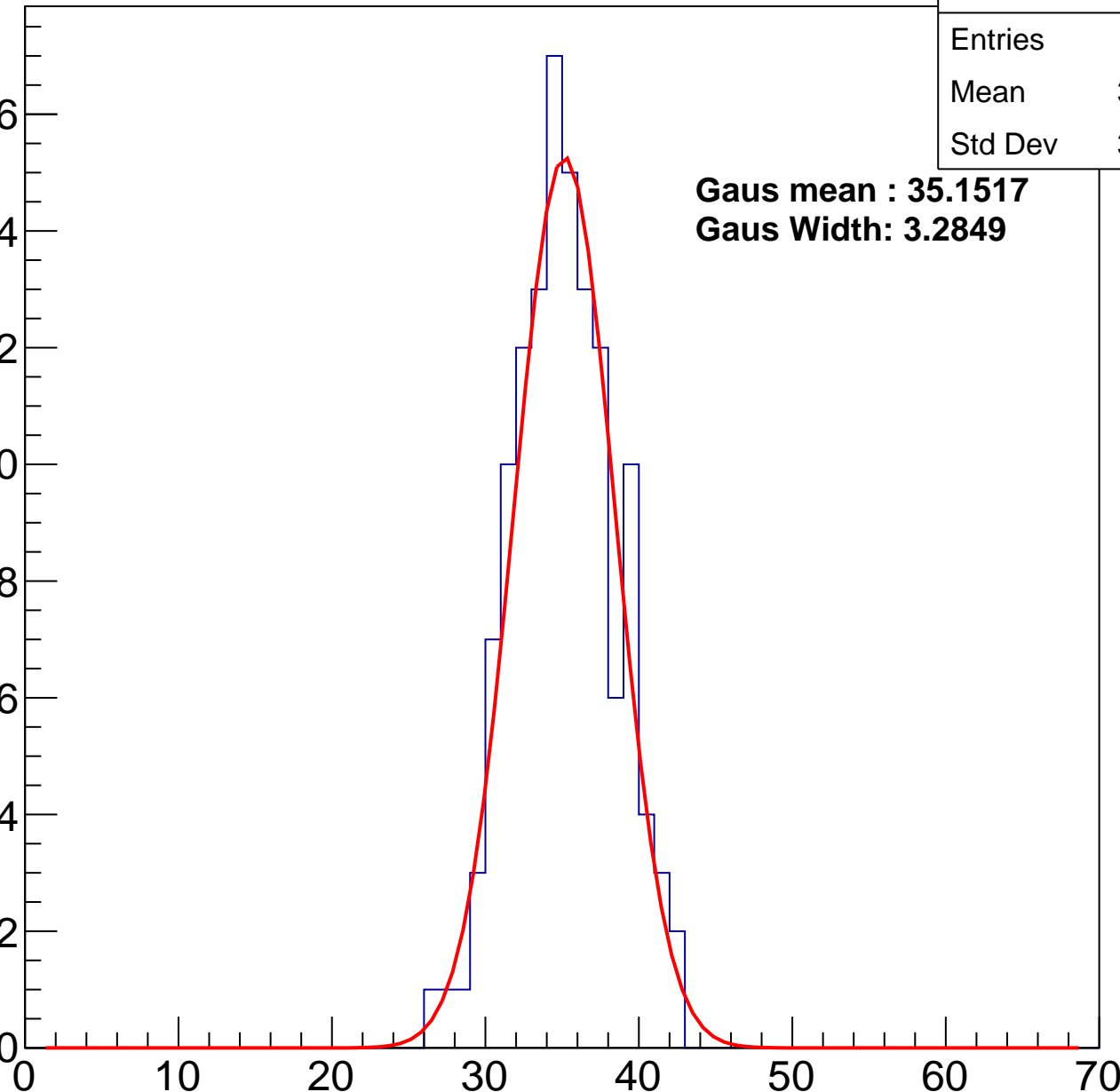
Gaus mean : 35.1517

Gaus Width: 3.2849

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 41.86 |
| Std Dev | 3.415 |

Gaus mean : 42.2307

Gaus Width: 3.5310

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

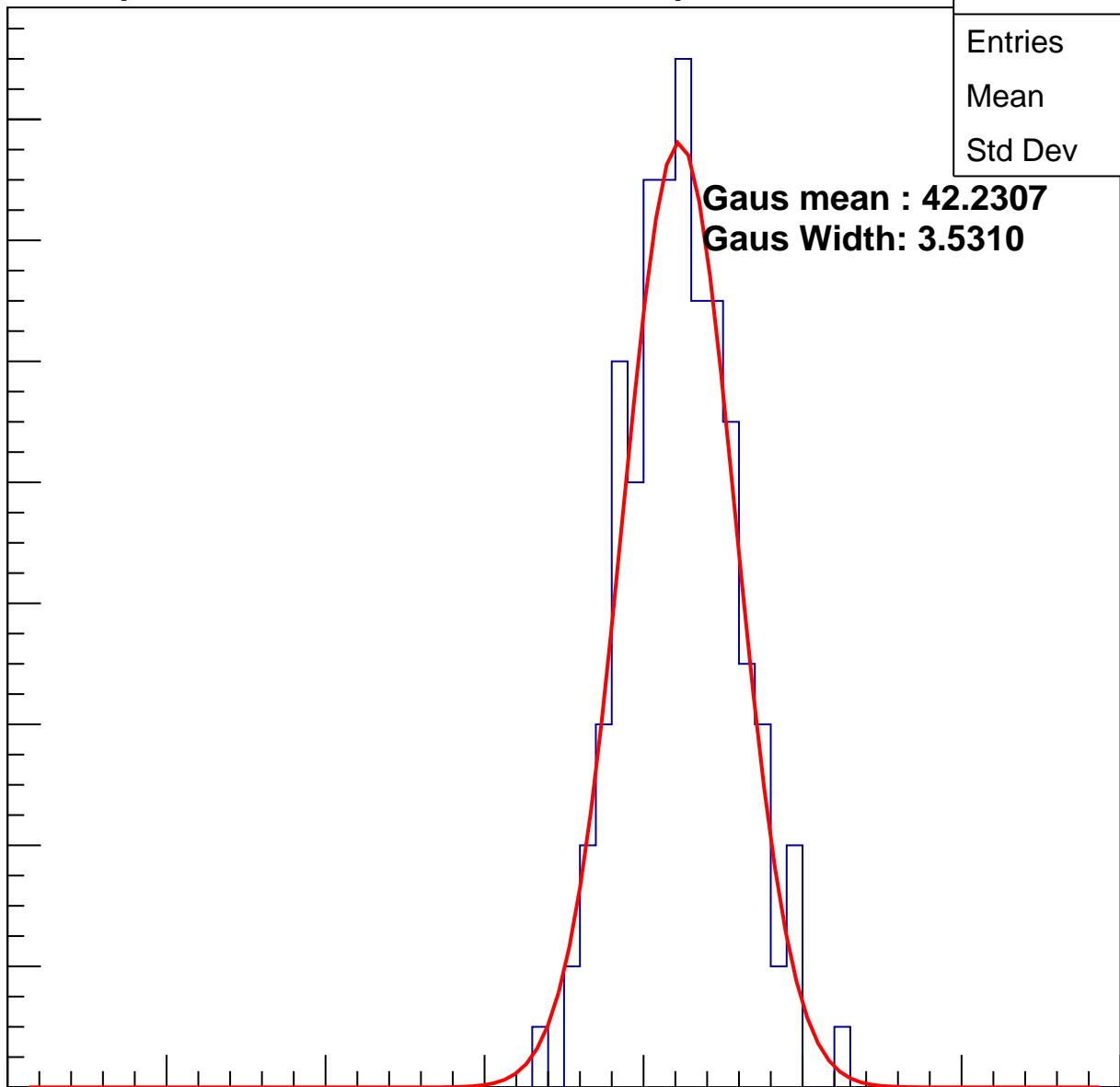
40

50

60

70

ampl



B0L101S, U22-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

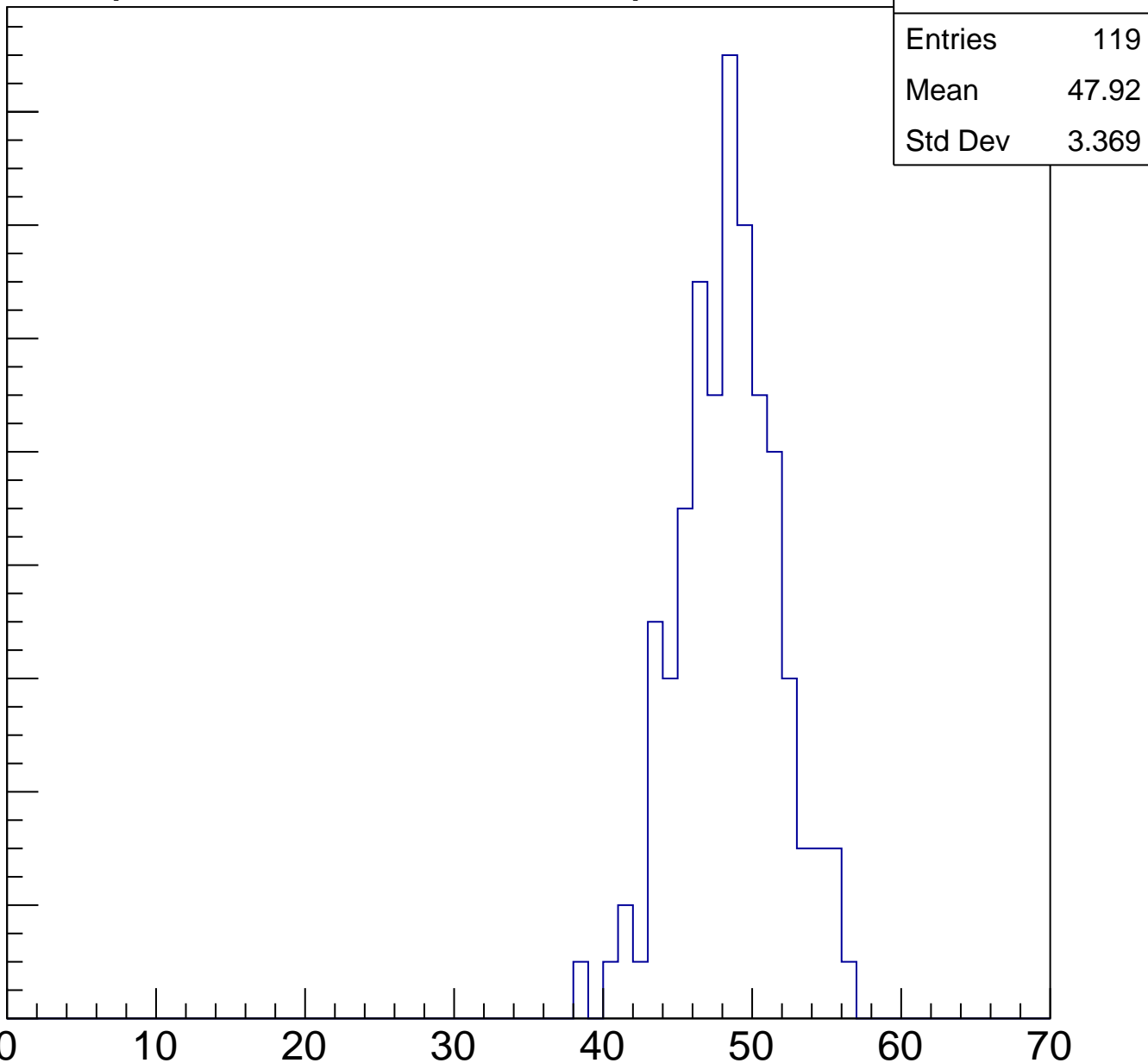
119

Mean

47.92

Std Dev

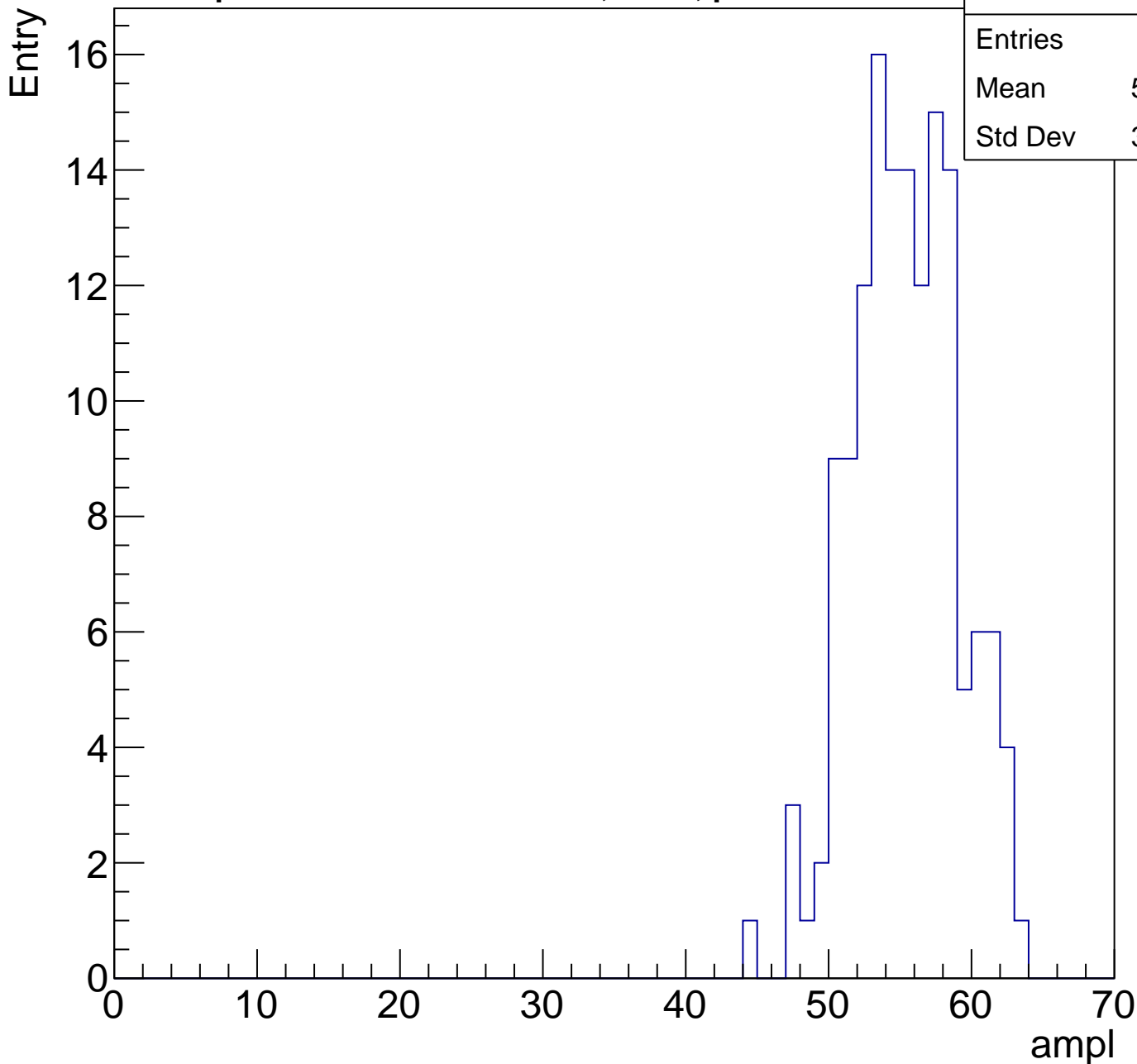
3.369



B0L101S, U22-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 54.92 |
| Std Dev | 3.625 |



B0L101S, U22-ch48, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

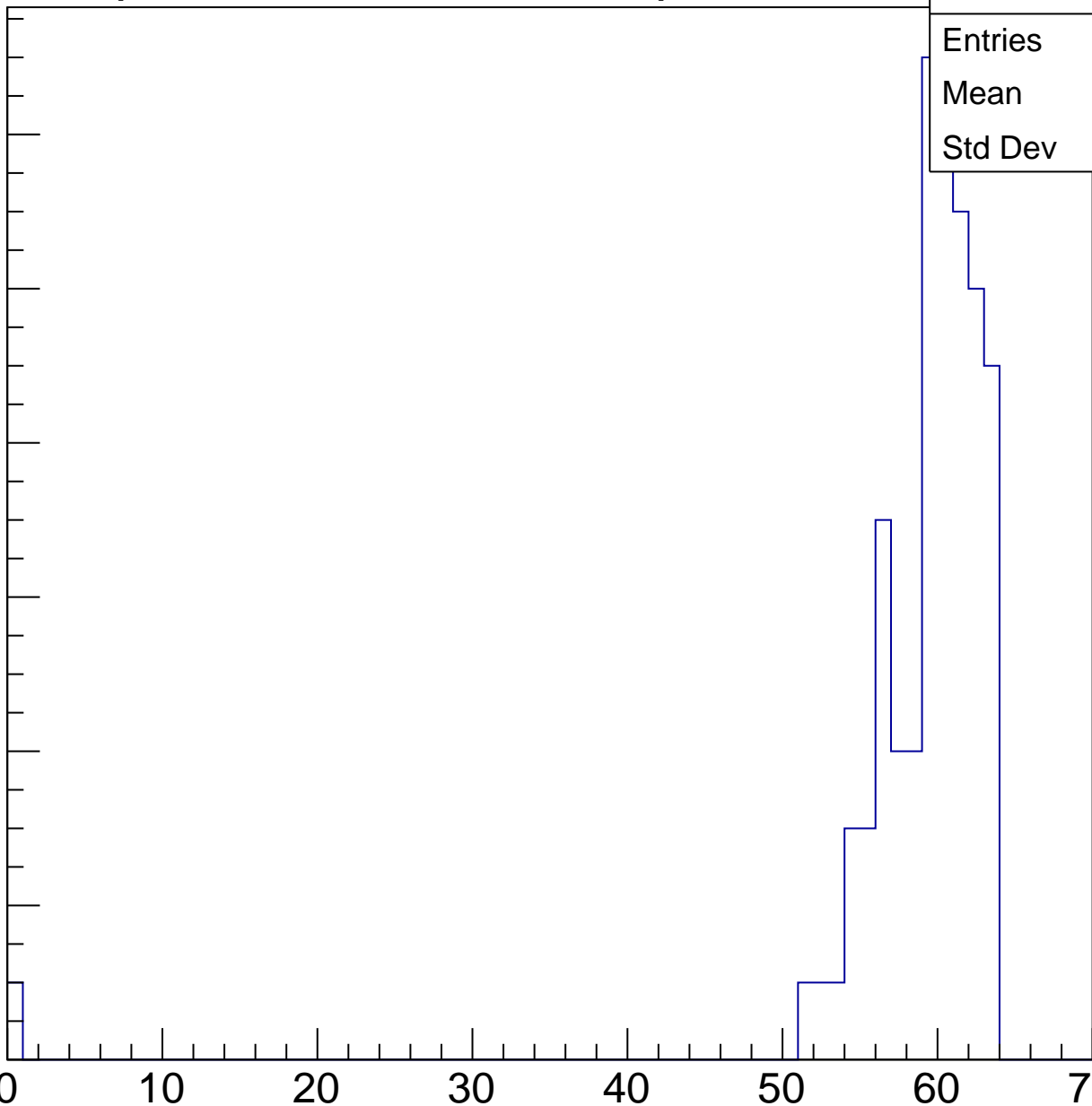
0

| | |
|---------|----|
| Entries | 81 |
|---------|----|

| | |
|------|-------|
| Mean | 58.52 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 7.122 |
|---------|-------|

ampl

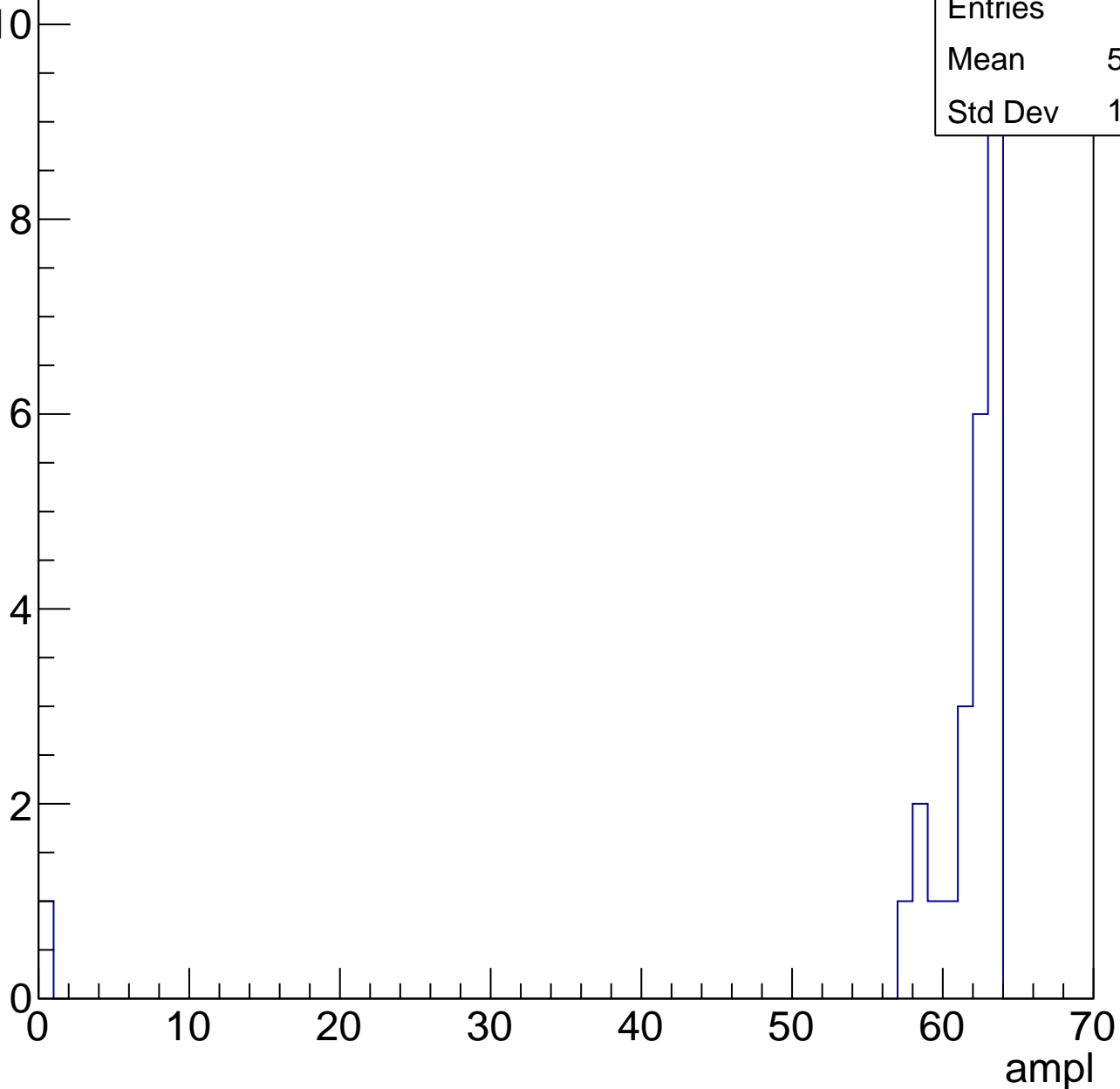


B0L101S, U22-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

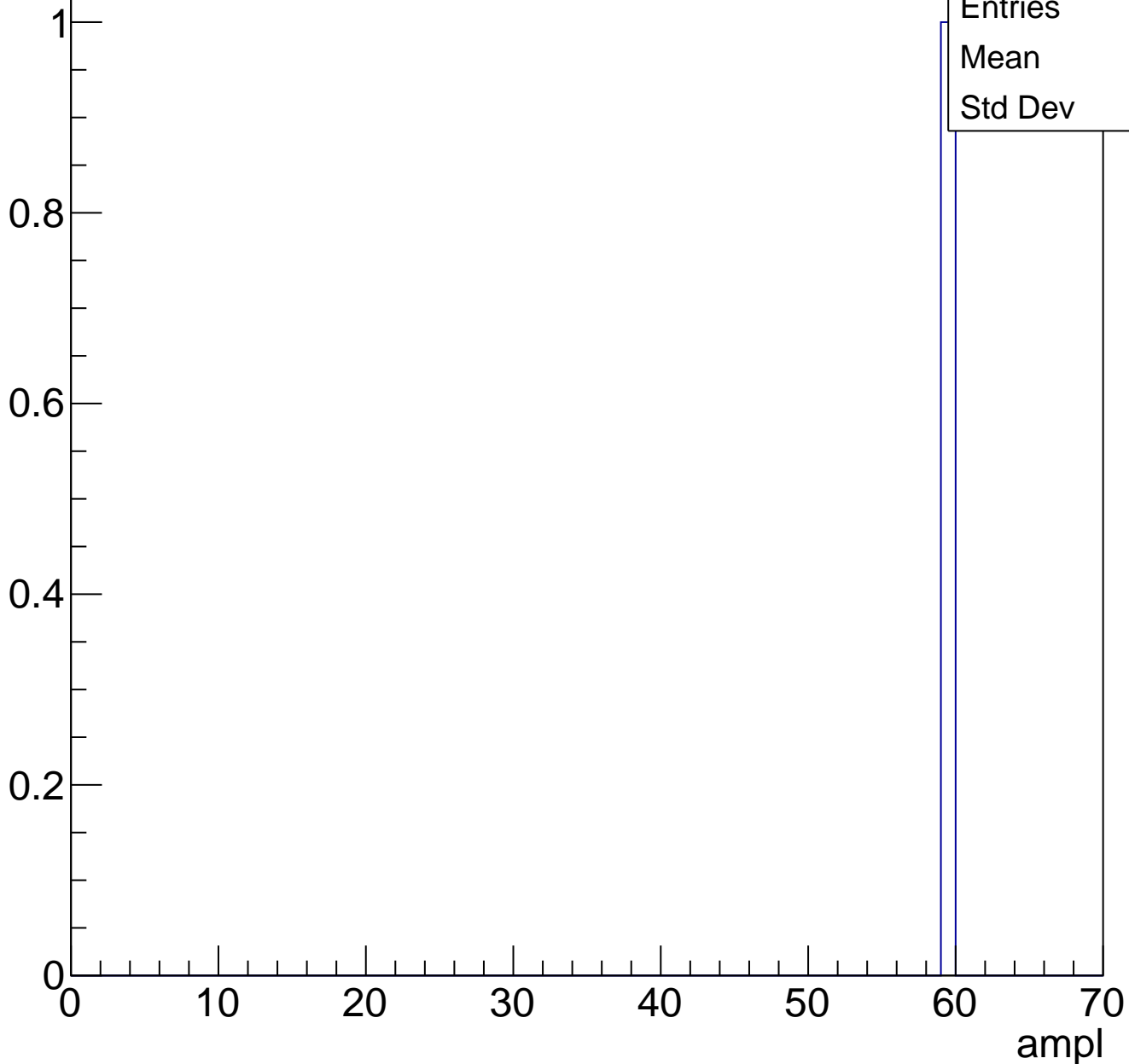
| | |
|---------|-------|
| Entries | 25 |
| Mean | 59.08 |
| Std Dev | 12.19 |



B0L101S, U22-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch49, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 111 |
| Mean | 29.71 |
| Std Dev | 4.151 |

Gaus mean : 30.4061

Gaus Width: 3.4491

Entry

14

12

10

8

6

4

2

0

0

10

20

30

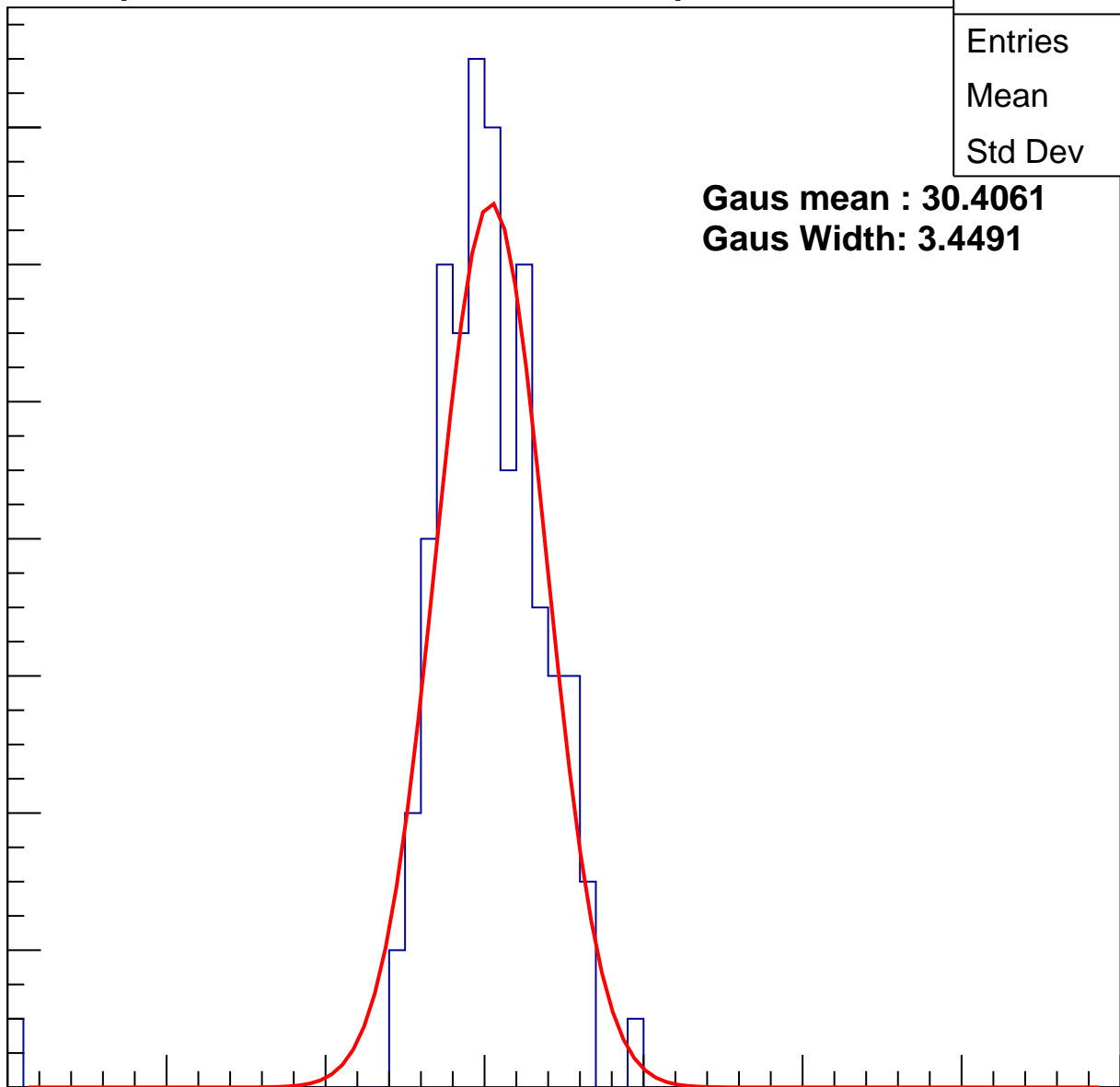
40

50

60

70

ampl



B0L101S, U22-ch49, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 36.88 |
| Std Dev | 3.897 |

Gaus mean : 37.2343

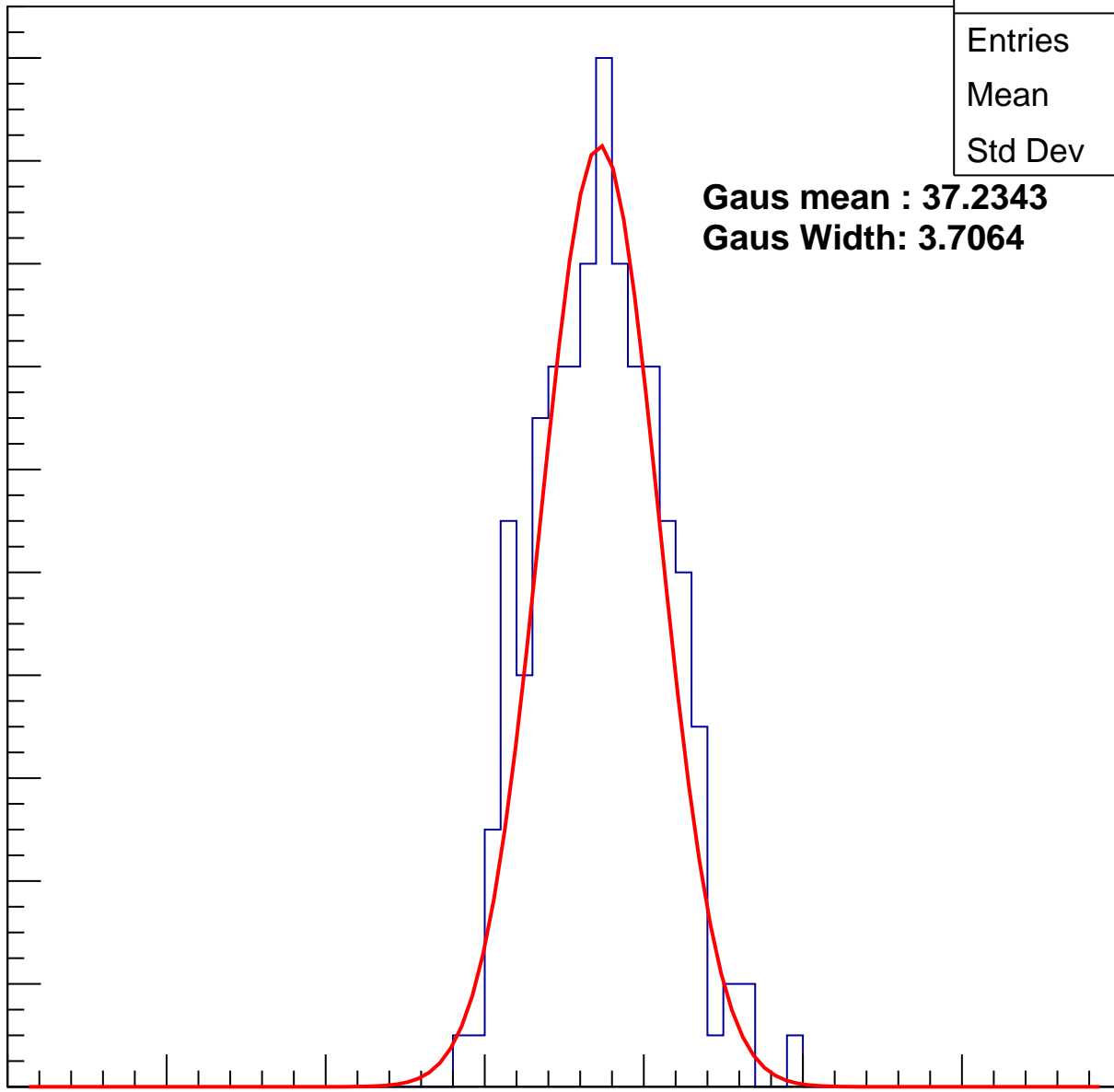
Gaus Width: 3.7064

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch49, adc2

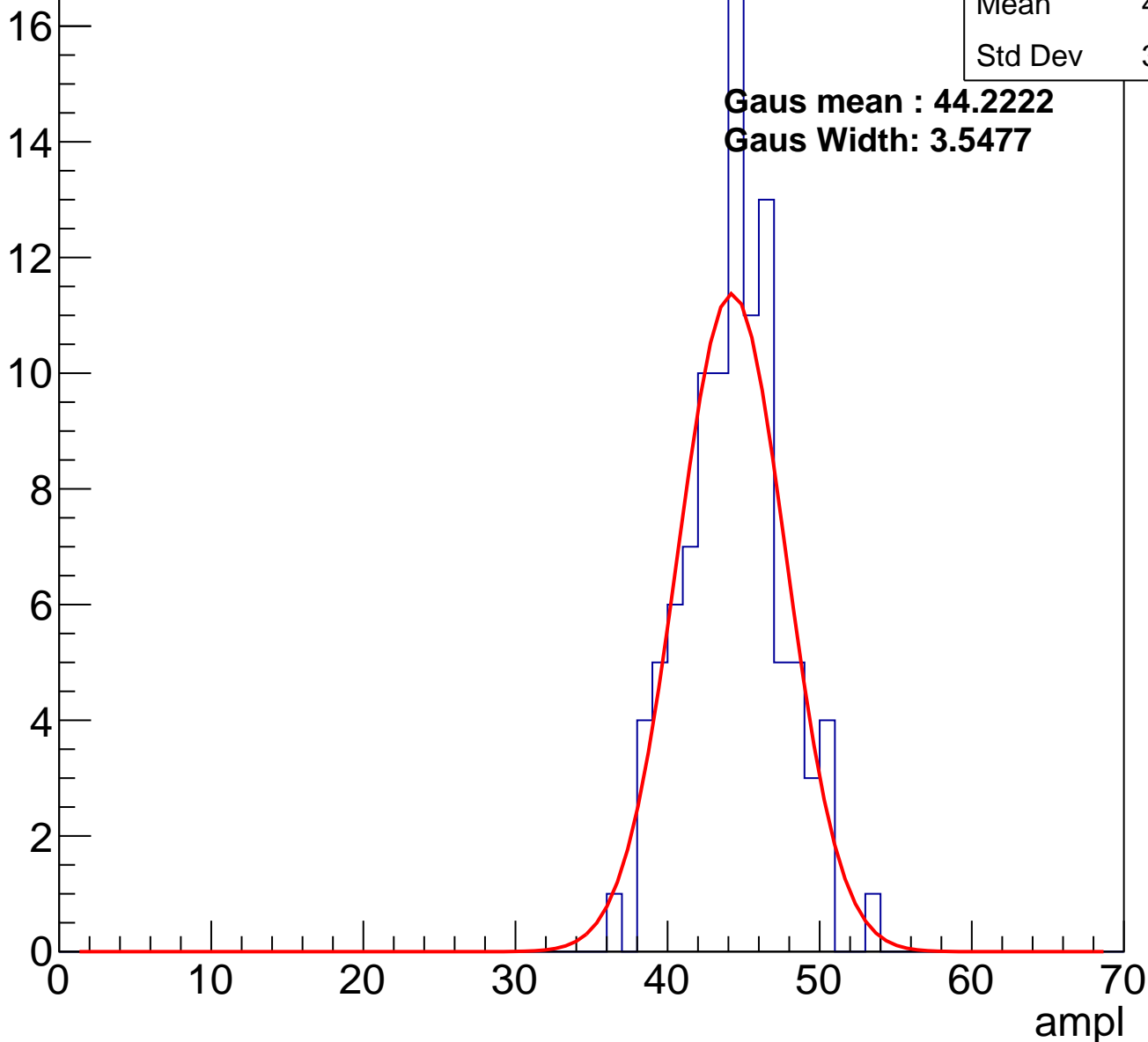
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 102 |
| Mean | 43.88 |
| Std Dev | 3.176 |

Gaus mean : 44.2222

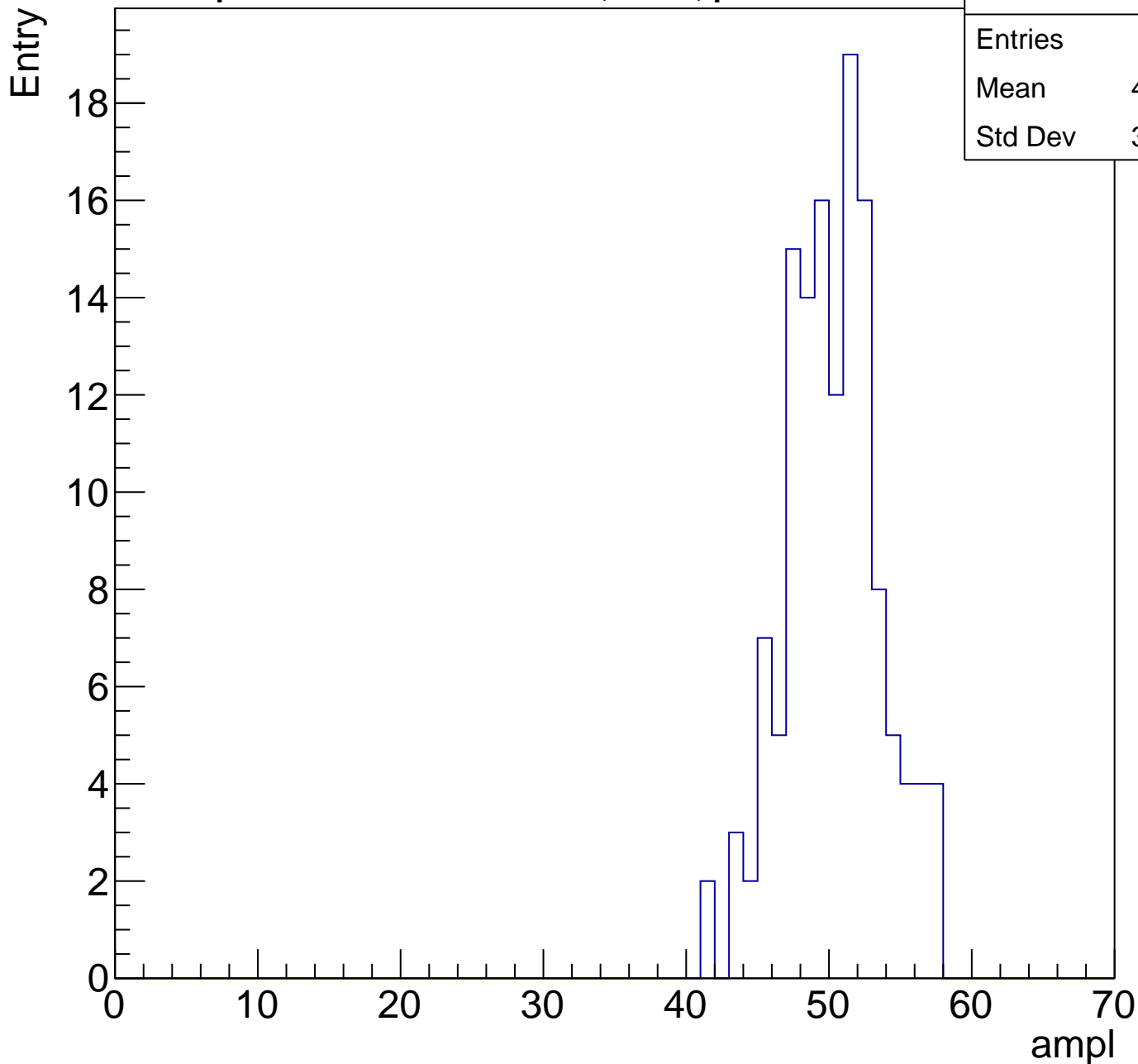
Gaus Width: 3.5477



B0L101S, U22-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

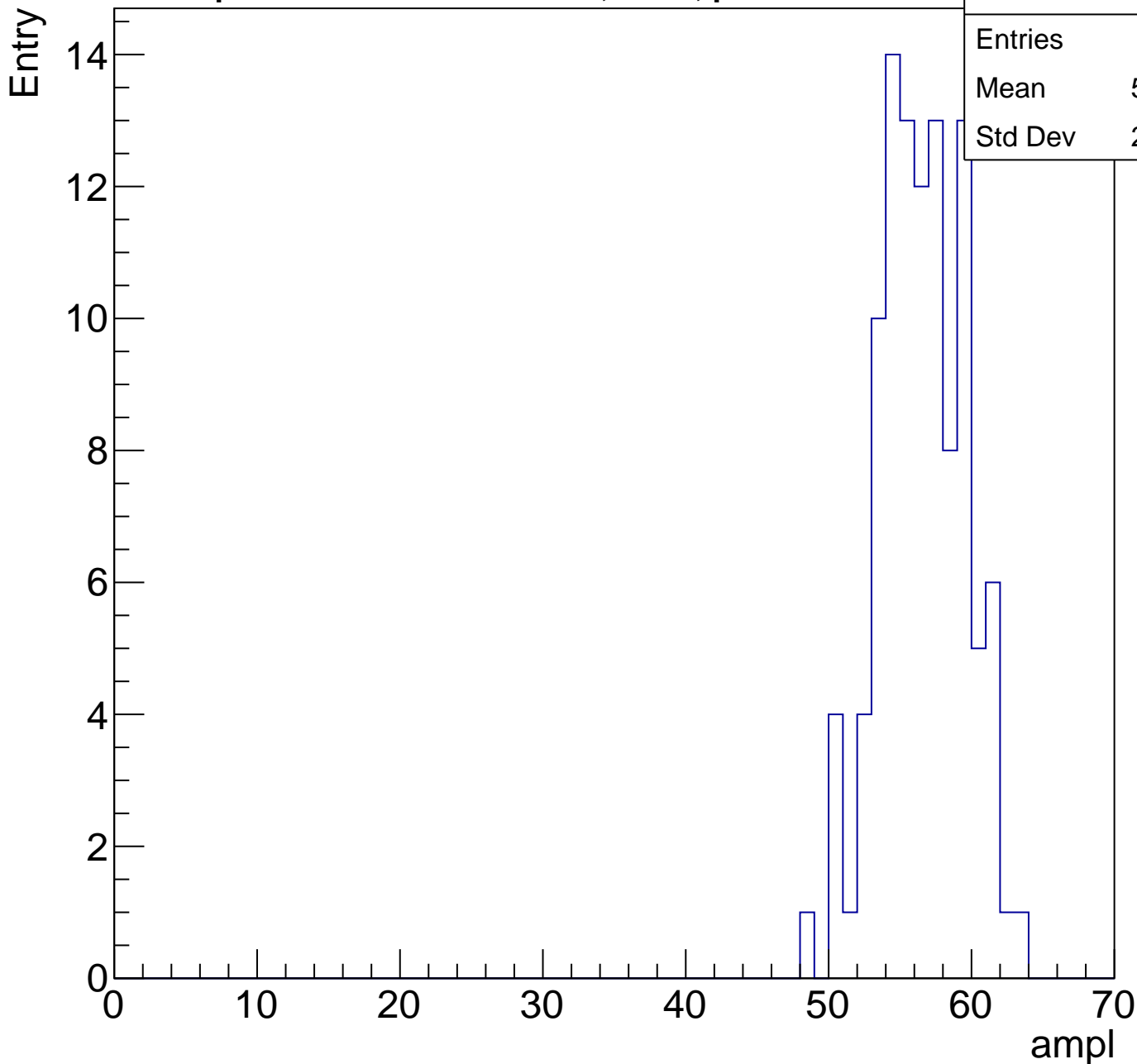
| | |
|---------|-------|
| Entries | 136 |
| Mean | 49.79 |
| Std Dev | 3.335 |



B0L101S, U22-ch49, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 106 |
| Mean | 56.07 |
| Std Dev | 2.966 |



B0L101S, U22-ch49, adc5

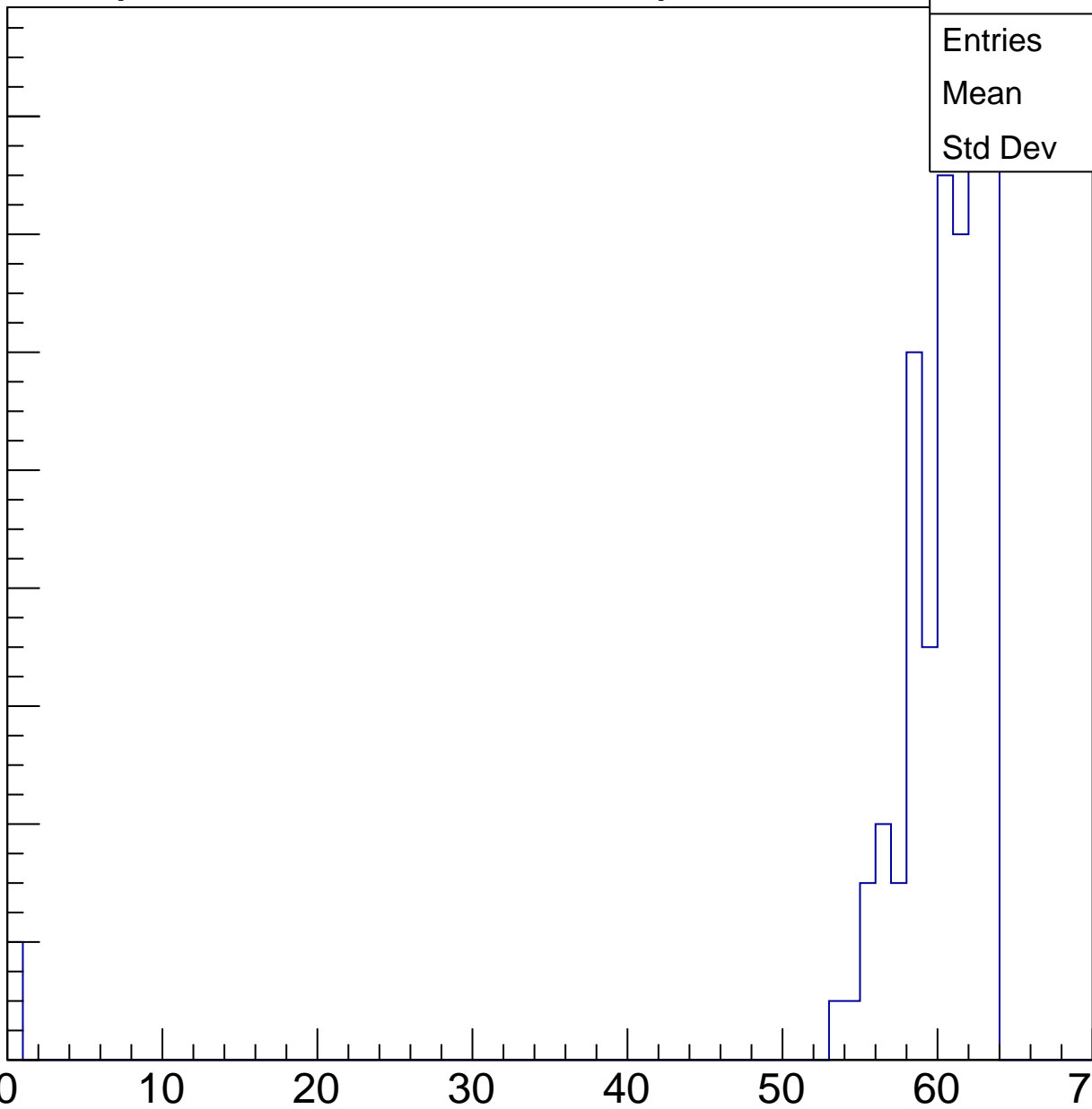
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 58.86 |
| Std Dev | 8.95 |

ampl



B0L101S, U22-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B0L101S, U22-ch50, adc0

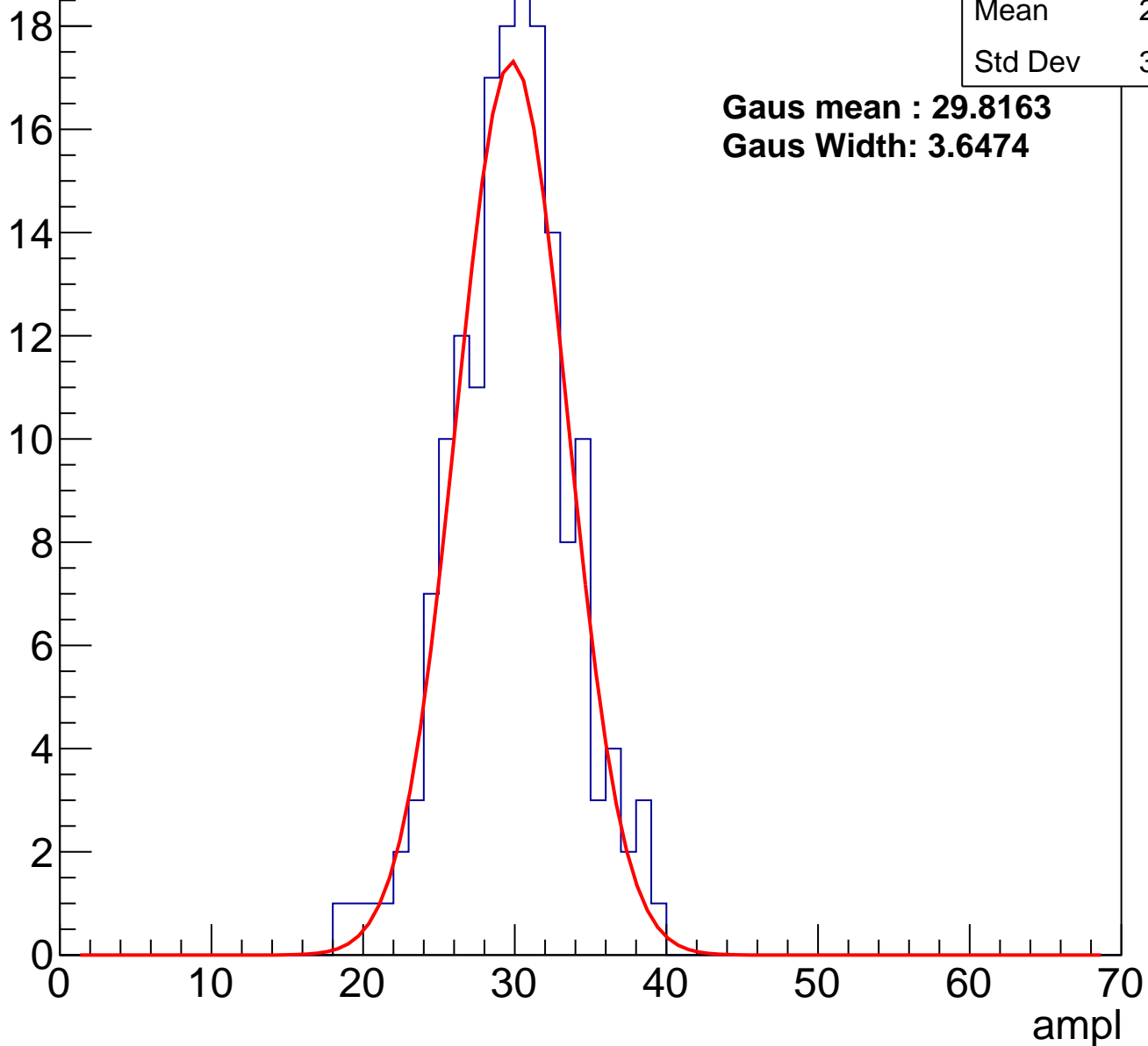
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 29.35 |
| Std Dev | 3.823 |

Gaus mean : 29.8163

Gaus Width: 3.6474

Entry



B0L101S, U22-ch50, adc1

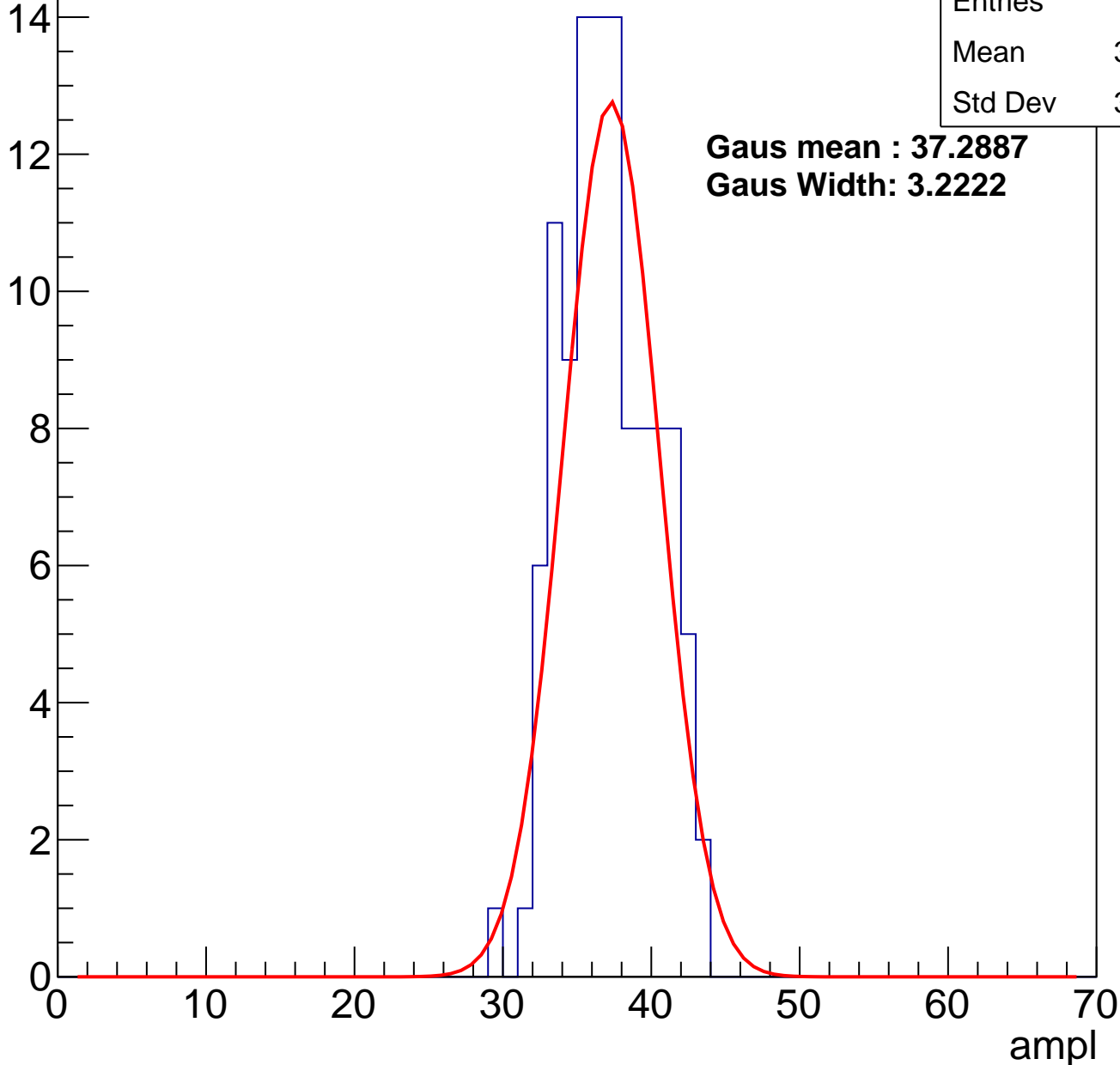
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 109 |
| Mean | 36.63 |
| Std Dev | 3.028 |

Gaus mean : 37.2887

Gaus Width: 3.2222

Entry



B0L101S, U22-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

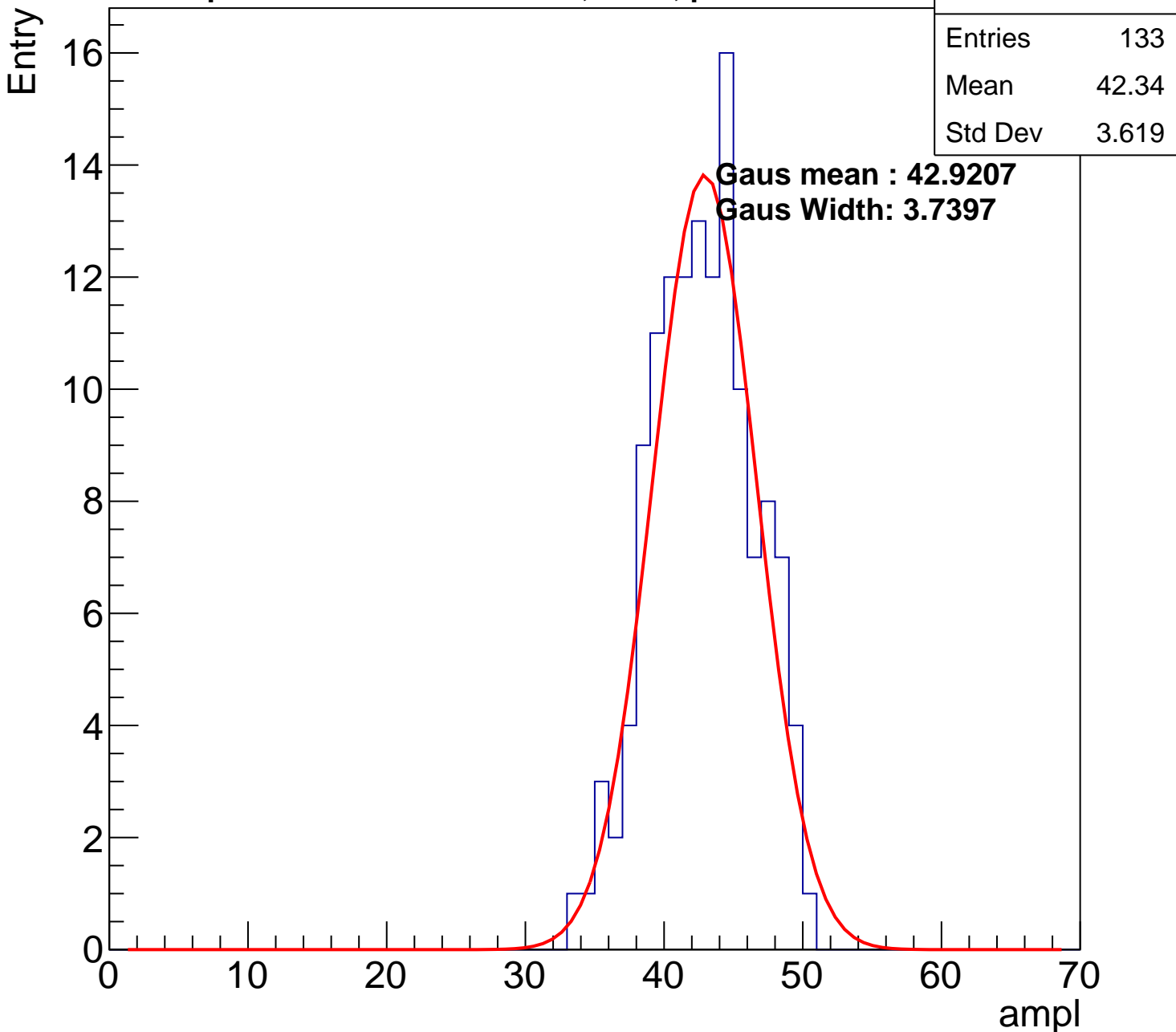
| | |
|---------|-------|
| Entries | 133 |
| Mean | 42.34 |
| Std Dev | 3.619 |

Gaus mean : 42.9207

Gaus Width: 3.7397

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

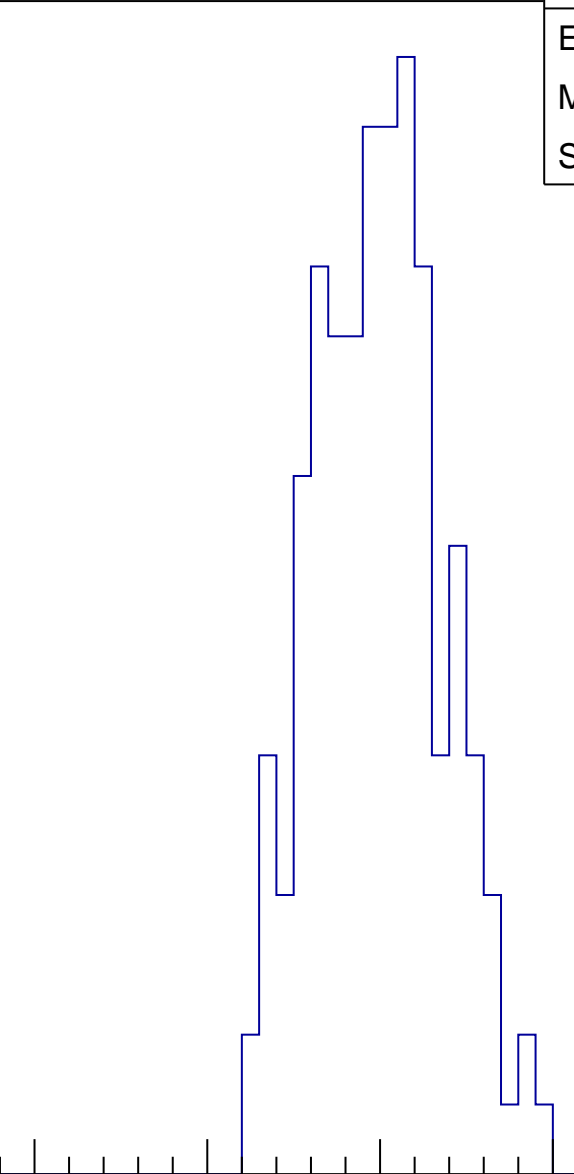
| | |
|---------|-------|
| Entries | 147 |
| Mean | 49.48 |
| Std Dev | 3.663 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

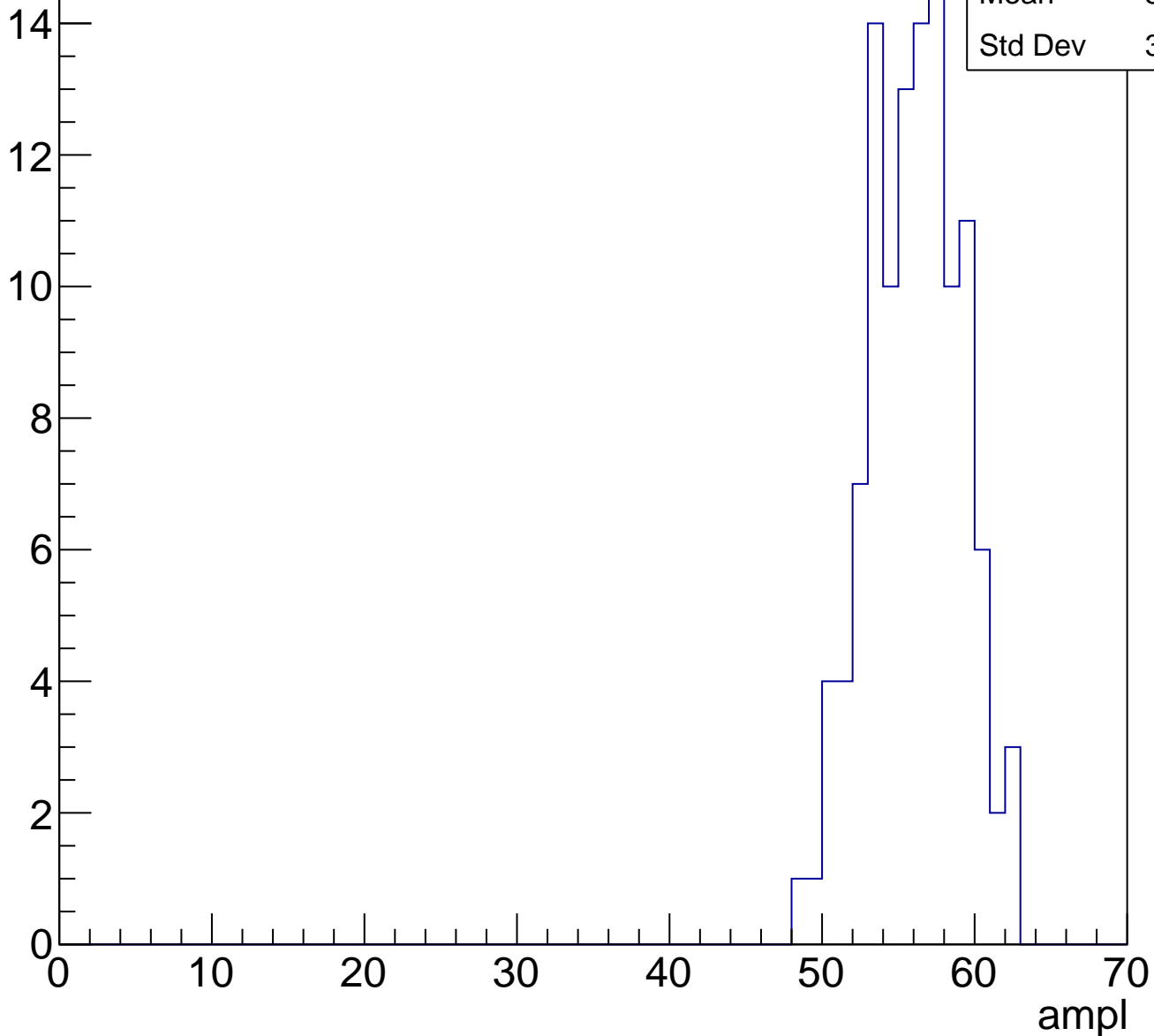
ampl



B0L101S, U22-ch50, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 115 |
| Mean | 55.63 |
| Std Dev | 3.008 |

B0L101S, U22-ch50, adc5

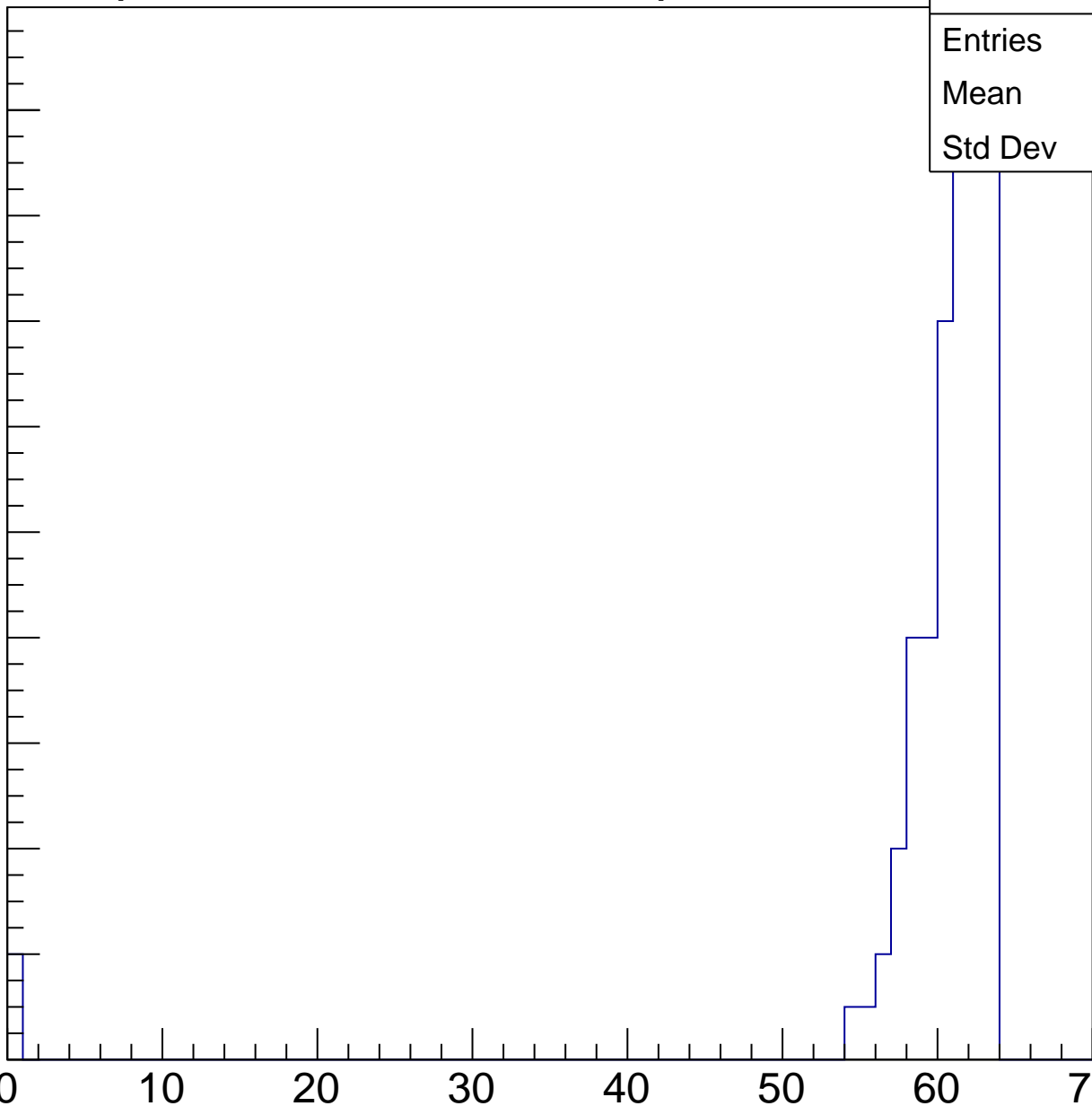
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 93 |
| Mean | 59.28 |
| Std Dev | 9.026 |

ampl



B0L101S, U22-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch51, adc0

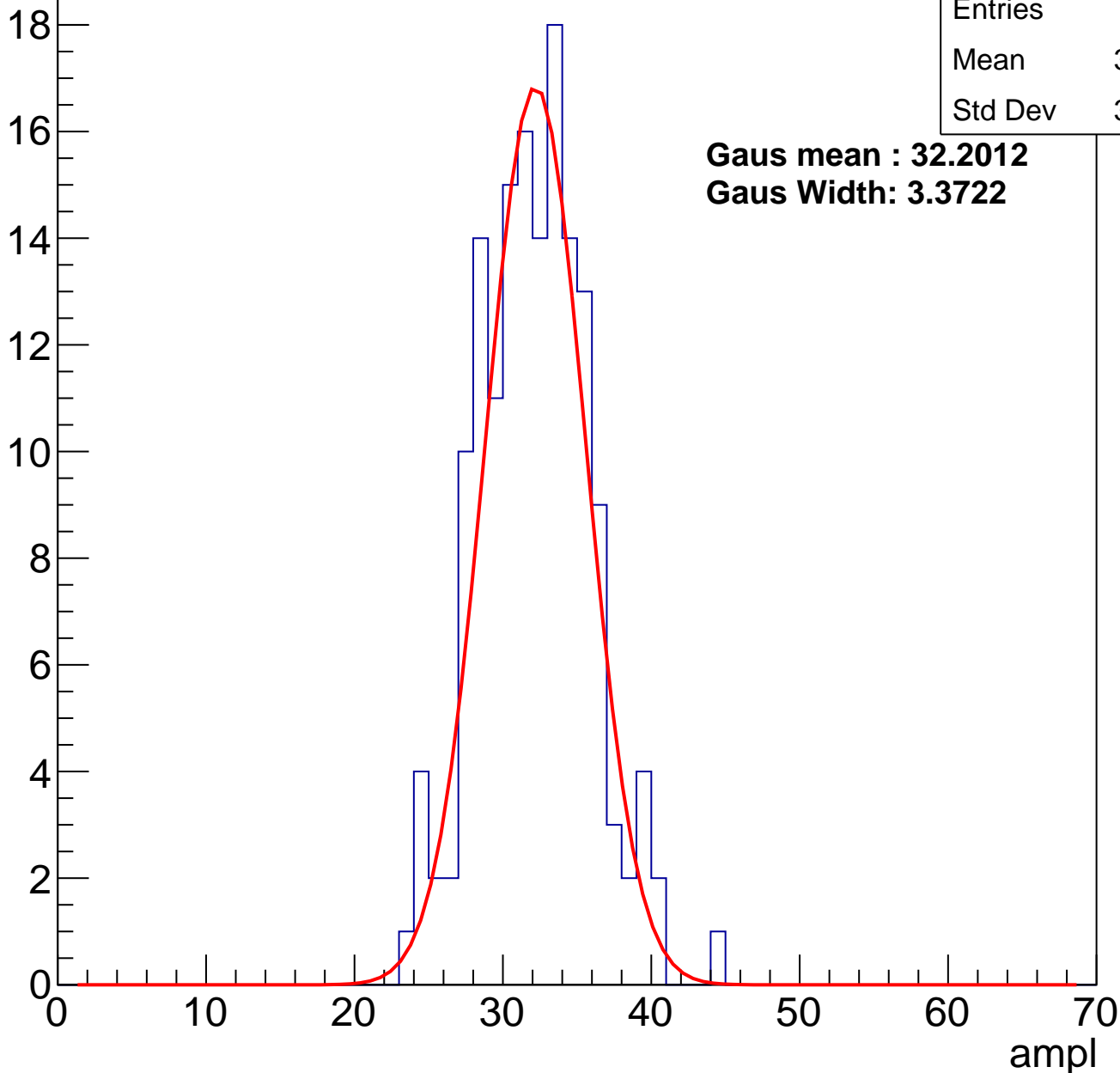
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 31.69 |
| Std Dev | 3.673 |

Gaus mean : 32.2012

Gaus Width: 3.3722

Entry



B0L101S, U22-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 38.33 |
| Std Dev | 3.696 |

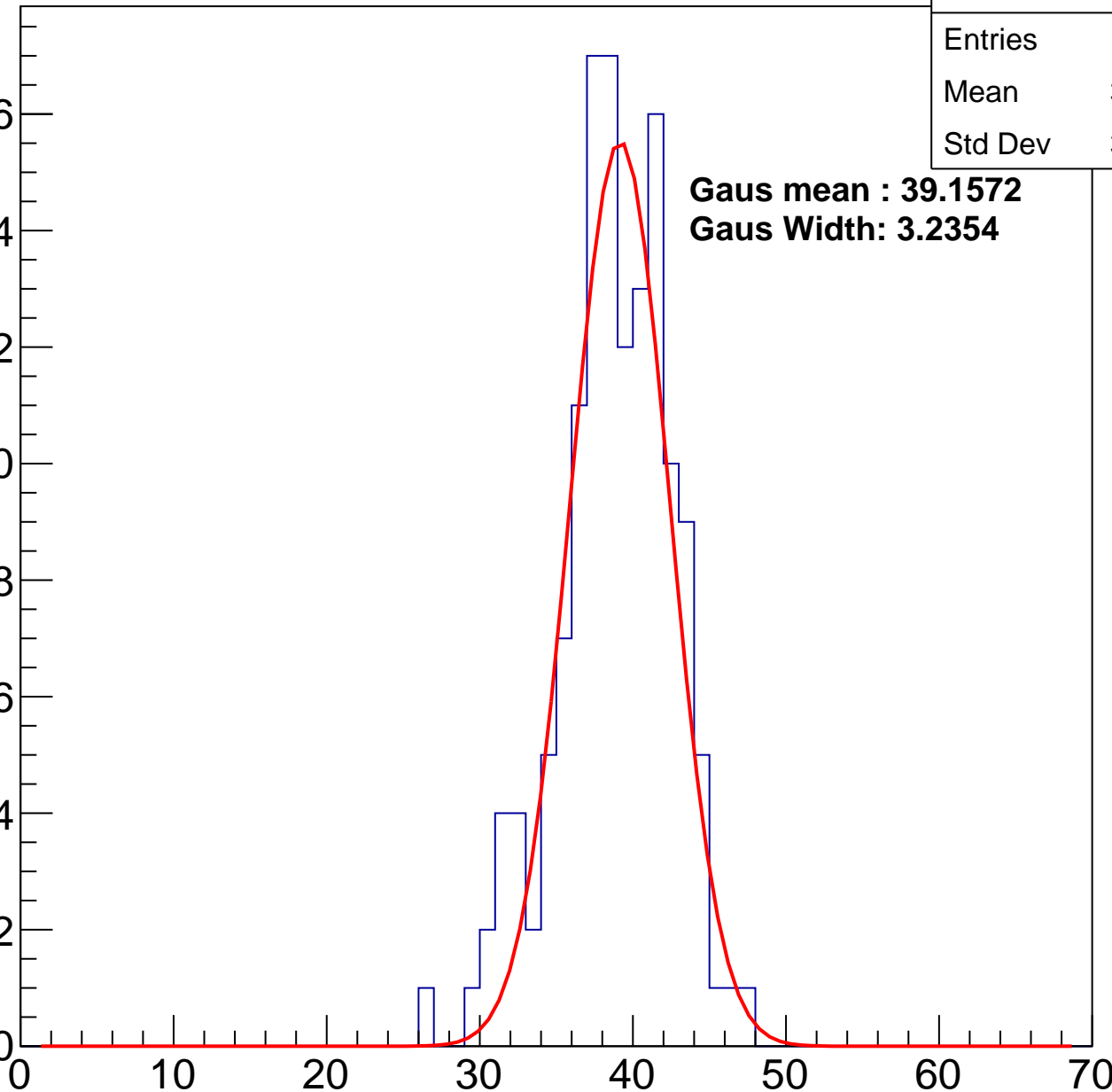
Gaus mean : 39.1572

Gaus Width: 3.2354

Entry

16
14
12
10
8
6
4
2
0

ampl



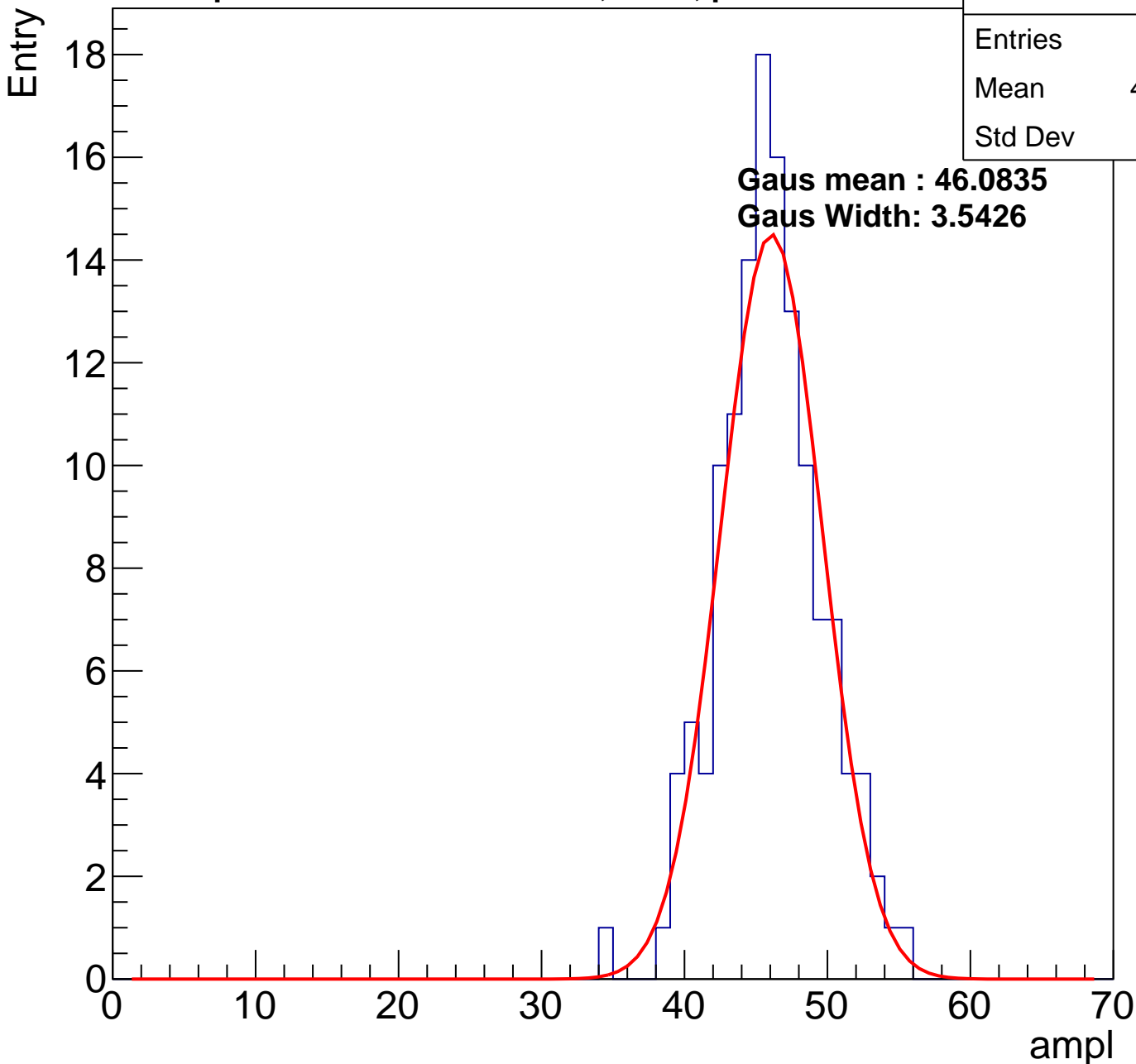
B0L101S, U22-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 45.55 |
| Std Dev | 3.58 |

Gaus mean : 46.0835

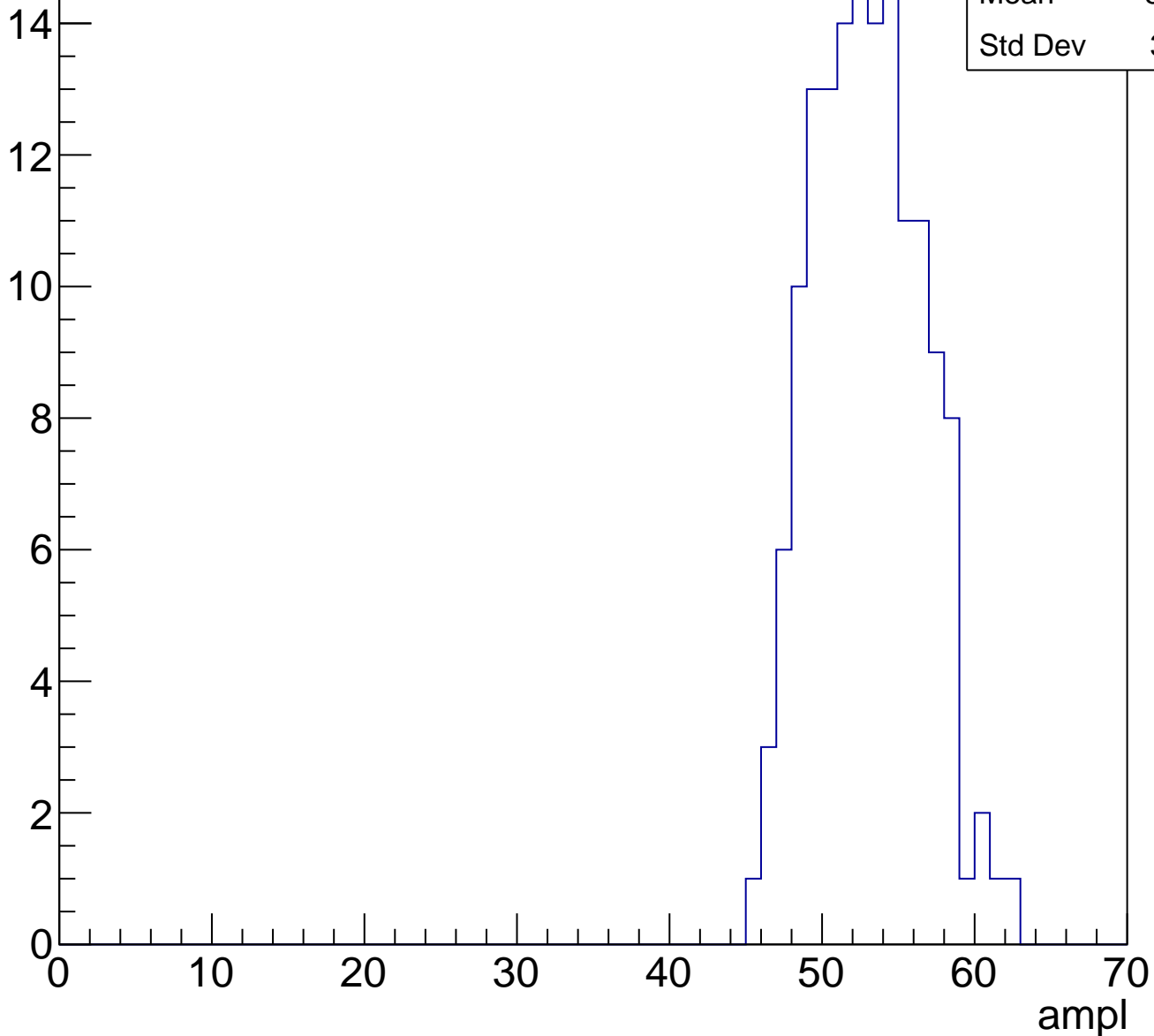
Gaus Width: 3.5426



B0L101S, U22-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



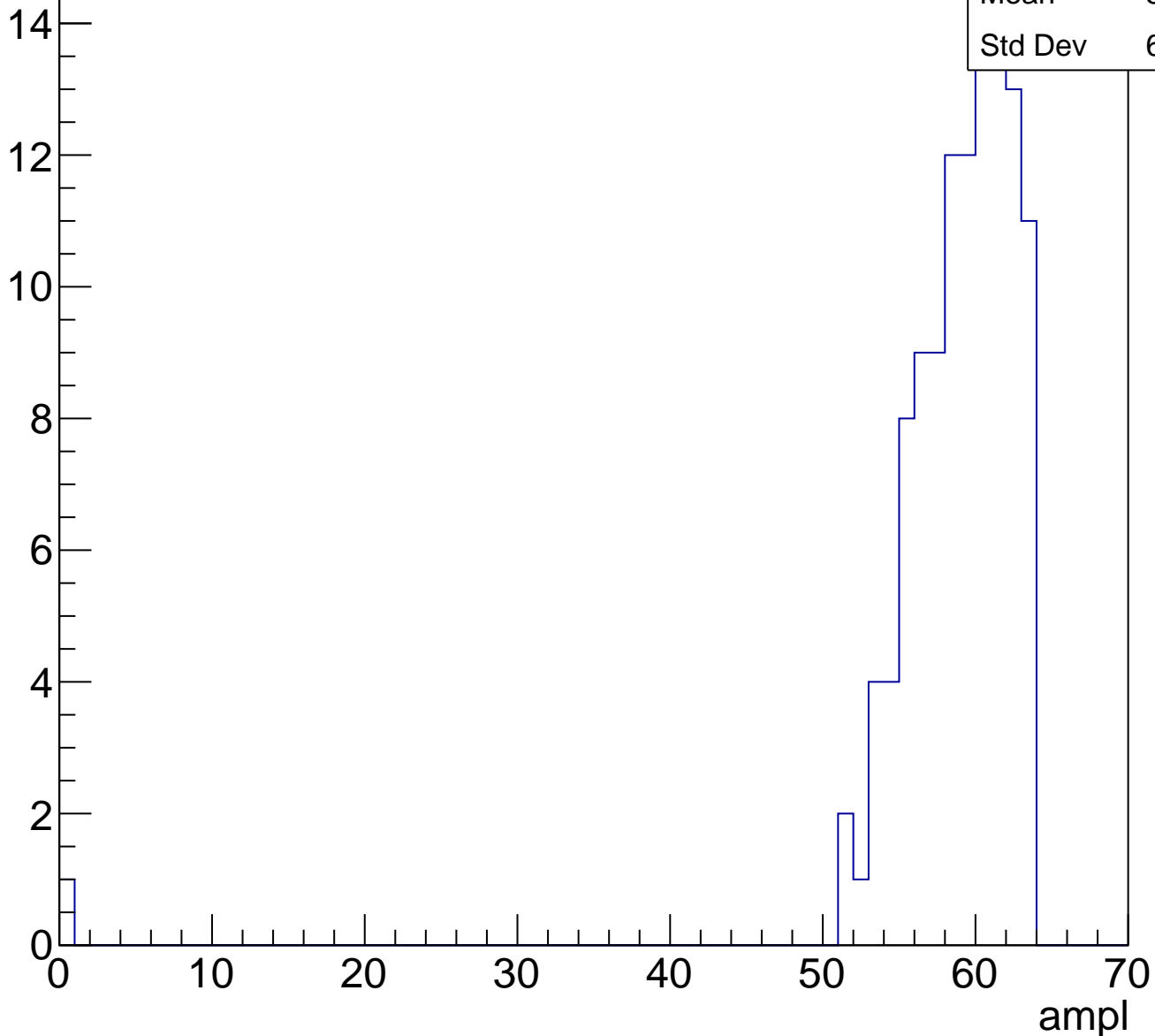
| | |
|---------|-------|
| Entries | 148 |
| Mean | 52.55 |
| Std Dev | 3.511 |

B0L101S, U22-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 115 |
| Mean | 58.23 |
| Std Dev | 6.227 |

Entry

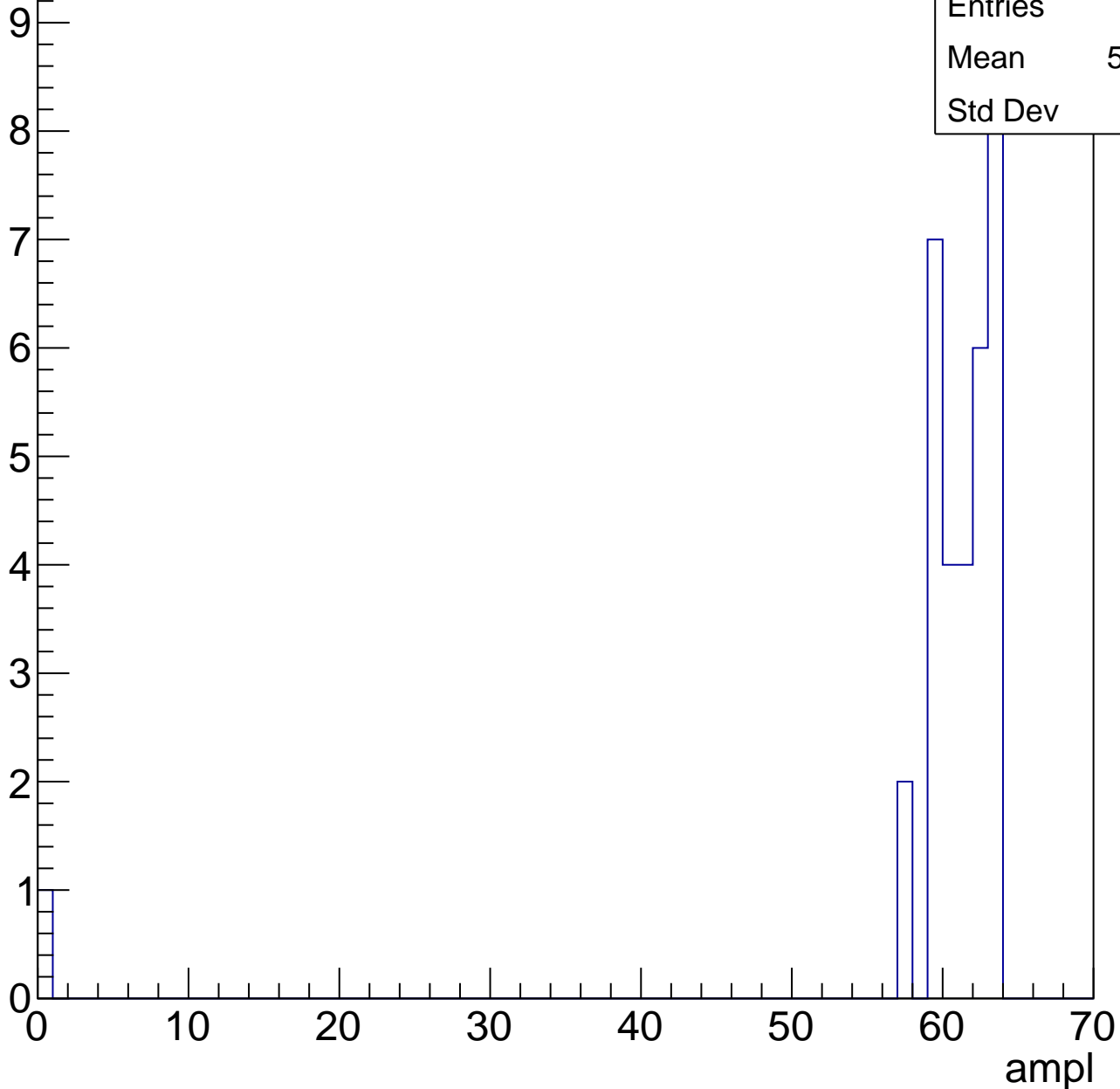


B0L101S, U22-ch51, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 33 |
| Mean | 59.09 |
| Std Dev | 10.6 |



B0L101S, U22-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L101S, U22-ch52, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 29.5 |
| Std Dev | 4.483 |

Gaus mean : 29.9542

Gaus Width: 3.6703

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

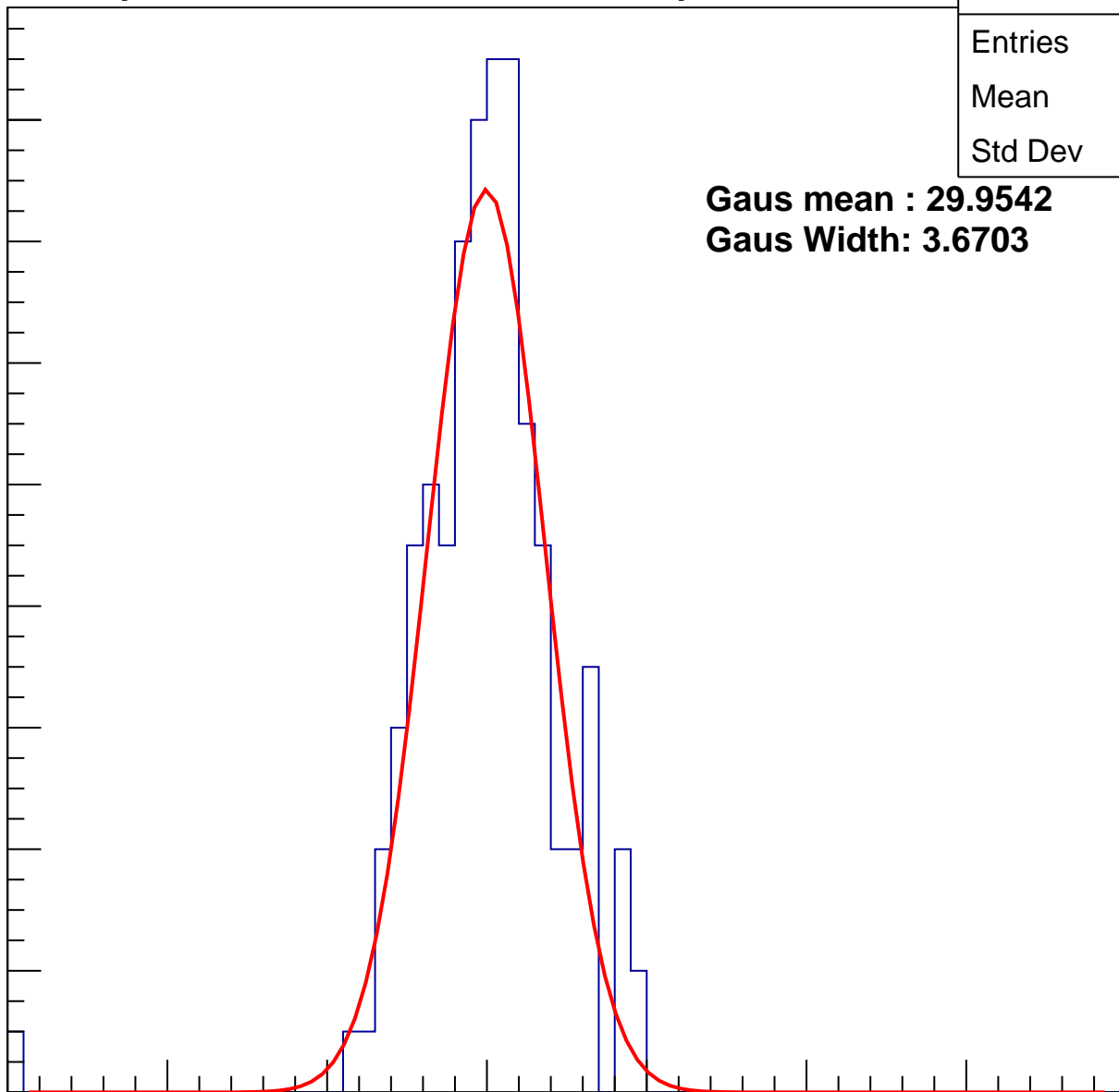
40

50

60

70

ampl



B0L101S, U22-ch52, adc1

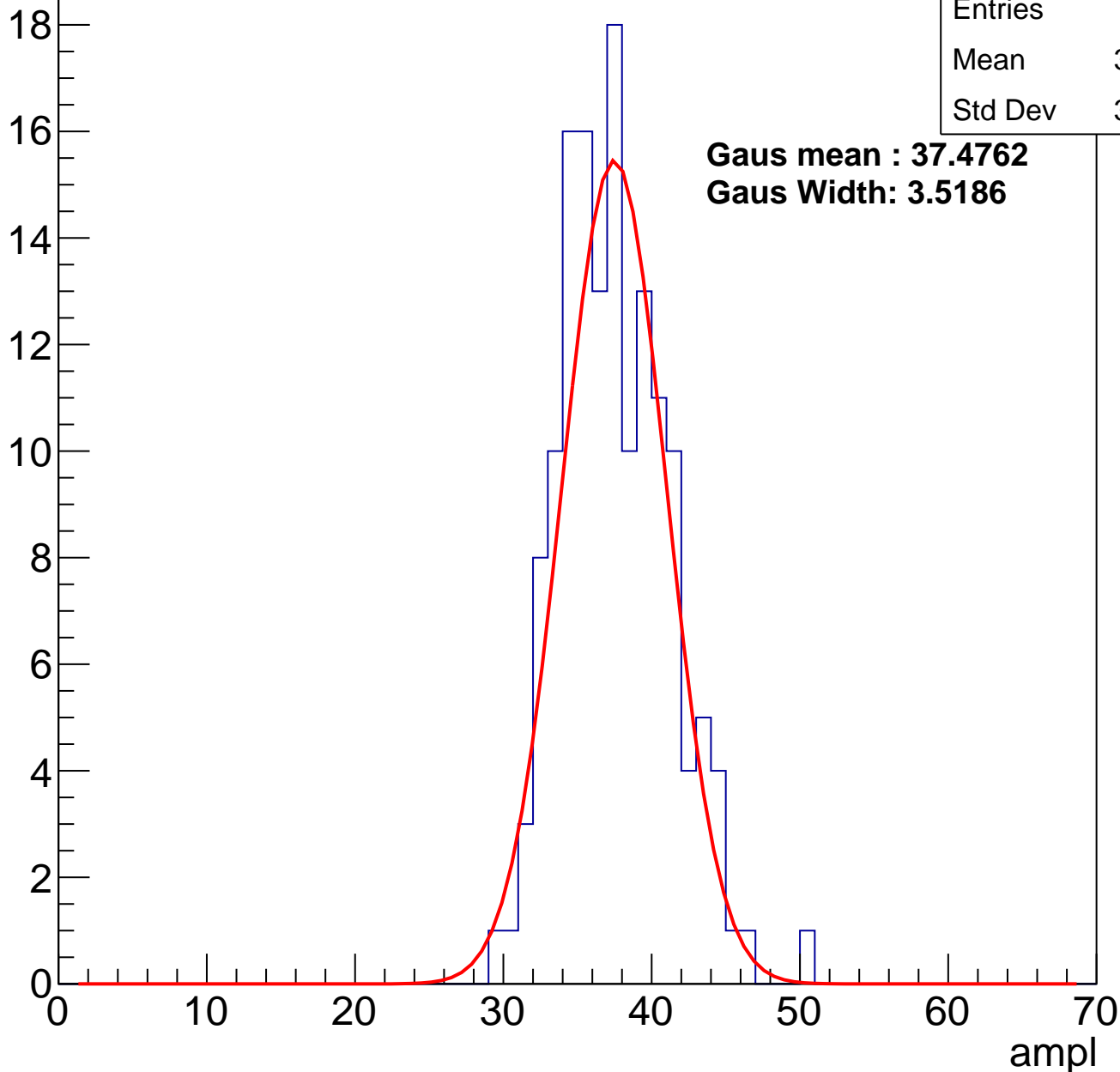
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 37.08 |
| Std Dev | 3.619 |

Gaus mean : 37.4762

Gaus Width: 3.5186

Entry



B0L101S, U22-ch52, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

124

Mean

43.69

Std Dev

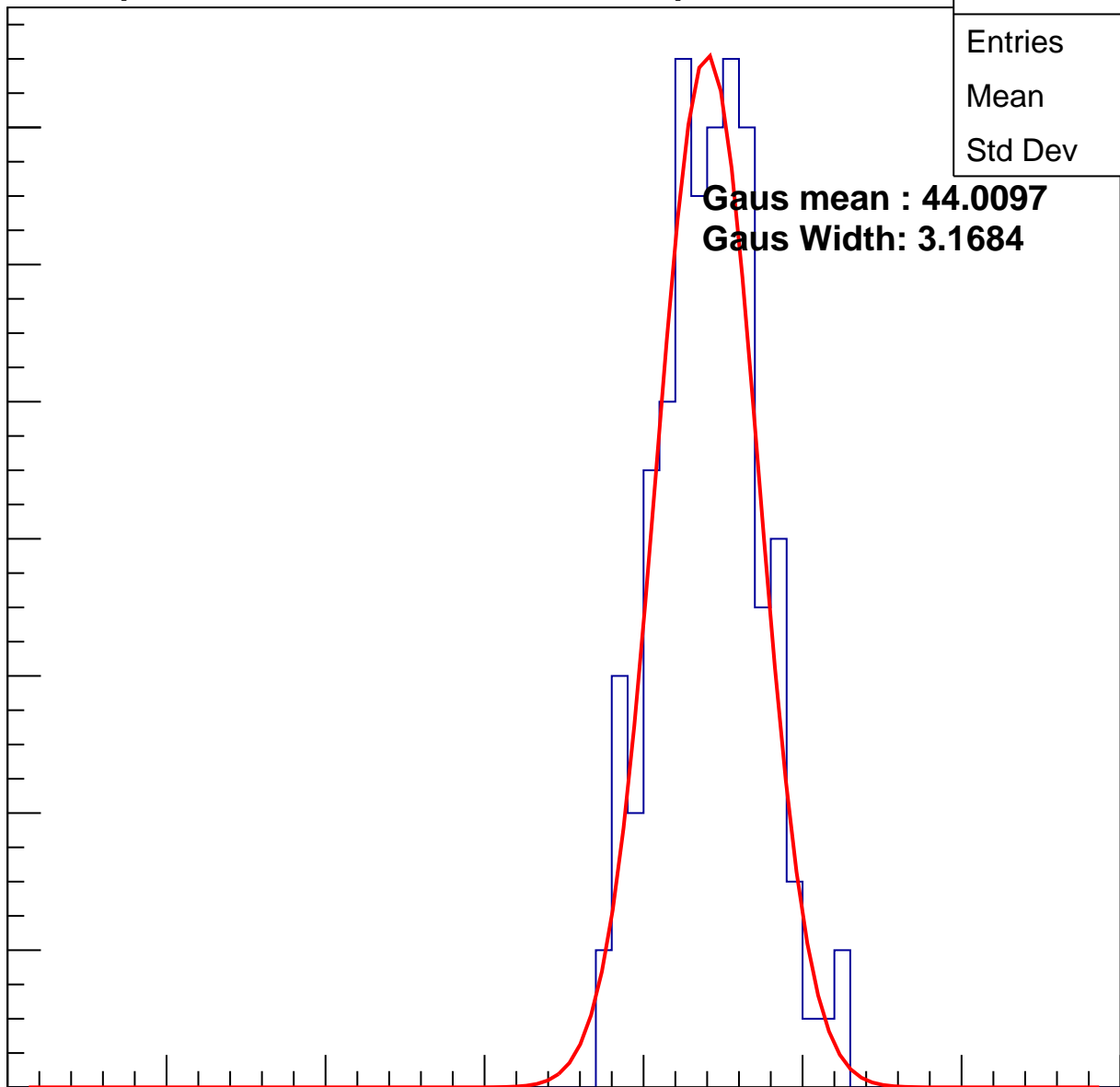
3.186

Gaus mean : 44.0097

Gaus Width: 3.1684

0 10 20 30 40 50 60 70

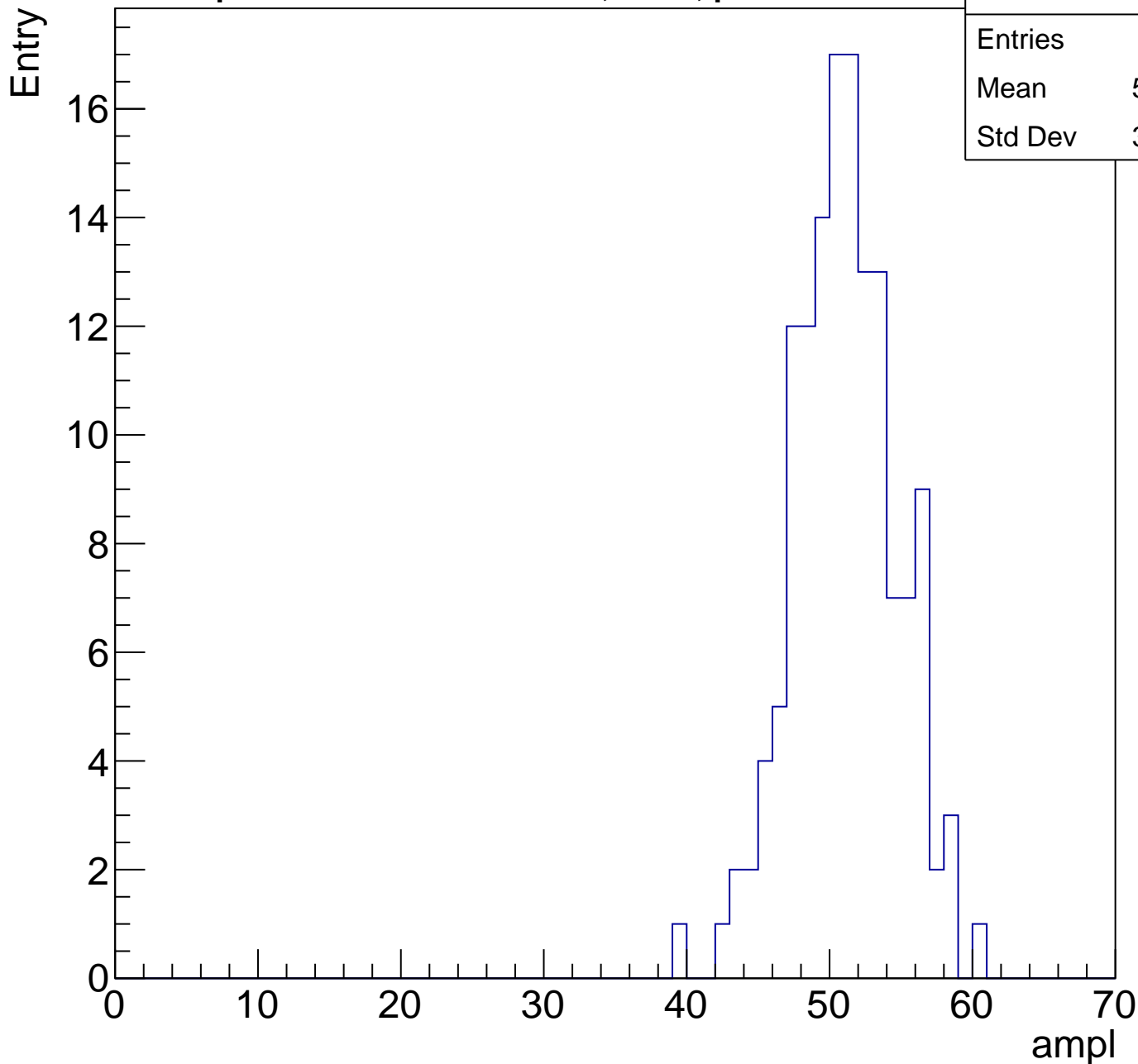
ampl



B0L101S, U22-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 142 |
| Mean | 50.62 |
| Std Dev | 3.597 |



B0L101S, U22-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

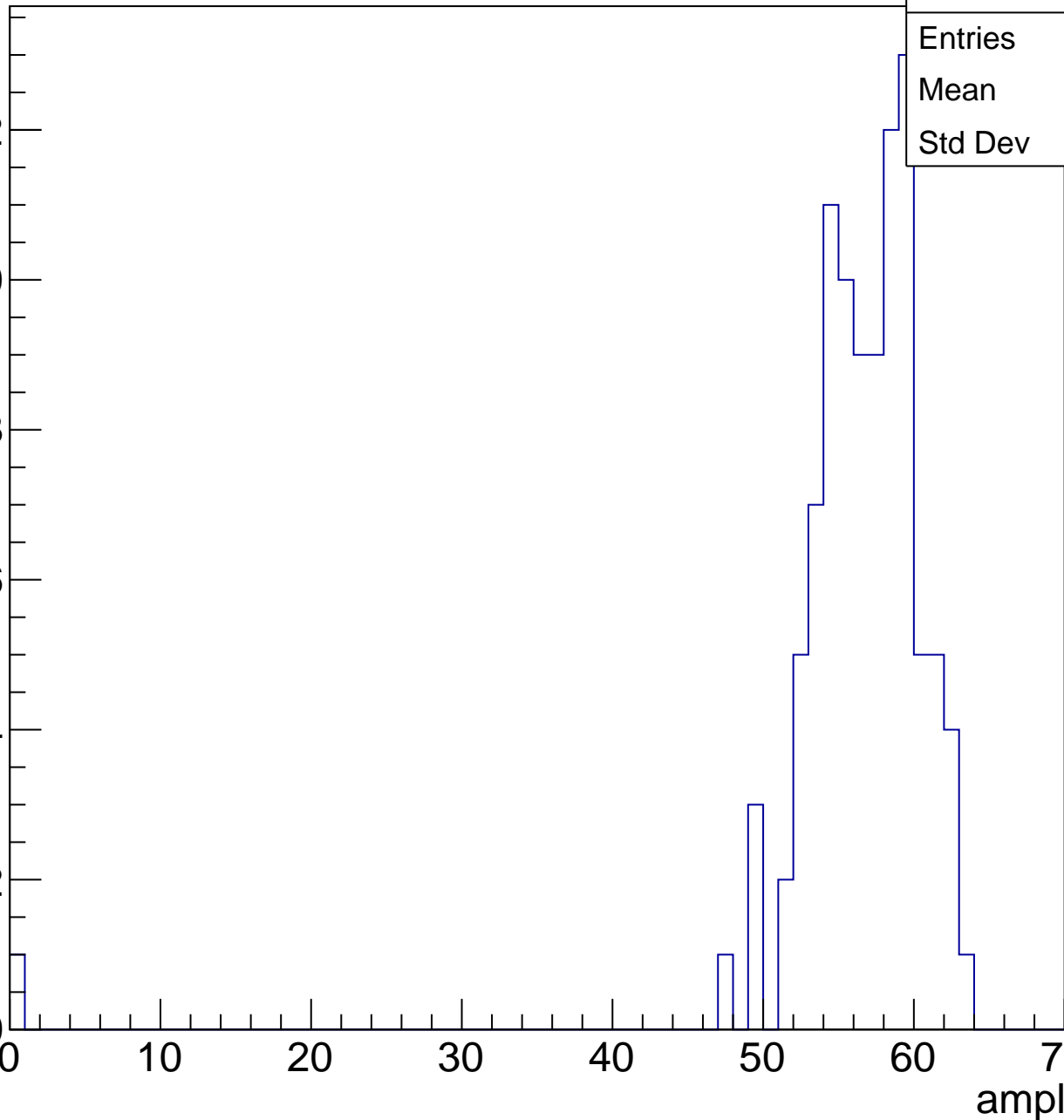
50

60

70

ampl

| | |
|---------|-------|
| Entries | 98 |
| Mean | 55.79 |
| Std Dev | 6.53 |



B0L101S, U22-ch52, adc5

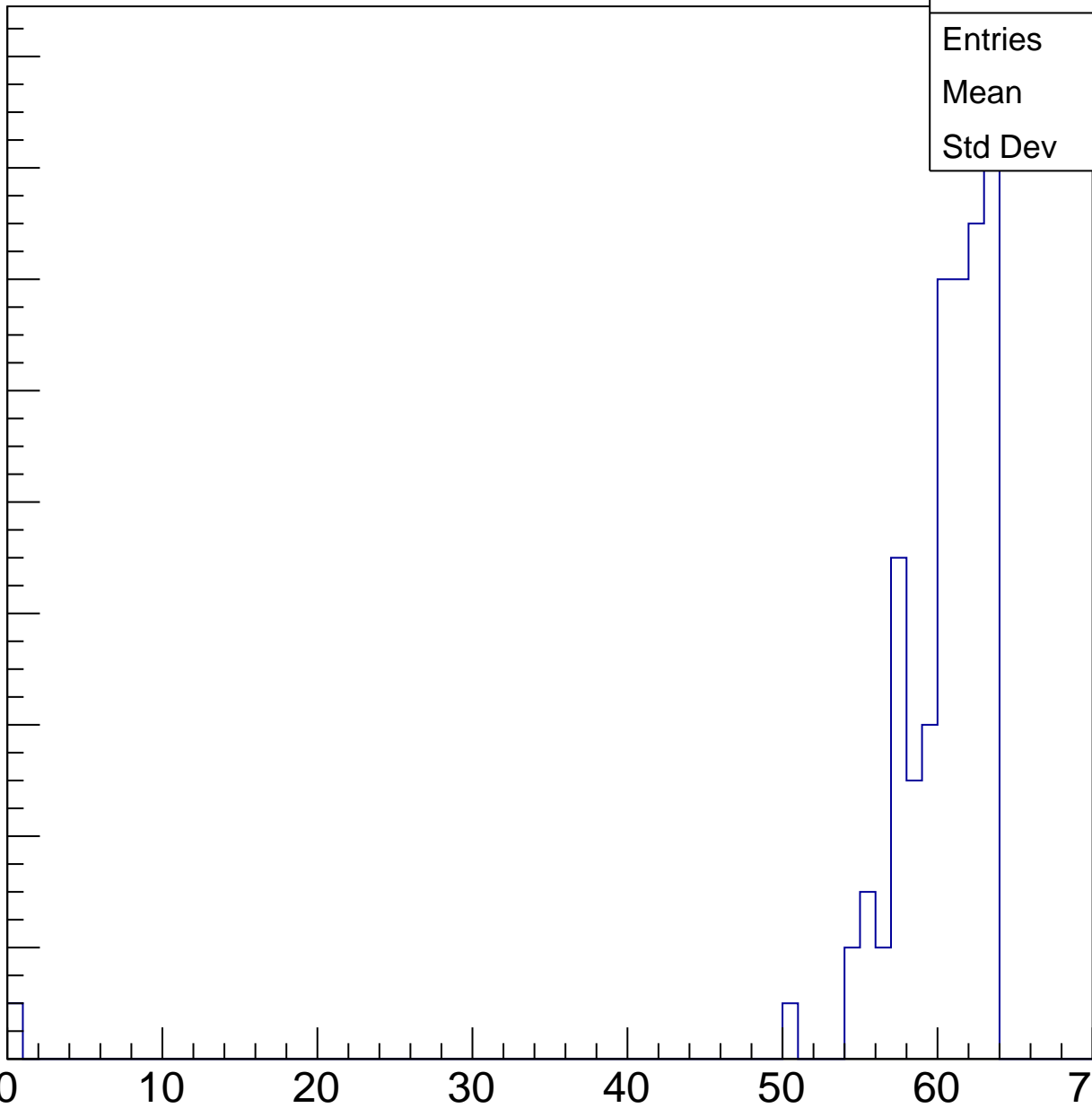
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 59.44 |
| Std Dev | 6.827 |

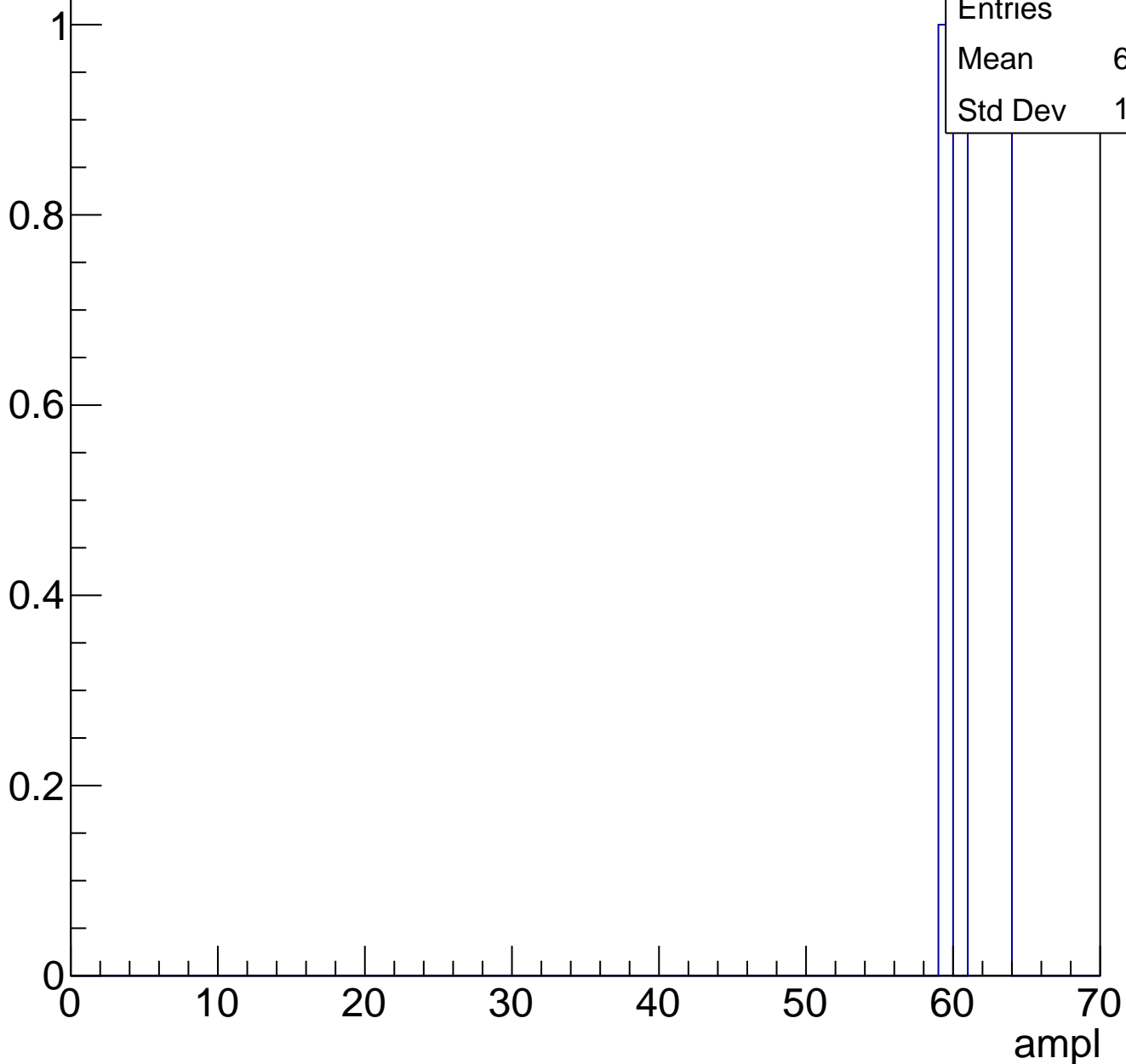
ampl



B0L101S, U22-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 29.83 |
| Std Dev | 5.295 |

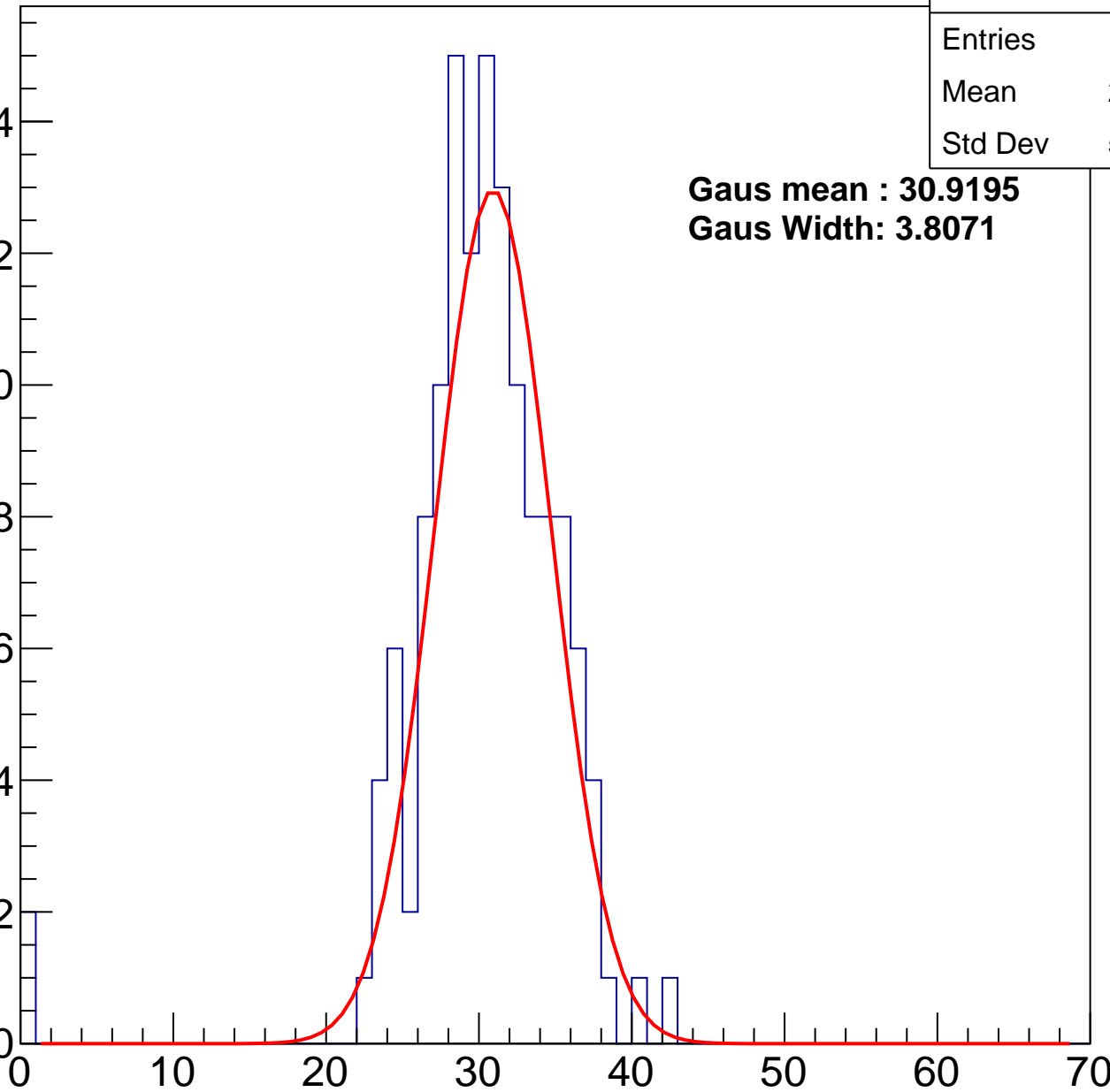
Gaus mean : 30.9195

Gaus Width: 3.8071

Entry

14
12
10
8
6
4
2
0

ampl

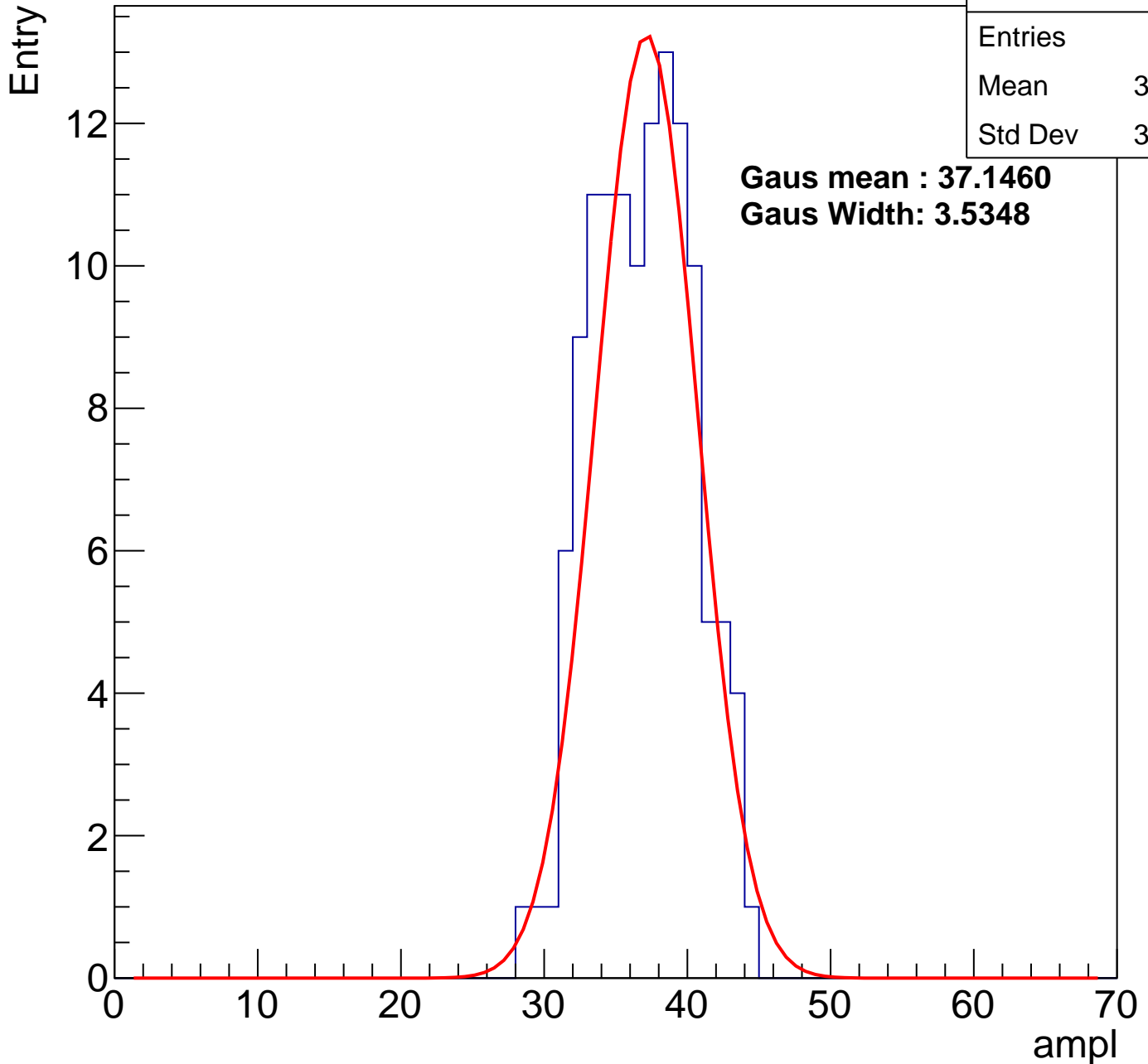


B0L101S, U22-ch53, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 36.42 |
| Std Dev | 3.459 |

Gaus mean : 37.1460
Gaus Width: 3.5348



B0L101S, U22-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

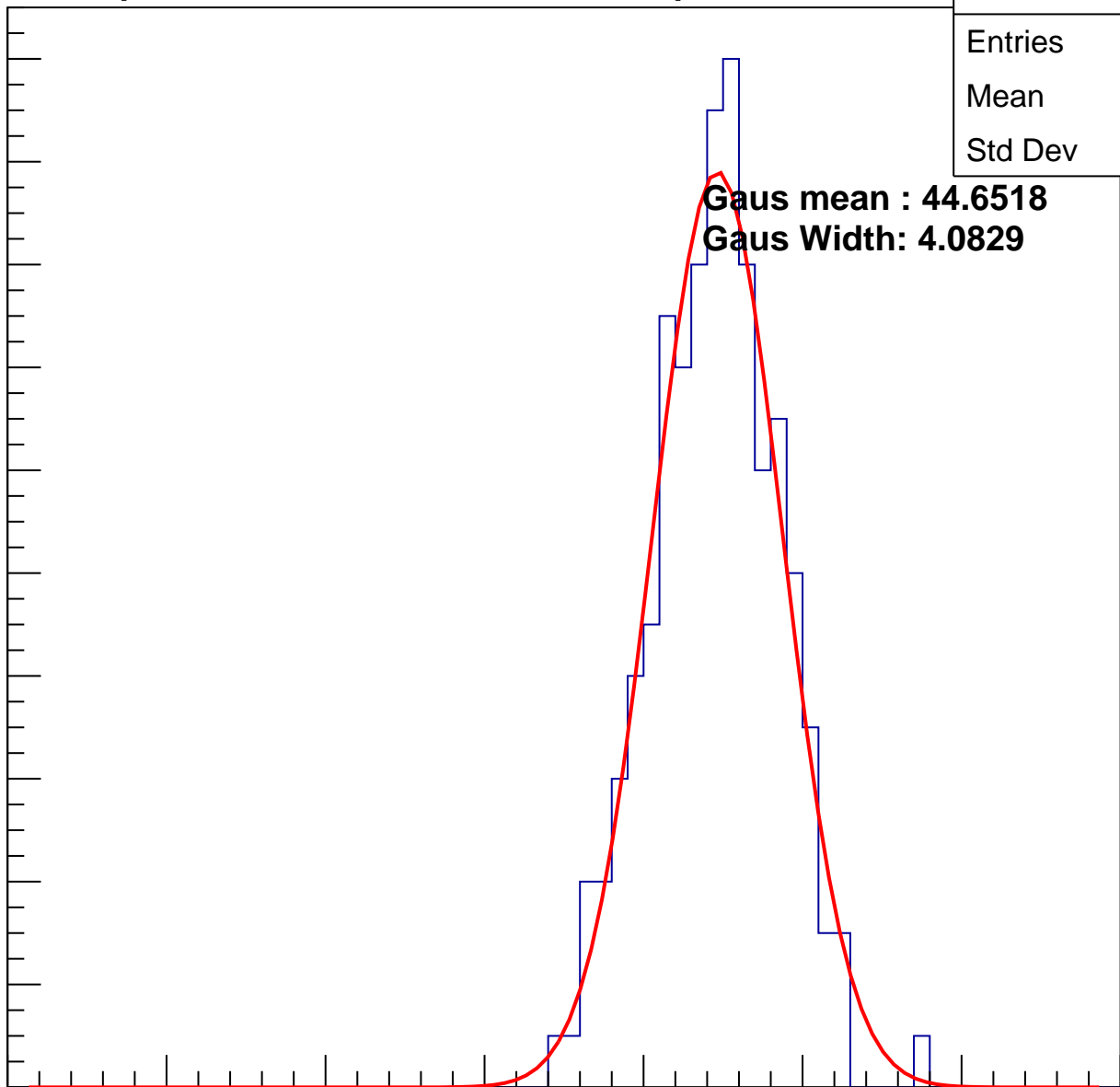
| | |
|---------|-------|
| Entries | 182 |
| Mean | 44.05 |
| Std Dev | 3.929 |

Gaus mean : 44.6518

Gaus Width: 4.0829

0 10 20 30 40 50 60 70

ampl

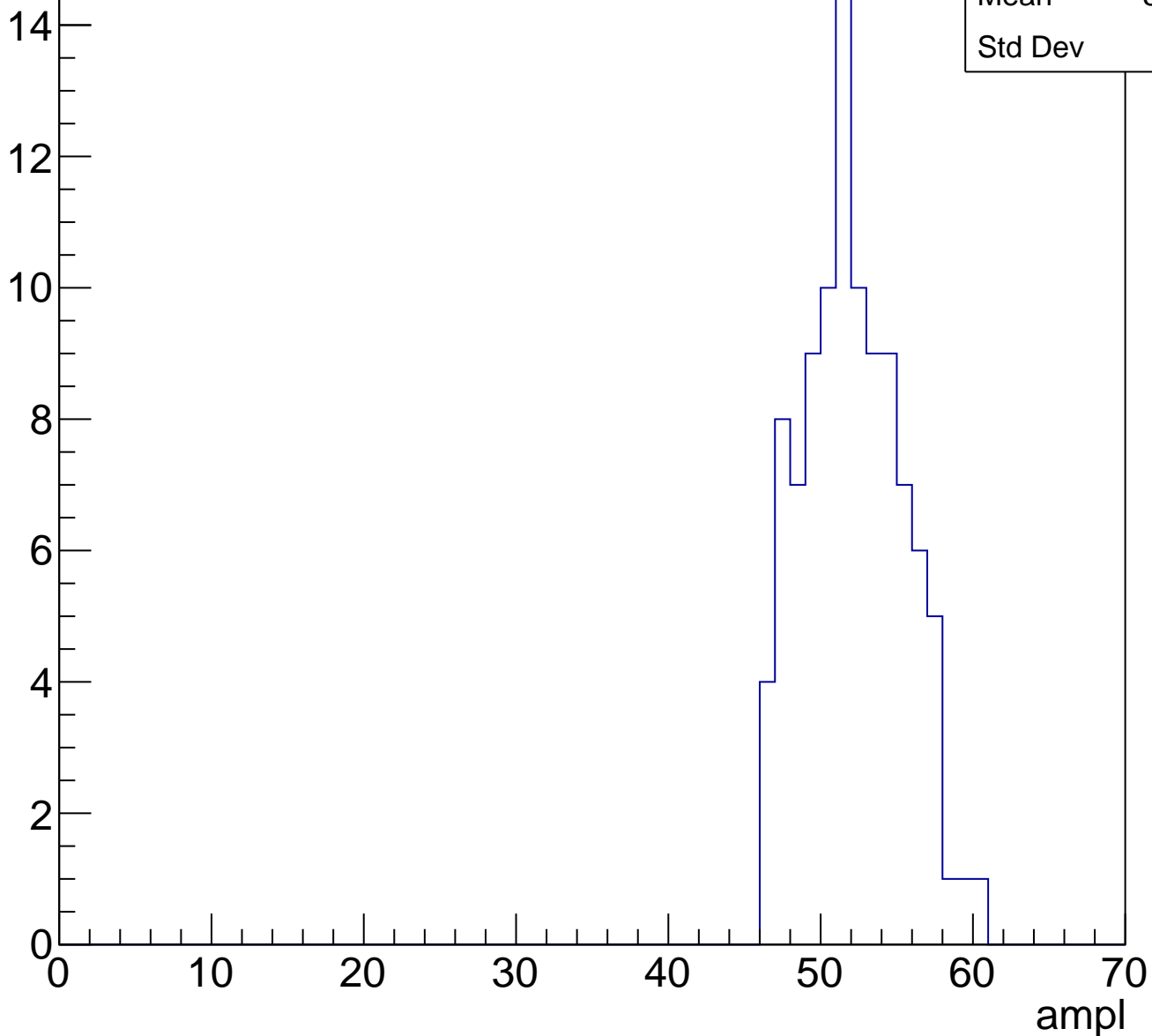


B0L101S, U22-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 102 |
| Mean | 51.65 |
| Std Dev | 3.22 |

Entry



B0L101S, U22-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 56.37 |
| Std Dev | 5.838 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

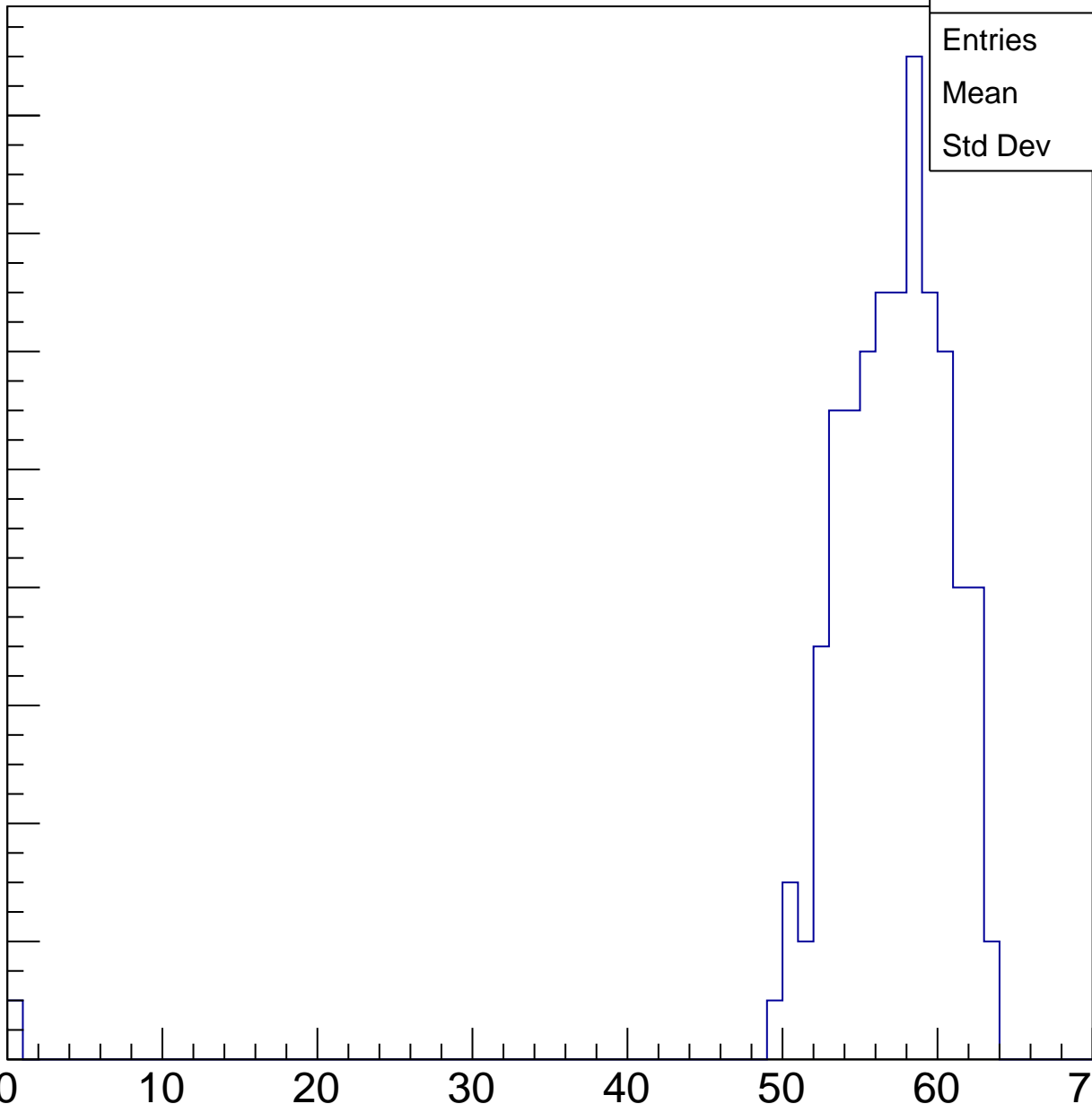
40

50

60

70

ampl



B0L101S, U22-ch53, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.73 |
| Std Dev | 8.573 |

ampl

0

10

20

30

40

50

60

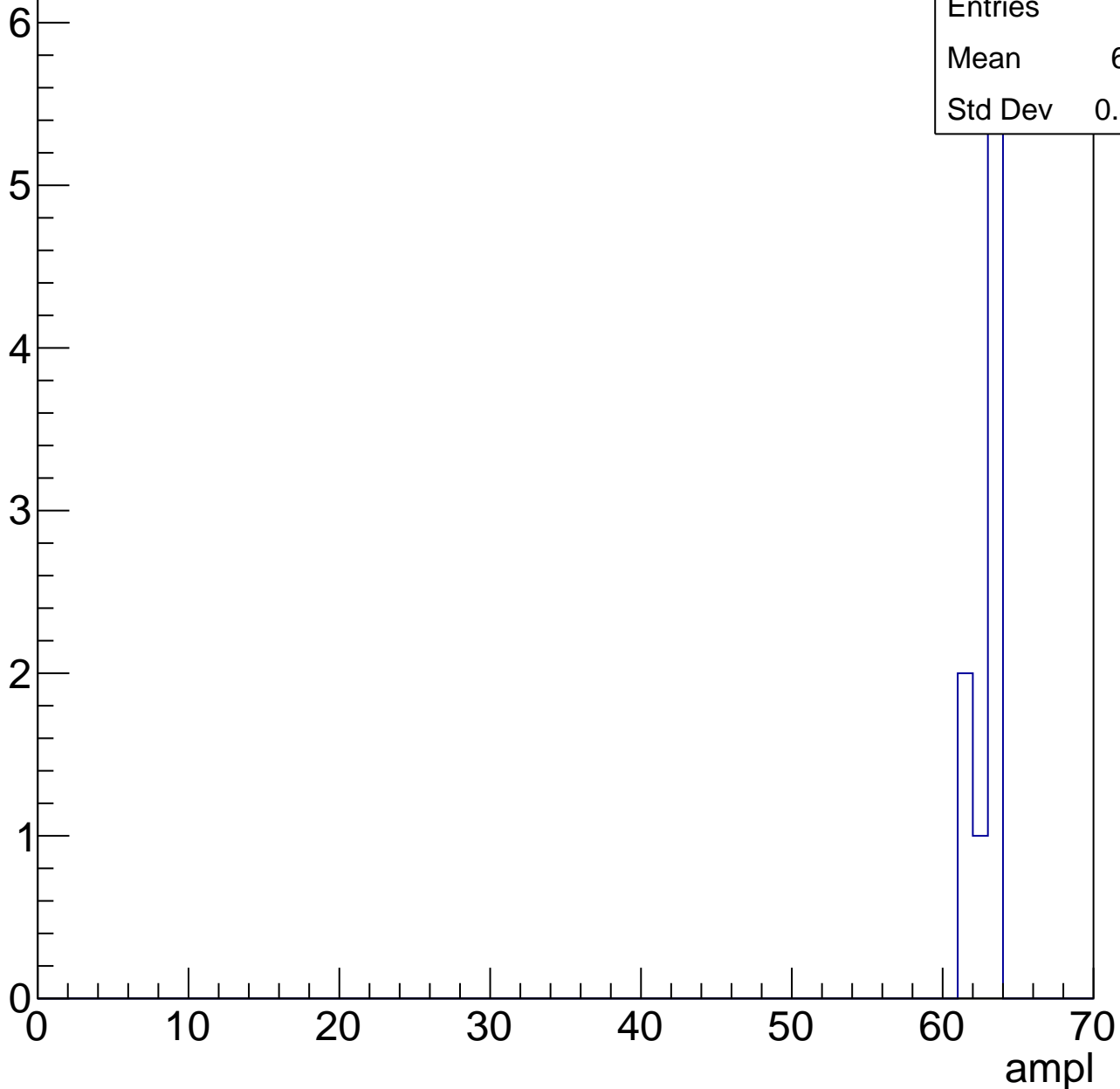
70

B0L101S, U22-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|--------|
| Entries | 9 |
| Mean | 62.44 |
| Std Dev | 0.8315 |



B0L101S, U22-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch54, adc0

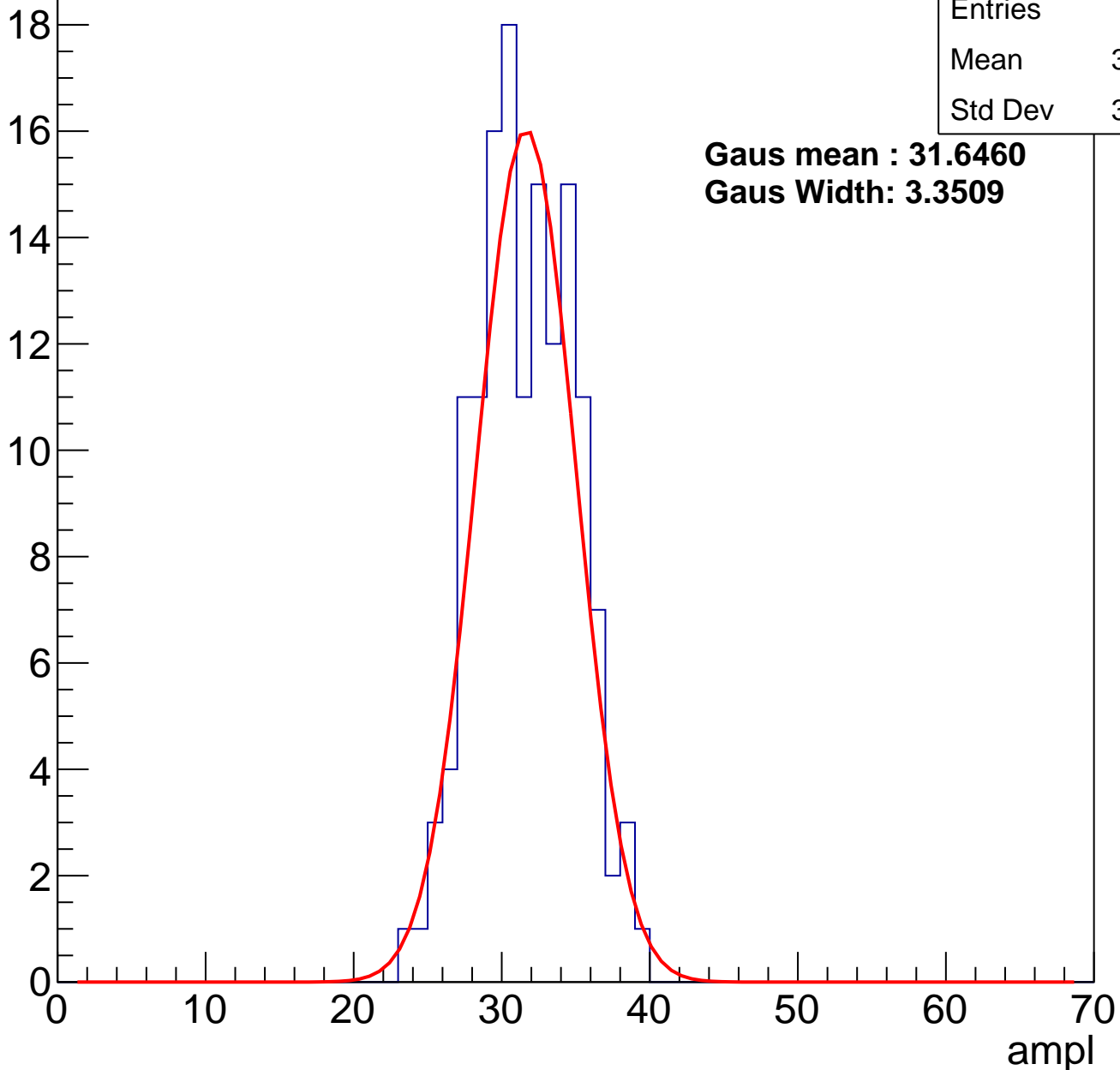
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 142 |
| Mean | 31.17 |
| Std Dev | 3.257 |

Gaus mean : 31.6460

Gaus Width: 3.3509

Entry

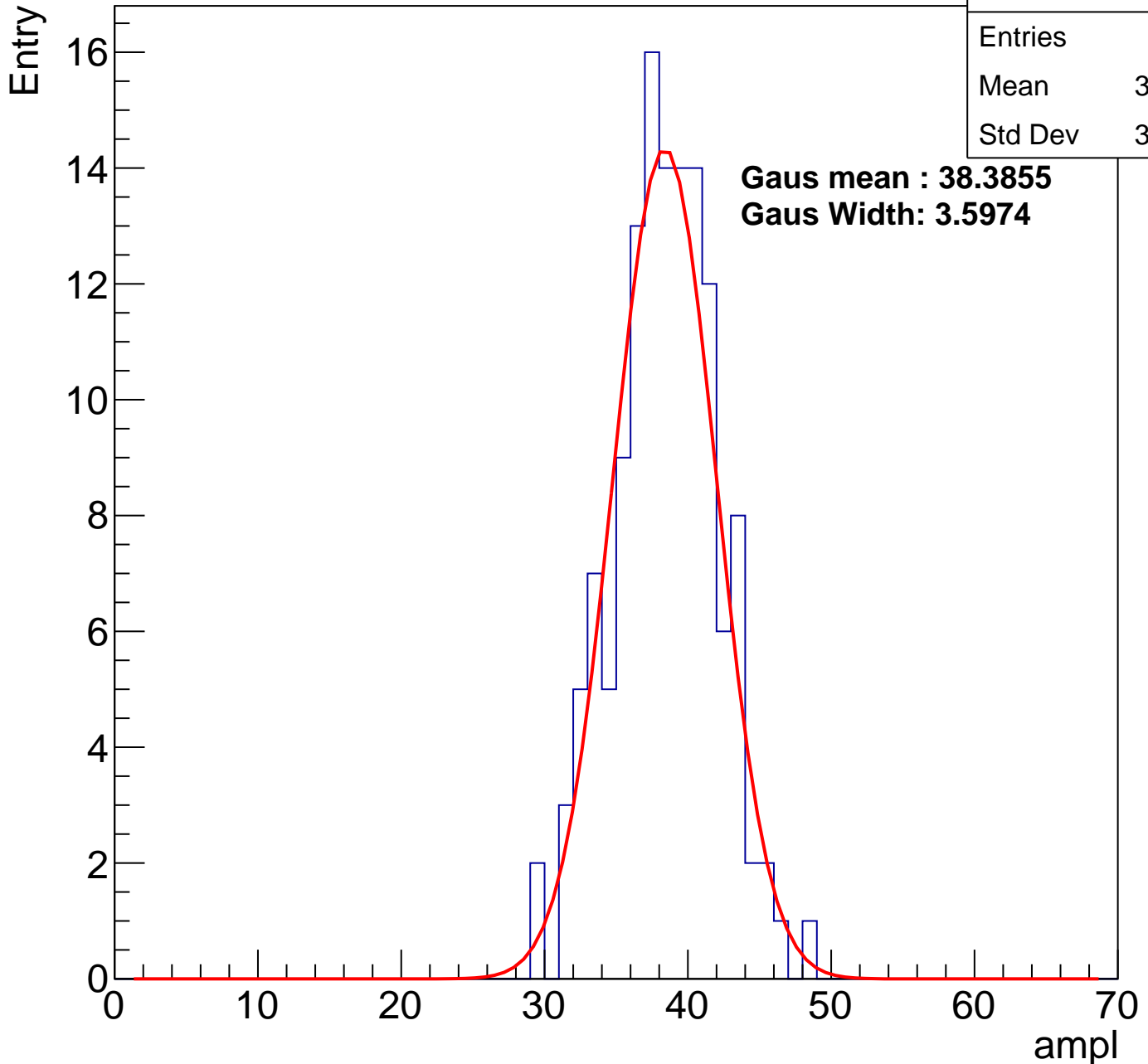


B0L101S, U22-ch54, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 37.95 |
| Std Dev | 3.562 |

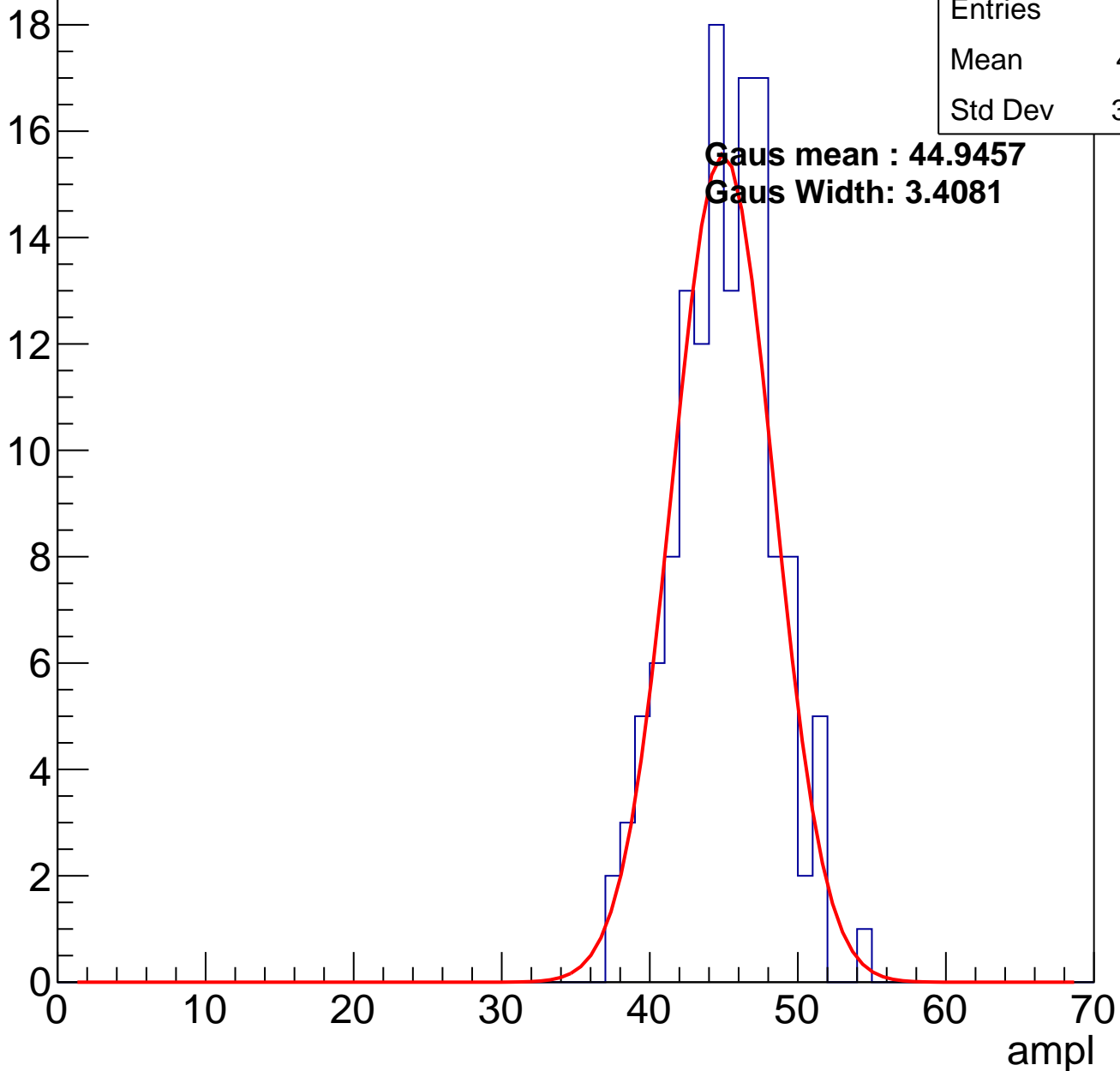
Gaus mean : 38.3855
Gaus Width: 3.5974



B0L101S, U22-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch54, adc3

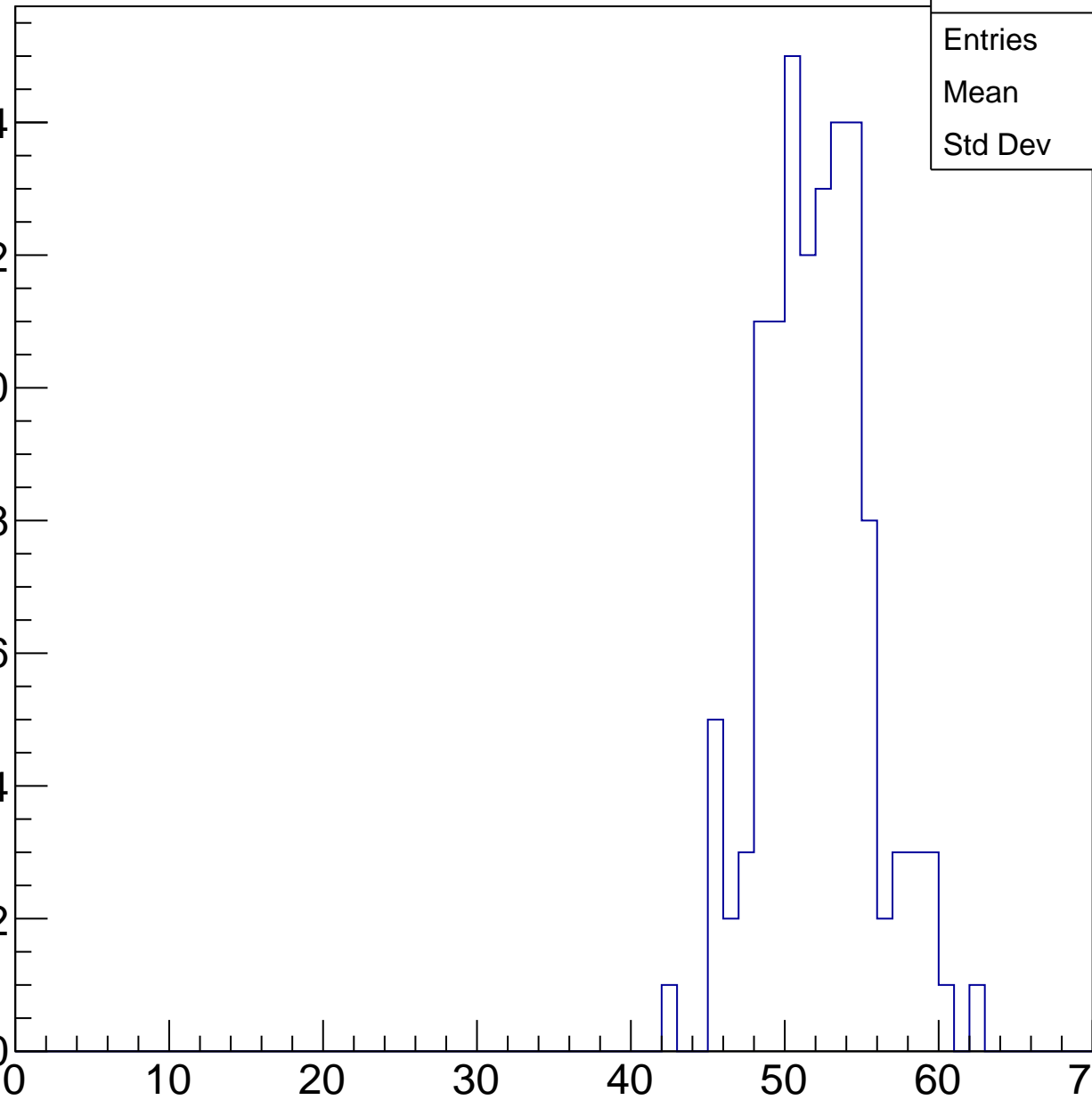
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 51.63 |
| Std Dev | 3.495 |

Entry

14
12
10
8
6
4
2
0

ampl

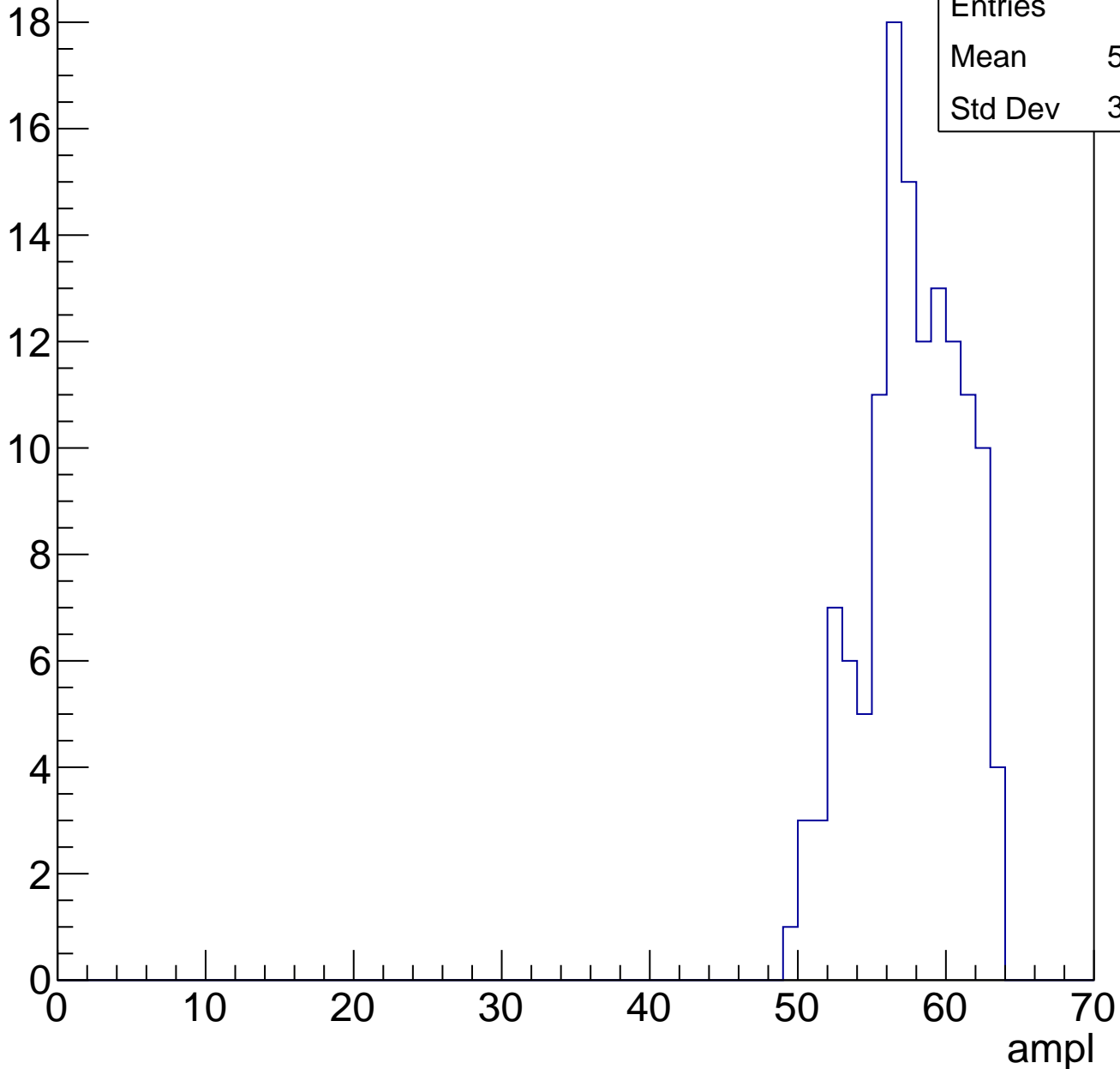


B0L101S, U22-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 57.24 |
| Std Dev | 3.306 |

Entry



B0L101S, U22-ch54, adc5

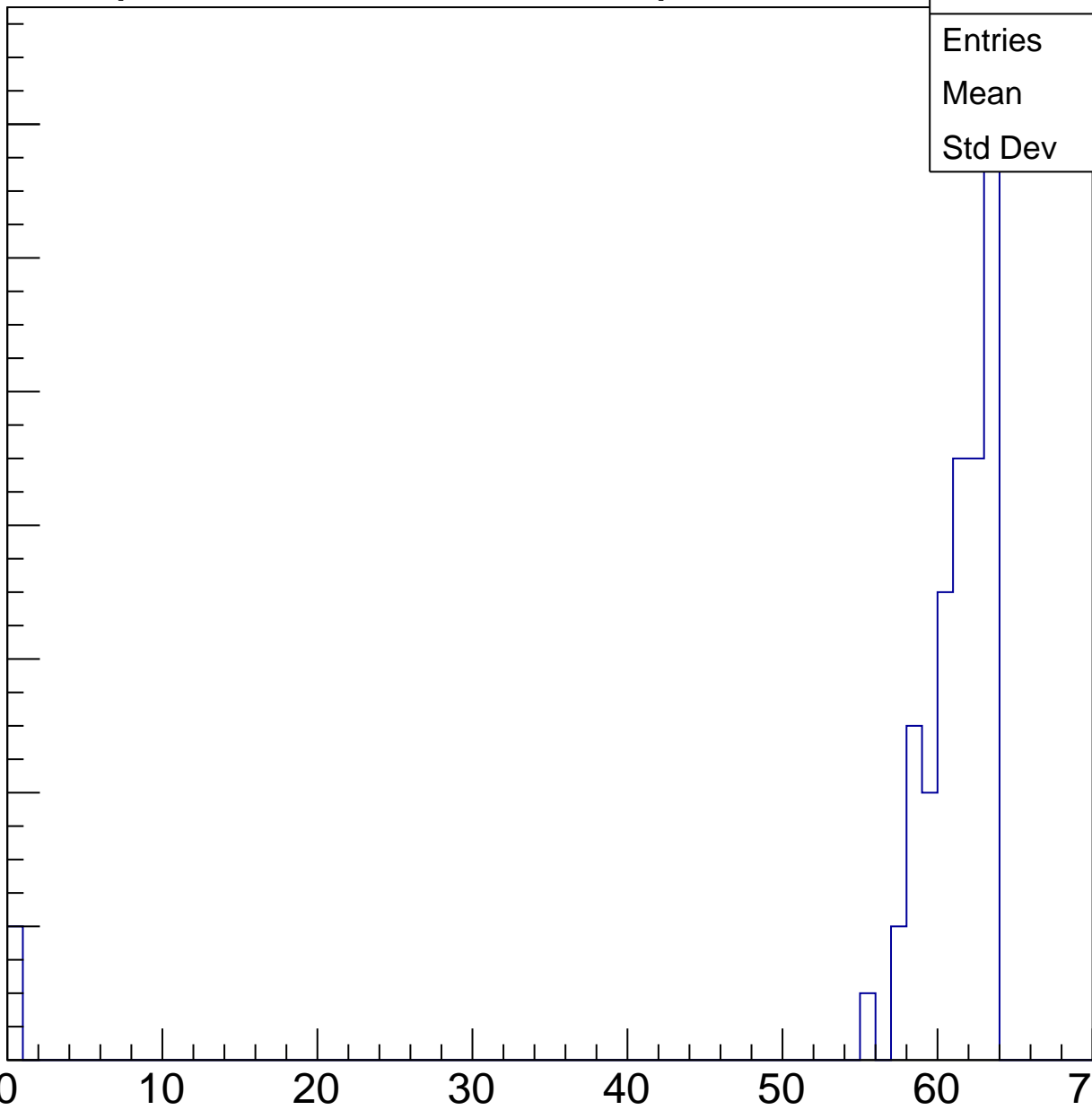
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.65 |
| Std Dev | 11.67 |

ampl



B0L101S, U22-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.667 |
| Std Dev | 10.84 |

B0L101S, U22-ch55, adc0

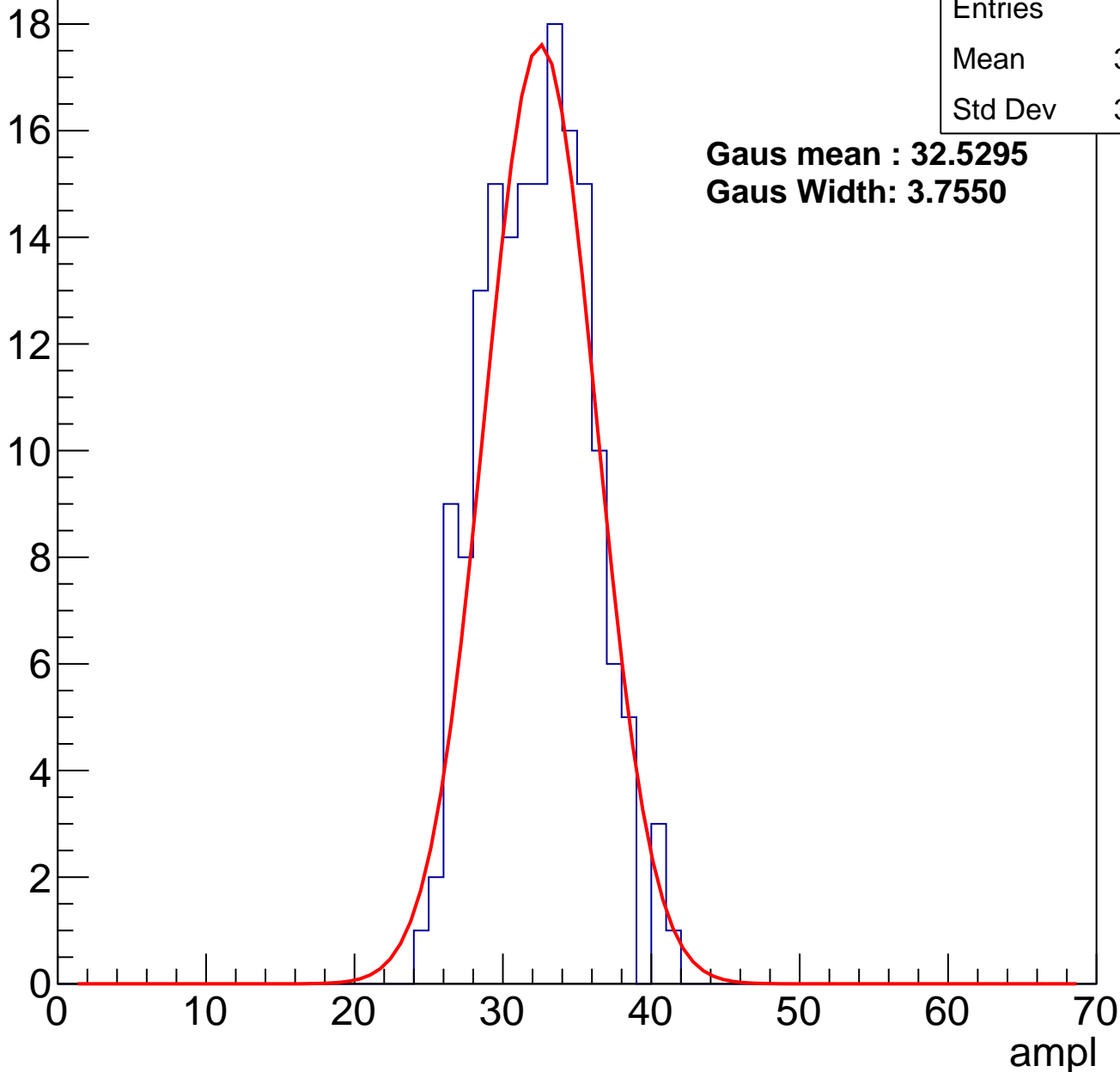
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 31.83 |
| Std Dev | 3.536 |

Gaus mean : 32.5295

Gaus Width: 3.7550

Entry

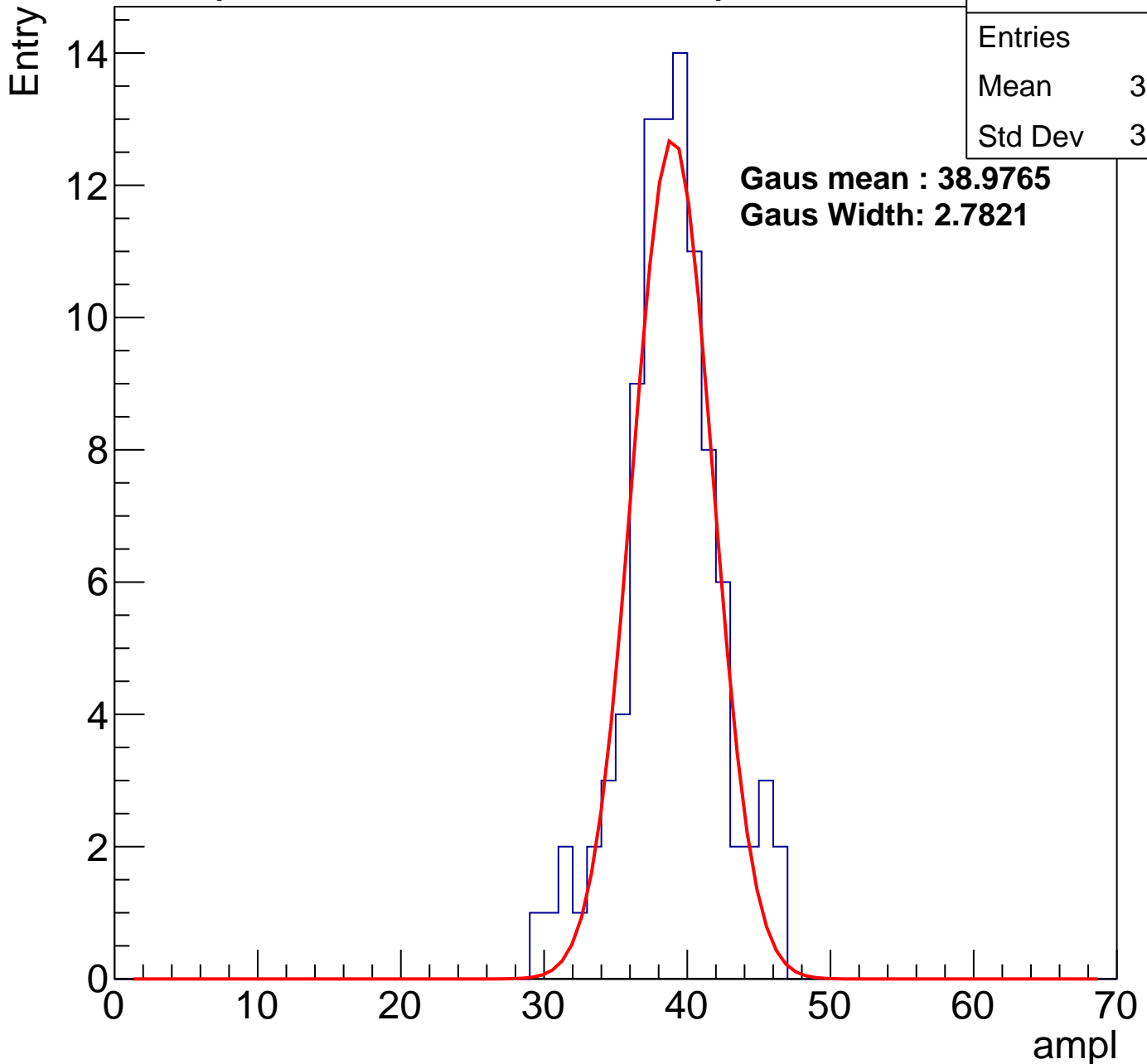


B0L101S, U22-ch55, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 97 |
| Mean | 38.42 |
| Std Dev | 3.318 |

Gaus mean : 38.9765
Gaus Width: 2.7821



B0L101S, U22-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 125 |
| Mean | 44.11 |
| Std Dev | 3.306 |

Gaus mean : 44.3902

Gaus Width: 3.1186

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

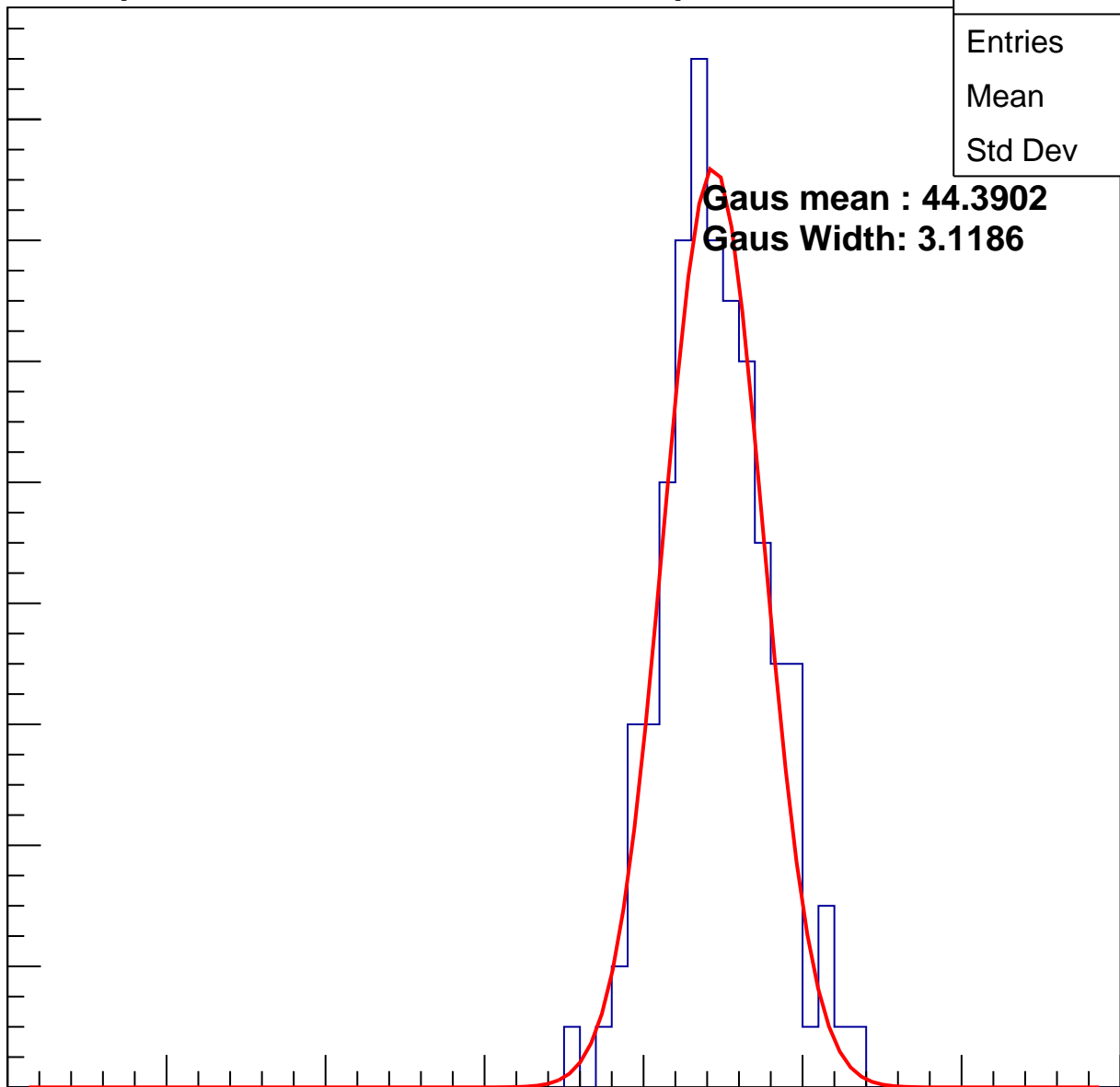
40

50

60

70

ampl

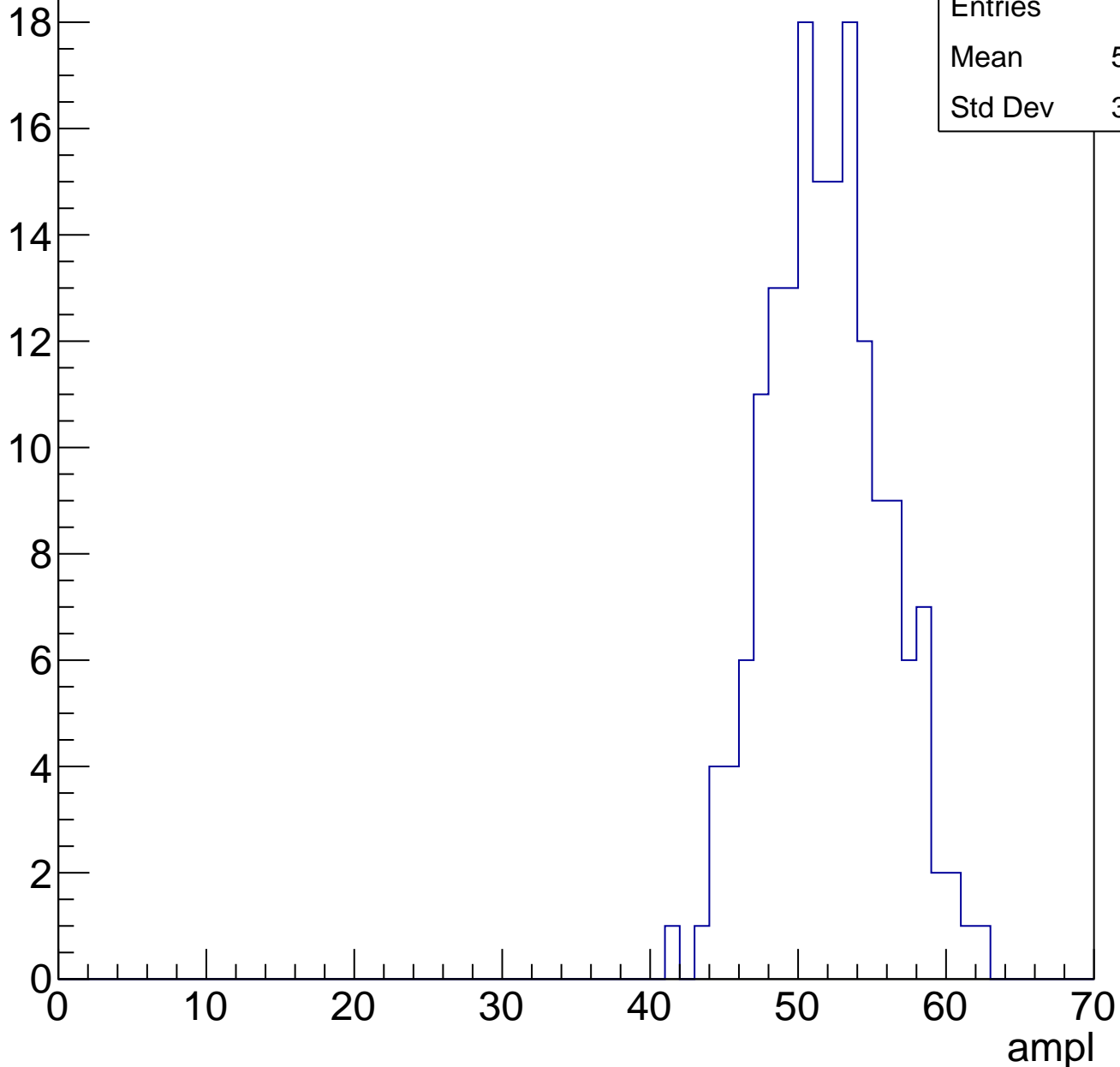


B0L101S, U22-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 168 |
| Mean | 51.48 |
| Std Dev | 3.943 |

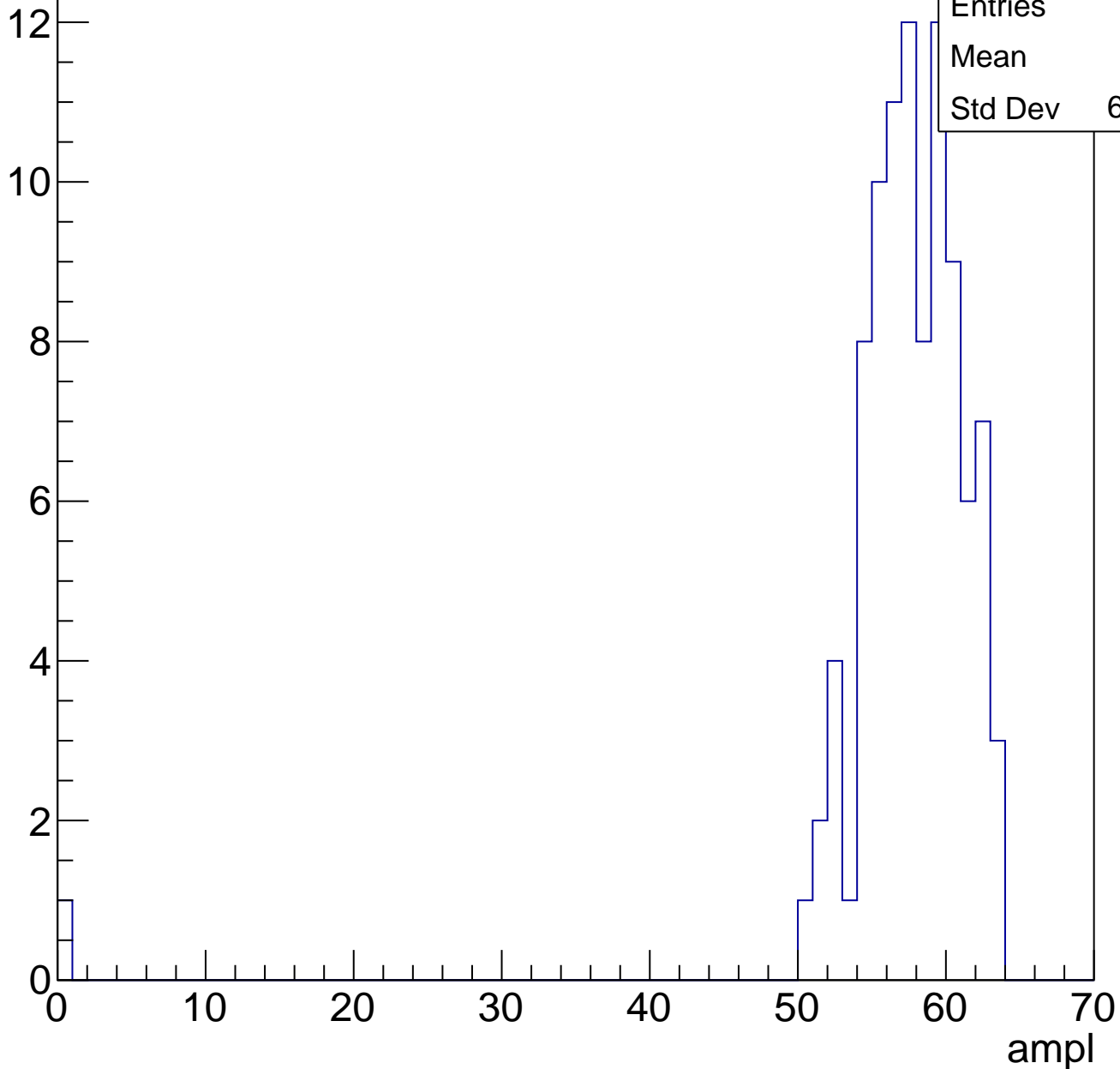
Entry



B0L101S, U22-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch55, adc5

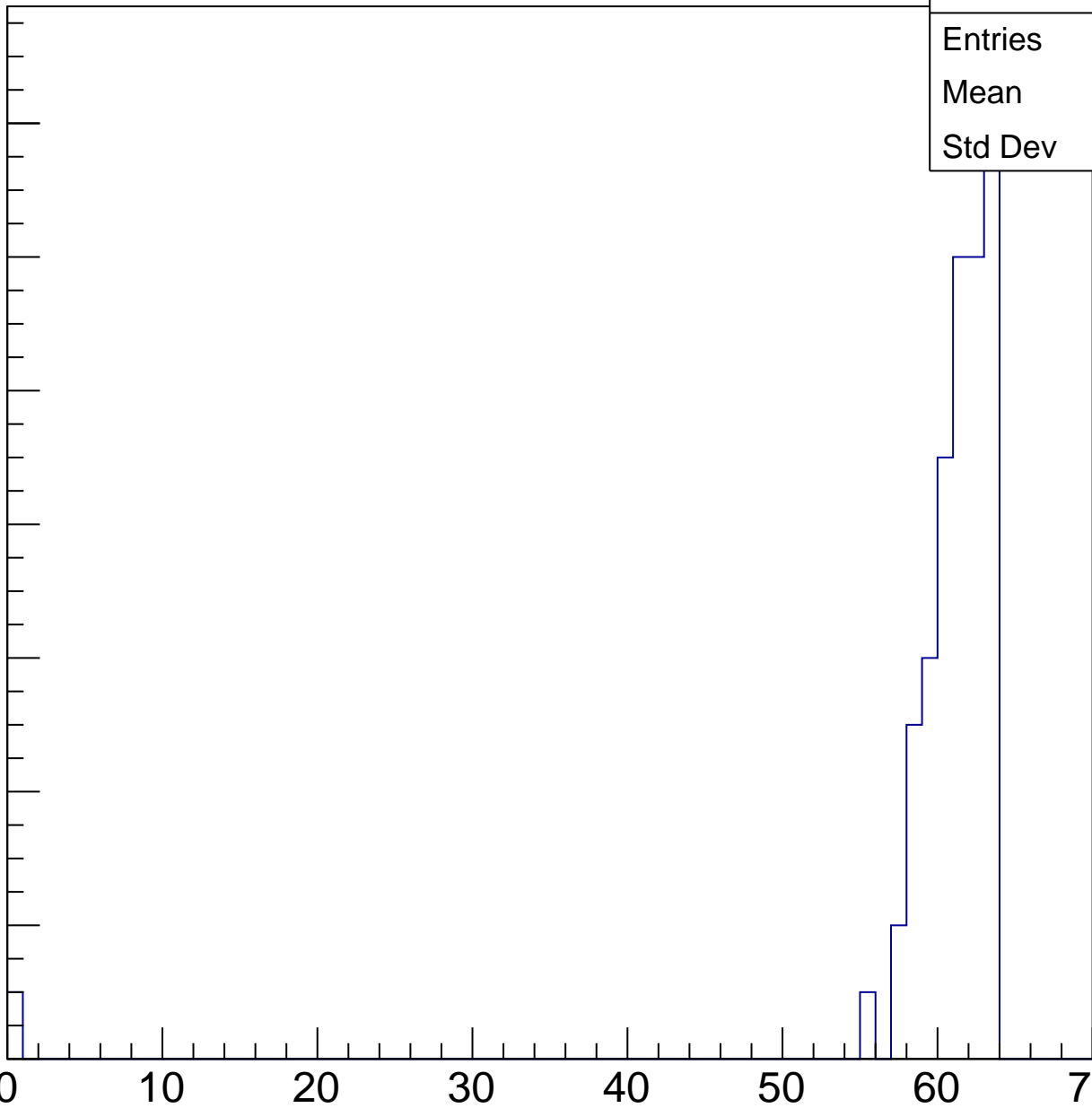
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 63 |
| Mean | 59.9 |
| Std Dev | 7.831 |

ampl



B0L101S, U22-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch56, adc0

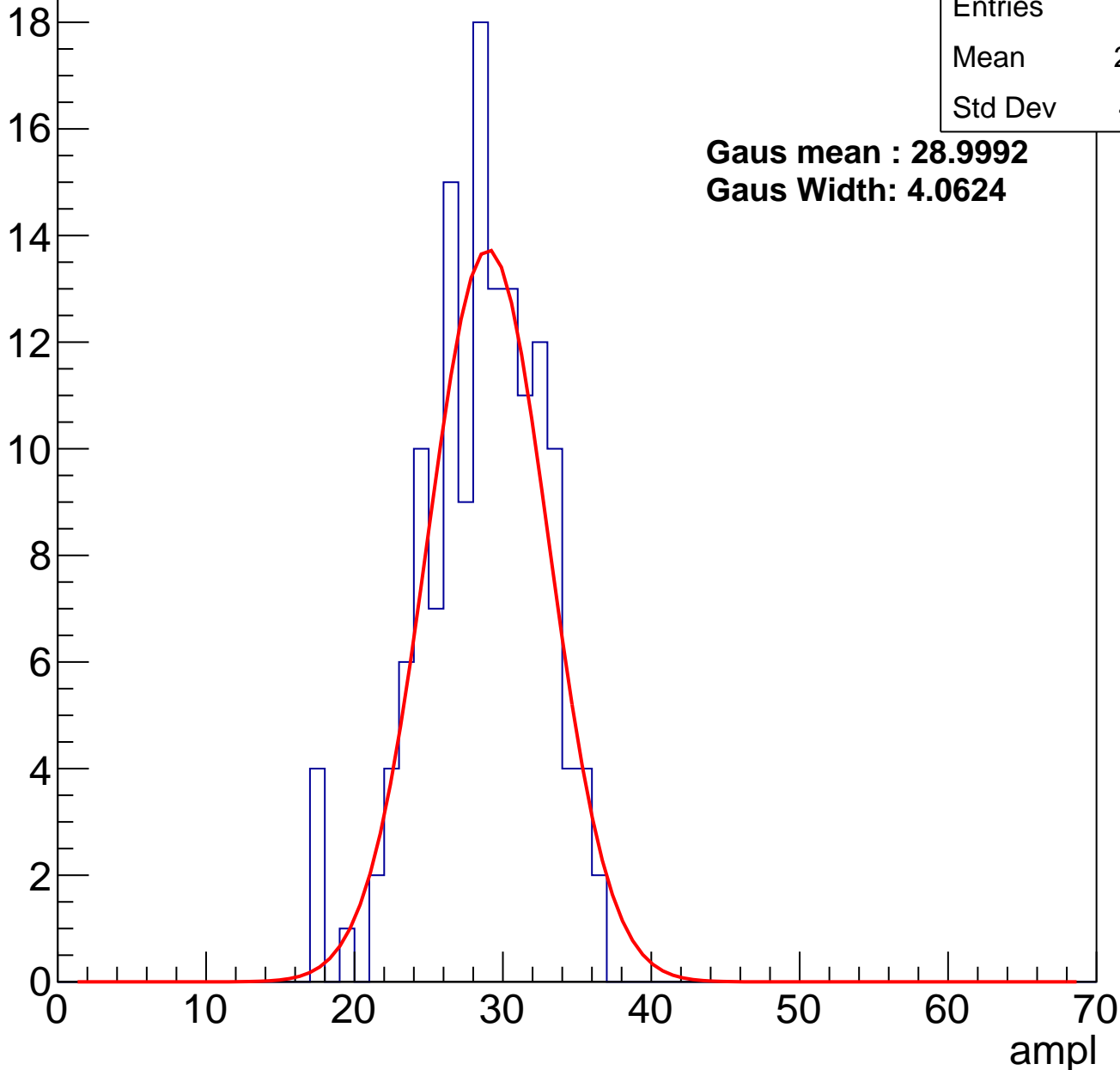
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 28.12 |
| Std Dev | 4.001 |

Gaus mean : 28.9992

Gaus Width: 4.0624

Entry



B0L101S, U22-ch56, adc1

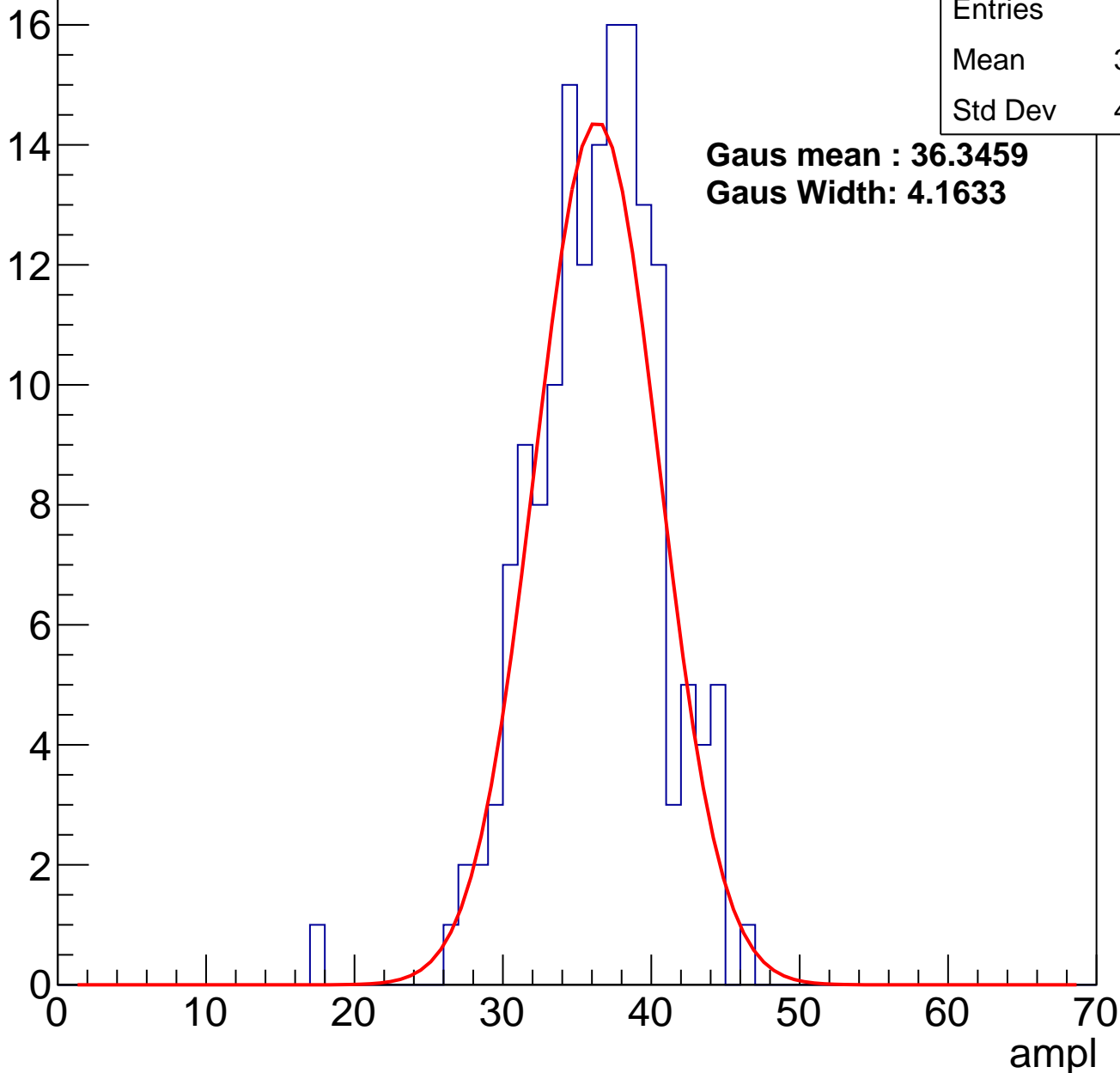
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 35.89 |
| Std Dev | 4.288 |

Gaus mean : 36.3459

Gaus Width: 4.1633

Entry



B0L101S, U22-ch56, adc2

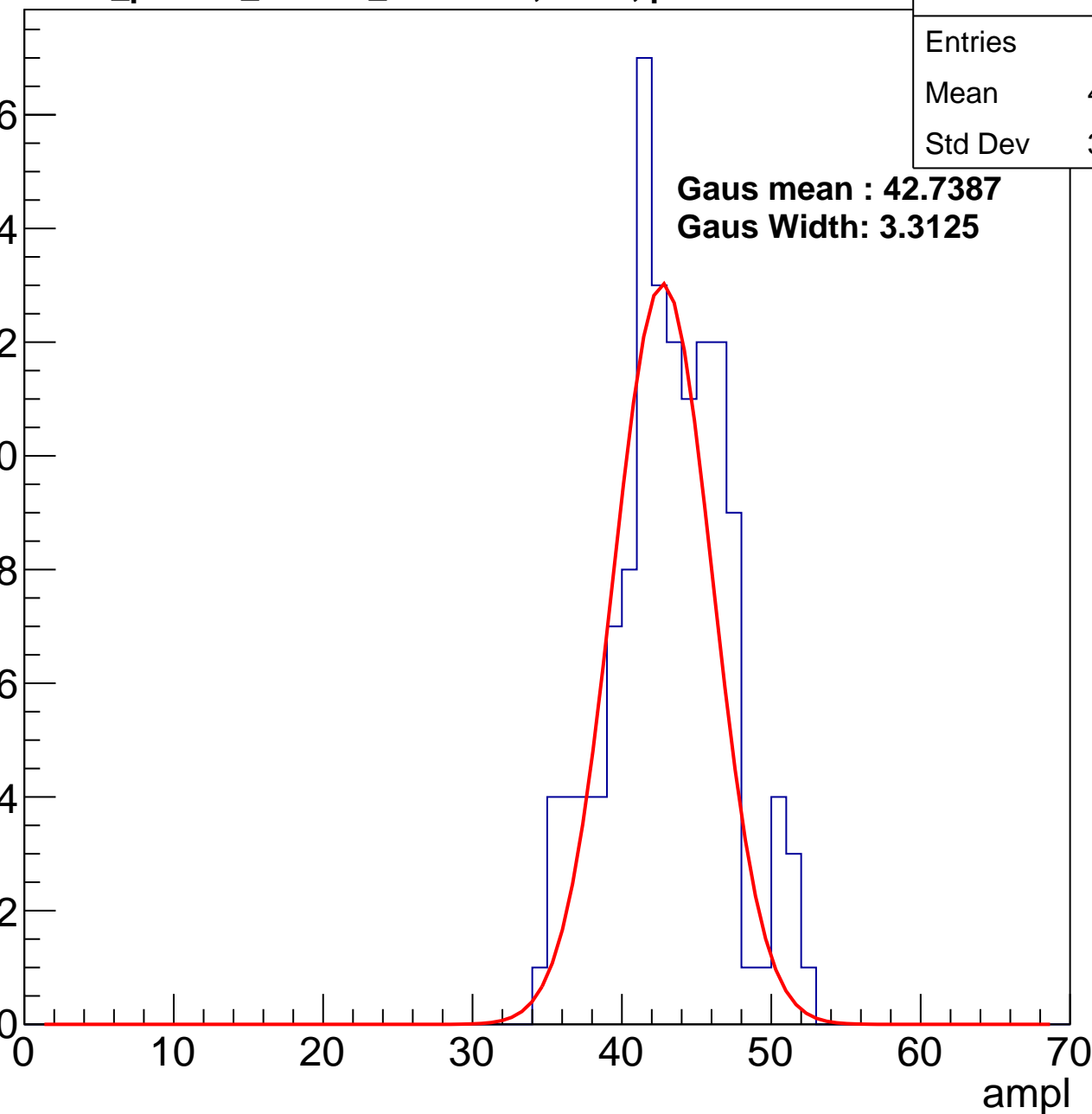
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 128 |
| Mean | 42.74 |
| Std Dev | 3.827 |

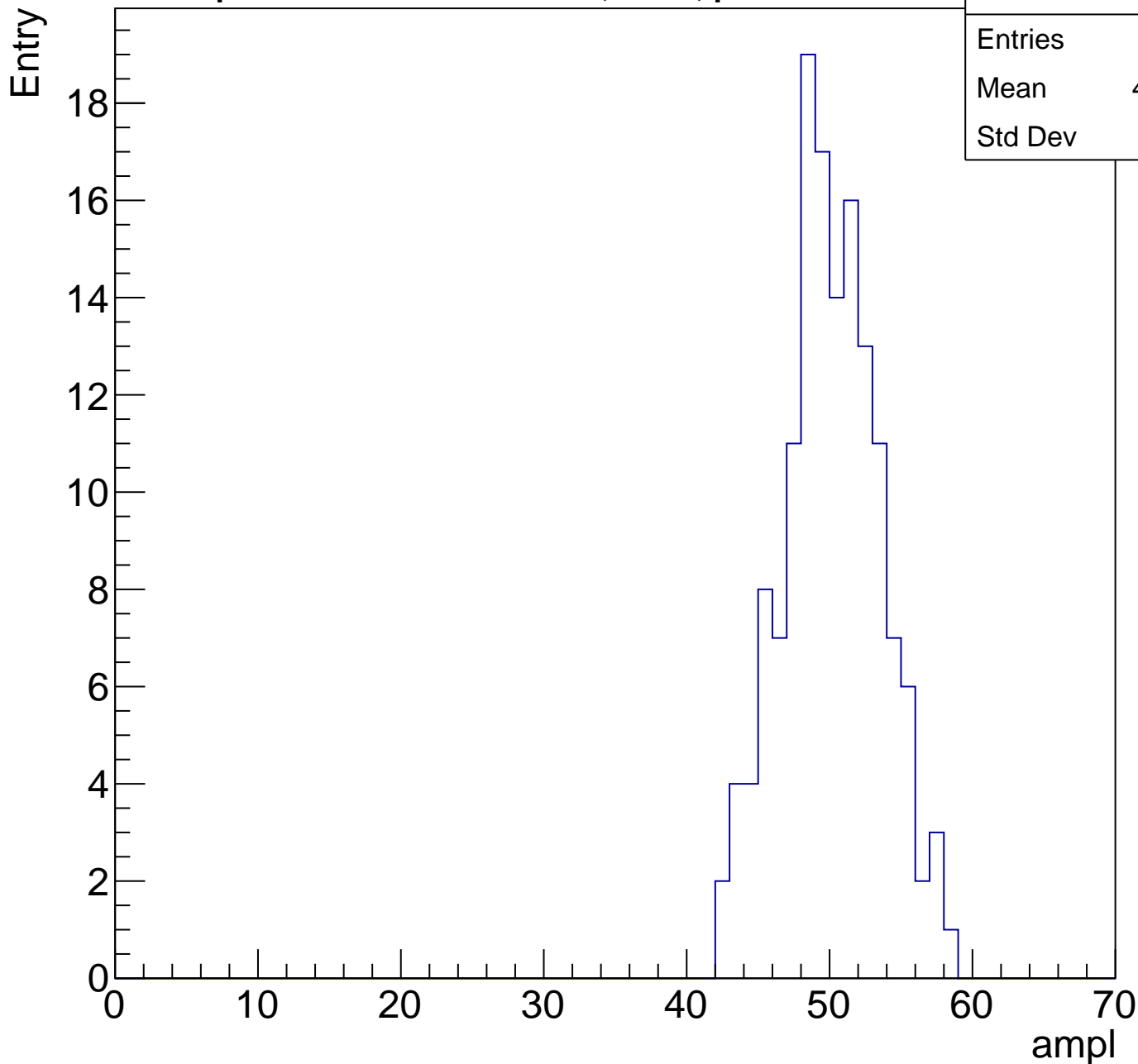
Gaus mean : 42.7387
Gaus Width: 3.3125



B0L101S, U22-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 49.66 |
| Std Dev | 3.39 |



B0L101S, U22-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

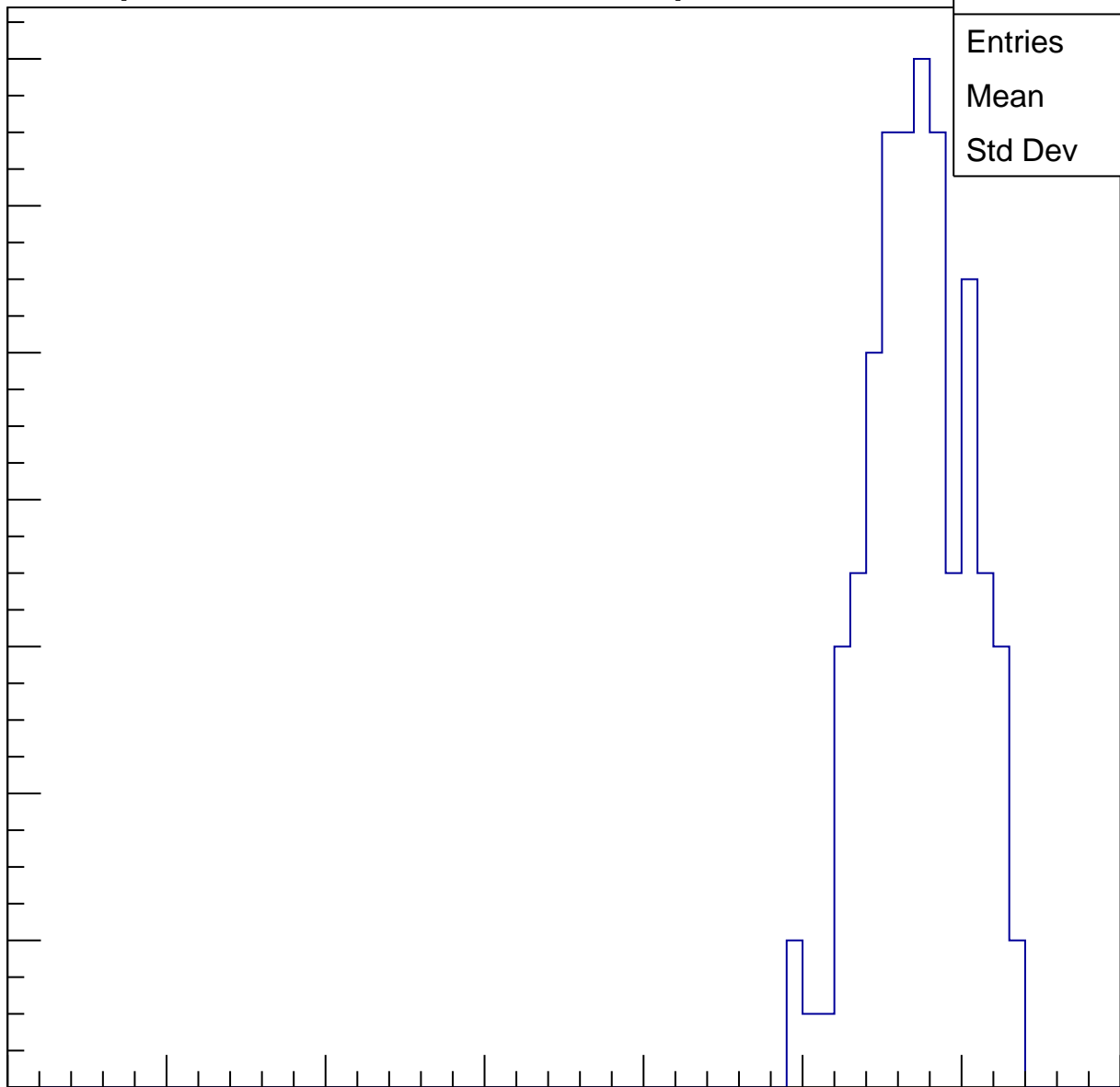
| | |
|---------|-------|
| Entries | 113 |
| Mean | 56.77 |
| Std Dev | 3.117 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch56, adc5

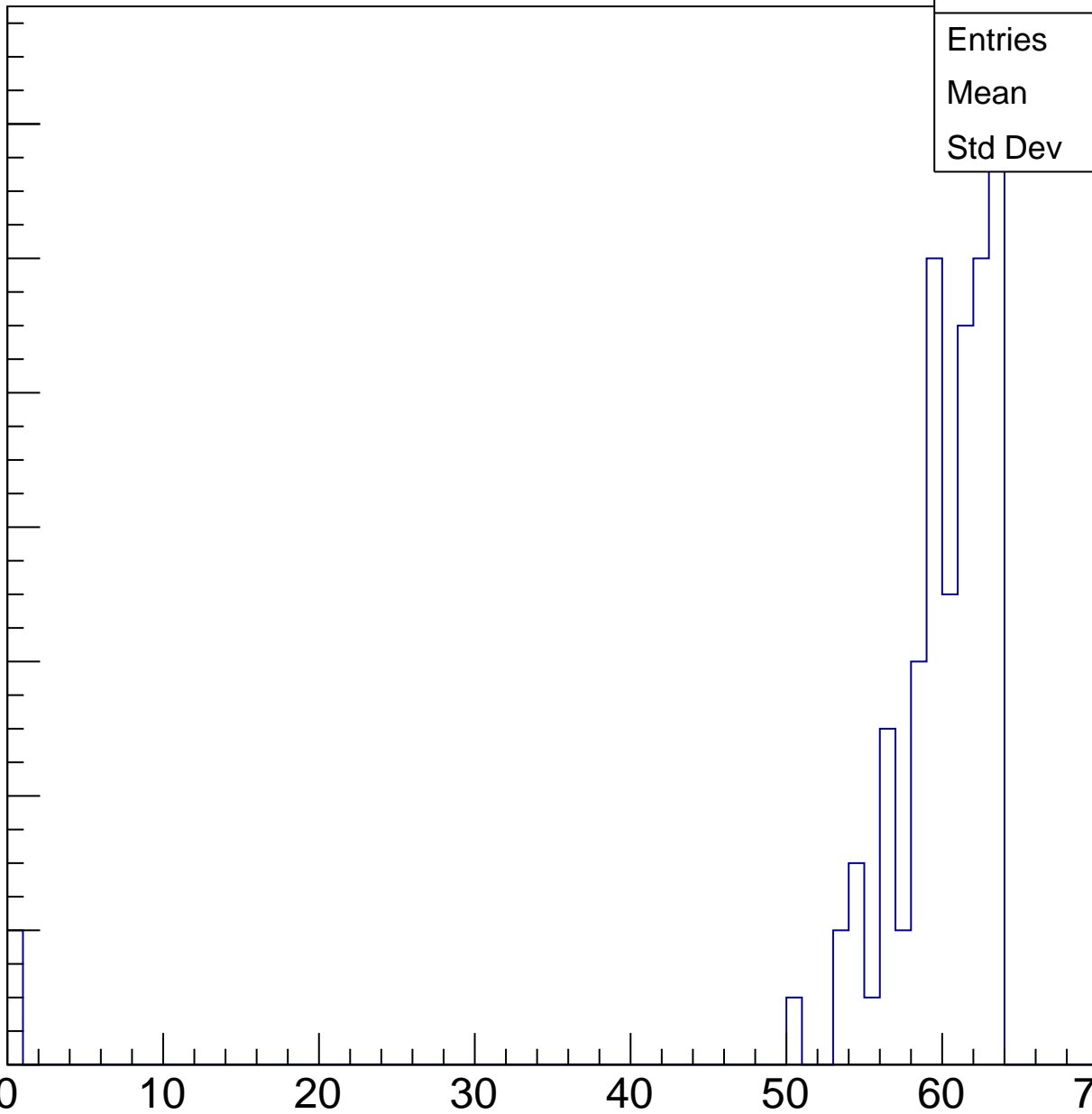
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 58.27 |
| Std Dev | 9.822 |

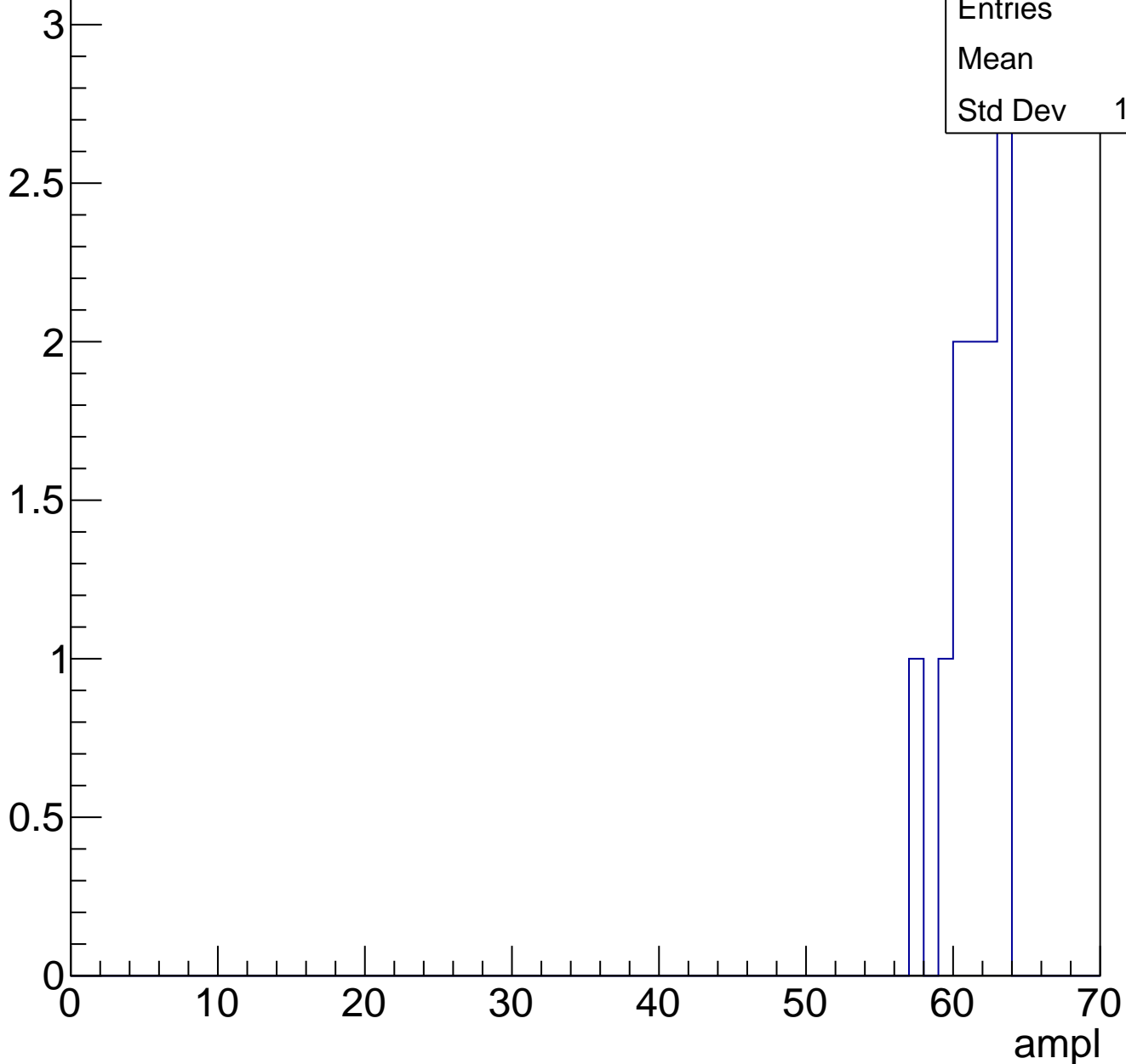
ampl



B0L101S, U22-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 30.93 |
| Std Dev | 3.789 |

Gaus mean : 31.8044

Gaus Width: 3.5446

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

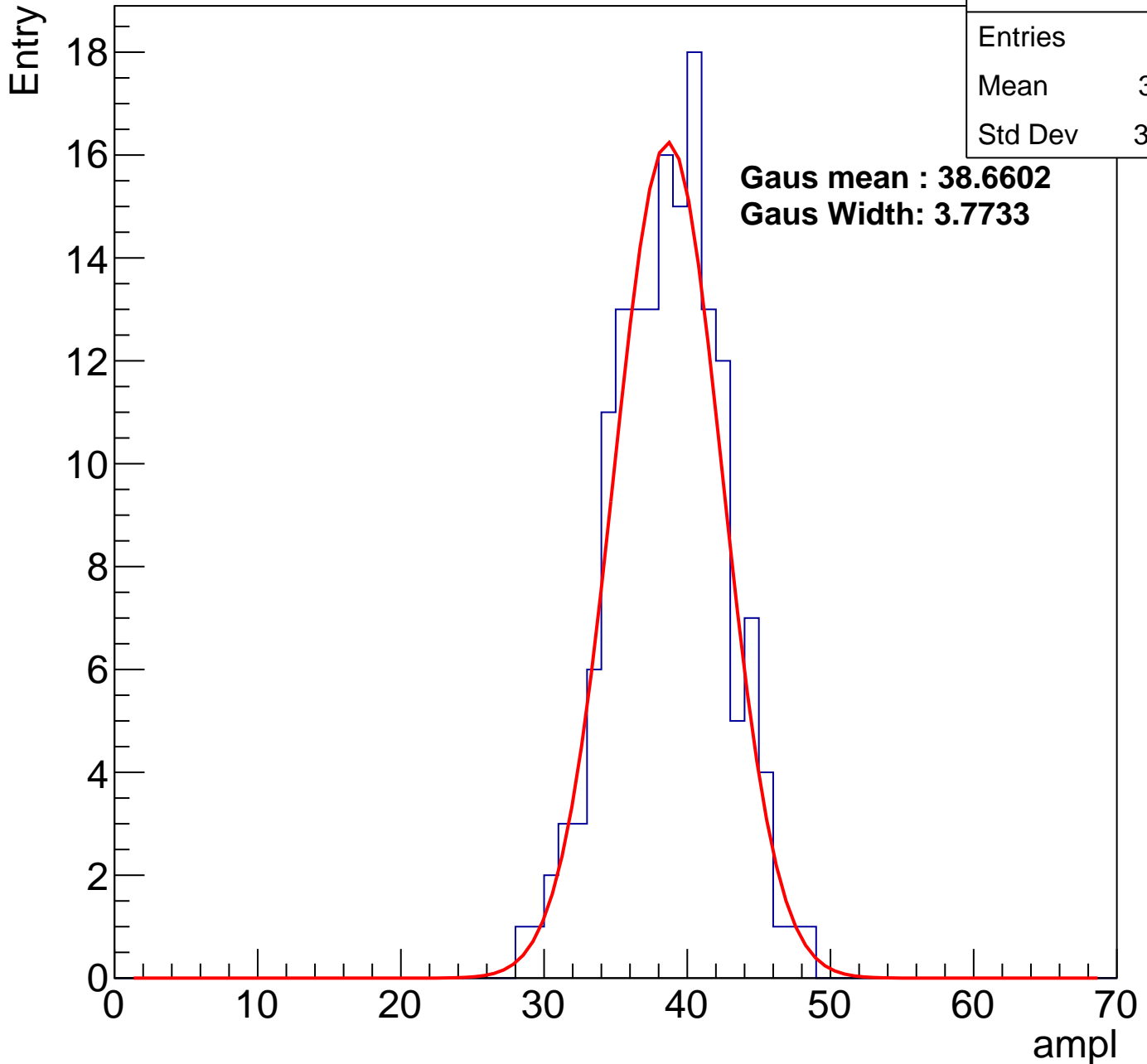
B0L101S, U22-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 38.21 |
| Std Dev | 3.786 |

Gaus mean : 38.6602

Gaus Width: 3.7733



B0L101S, U22-ch57, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 109 |
| Mean | 45.19 |
| Std Dev | 3.181 |

Gaus mean : 45.5304

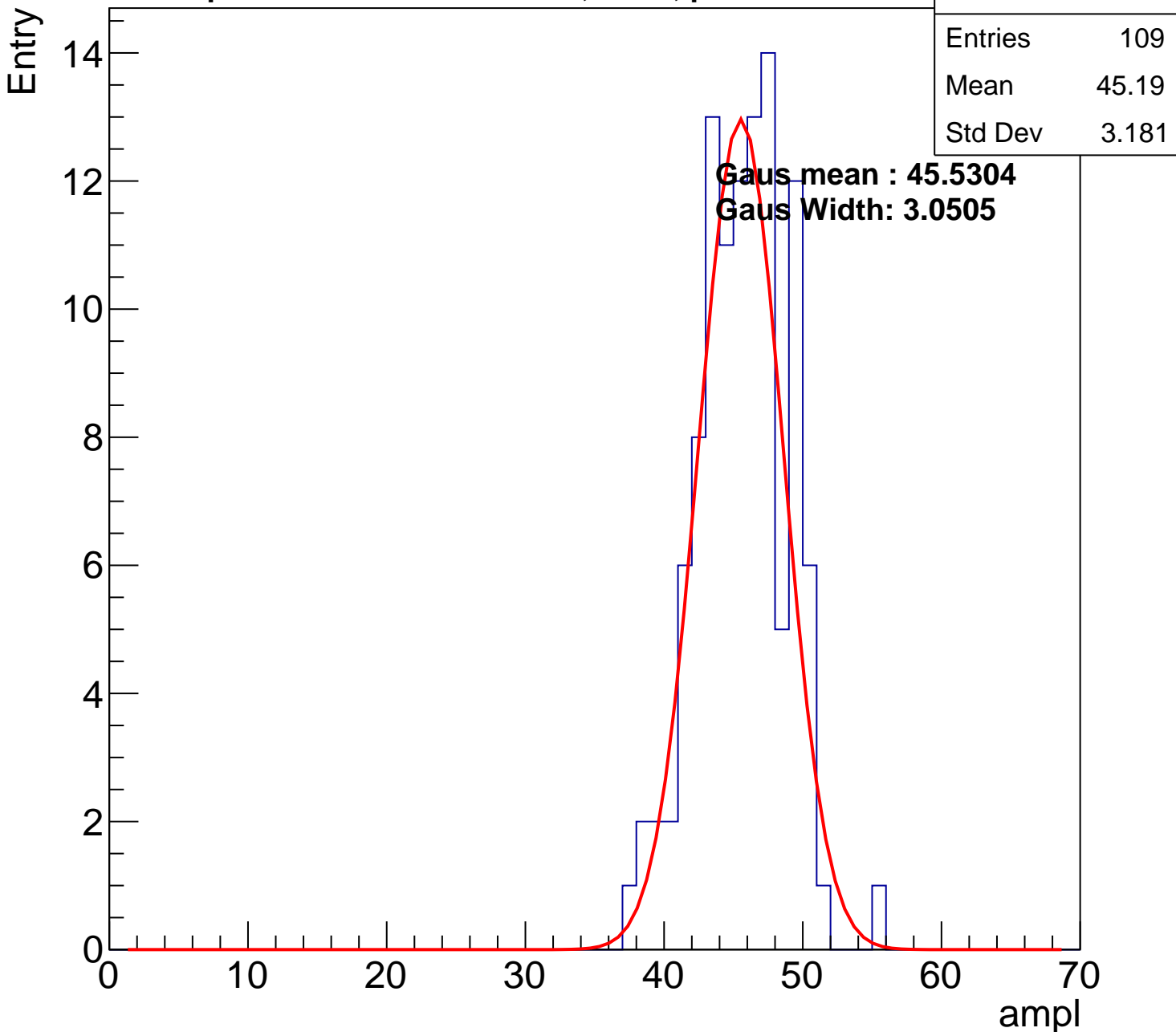
Gaus Width: 3.0505

Entry

14
12
10
8
6
4
2
0

ampl

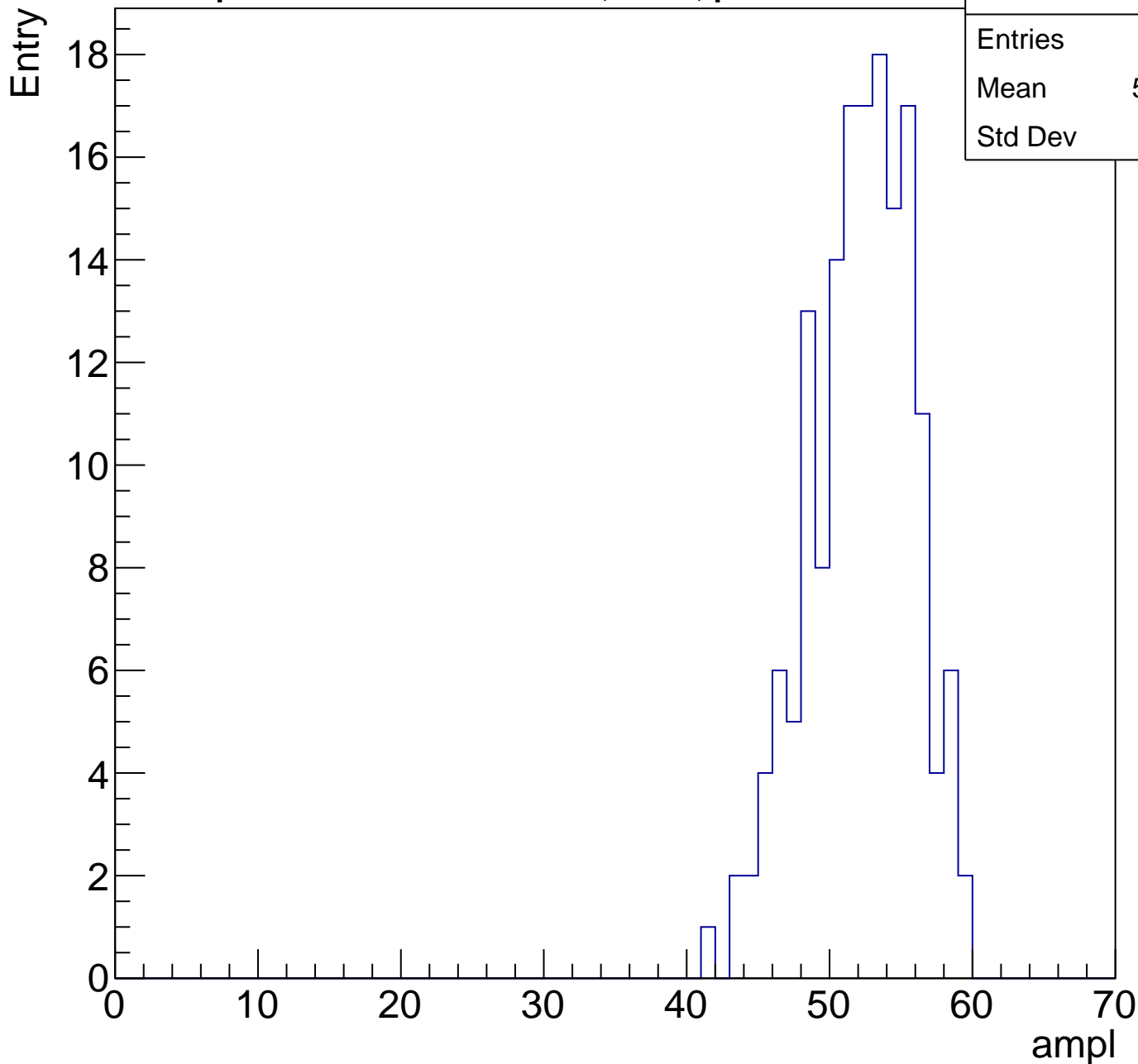
0 10 20 30 40 50 60 70



B0L101S, U22-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 162 |
| Mean | 51.74 |
| Std Dev | 3.61 |



B0L101S, U22-ch57, adc4

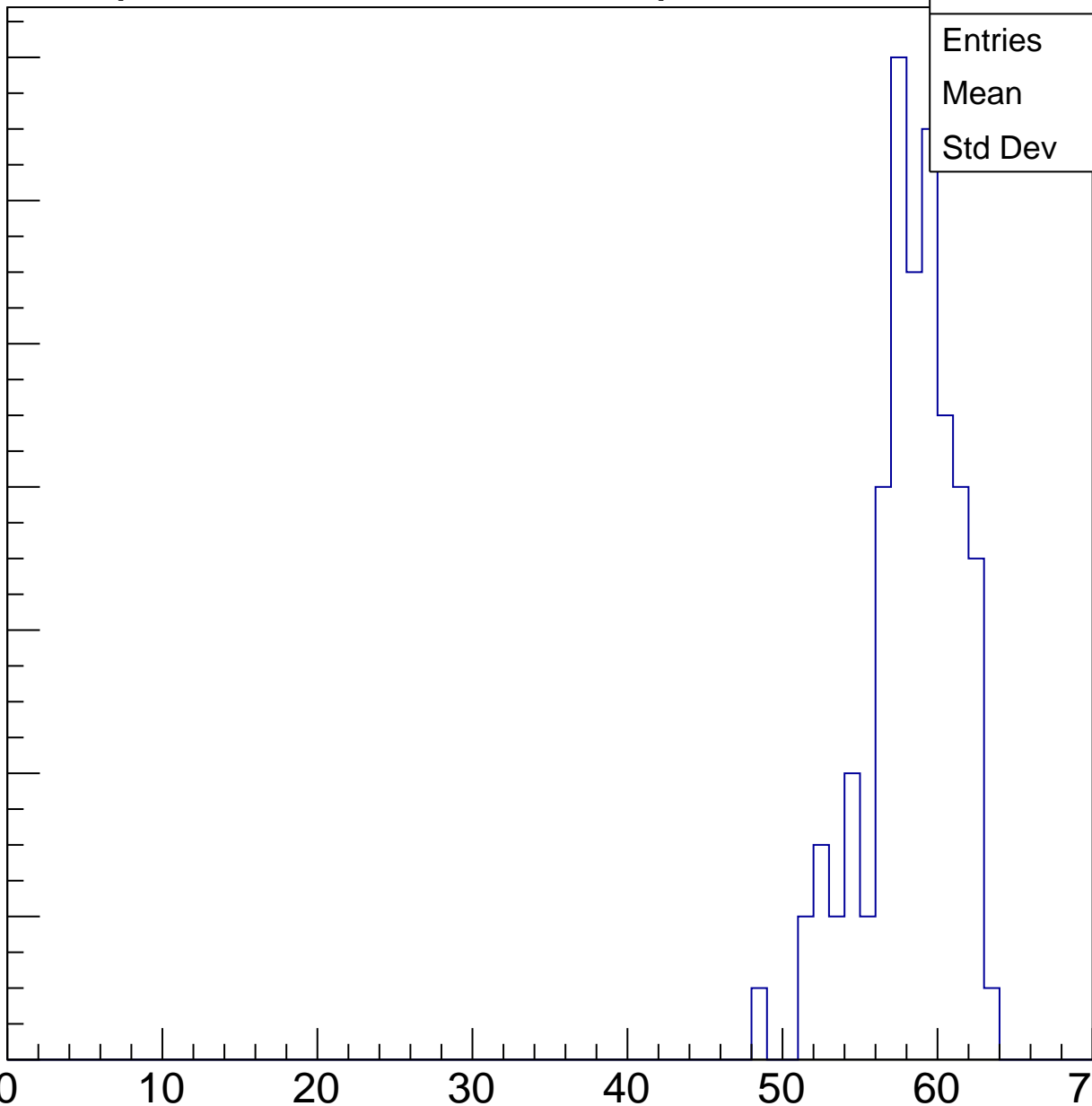
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 57.81 |
| Std Dev | 2.949 |

ampl



B0L101S, U22-ch57, adc5

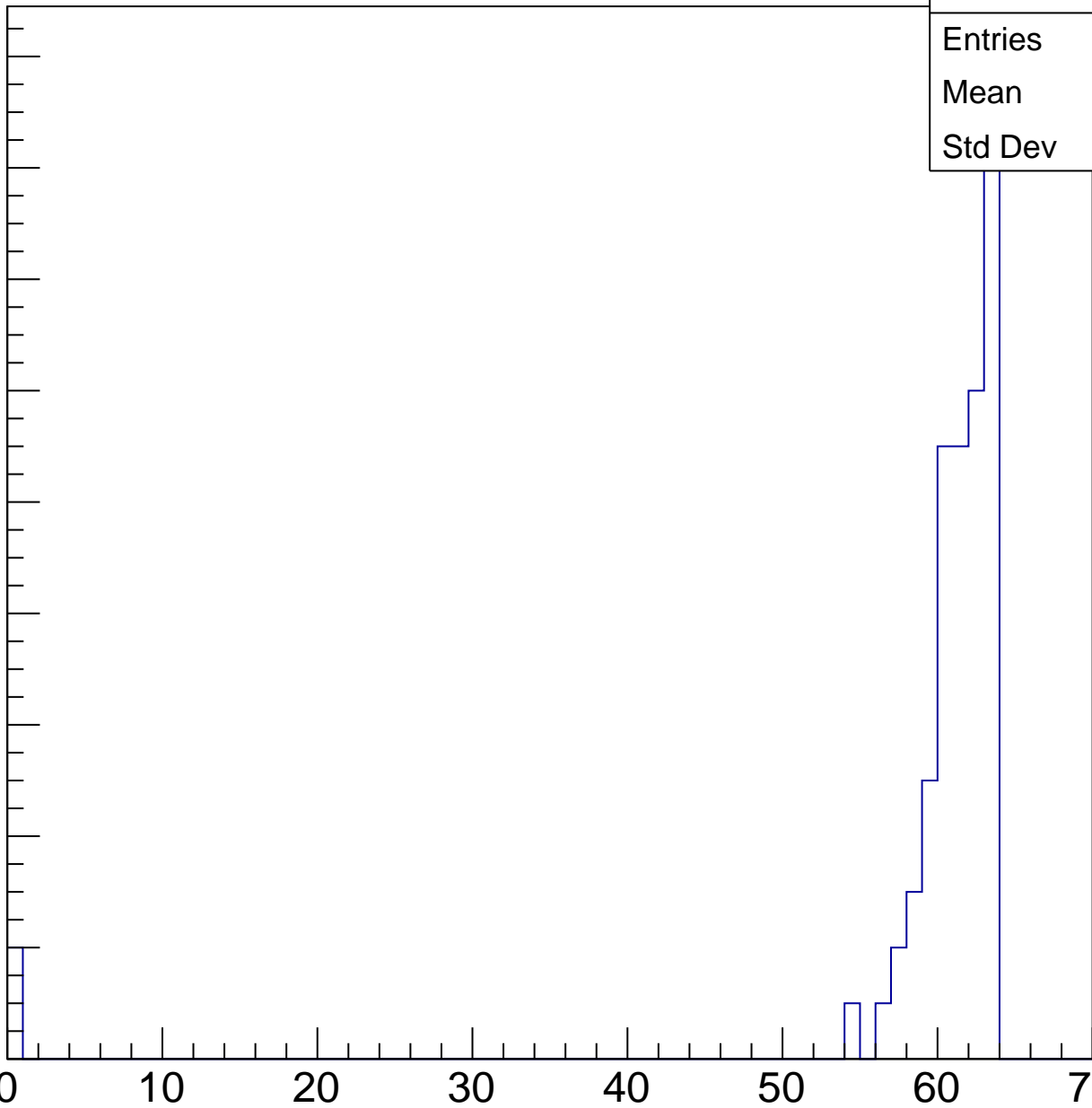
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 66 |
| Mean | 59.12 |
| Std Dev | 10.63 |

ampl



B0L101S, U22-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch58, adc0

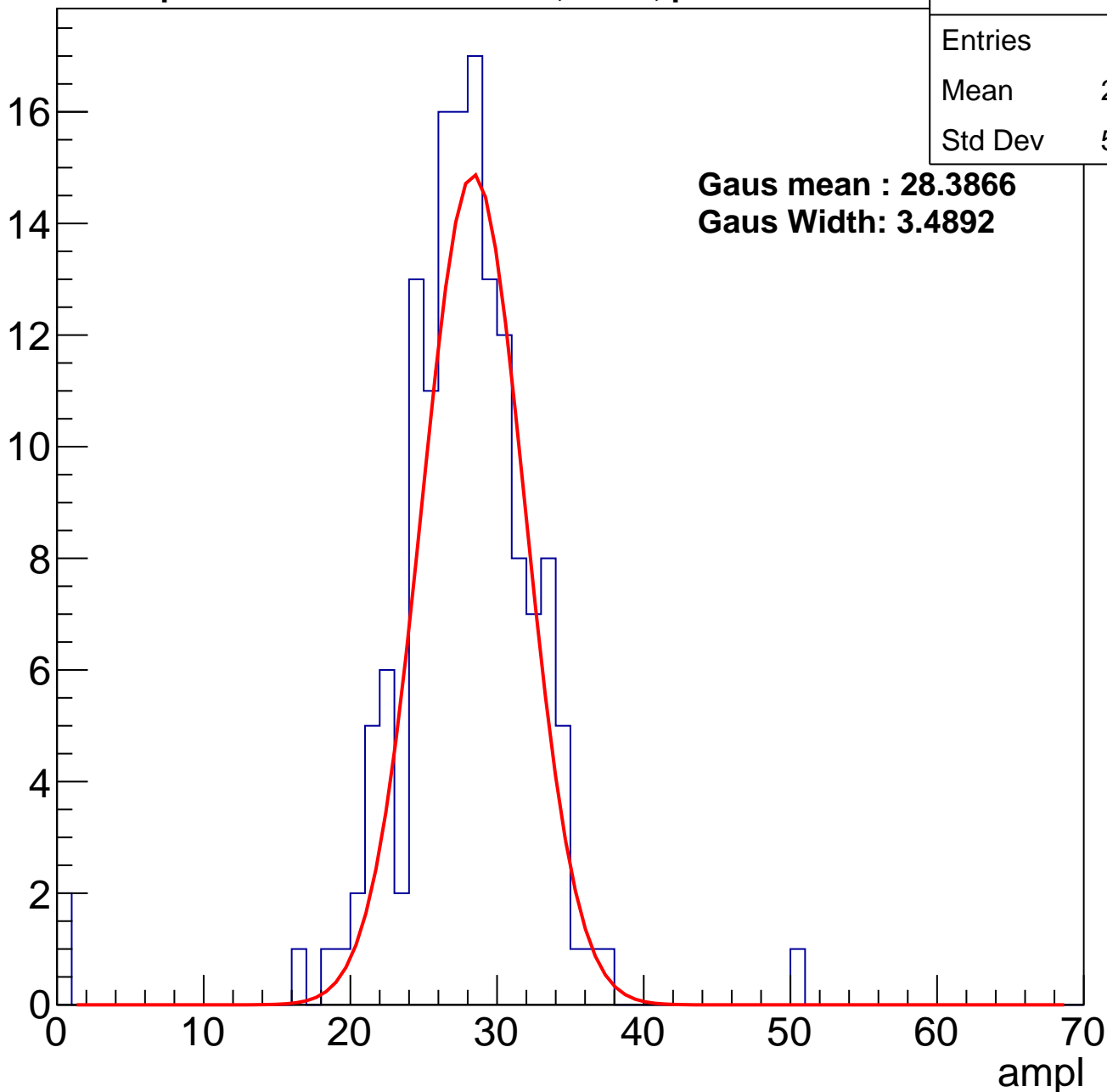
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 27.25 |
| Std Dev | 5.253 |

Gaus mean : 28.3866

Gaus Width: 3.4892

Entry



B0L101S, U22-ch58, adc1

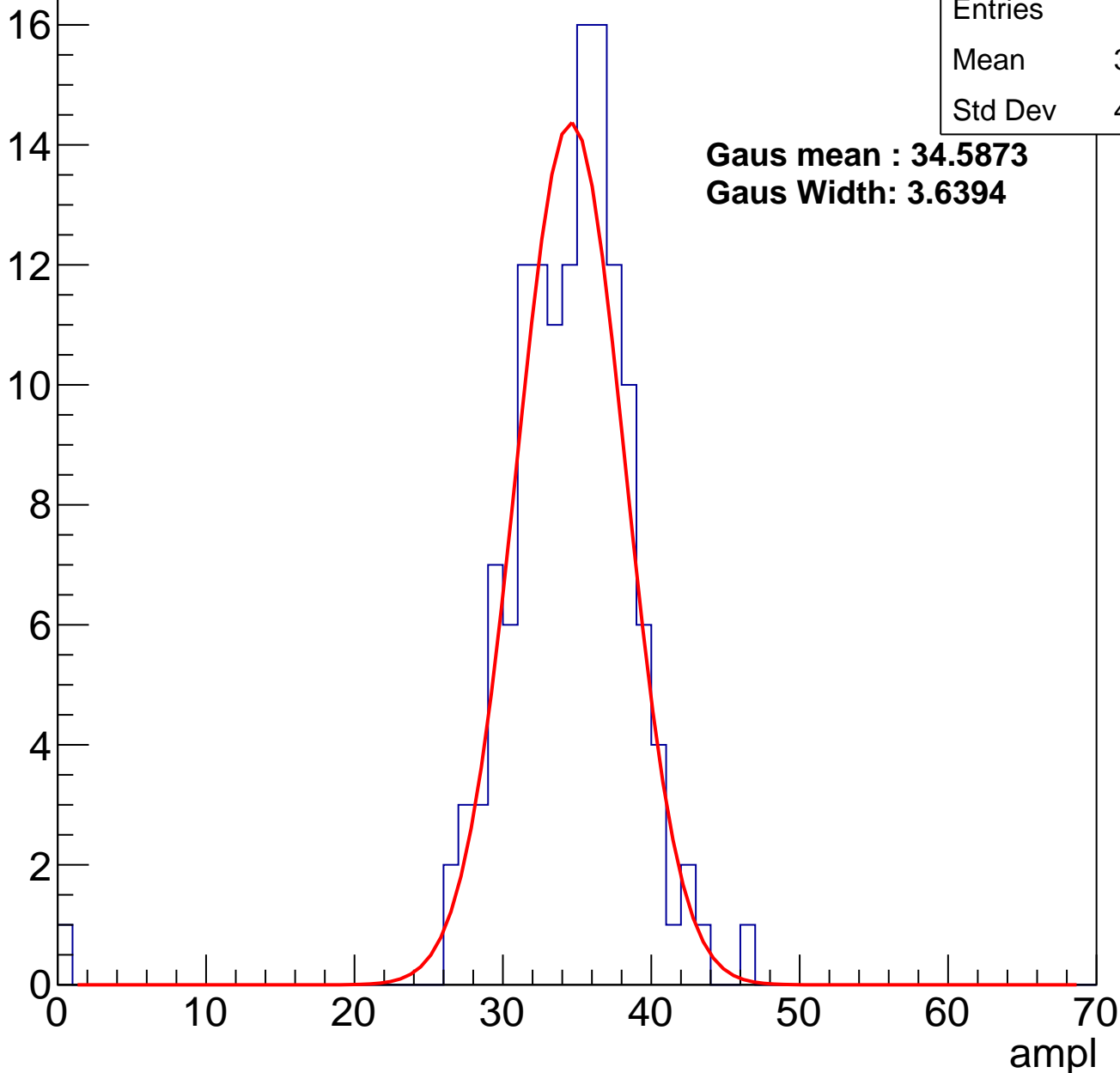
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 34.02 |
| Std Dev | 4.679 |

Gaus mean : 34.5873

Gaus Width: 3.6394

Entry



B0L101S, U22-ch58, adc2

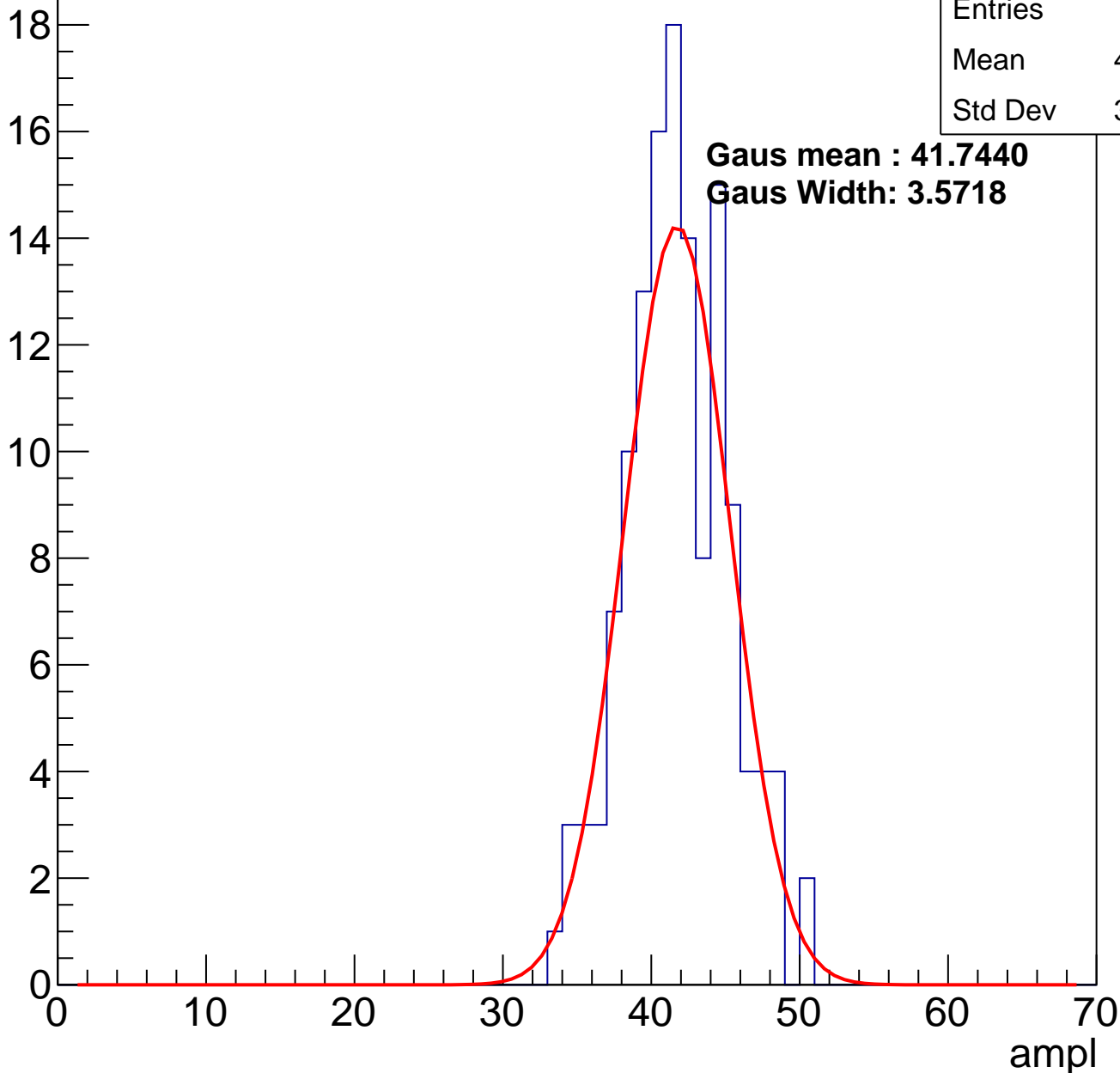
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 41.29 |
| Std Dev | 3.433 |

Gaus mean : 41.7440

Gaus Width: 3.5718

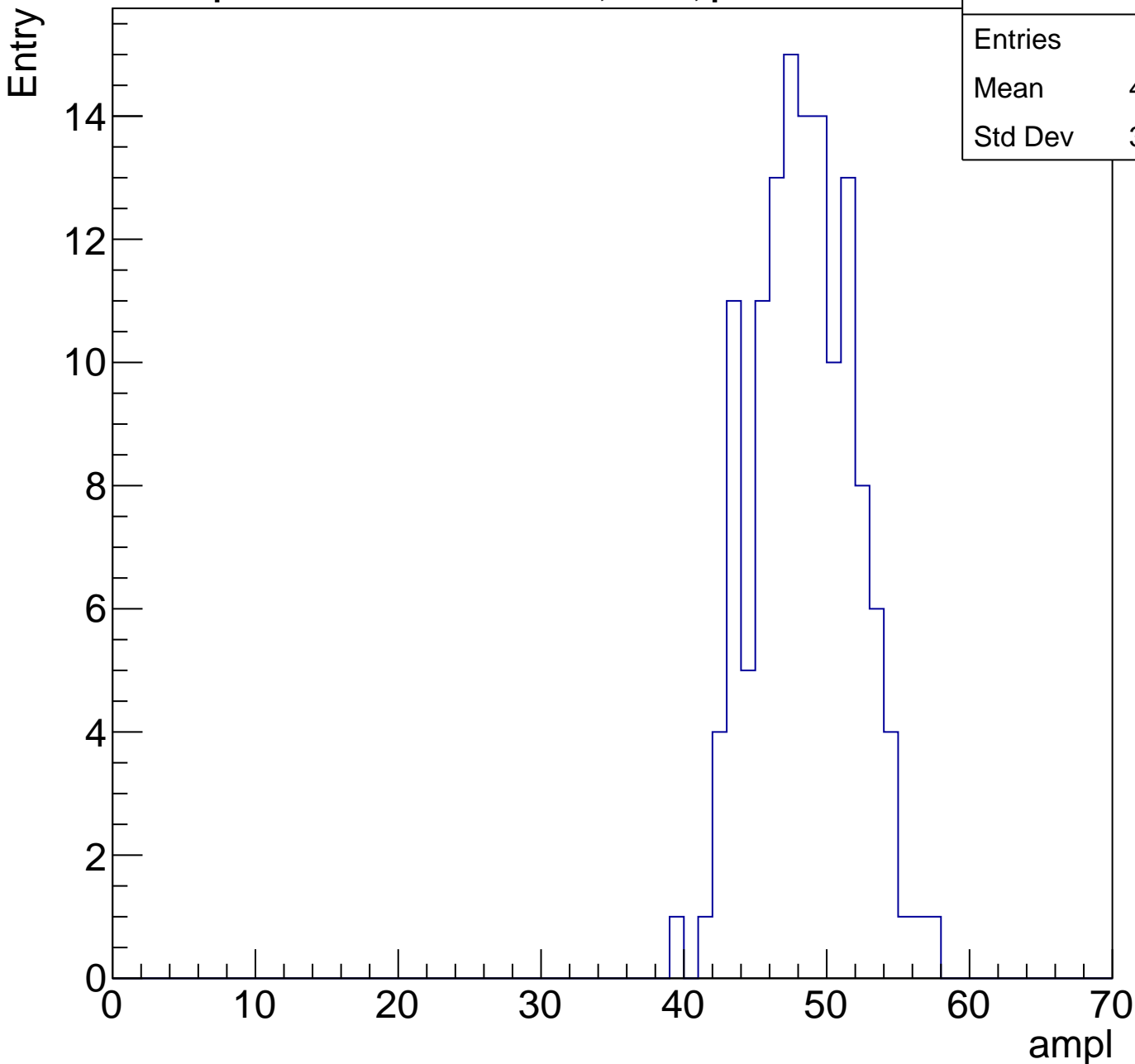
Entry



B0L101S, U22-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 47.95 |
| Std Dev | 3.449 |



B0L101S, U22-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

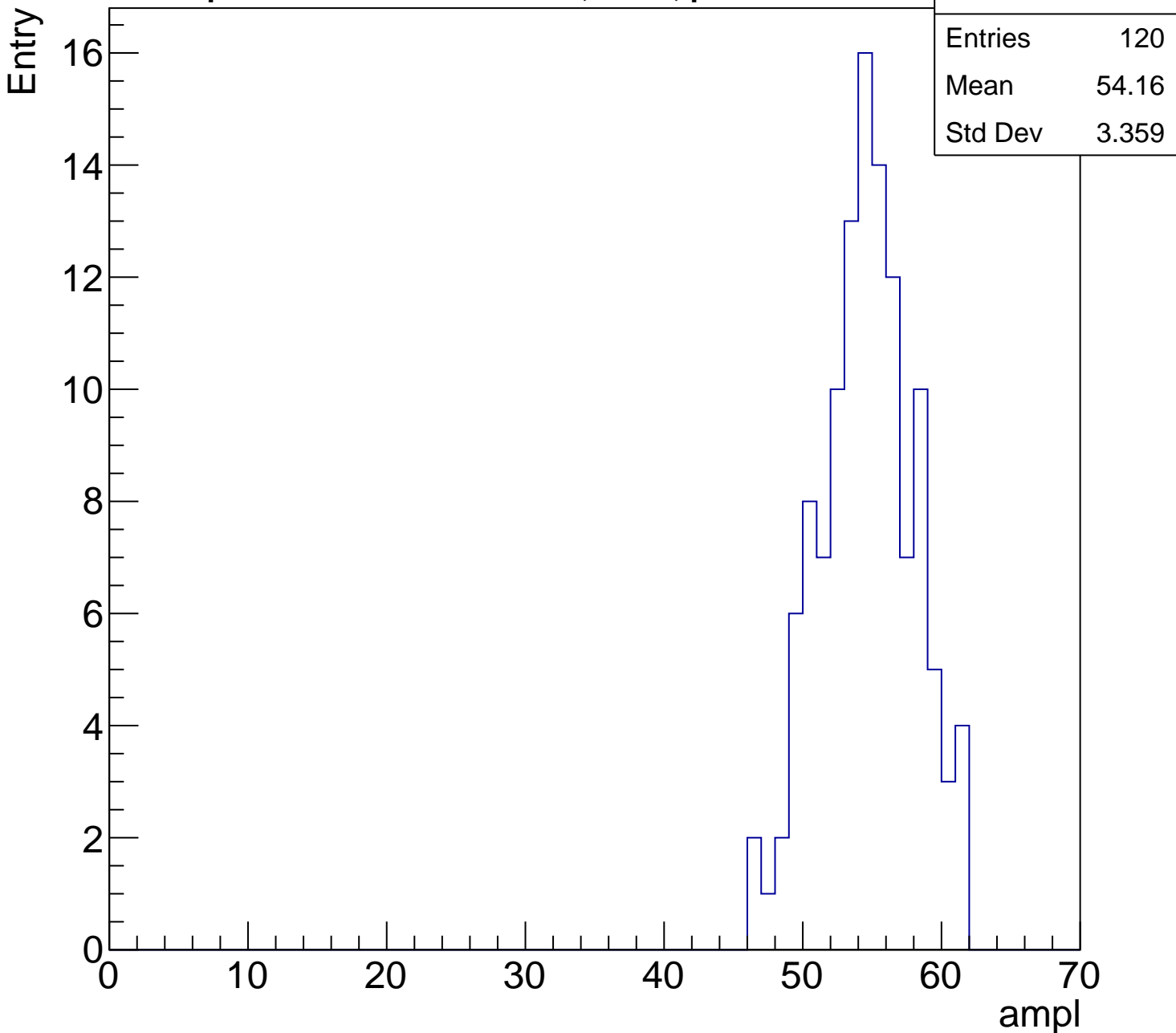
| | |
|---------|-------|
| Entries | 120 |
| Mean | 54.16 |
| Std Dev | 3.359 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch58, adc5

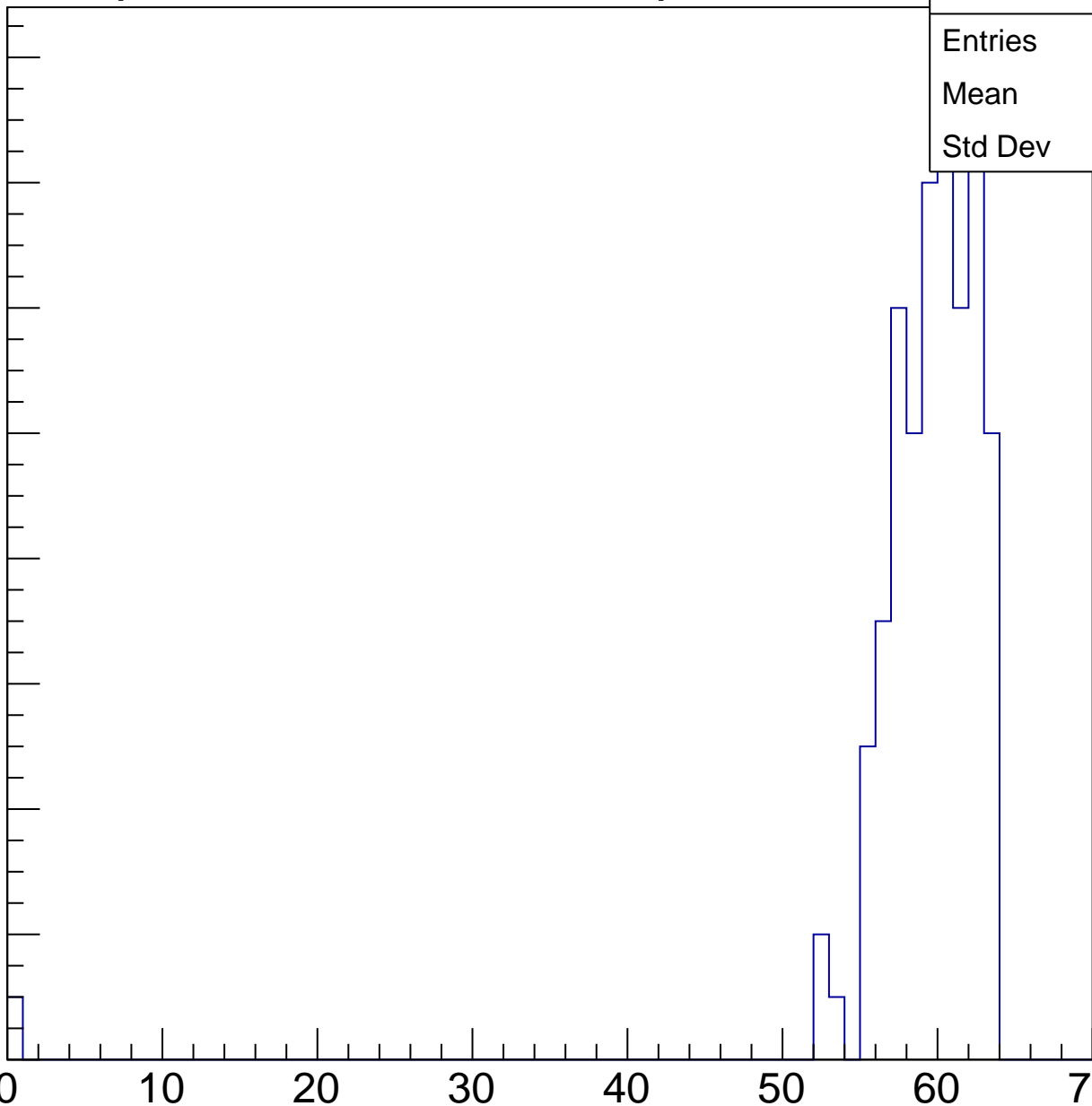
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 106 |
| Mean | 58.75 |
| Std Dev | 6.278 |

ampl

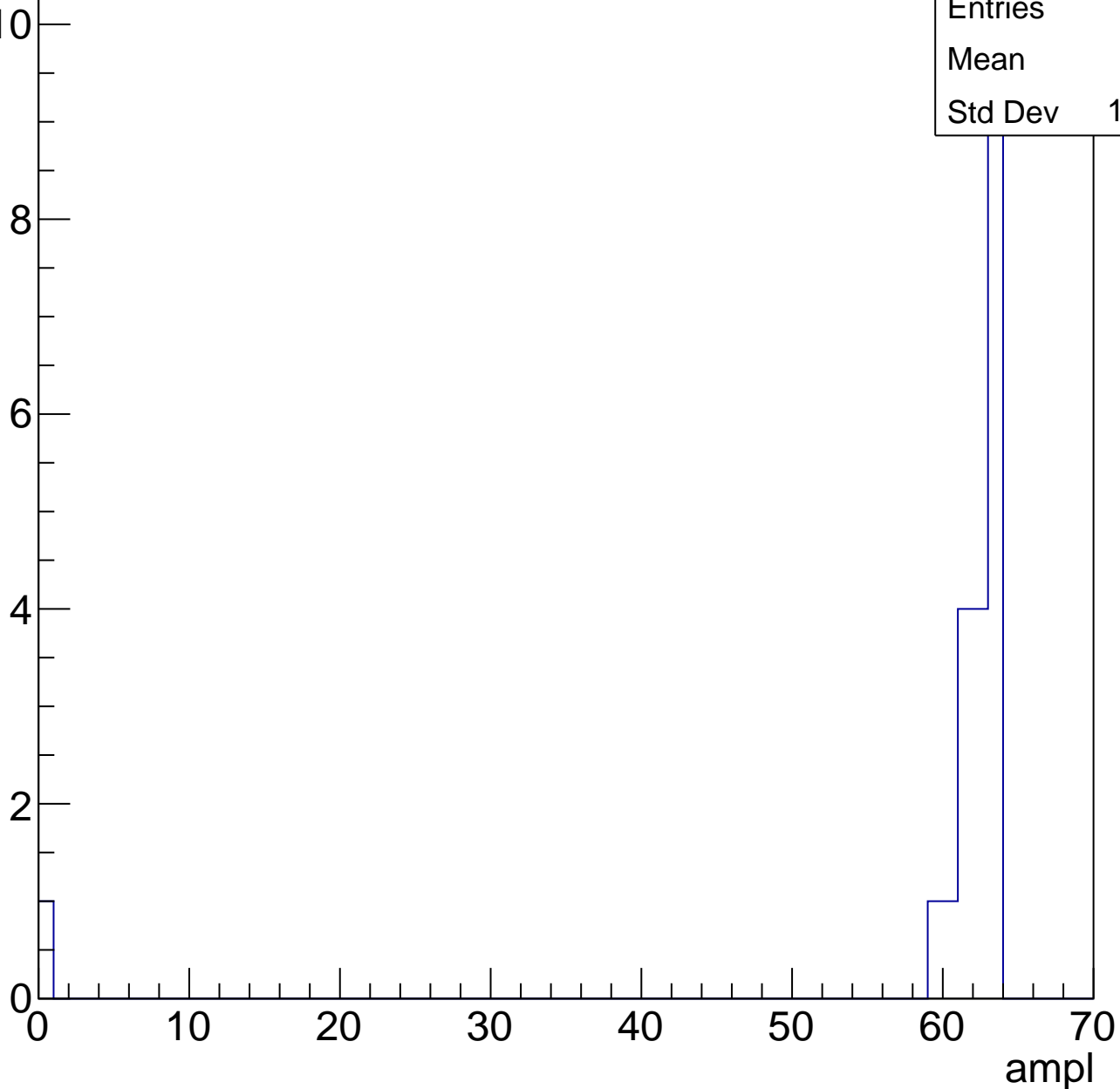


B0L101S, U22-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 21 |
| Mean | 59.1 |
| Std Dev | 13.26 |



B0L101S, U22-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

146

Mean

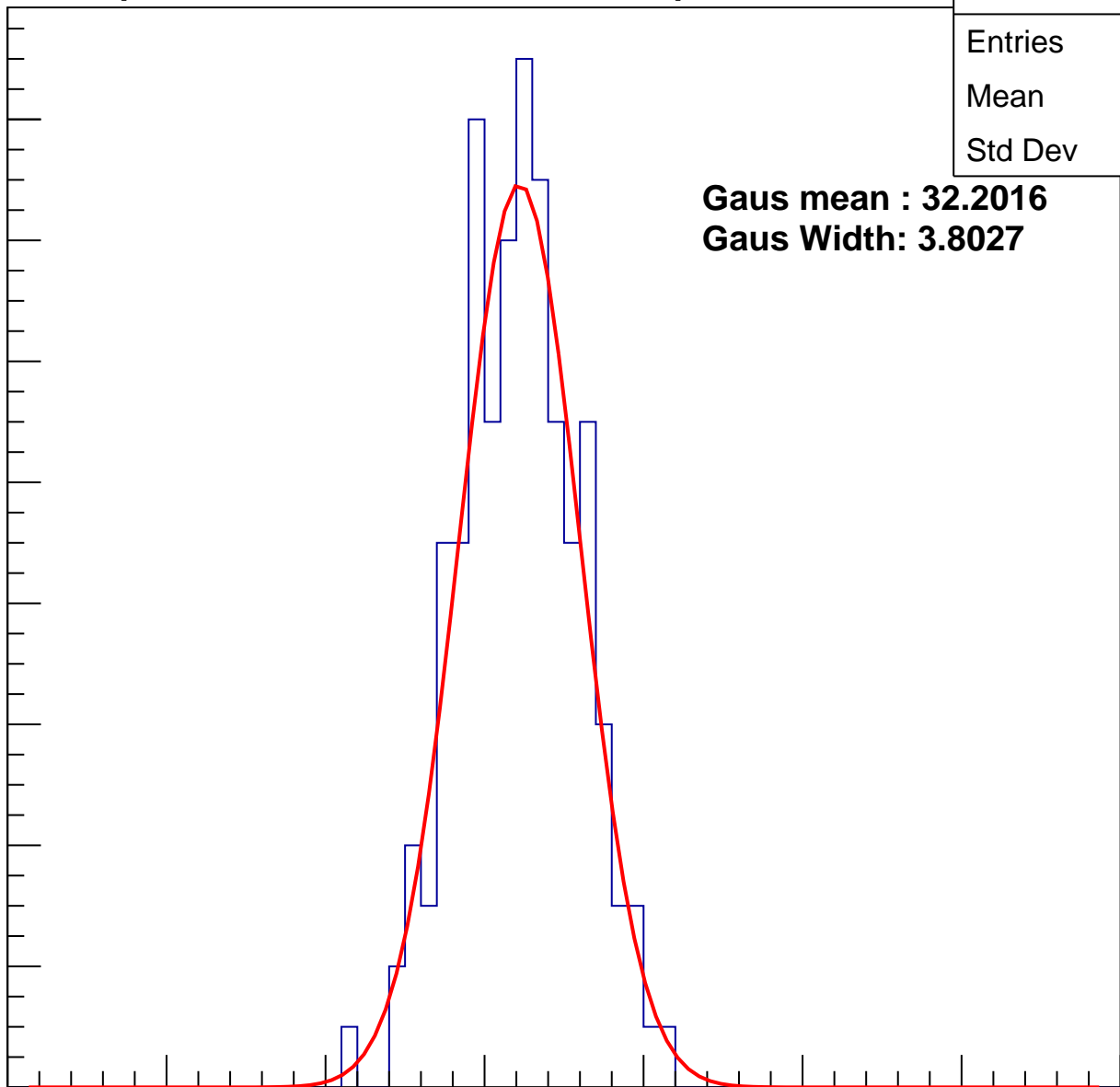
31.7

Std Dev

3.68

Gaus mean : 32.2016

Gaus Width: 3.8027



B0L101S, U22-ch59, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 37.51 |
| Std Dev | 3.406 |

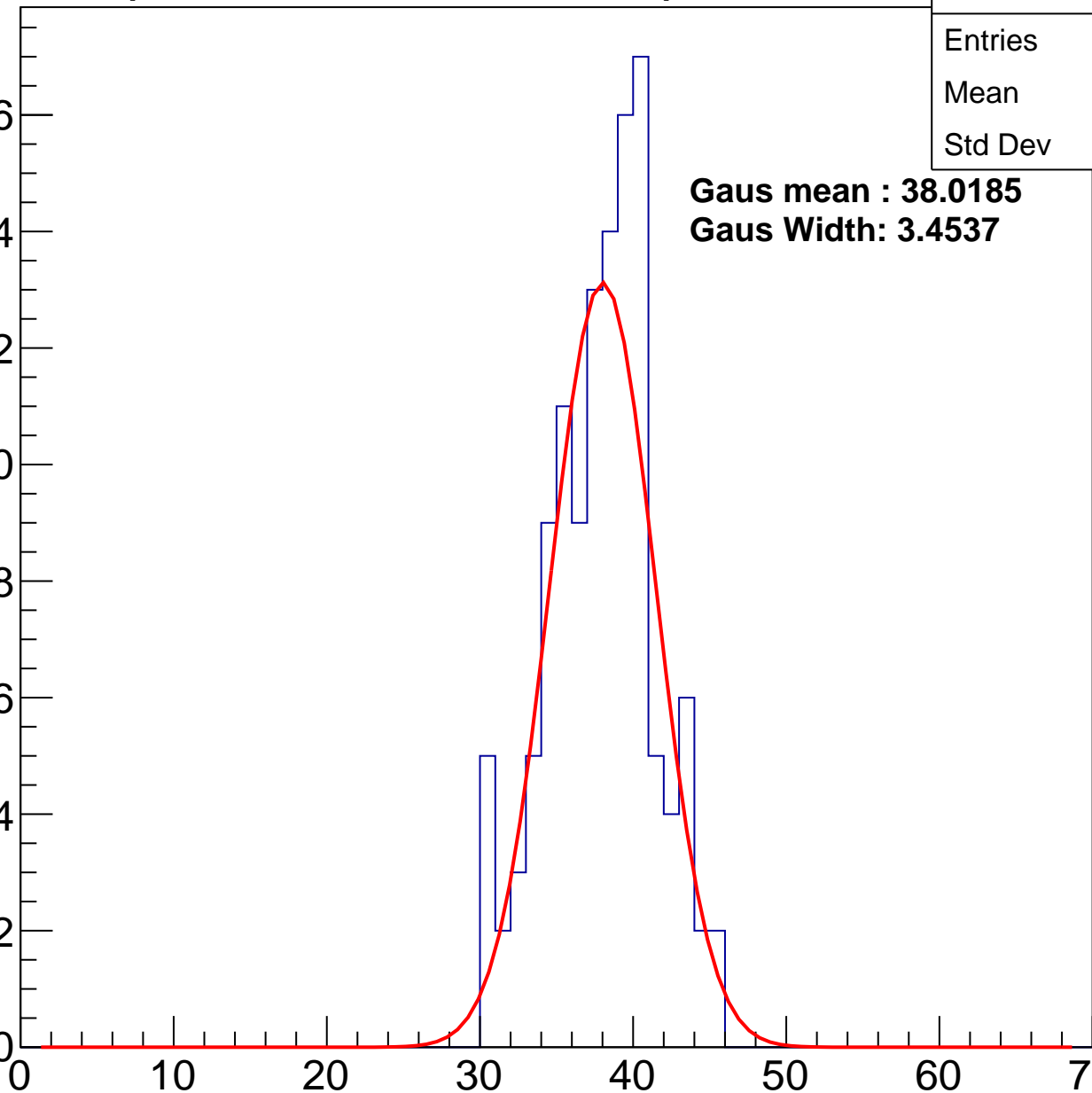
Gaus mean : 38.0185

Gaus Width: 3.4537

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch59, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

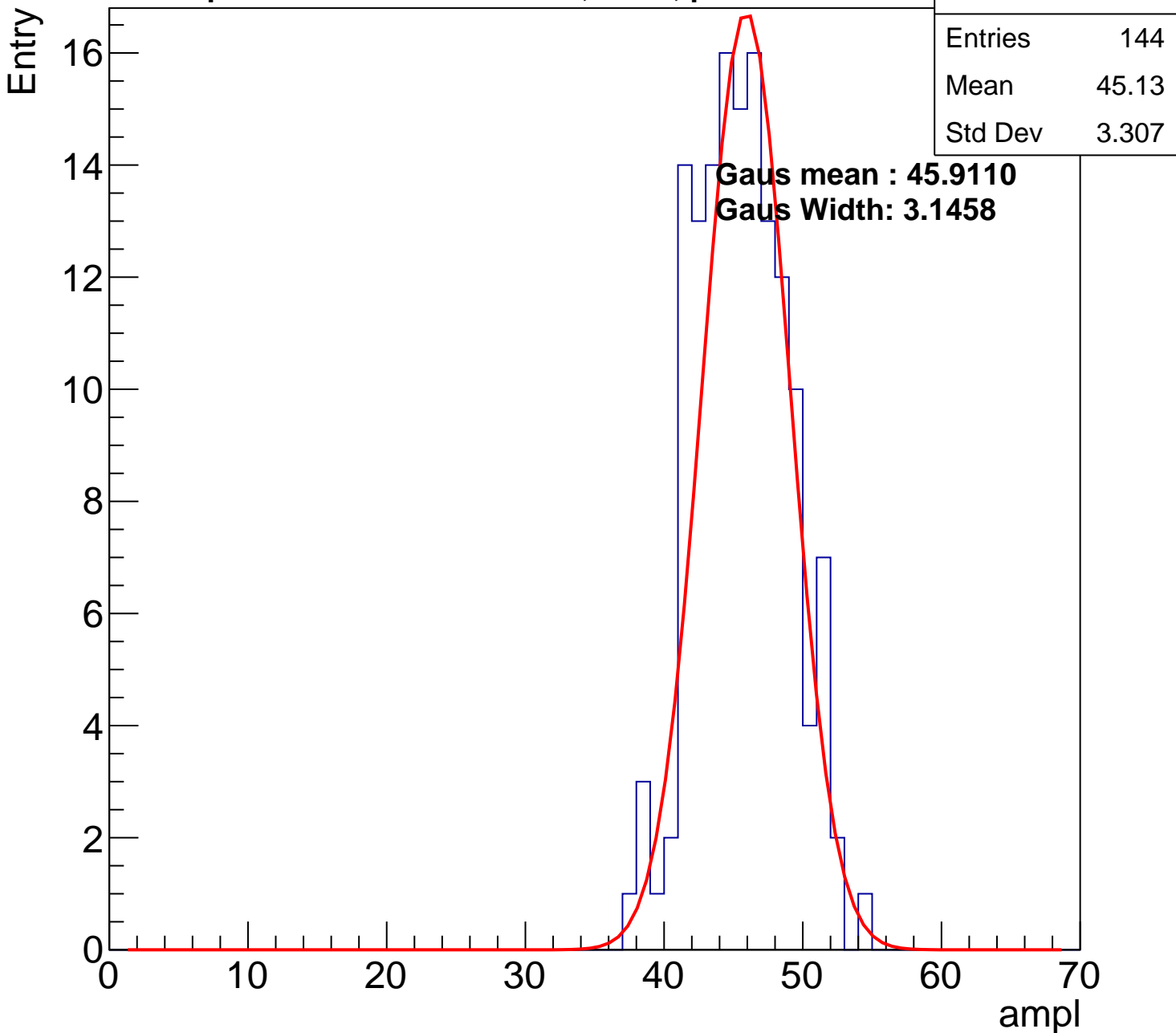
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 144 |
| Mean | 45.13 |
| Std Dev | 3.307 |

Gaus mean : 45.9110
Gaus Width: 3.1458

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch59, adc3

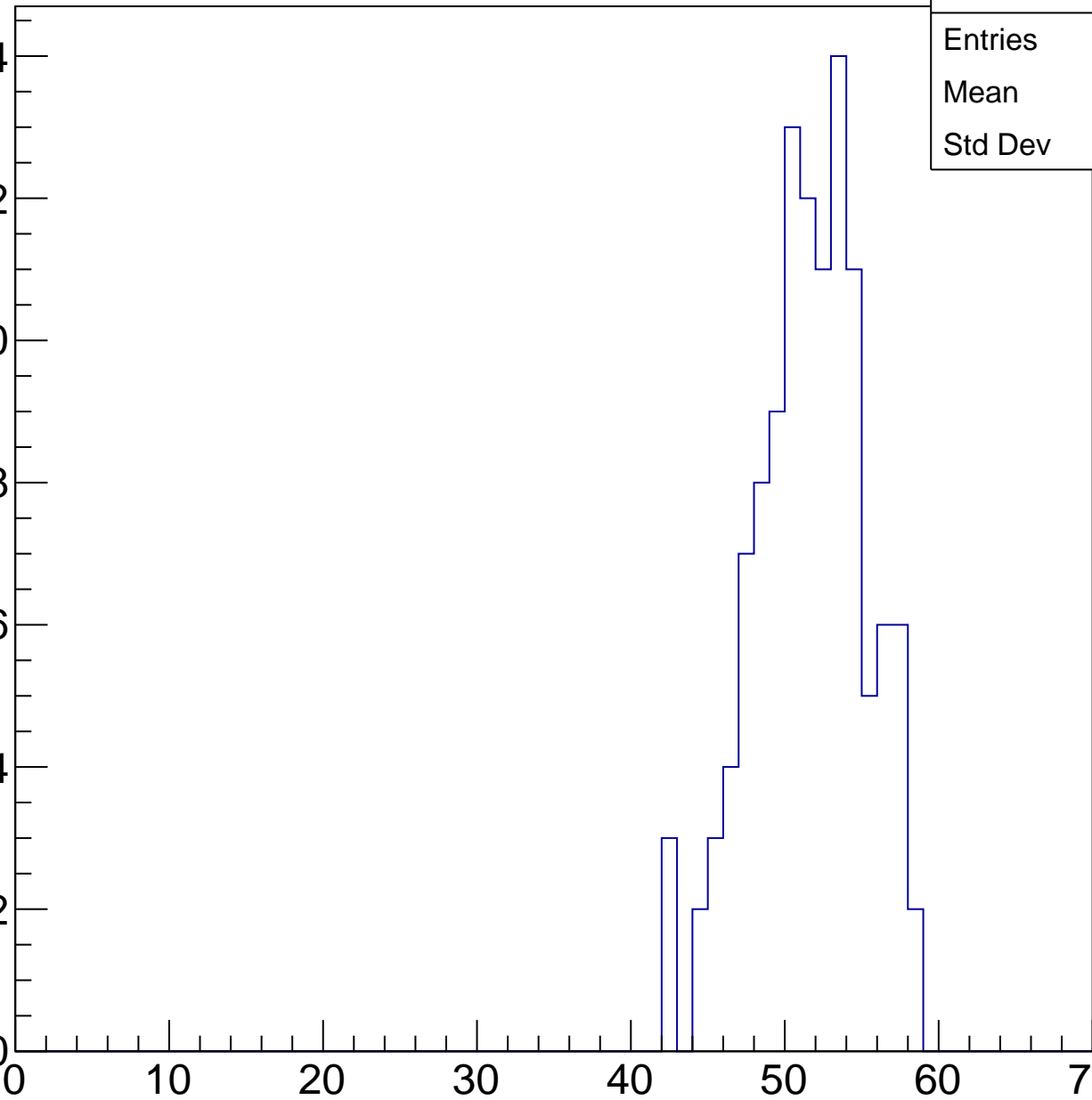
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 116 |
| Mean | 51.09 |
| Std Dev | 3.585 |

ampl



B0L101S, U22-ch59, adc4

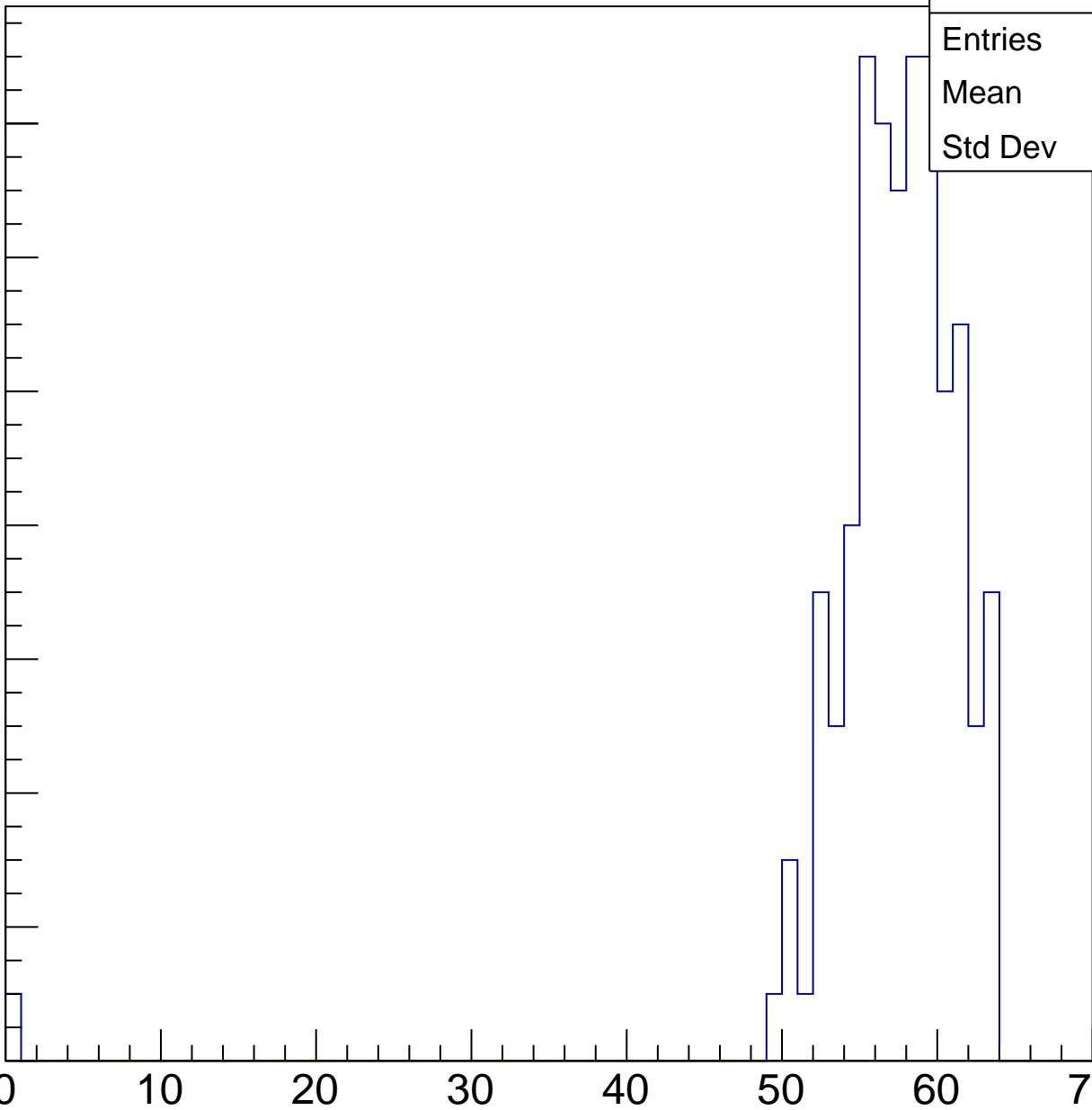
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 131 |
| Mean | 56.78 |
| Std Dev | 5.931 |

ampl



B0L101S, U22-ch59, adc5

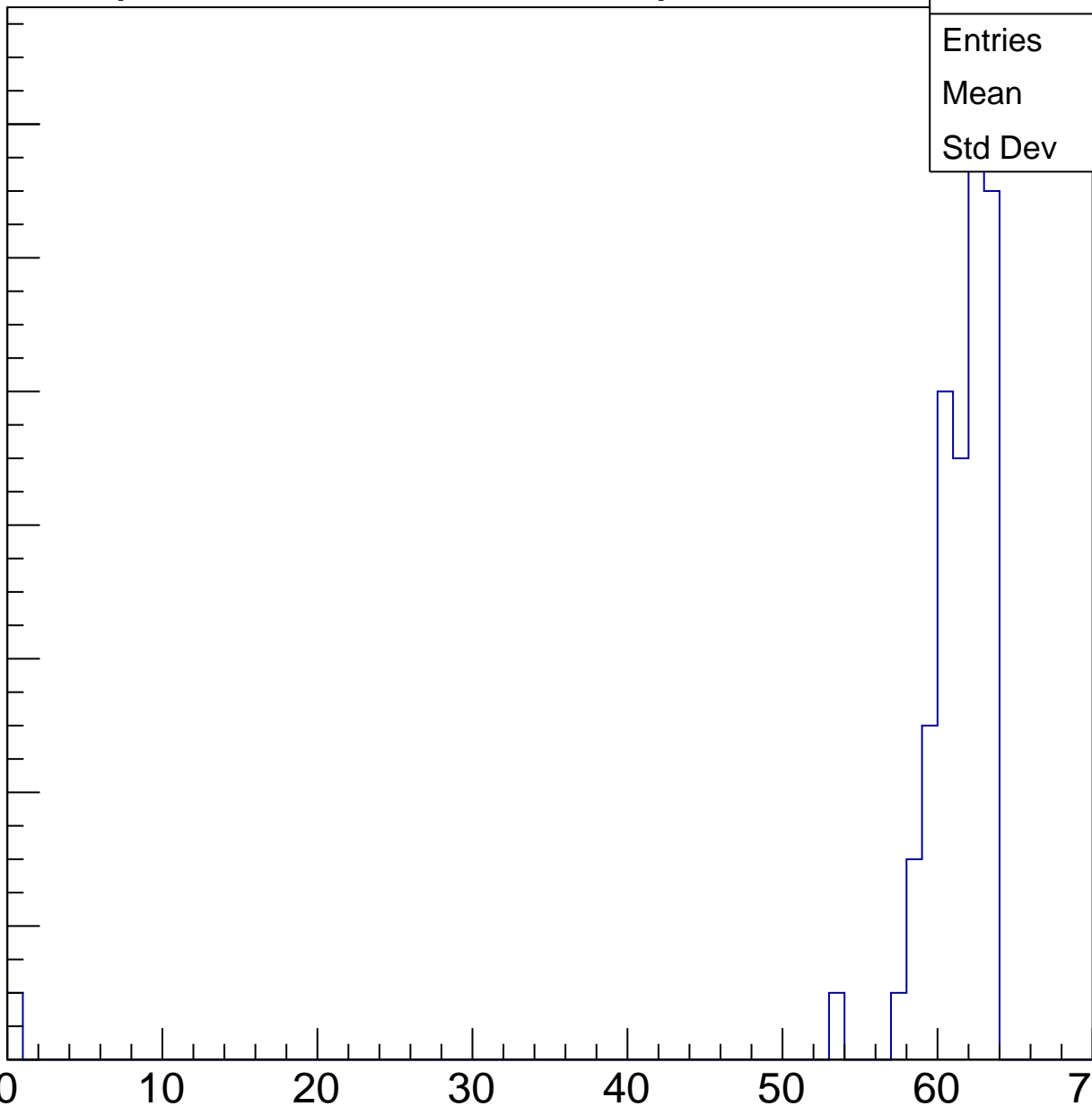
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 59.95 |
| Std Dev | 8.159 |

ampl



B0L101S, U22-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B0L101S, U22-ch60, adc0

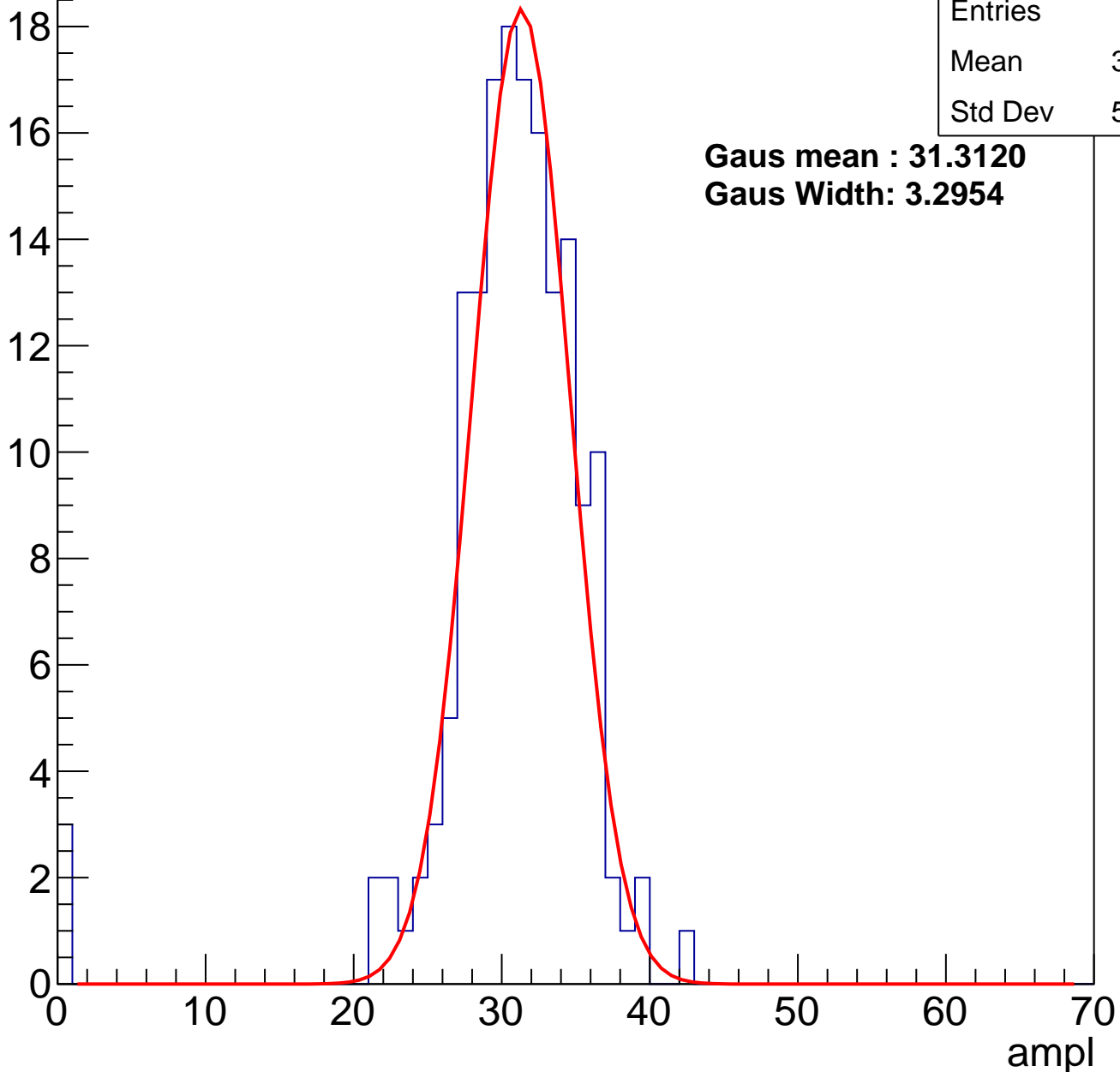
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 164 |
| Mean | 30.25 |
| Std Dev | 5.475 |

Gaus mean : 31.3120

Gaus Width: 3.2954

Entry



B0L101S, U22-ch60, adc1

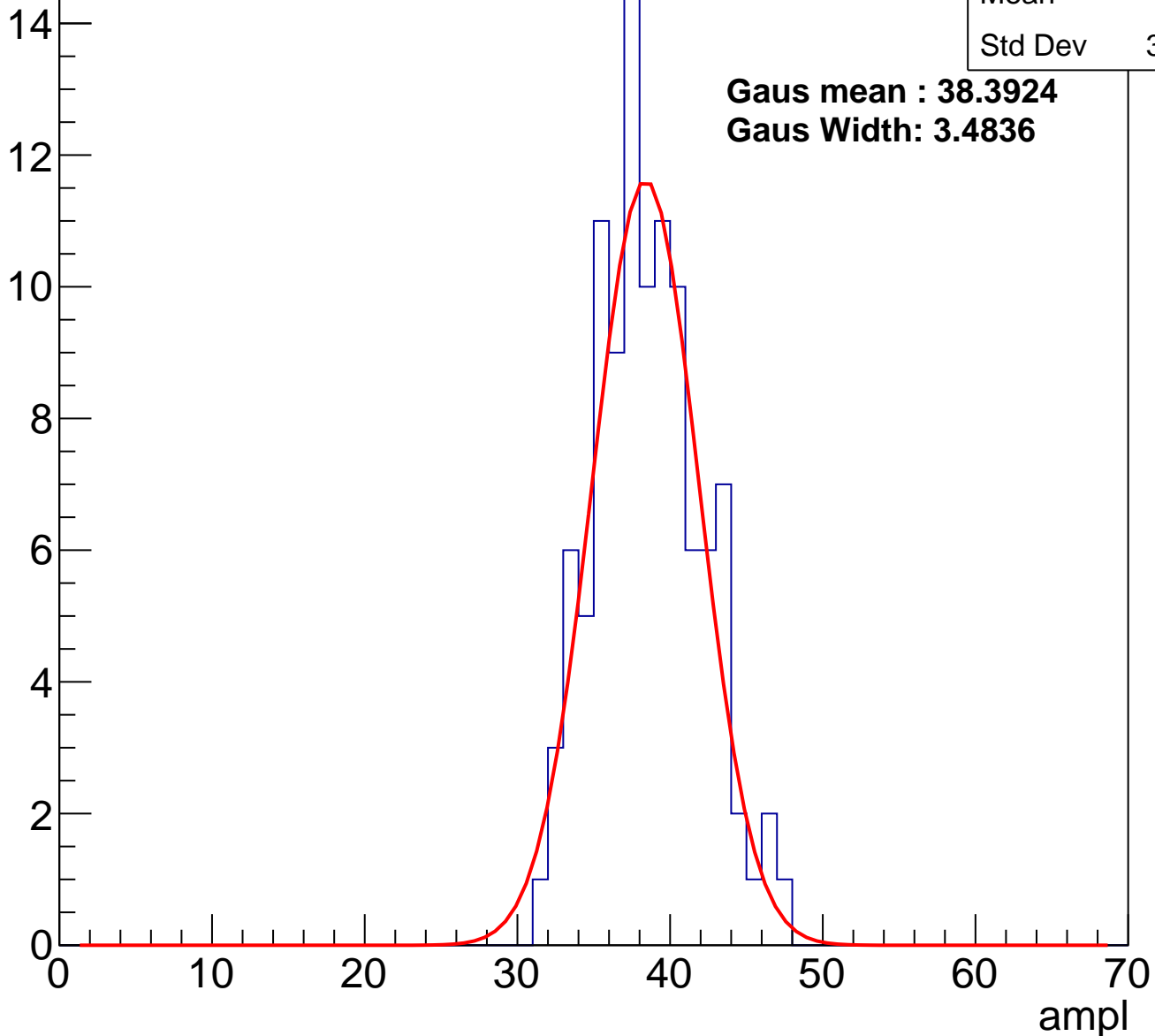
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 106 |
| Mean | 38.1 |
| Std Dev | 3.423 |

Gaus mean : 38.3924

Gaus Width: 3.4836

Entry



B0L101S, U22-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

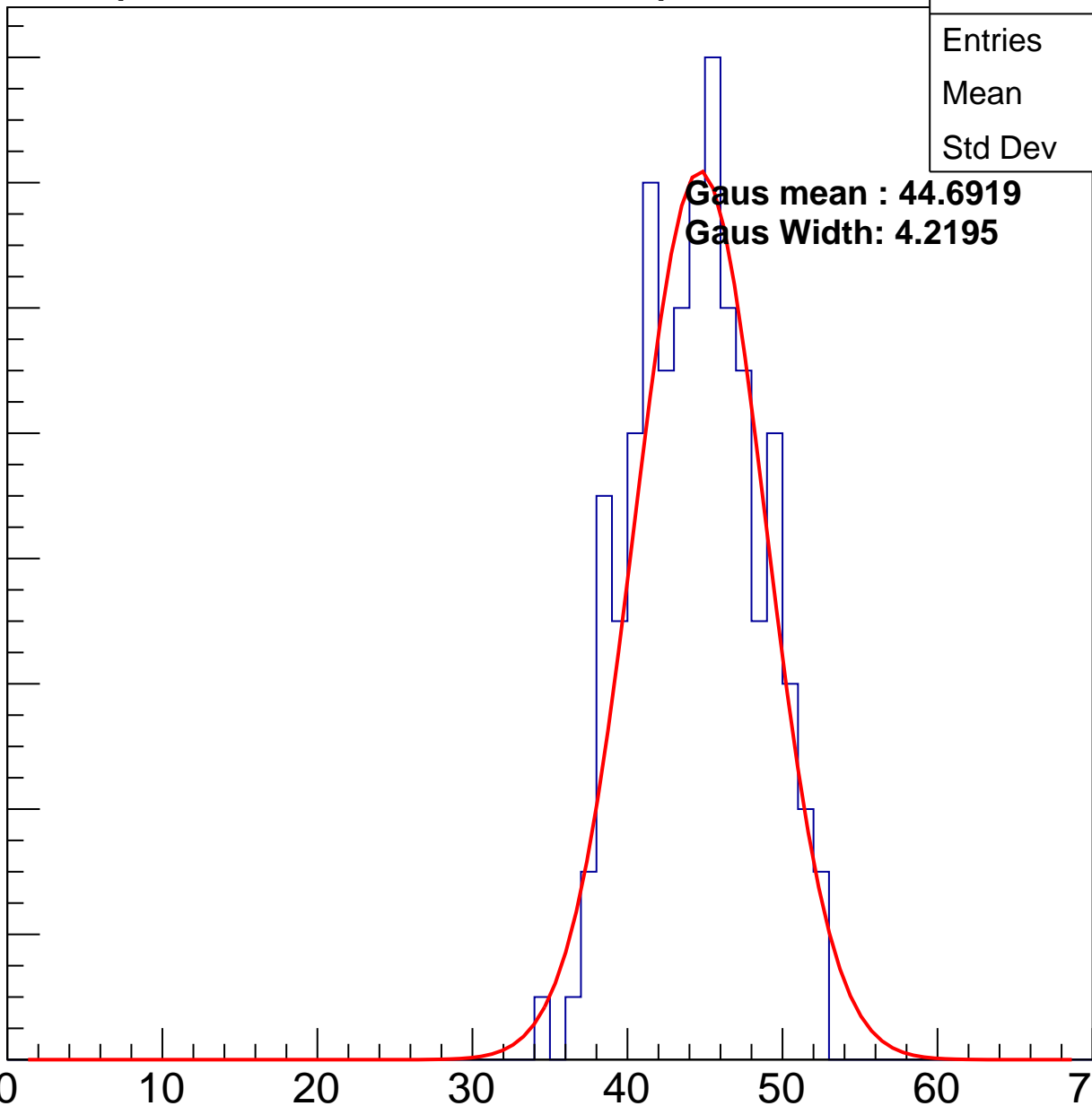
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 151 |
| Mean | 43.97 |
| Std Dev | 3.886 |

Gaus mean : 44.6919

Gaus Width: 4.2195

0 10 20 30 40 50 60 70
ampl



B0L101S, U22-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

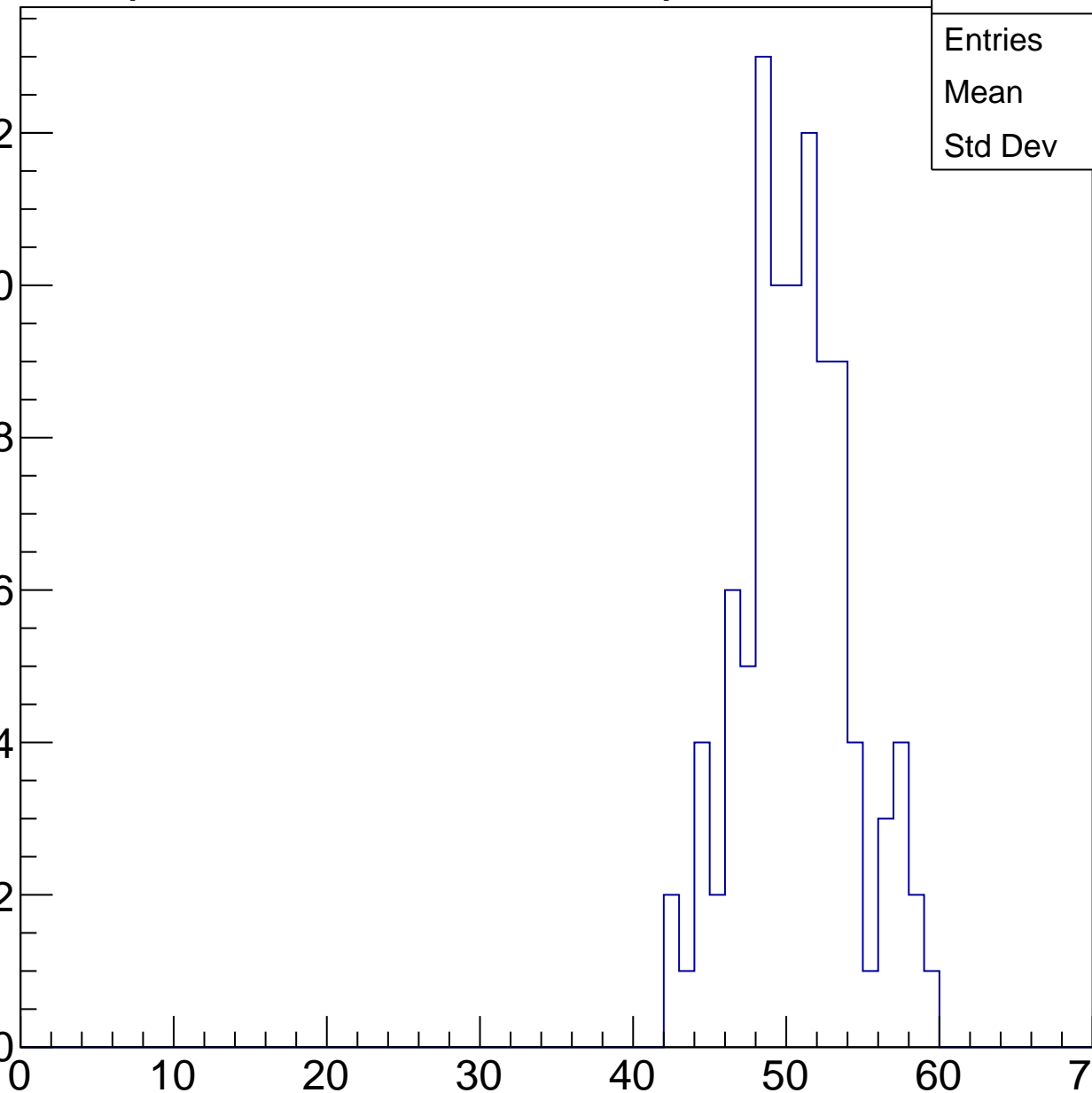
4

2

0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 50.17 |
| Std Dev | 3.645 |

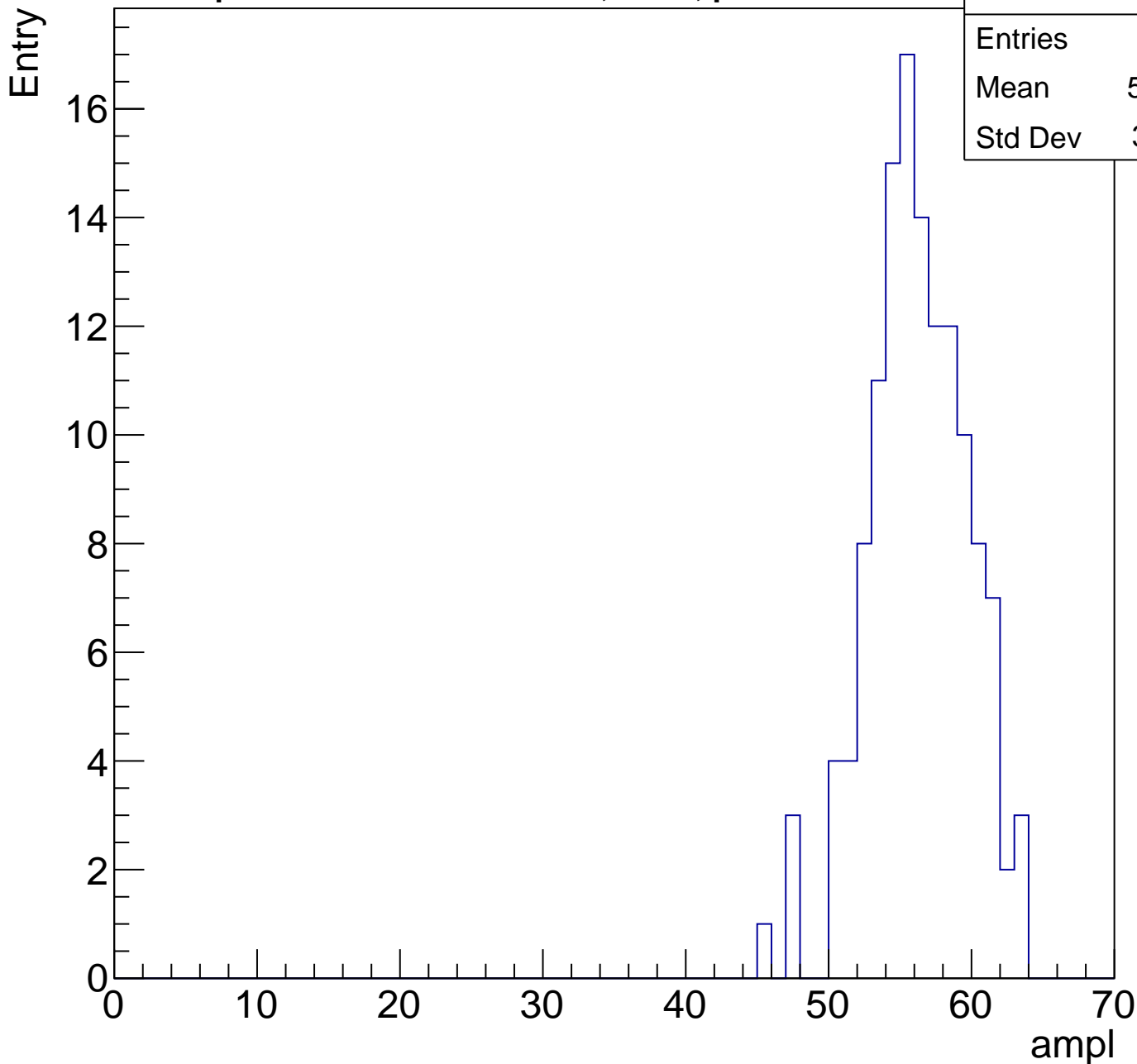
ampl



B0L101S, U22-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 55.79 |
| Std Dev | 3.471 |



B0L101S, U22-ch60, adc5

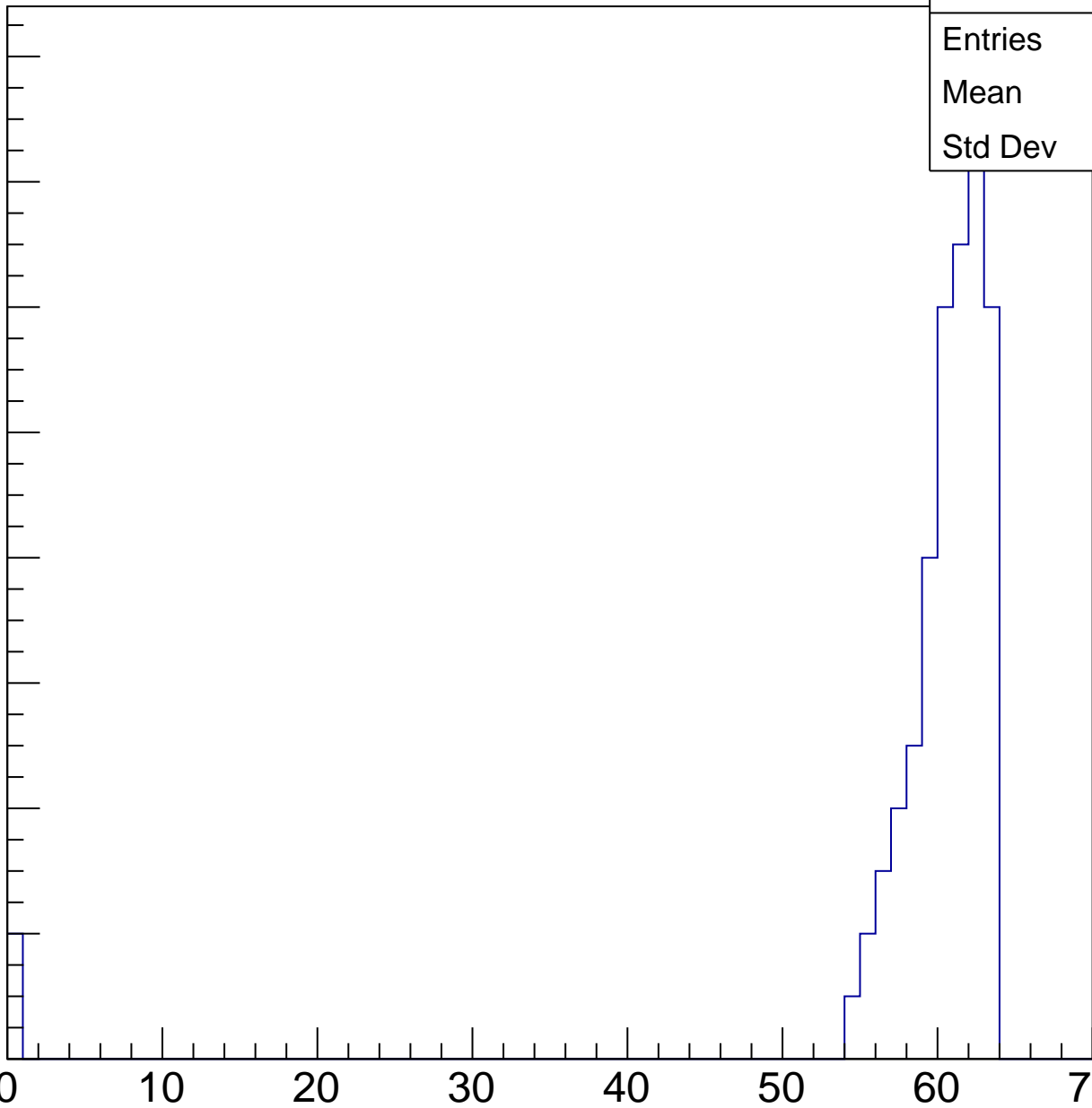
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 78 |
| Mean | 58.76 |
| Std Dev | 9.781 |

ampl

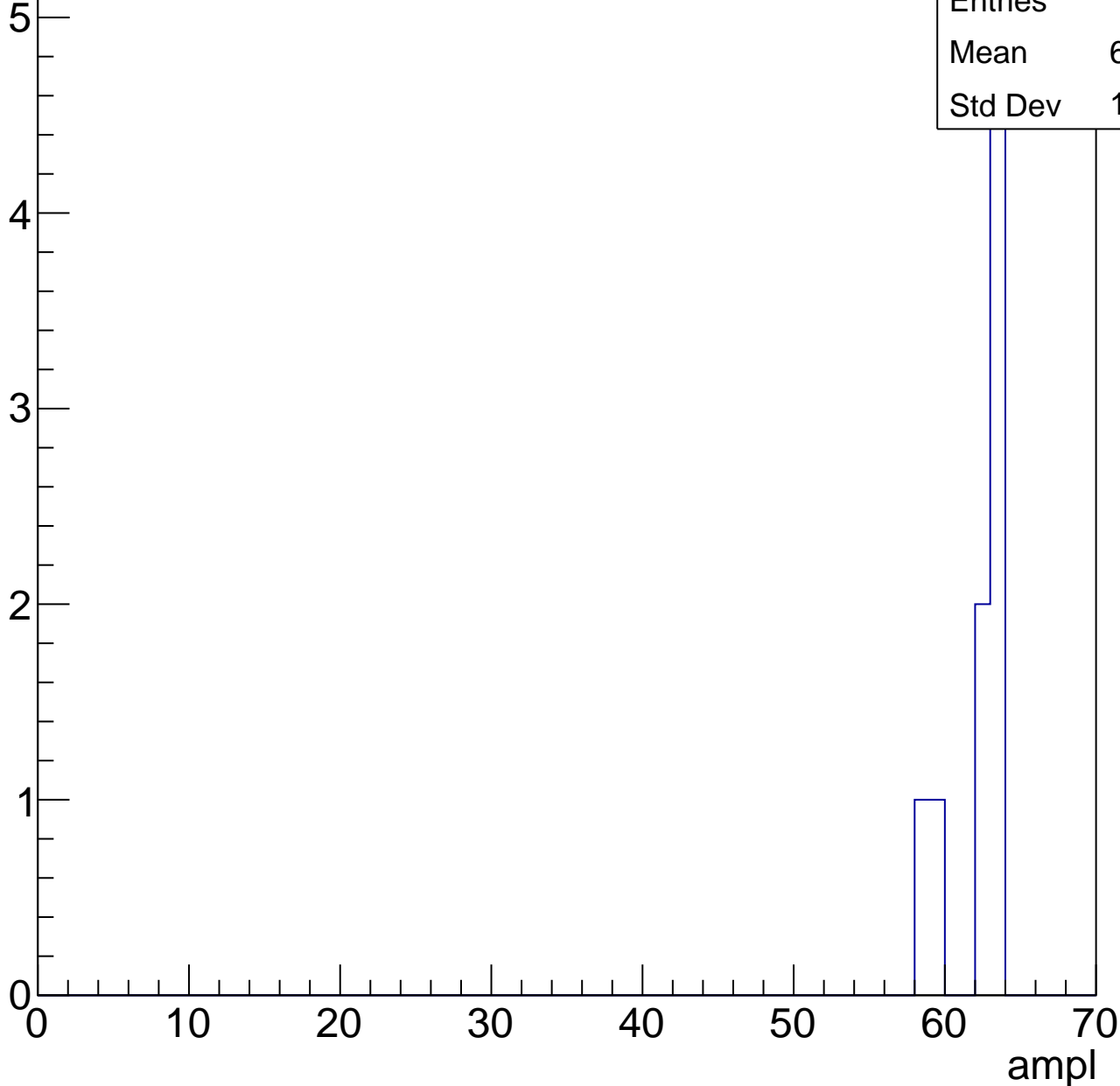


B0L101S, U22-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 61.78 |
| Std Dev | 1.812 |



B0L101S, U22-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 30.15 |
| Std Dev | 4.5 |

Gaus mean : 30.9041

Gaus Width: 3.8459

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

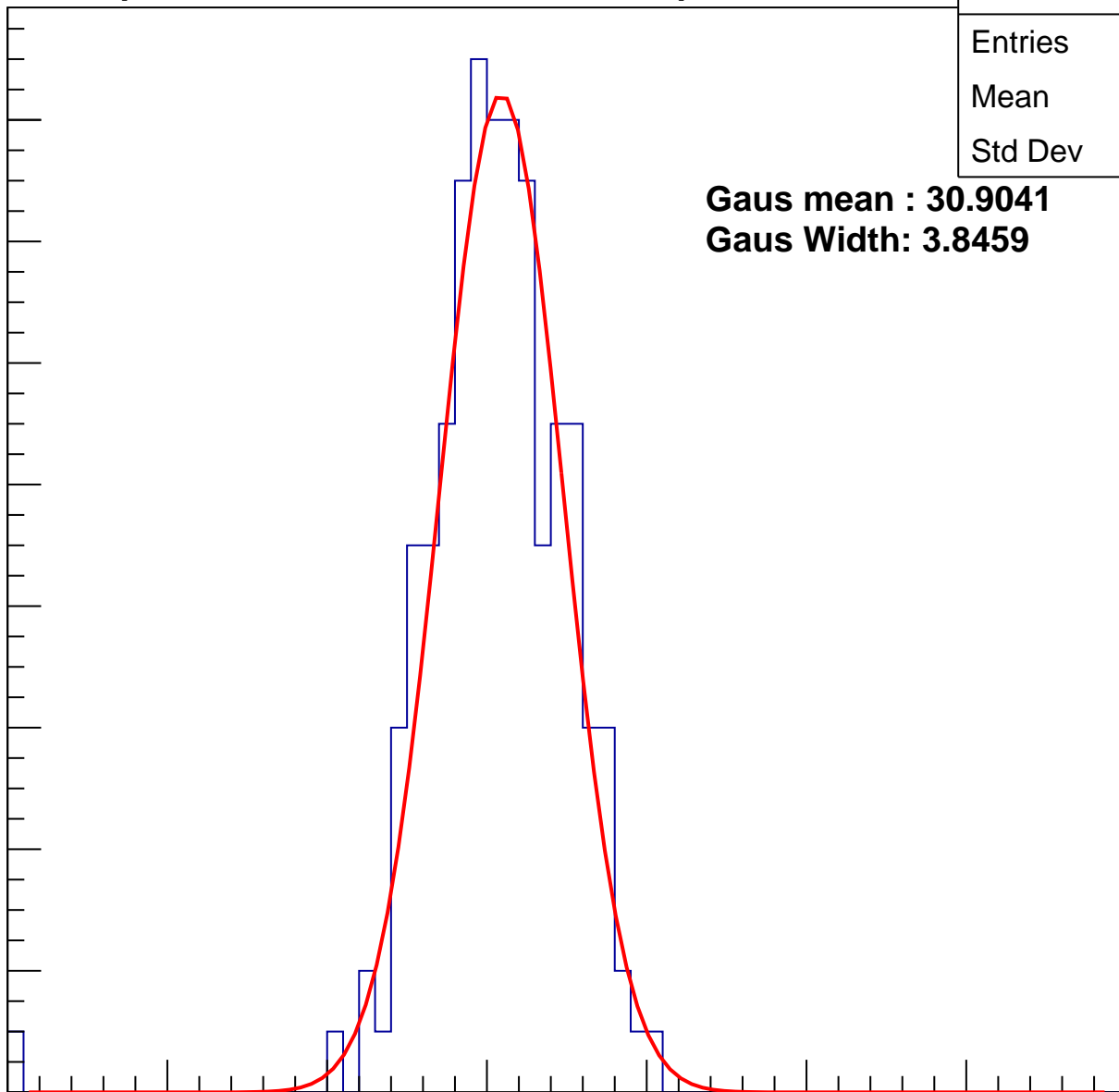
40

50

60

70

ampl



B0L101S, U22-ch61, adc1

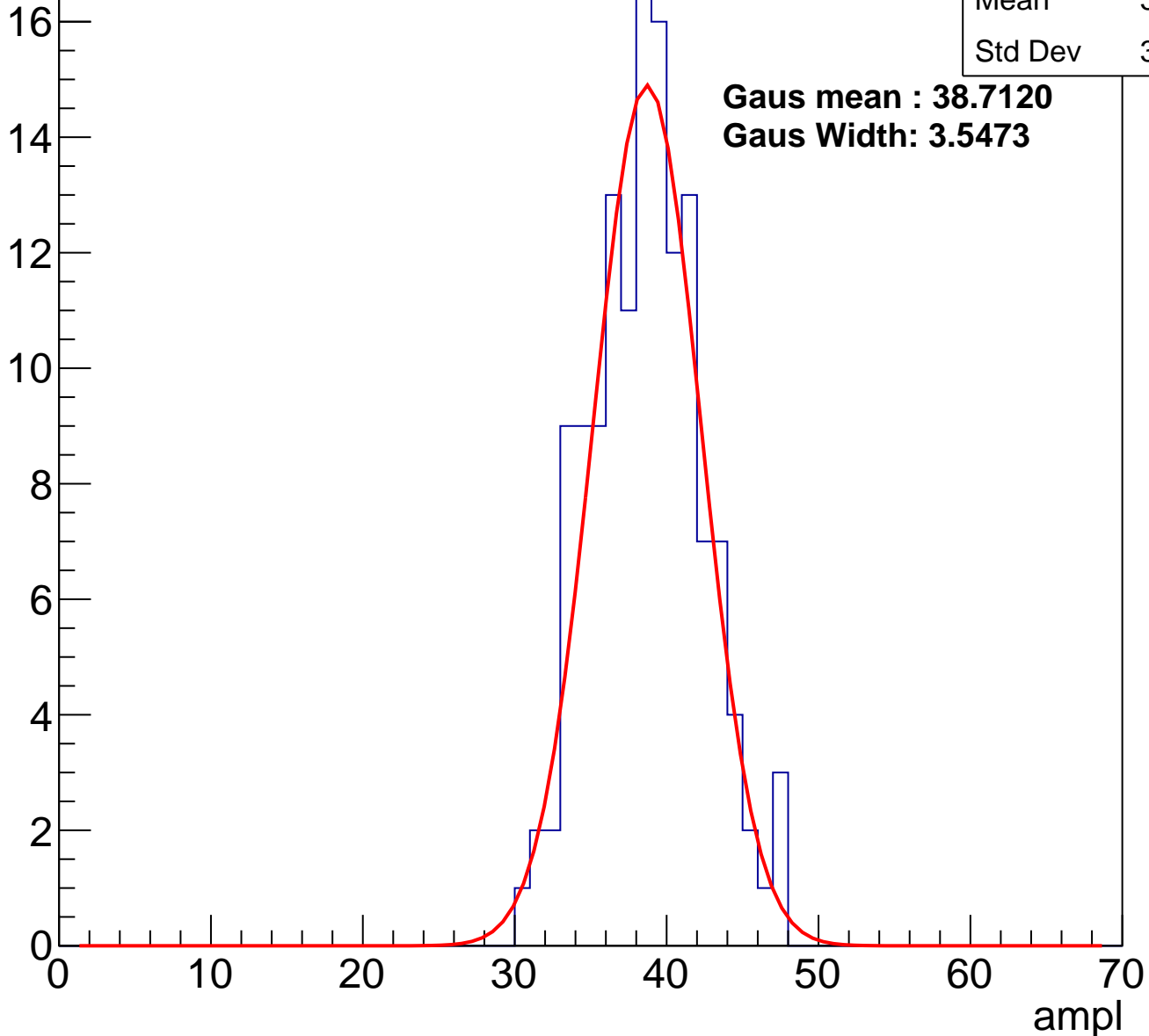
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 38.26 |
| Std Dev | 3.566 |

Gaus mean : 38.7120

Gaus Width: 3.5473

Entry



B0L101S, U22-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

135

Mean

45.1

Std Dev

3.791

Gaus mean : 45.6385

Gaus Width: 3.7523

0

10

20

30

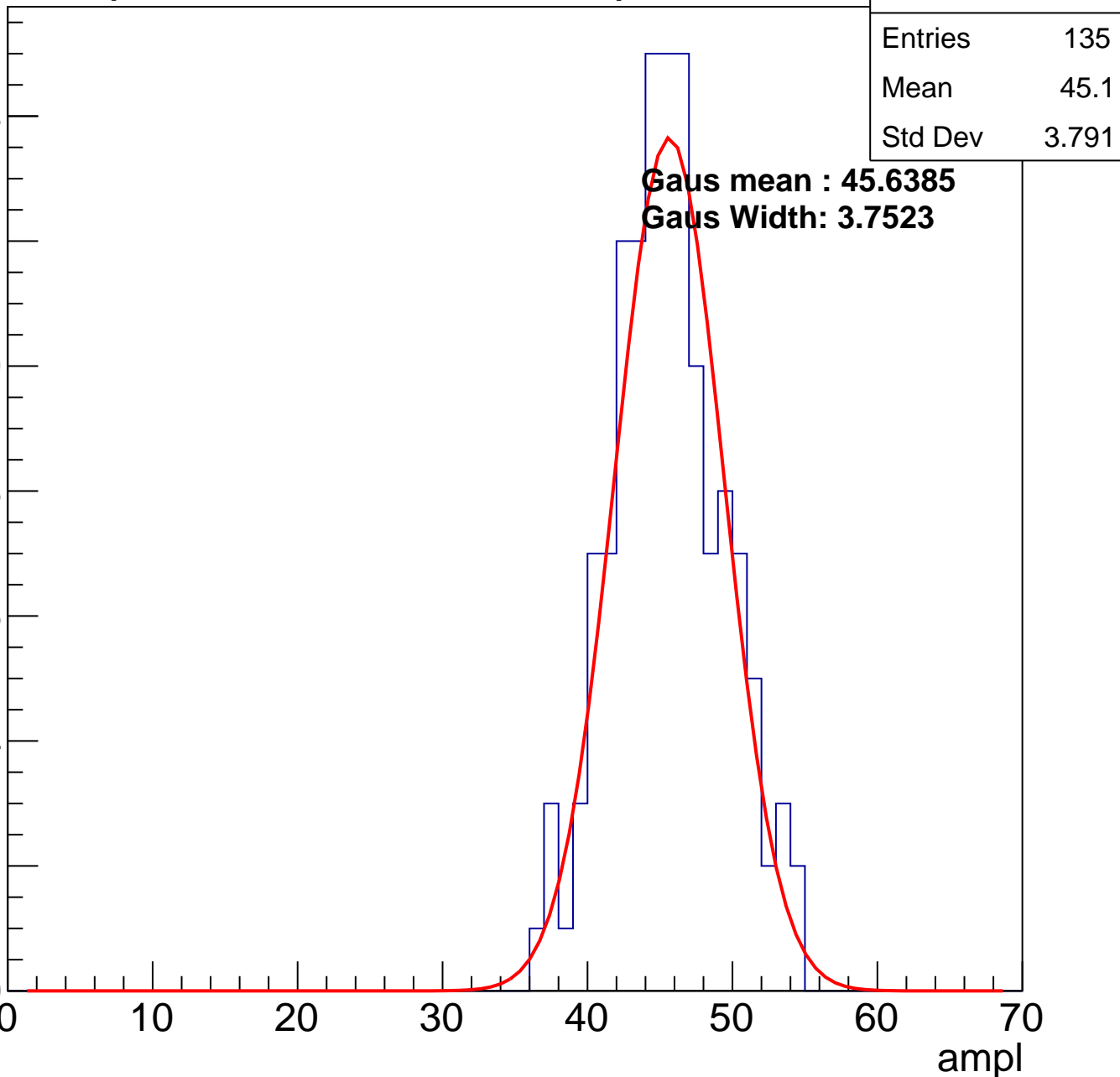
40

50

60

70

ampl



B0L101S, U22-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

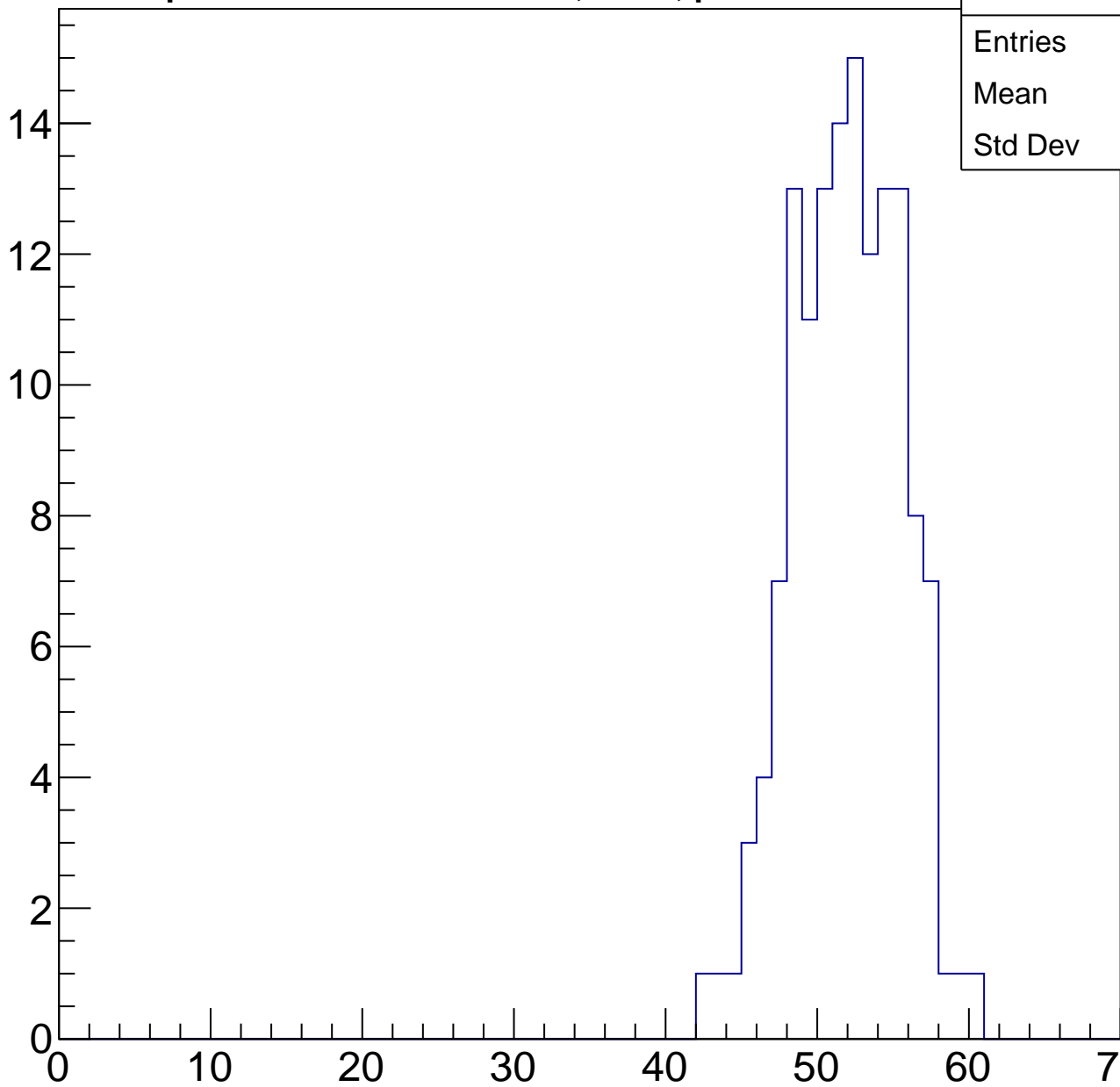
| | |
|---------|-------|
| Entries | 139 |
| Mean | 51.52 |
| Std Dev | 3.481 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch61, adc4

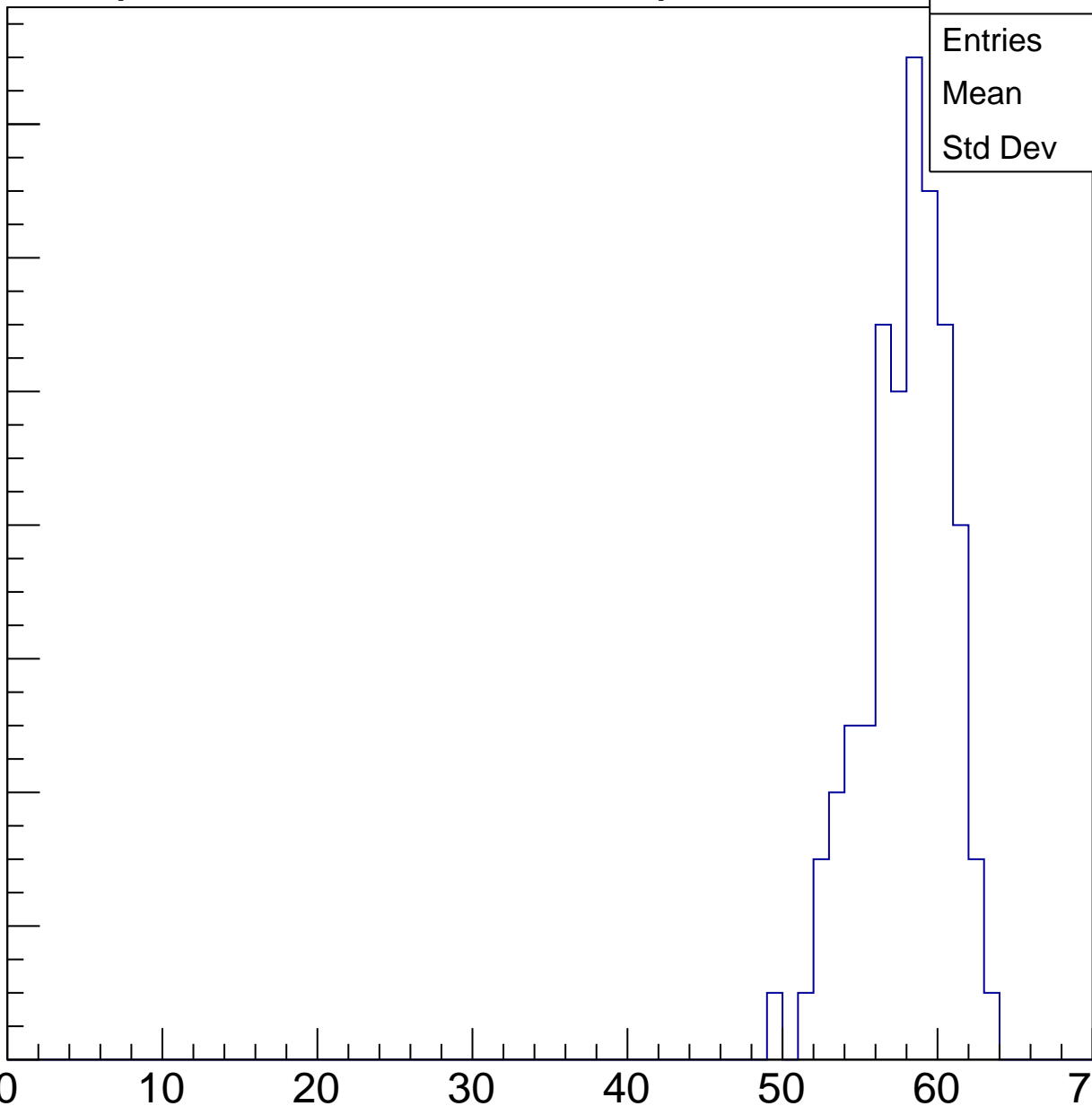
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 57.51 |
| Std Dev | 2.767 |

ampl



B0L101S, U22-ch61, adc5

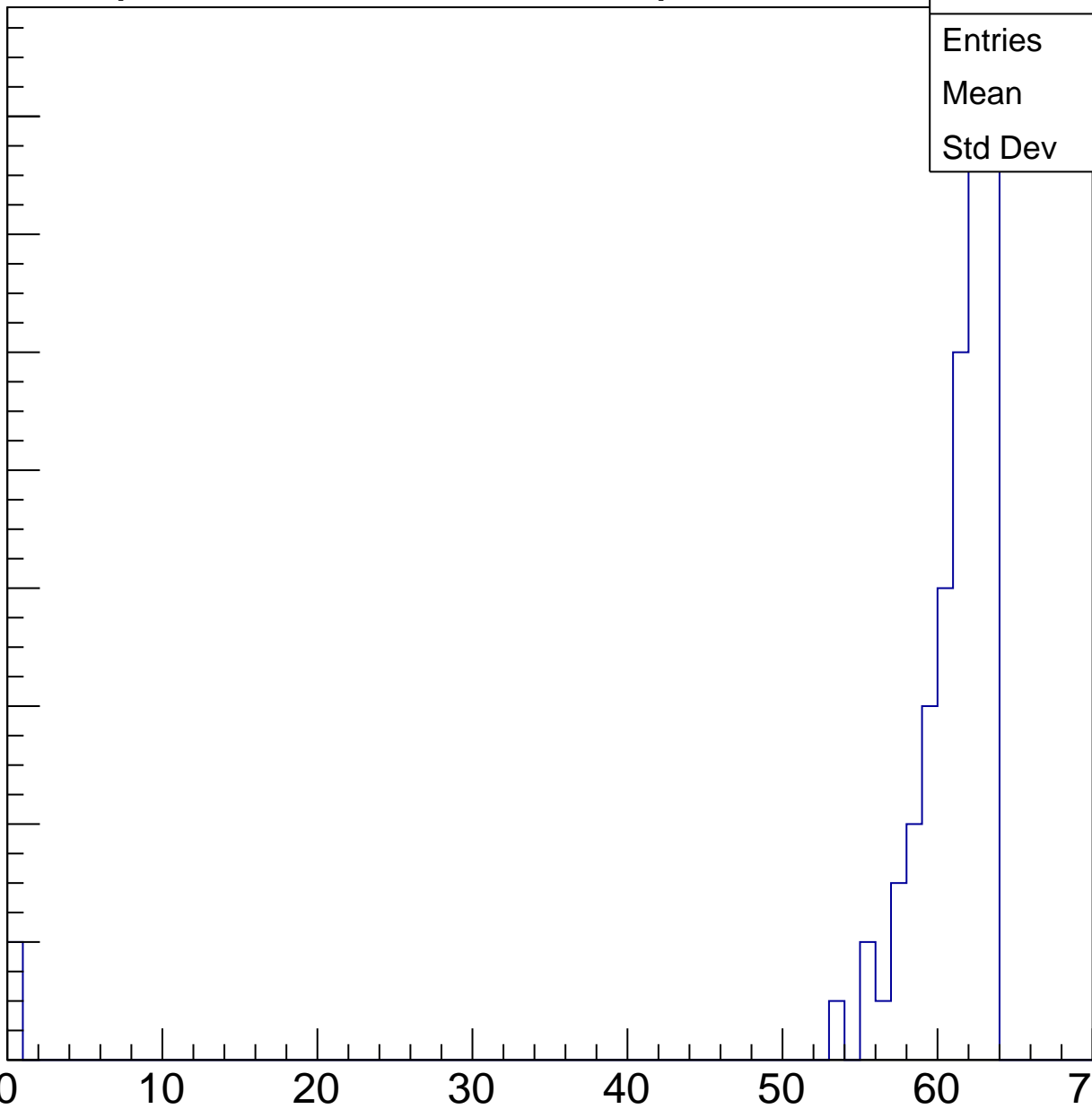
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 59.04 |
| Std Dev | 10.22 |

ampl



B0L101S, U22-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

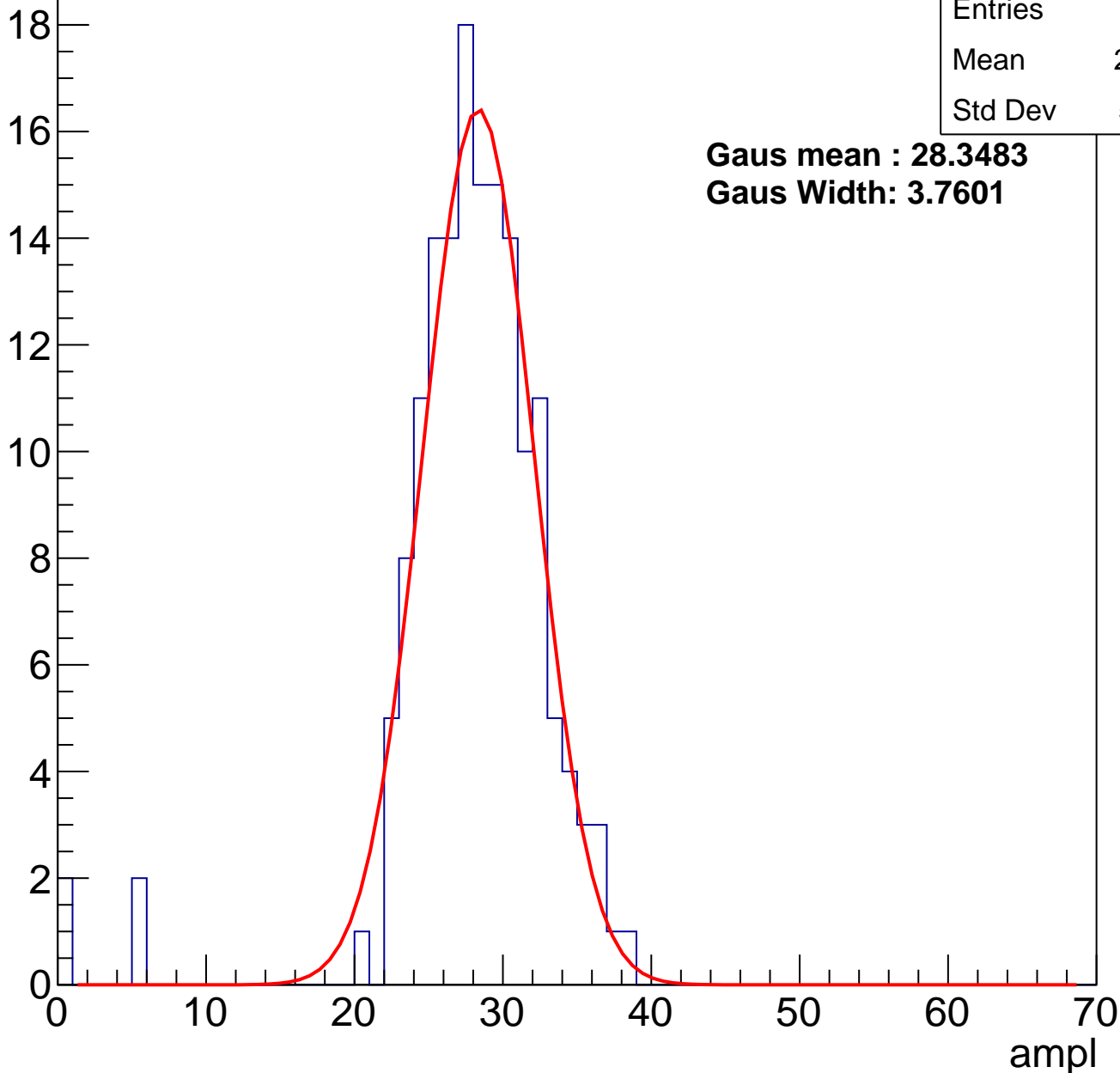
Entry



B0L101S, U22-ch62, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



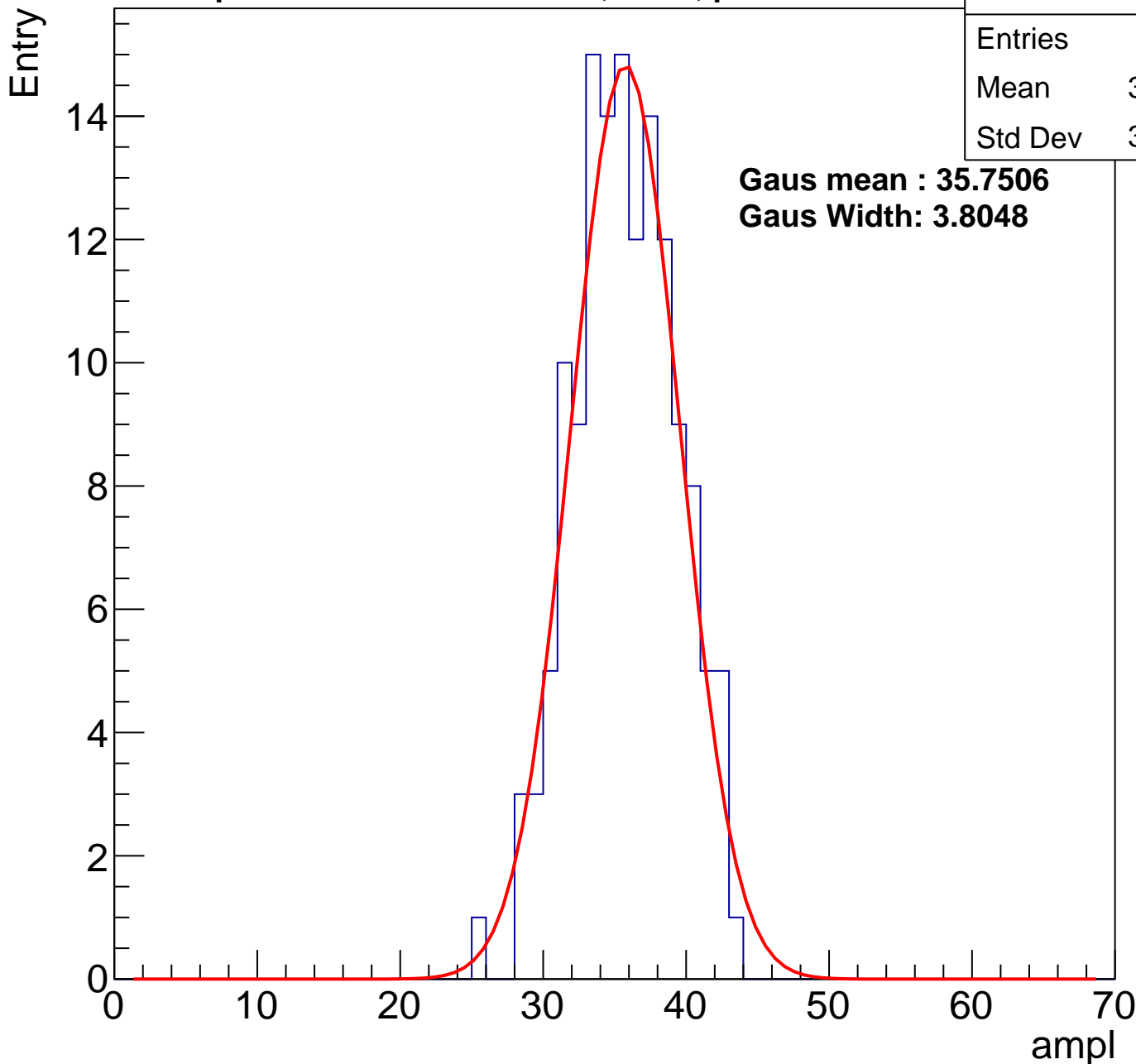
B0L101S, U22-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 35.28 |
| Std Dev | 3.566 |

Gaus mean : 35.7506

Gaus Width: 3.8048



B0L101S, U22-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

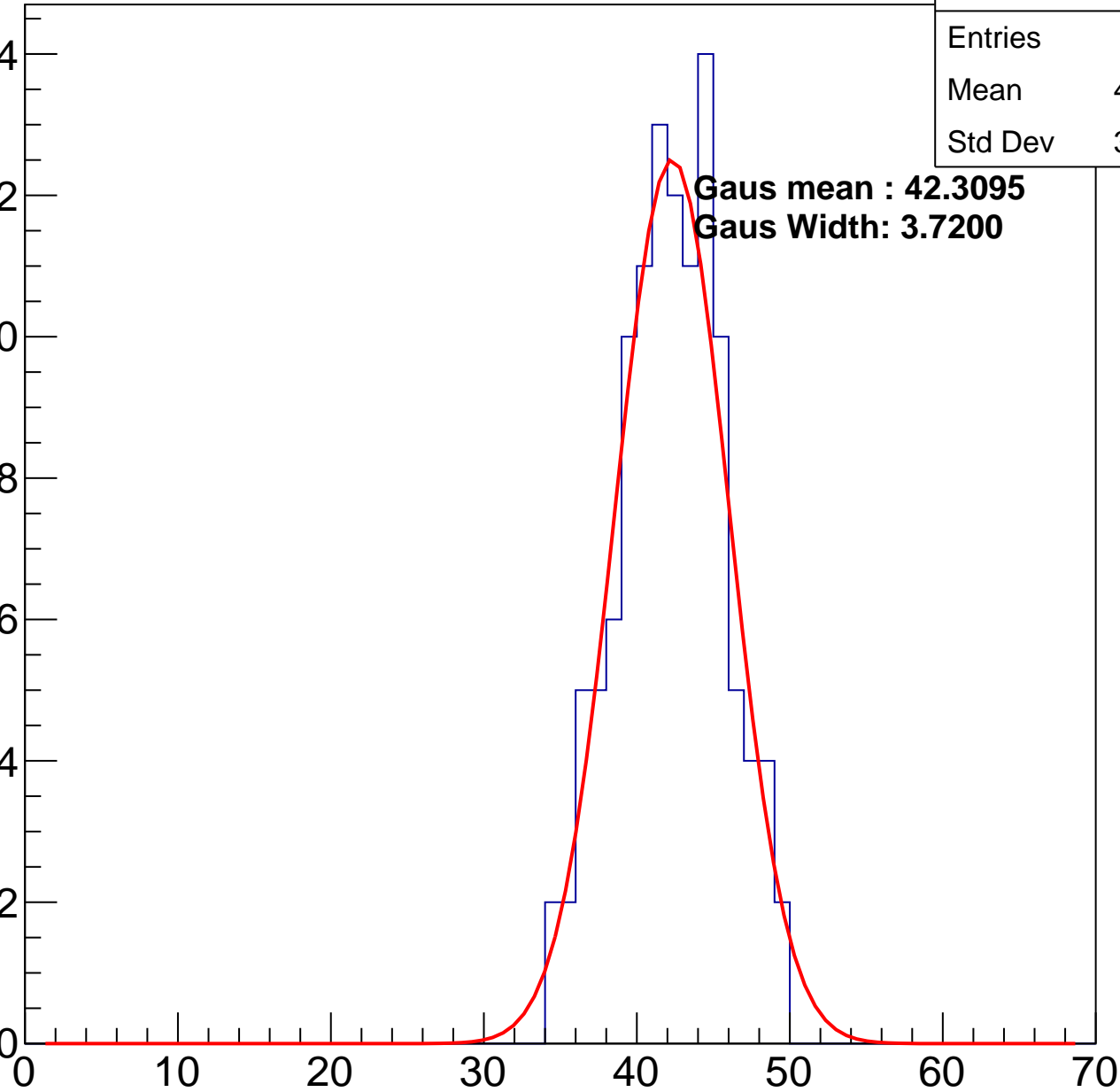
Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 116 |
| Mean | 41.77 |
| Std Dev | 3.407 |

Gaus mean : 42.3095
Gaus Width: 3.7200

ampl



B0L101S, U22-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 48.68 |
| Std Dev | 3.932 |

Entry

14

12

10

8

6

4

2

0

0

10

20

30

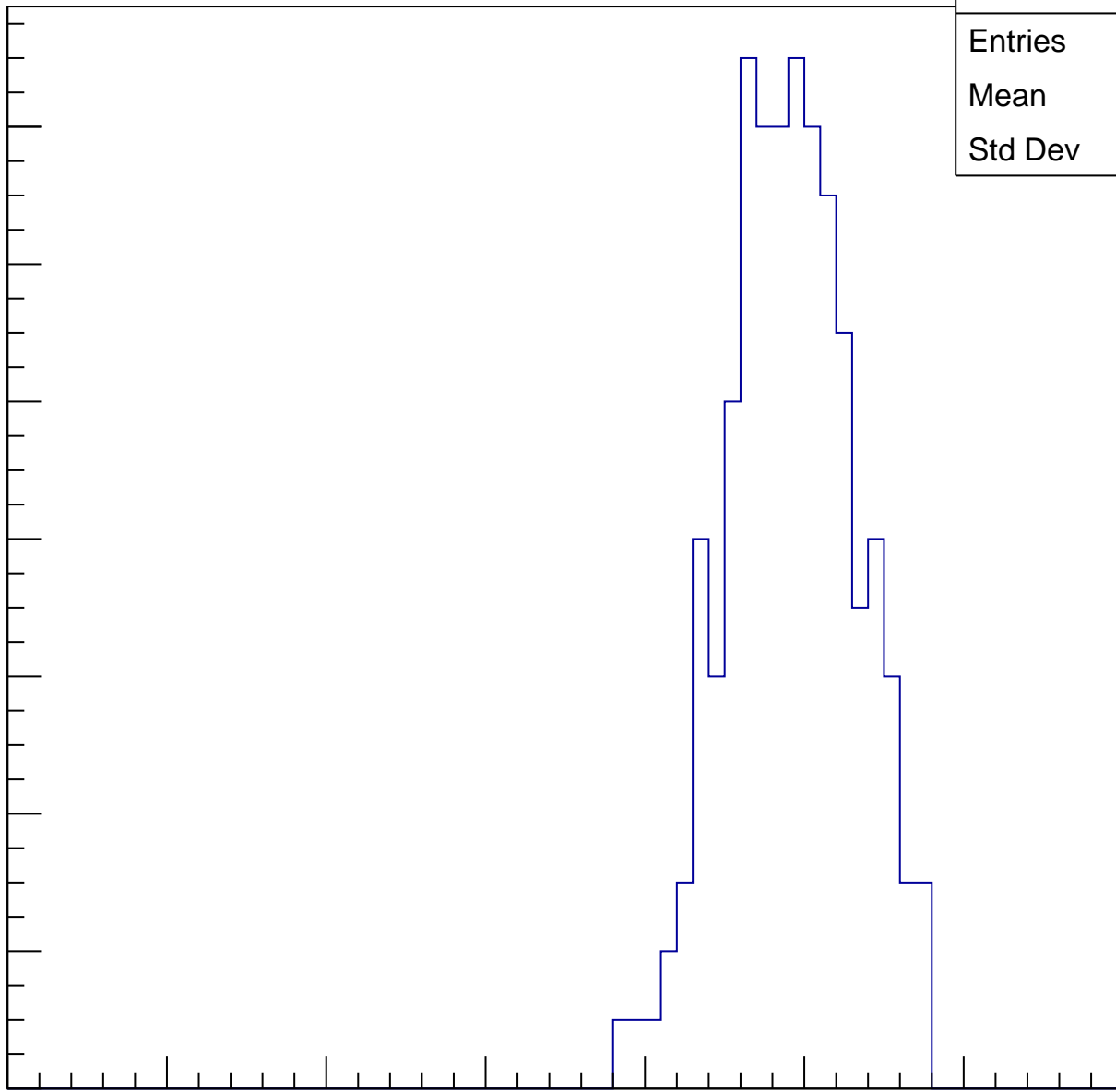
40

50

60

70

ampl

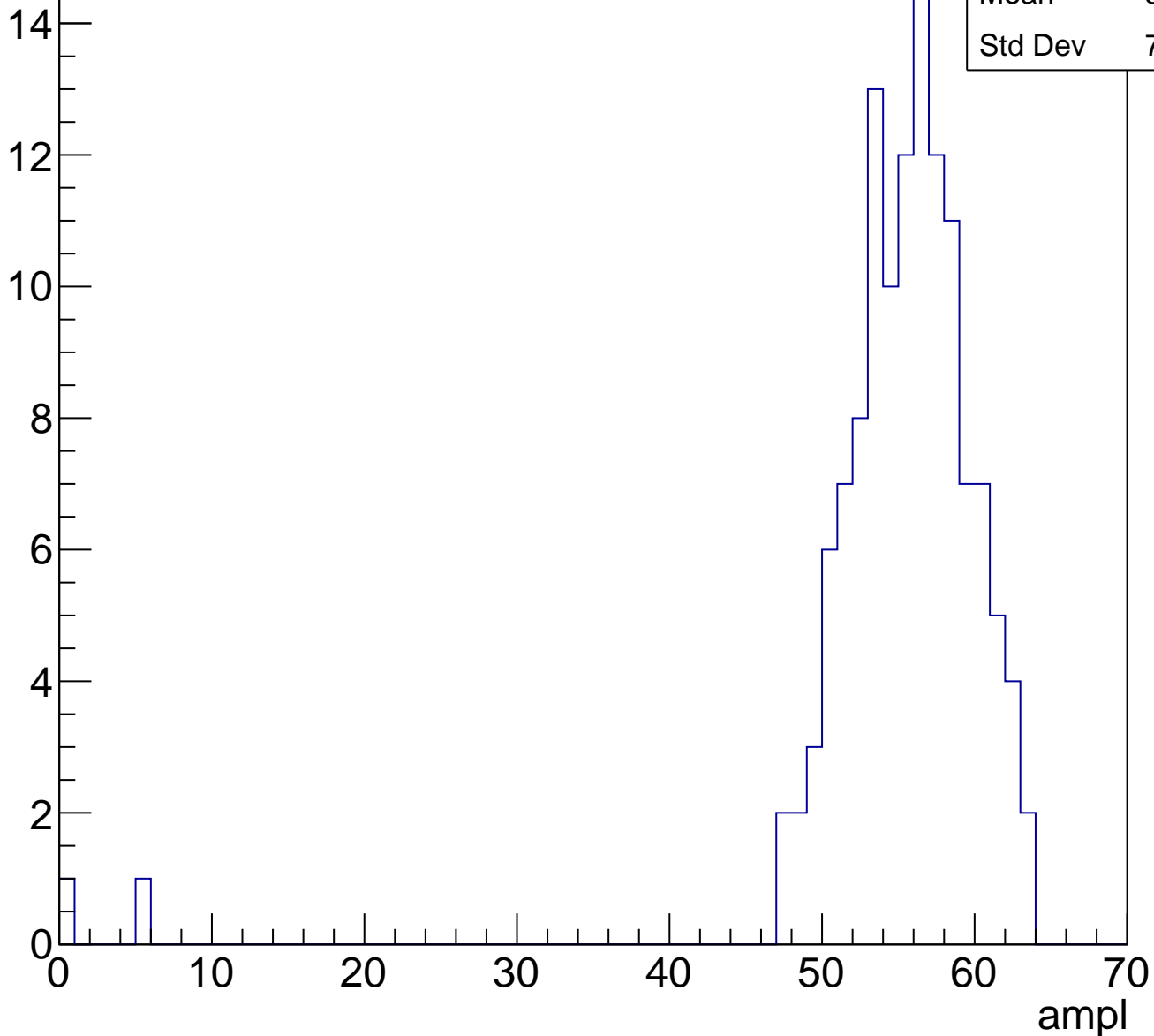


B0L101S, U22-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 128 |
| Mean | 54.52 |
| Std Dev | 7.489 |

Entry



B0L101S, U22-ch62, adc5

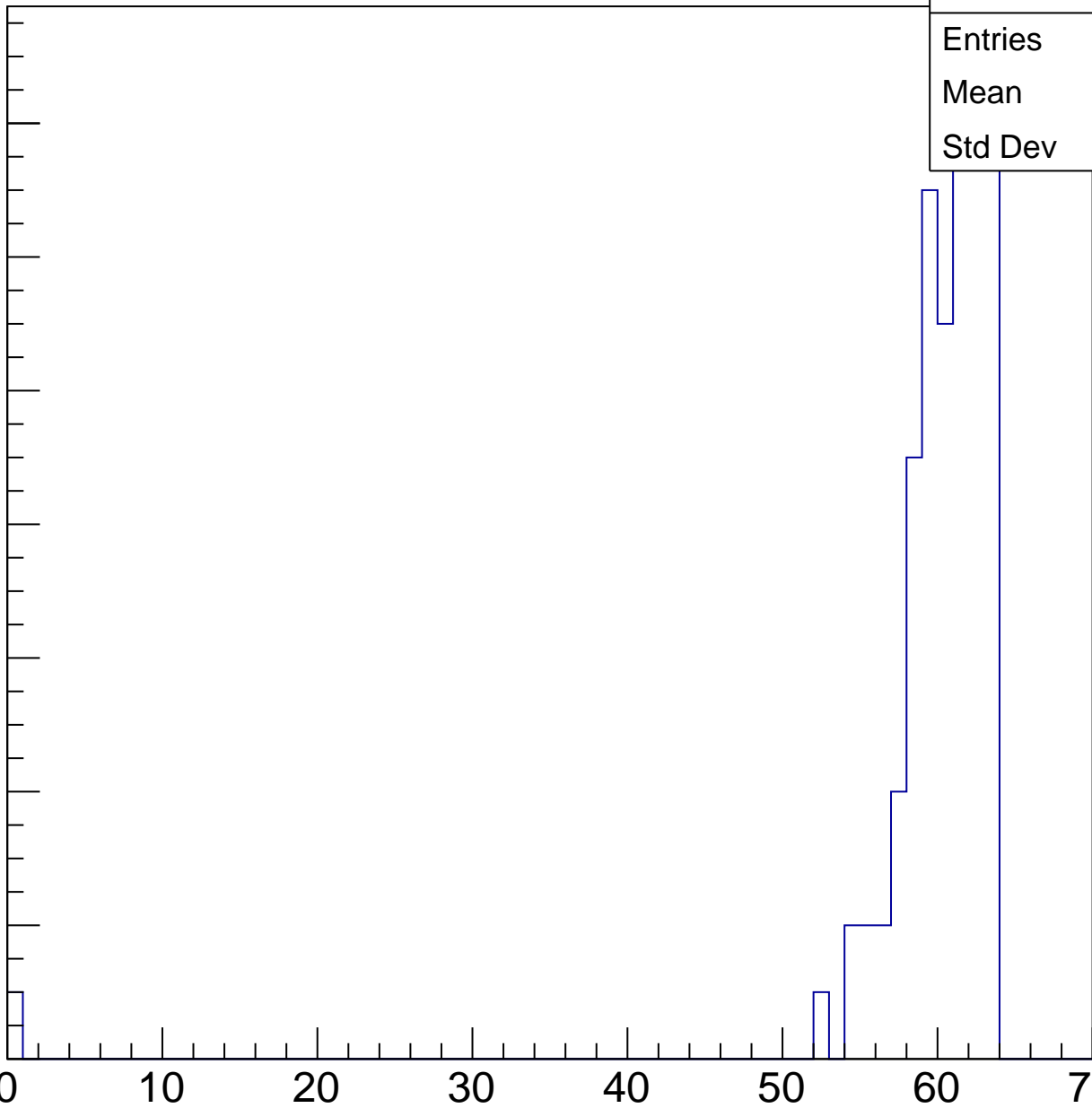
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 59.43 |
| Std Dev | 6.736 |

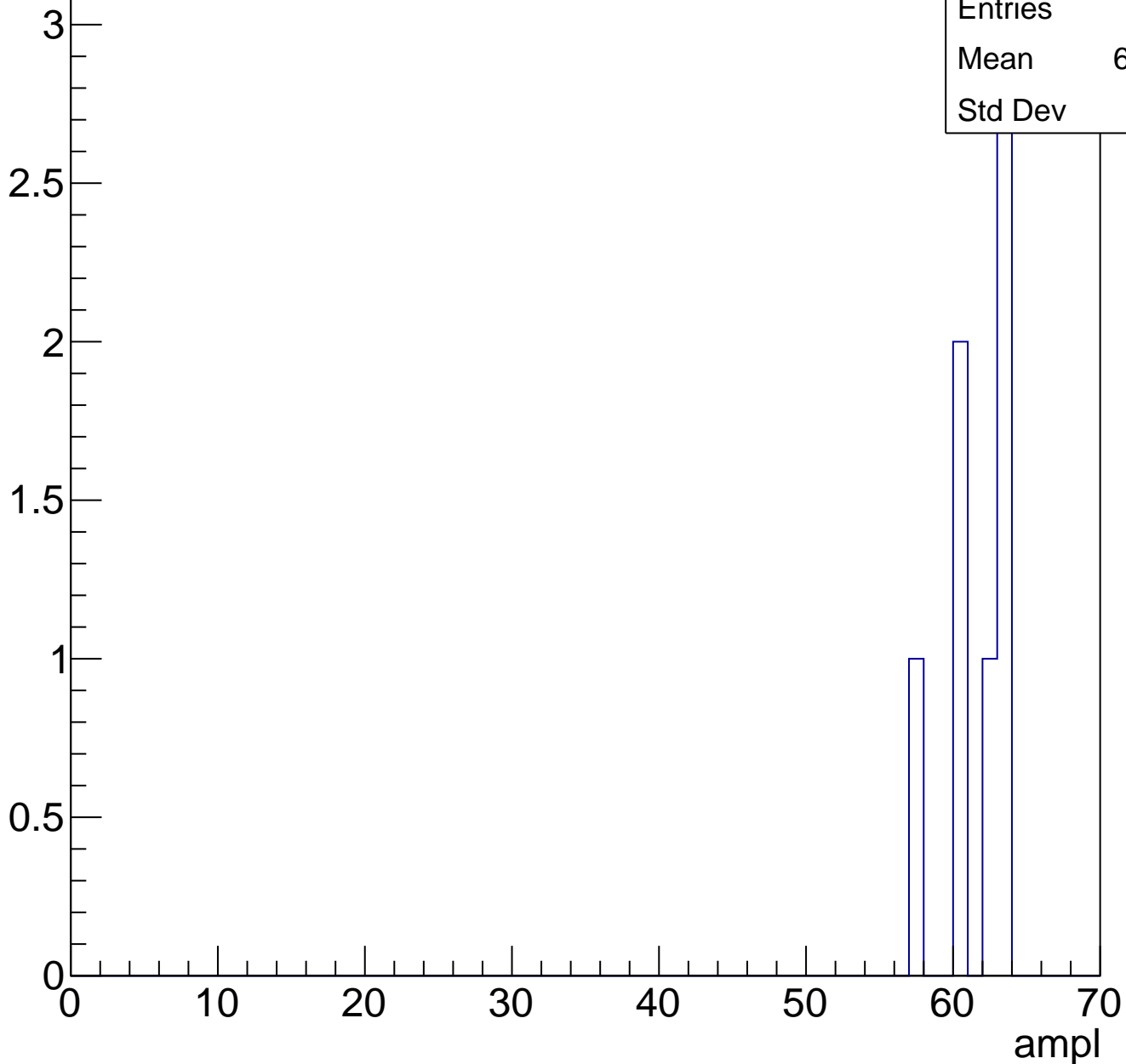
ampl



B0L101S, U22-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

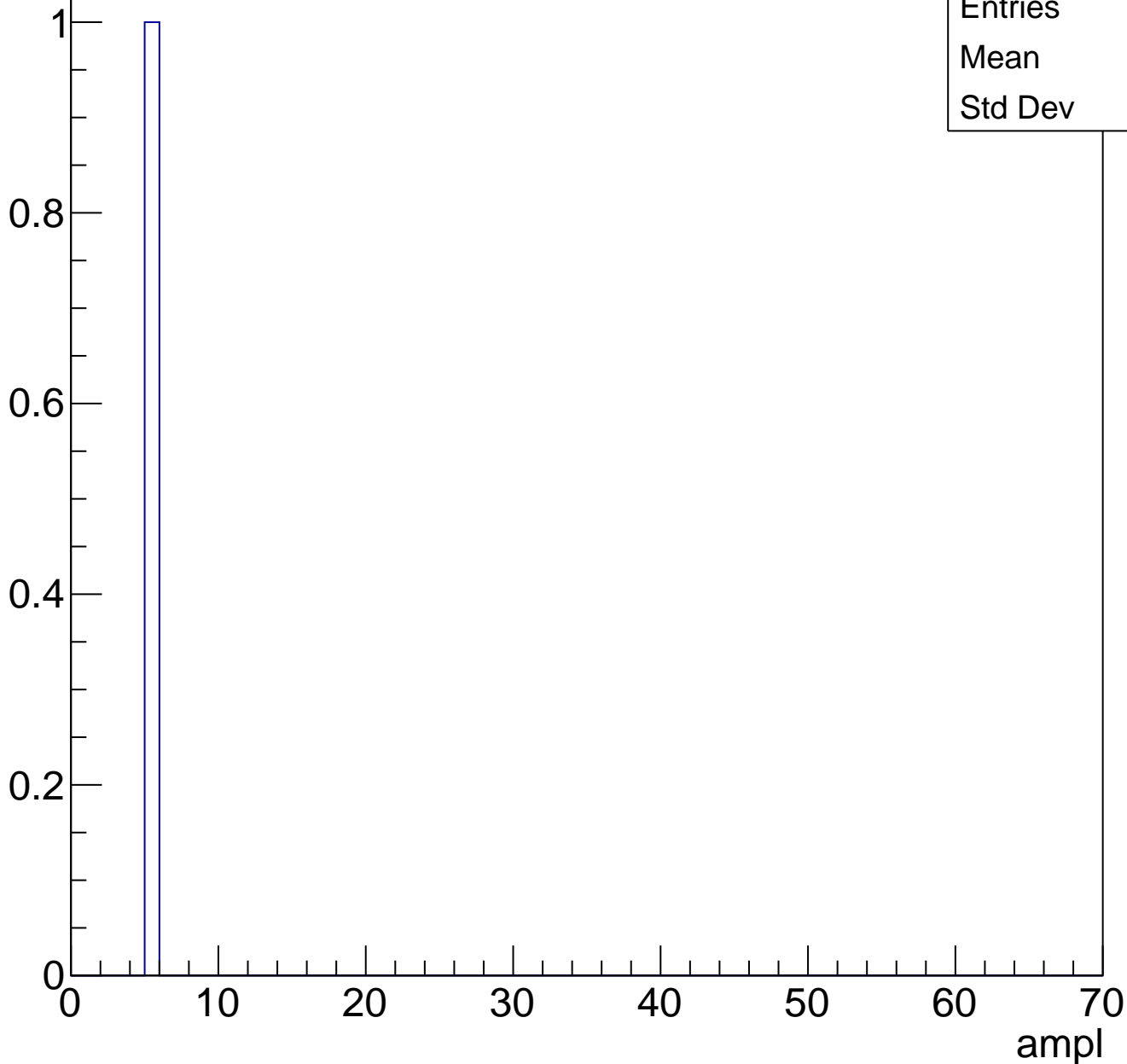
Entry



B0L101S, U22-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 30.55 |
| Std Dev | 3.602 |

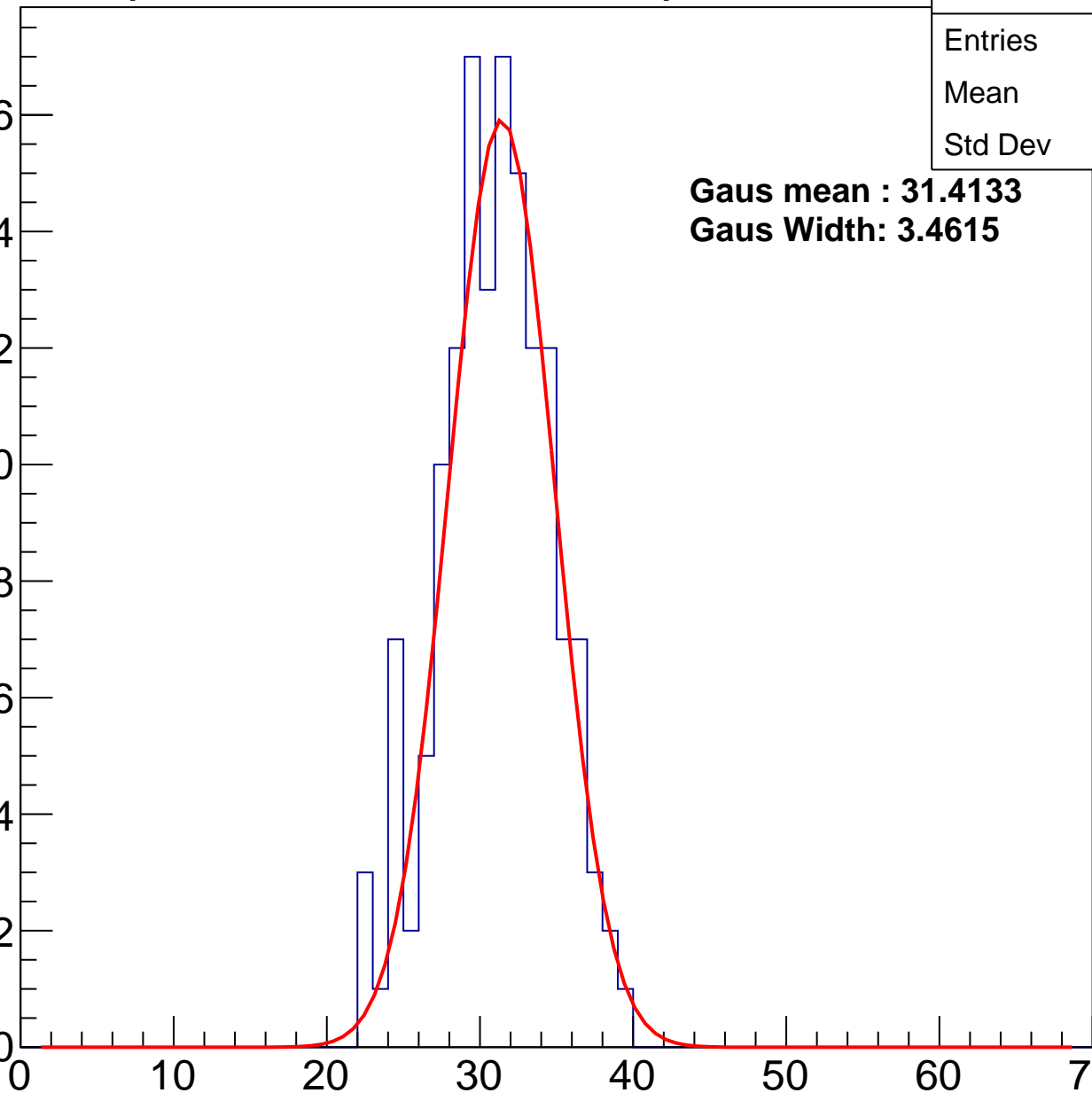
Gaus mean : 31.4133

Gaus Width: 3.4615

Entry

16
14
12
10
8
6
4
2
0

ampl

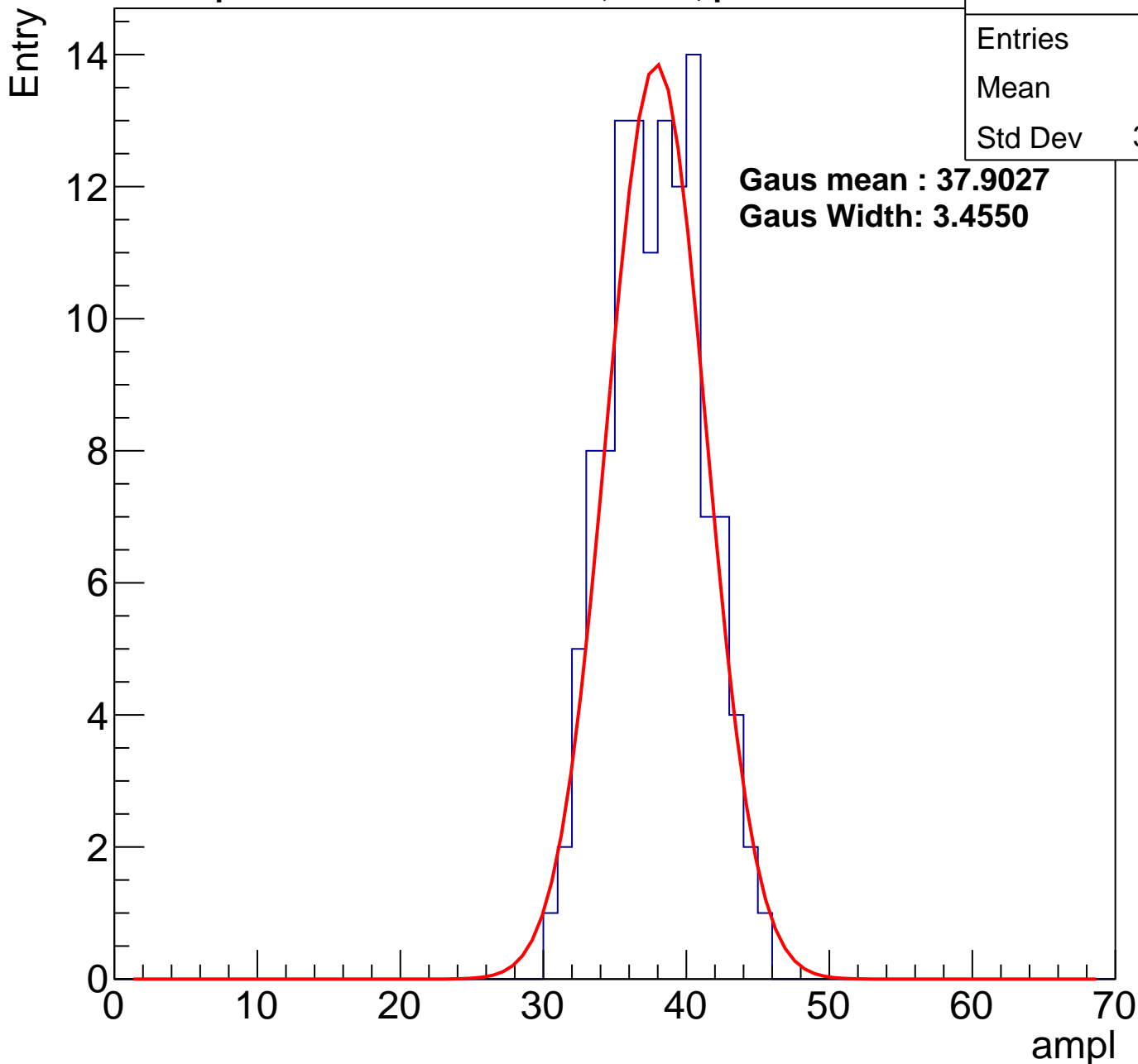


B0L101S, U22-ch63, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 121 |
| Mean | 37.4 |
| Std Dev | 3.241 |

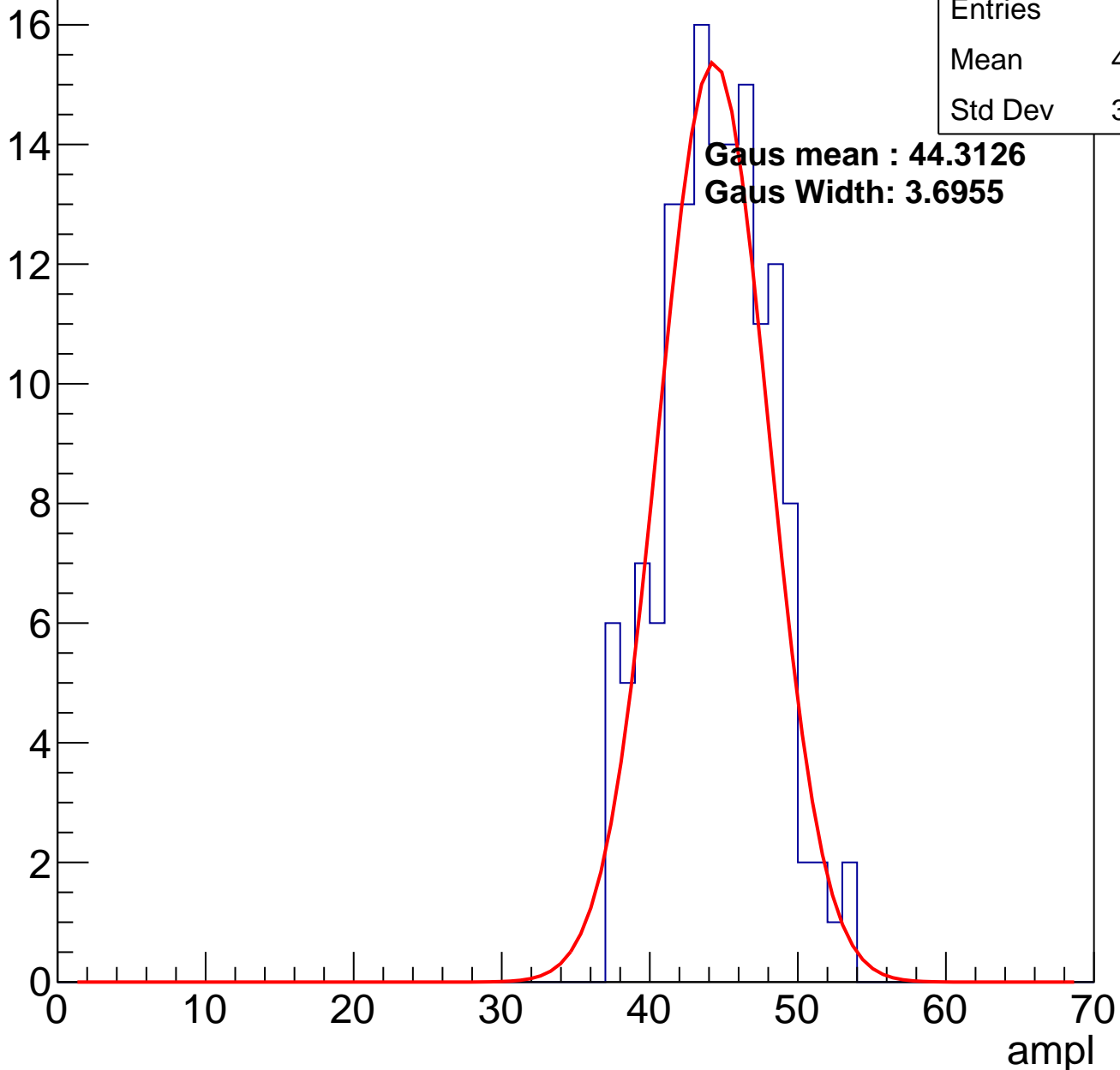
Gaus mean : 37.9027
Gaus Width: 3.4550



B0L101S, U22-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch63, adc3

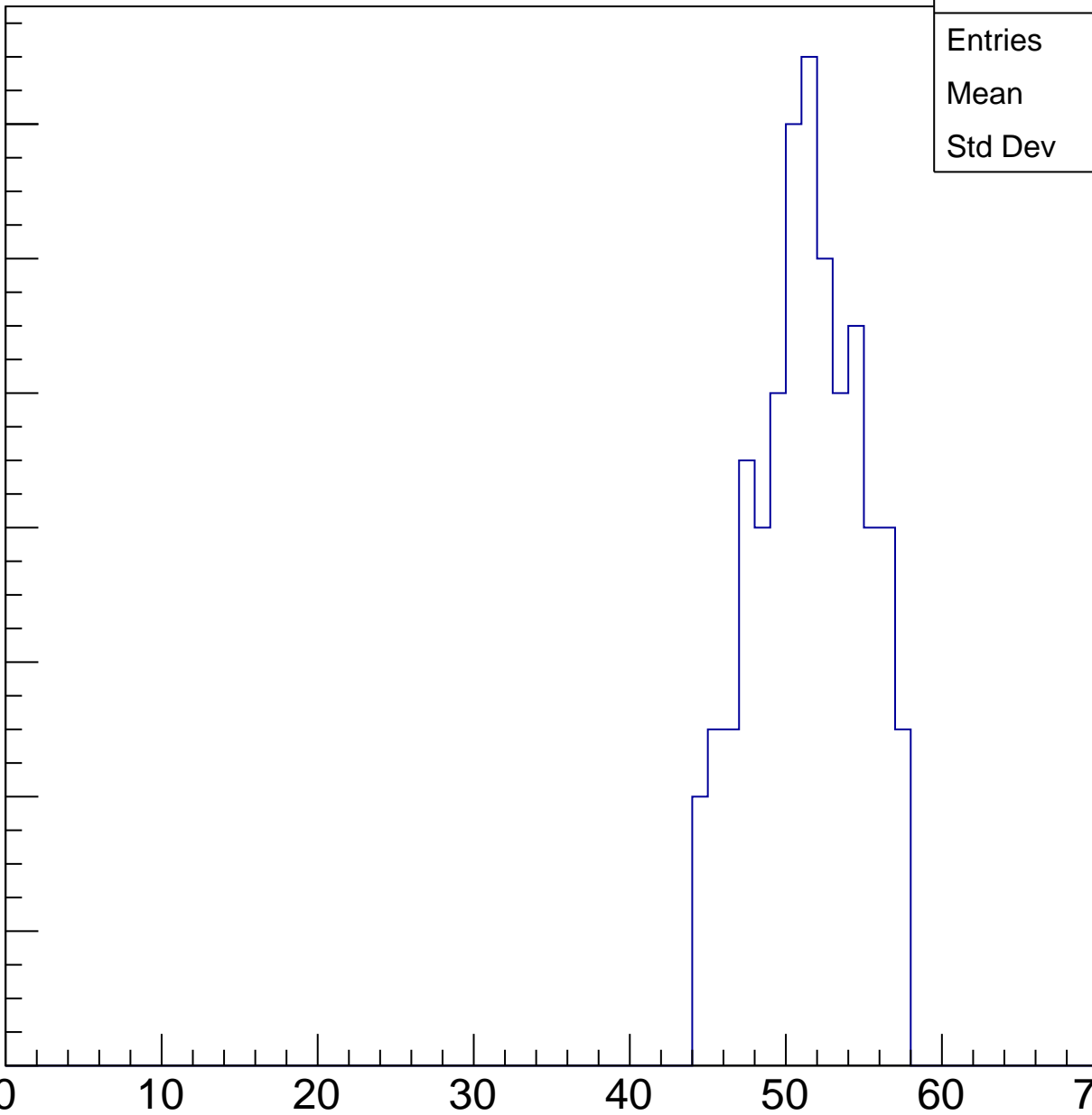
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 124 |
| Mean | 50.92 |
| Std Dev | 3.395 |

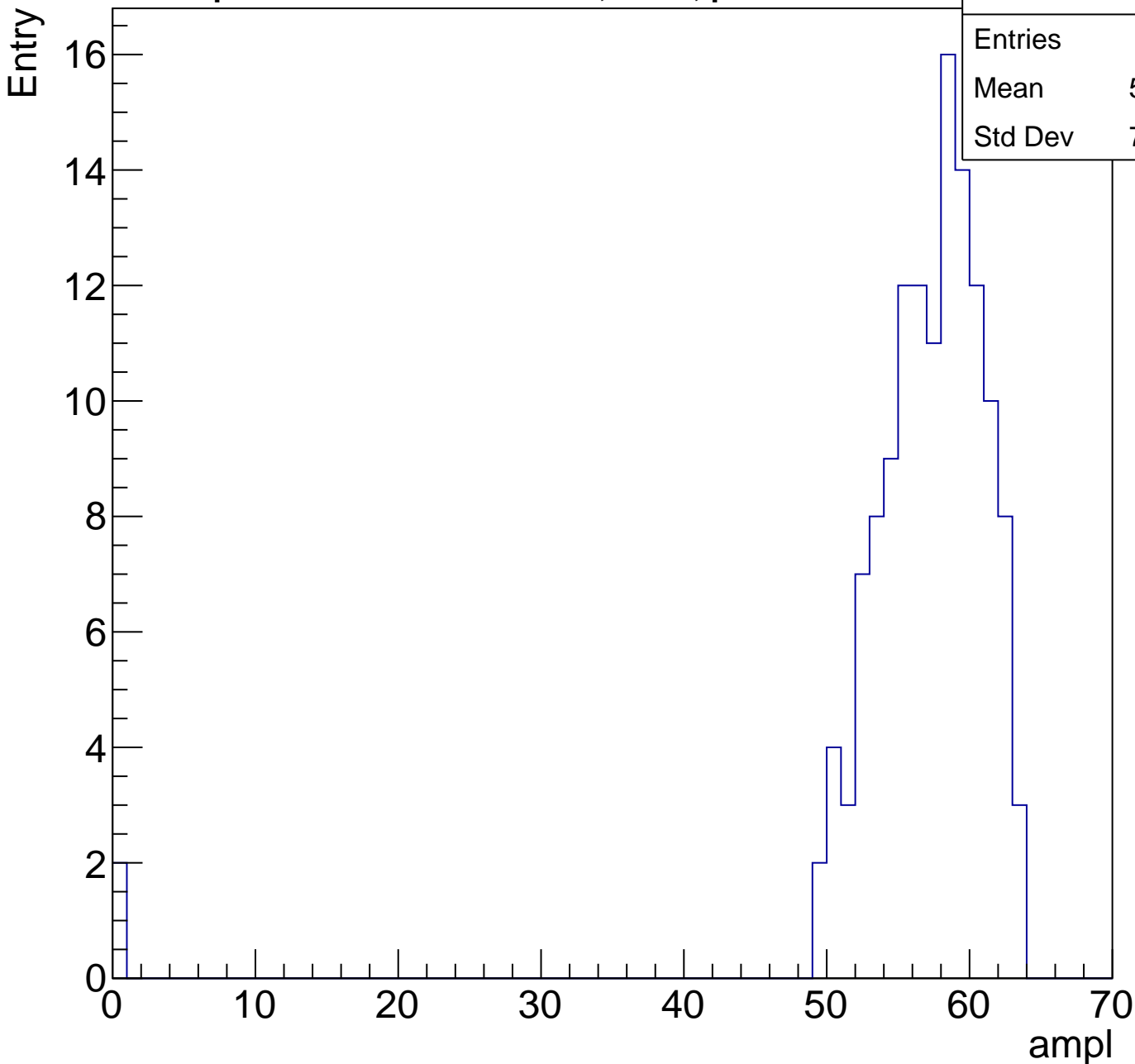
ampl



B0L101S, U22-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 56.04 |
| Std Dev | 7.706 |



B0L101S, U22-ch63, adc5

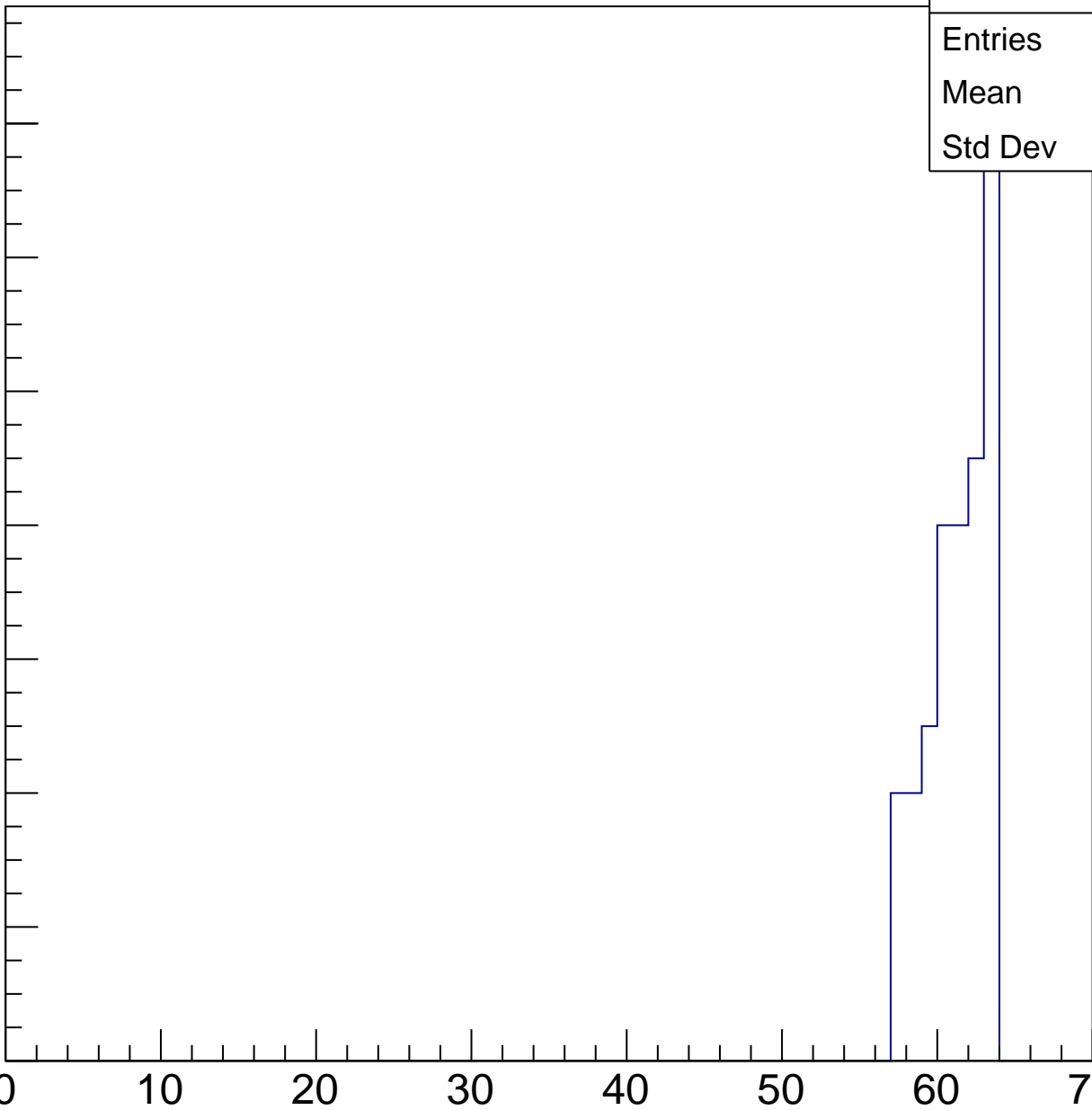
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 60.87 |
| Std Dev | 1.923 |

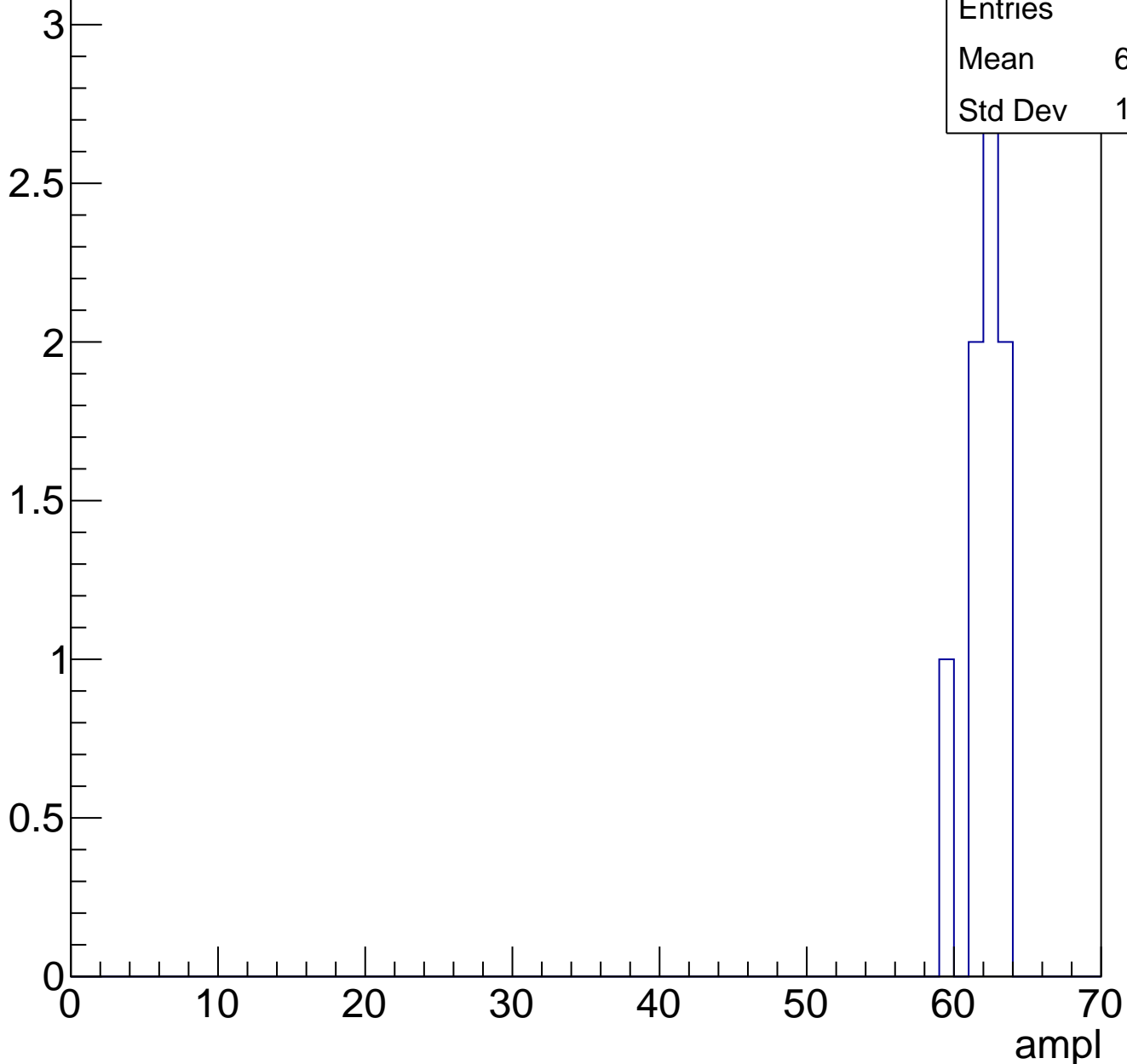
ampl



B0L101S, U22-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 129 |
| Mean | 30 |
| Std Dev | 3.675 |

Gaus mean : 30.4830

Gaus Width: 3.6468

Entry

12

10

8

6

4

2

0

0

10

20

30

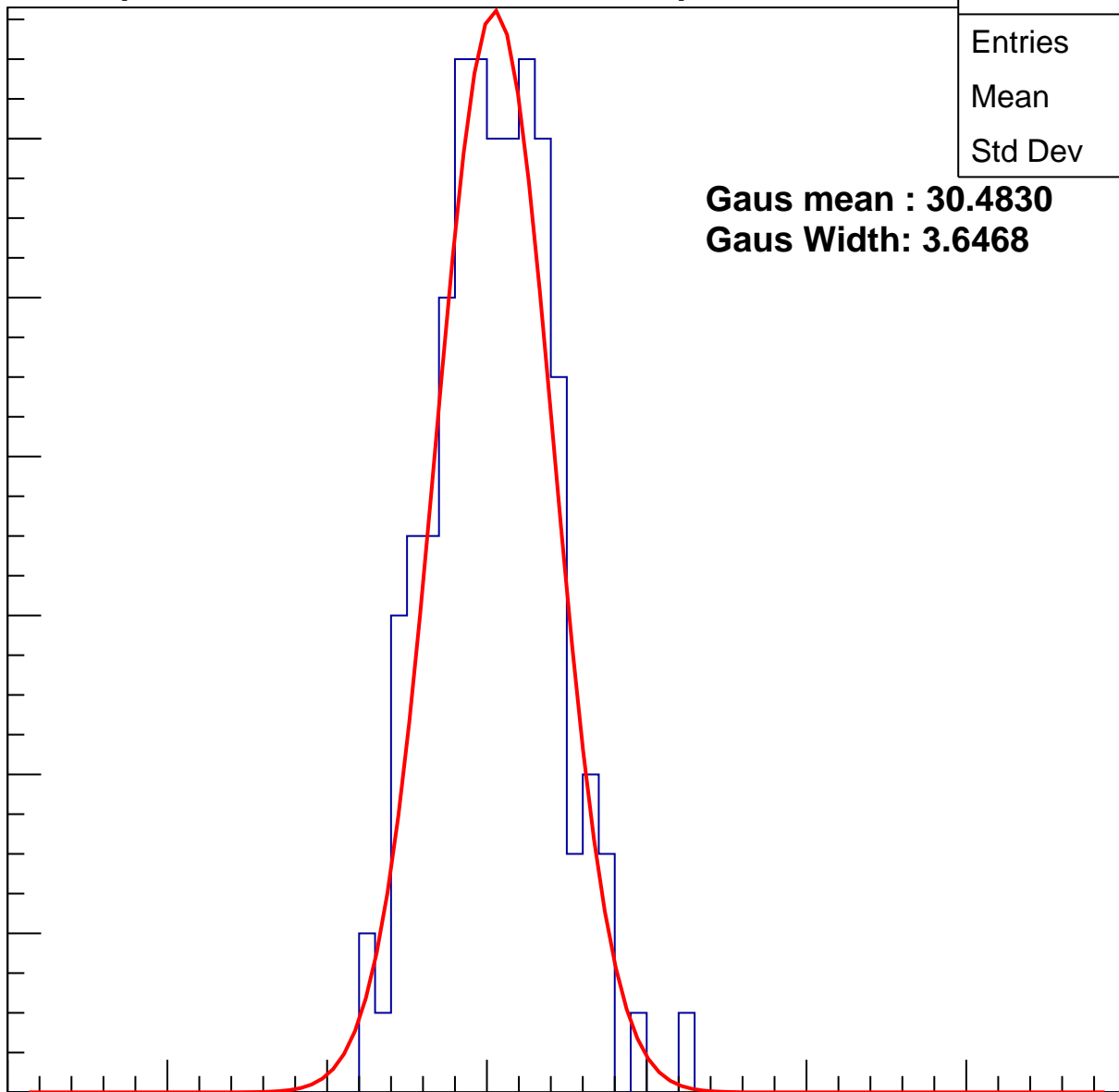
40

50

60

70

ampl



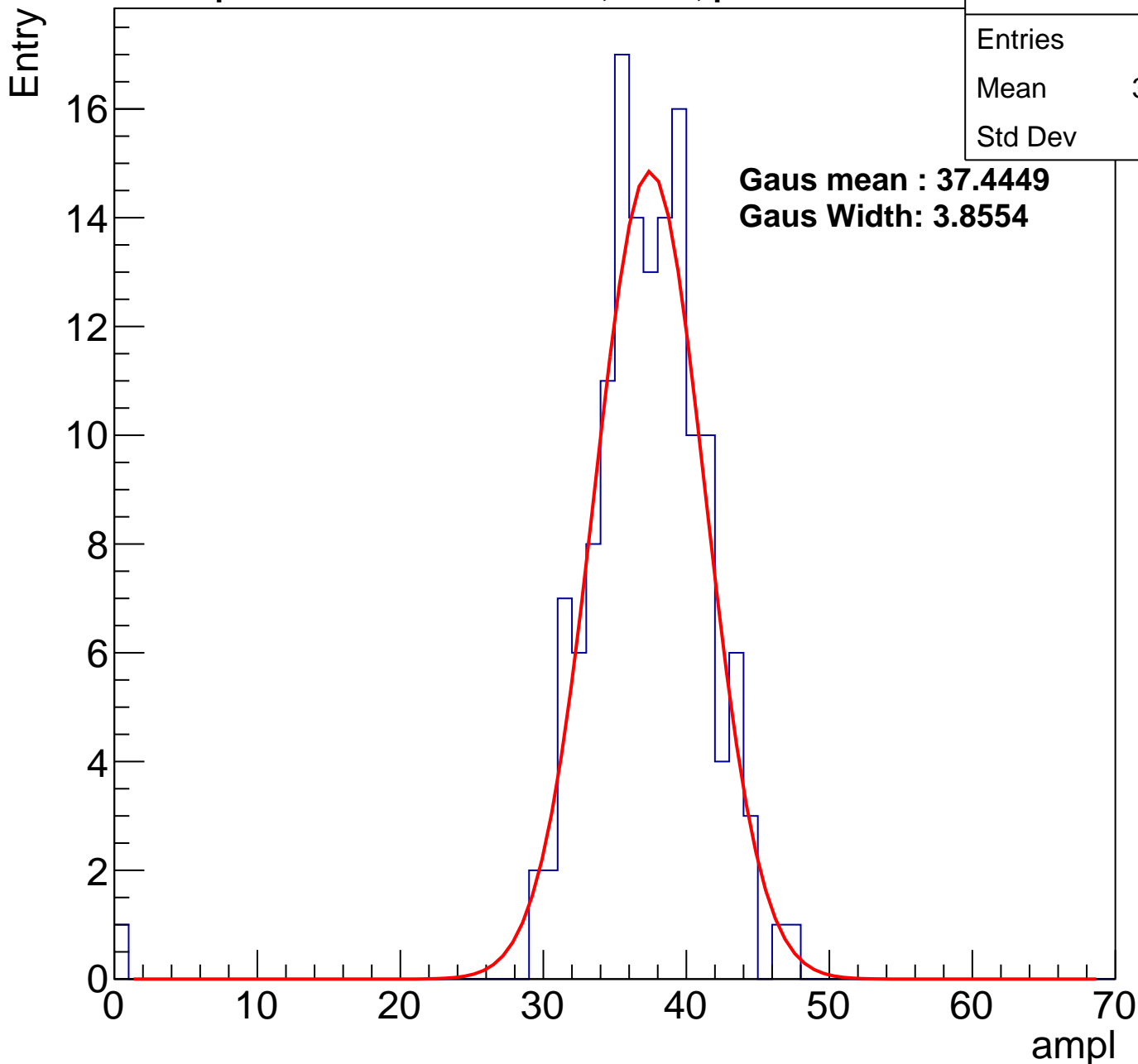
B0L101S, U22-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 36.73 |
| Std Dev | 4.73 |

Gaus mean : 37.4449

Gaus Width: 3.8554



B0L101S, U22-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

124

Mean

43.52

Std Dev

3.423

Gaus mean : 44.1115

Gaus Width: 3.5408

0

10

20

30

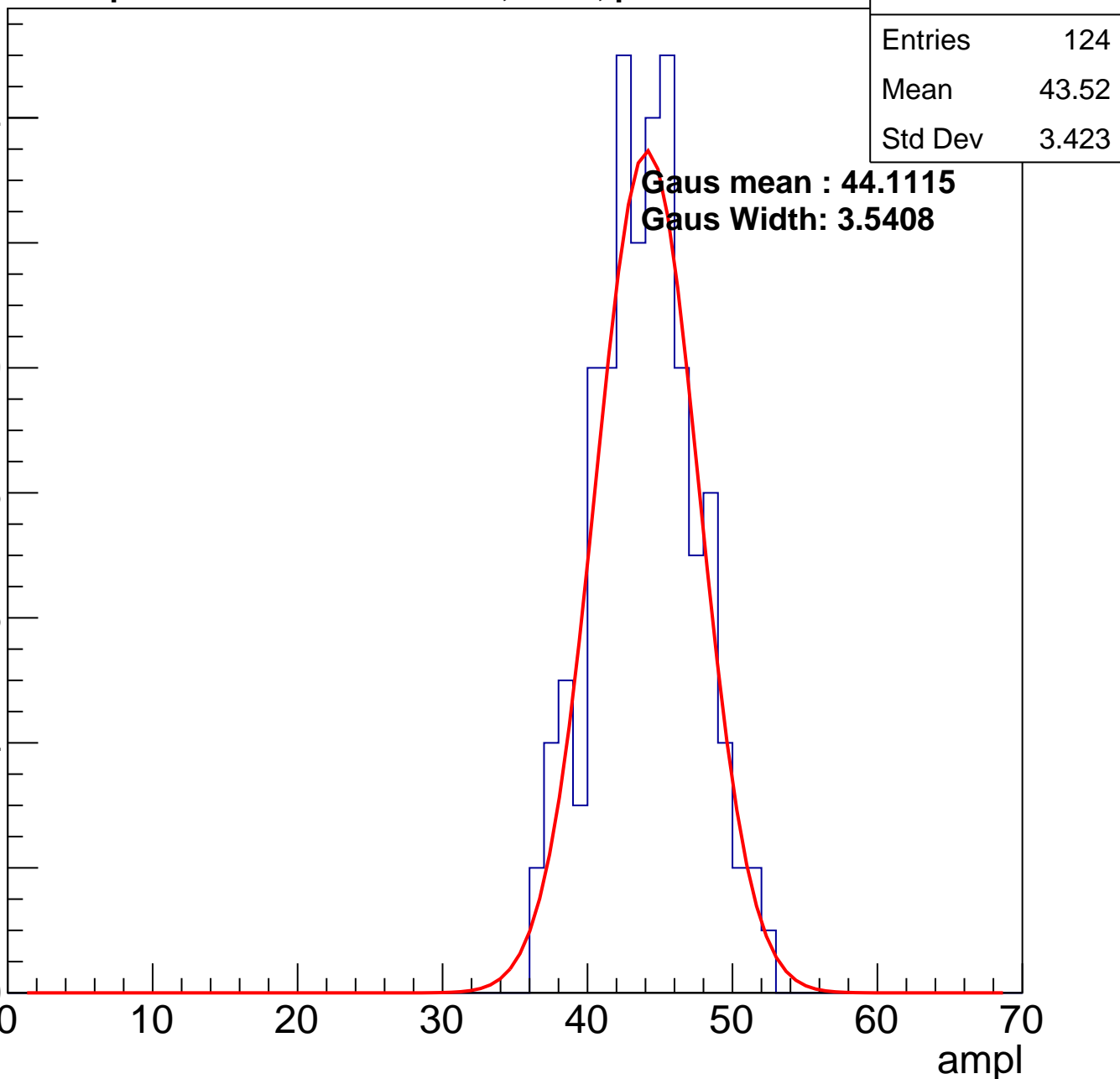
40

50

60

70

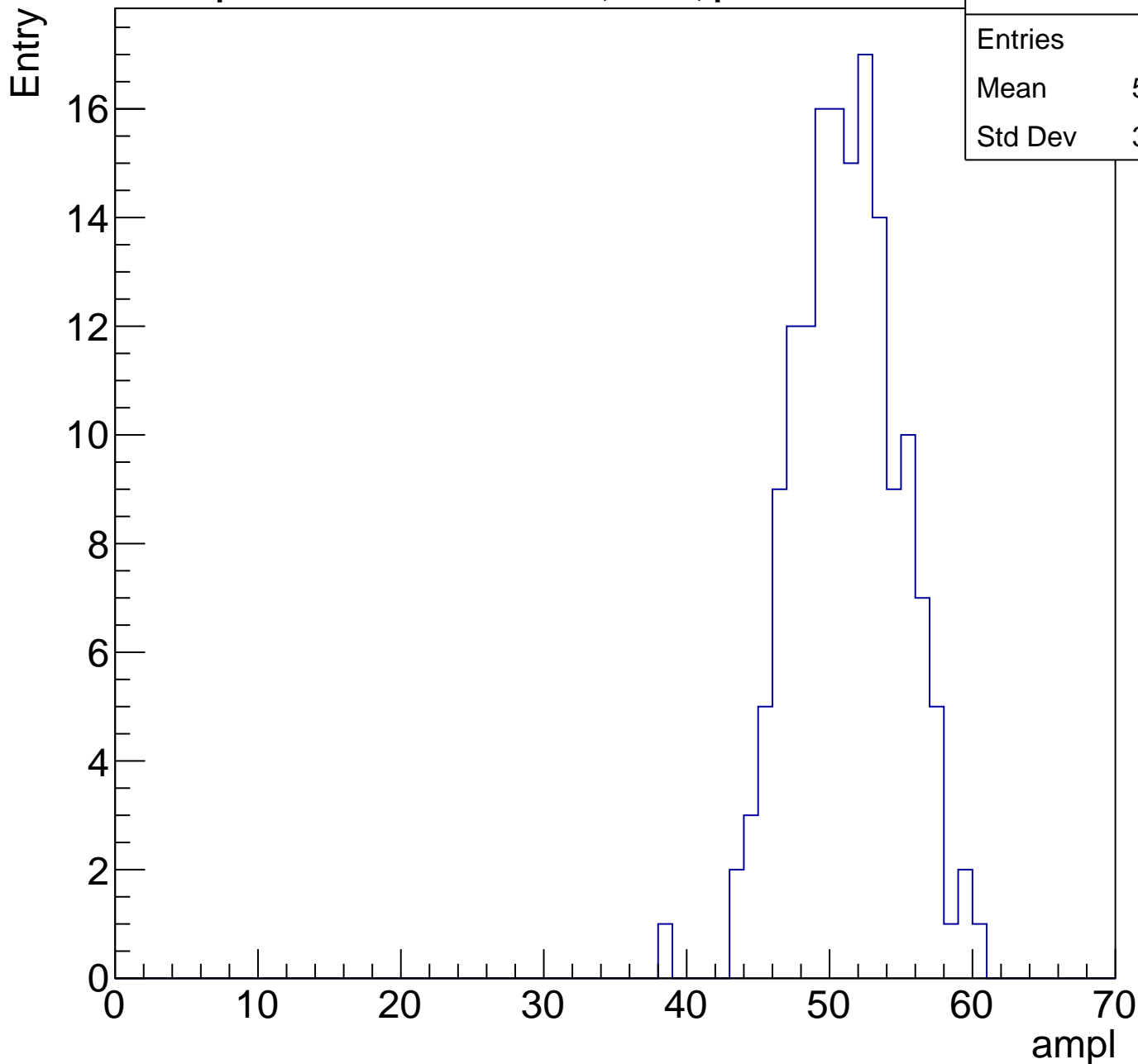
ampl



B0L101S, U22-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

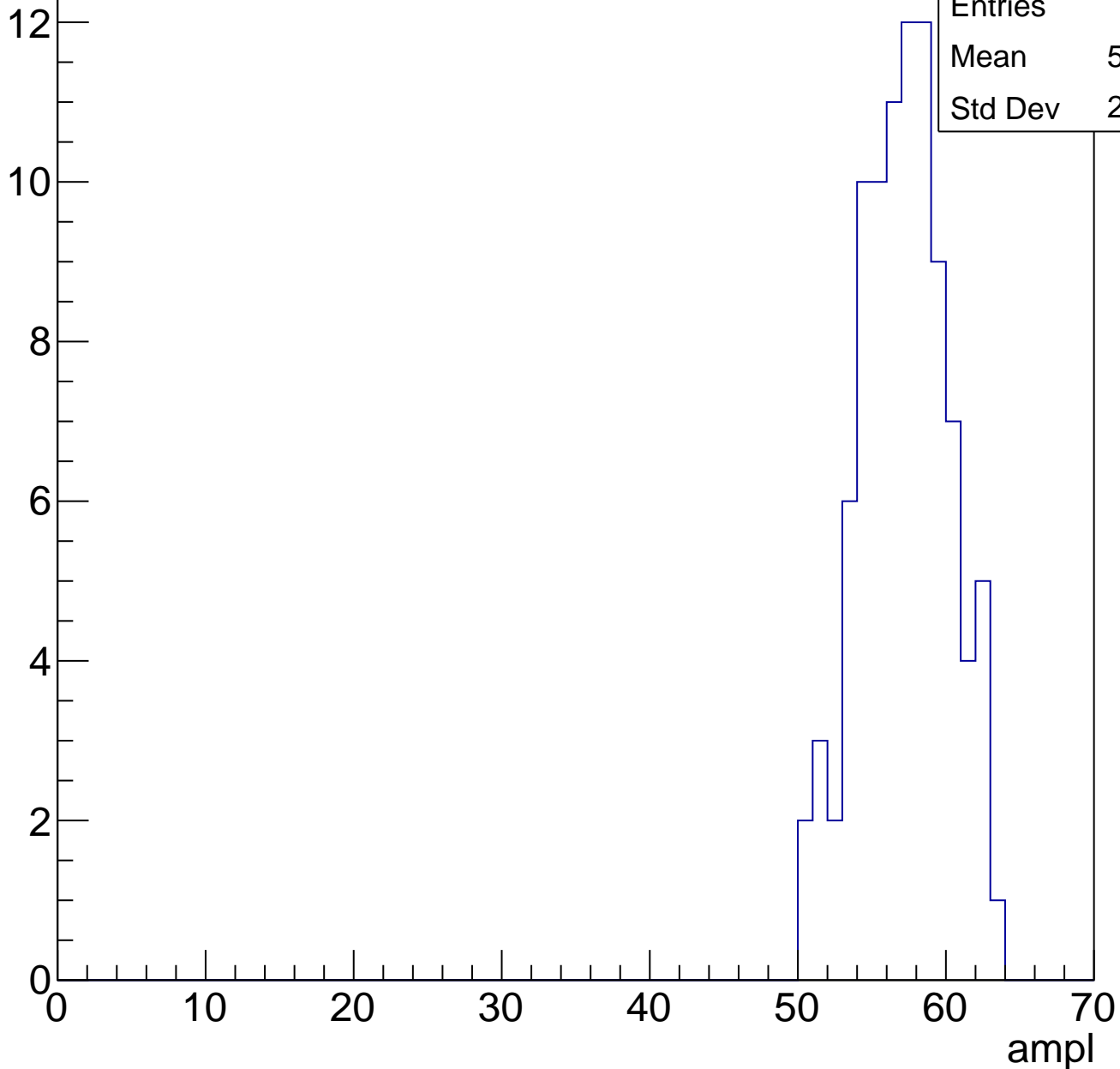
| | |
|---------|-------|
| Entries | 157 |
| Mean | 50.69 |
| Std Dev | 3.693 |



B0L101S, U22-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch64, adc5

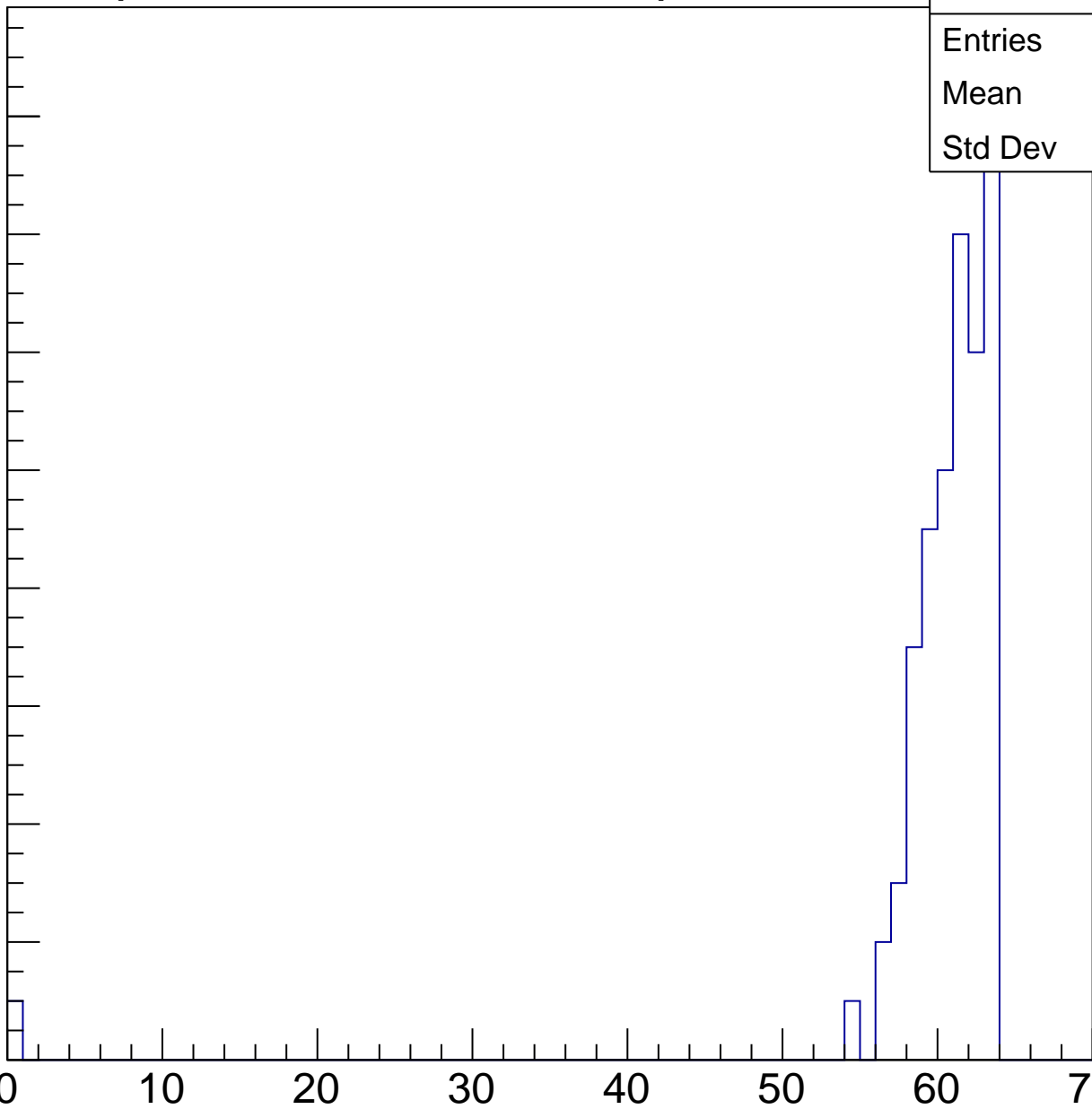
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 59.78 |
| Std Dev | 7.203 |

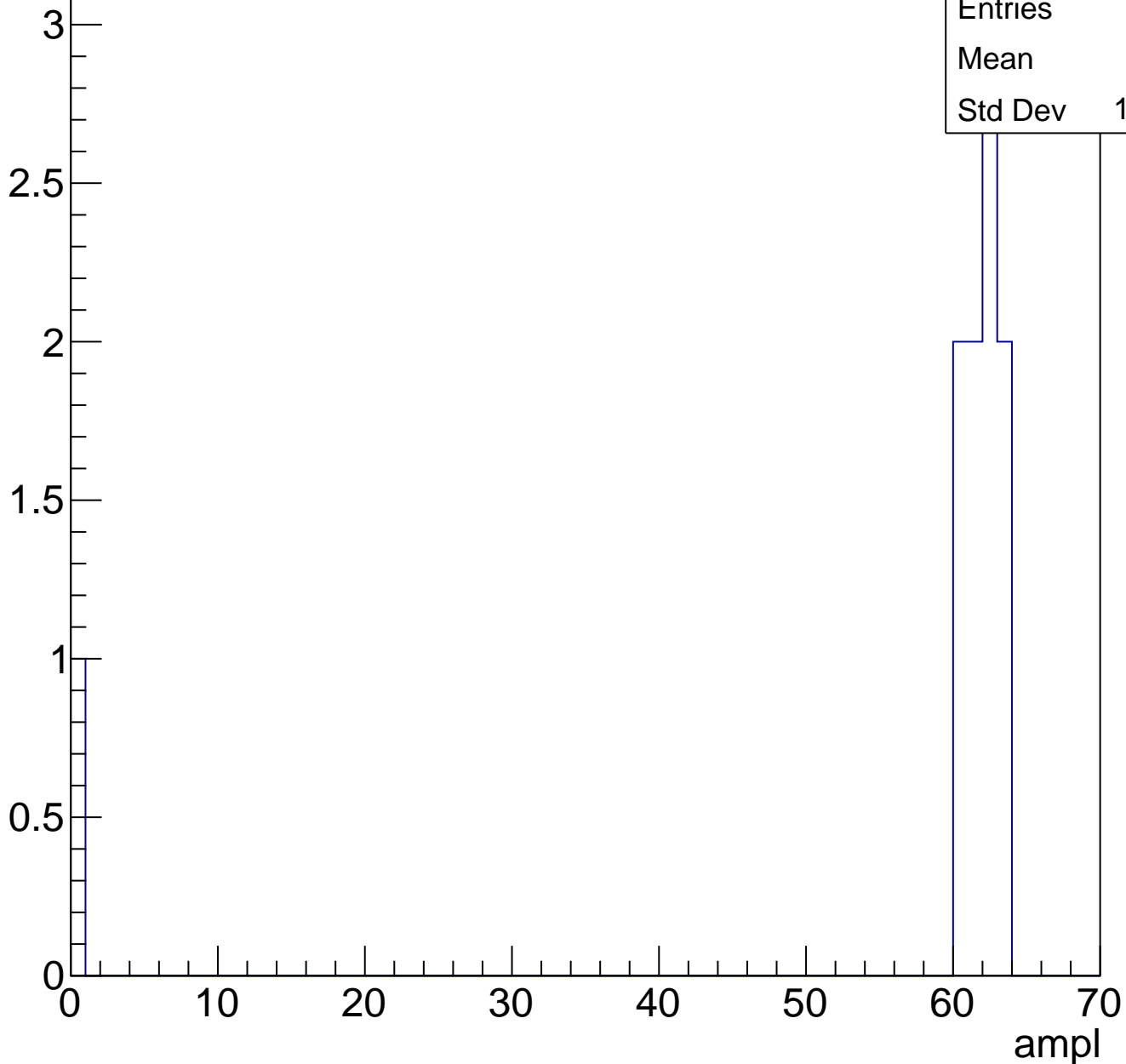
ampl



B0L101S, U22-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

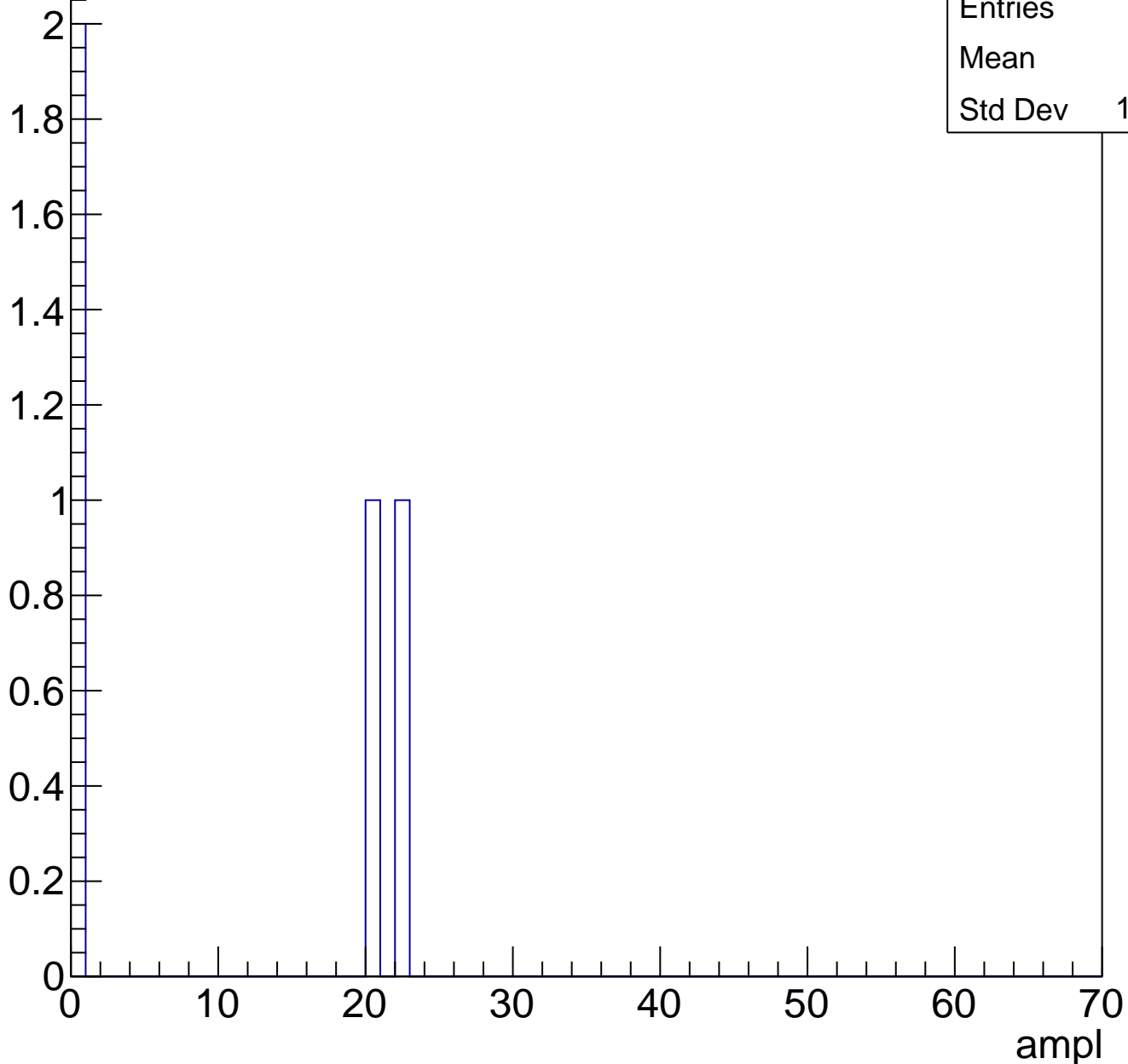
Entry



B0L101S, U22-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 10.5 |
| Std Dev | 10.52 |

B0L101S, U22-ch65, adc0

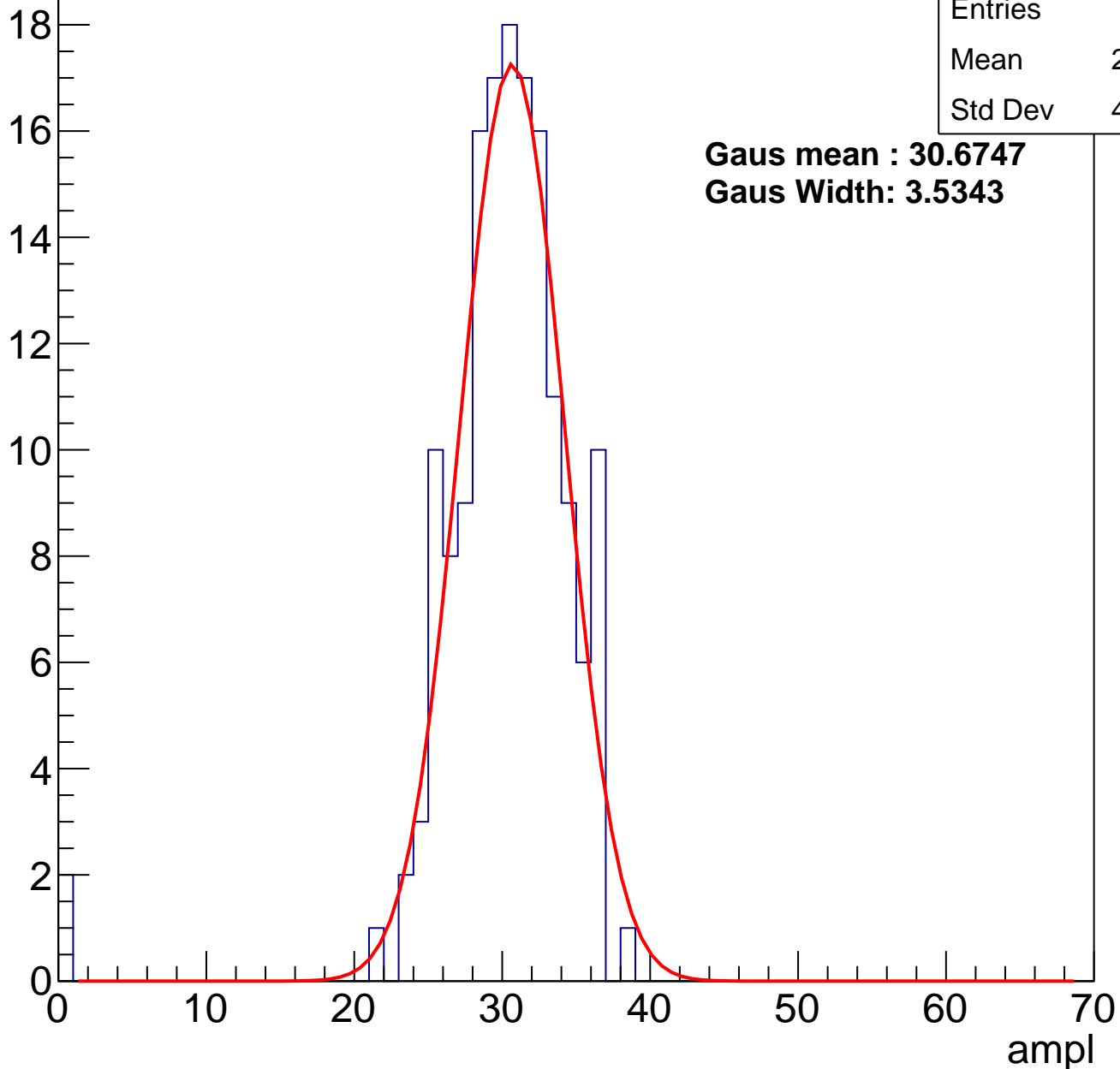
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 156 |
| Mean | 29.72 |
| Std Dev | 4.746 |

Gaus mean : 30.6747

Gaus Width: 3.5343

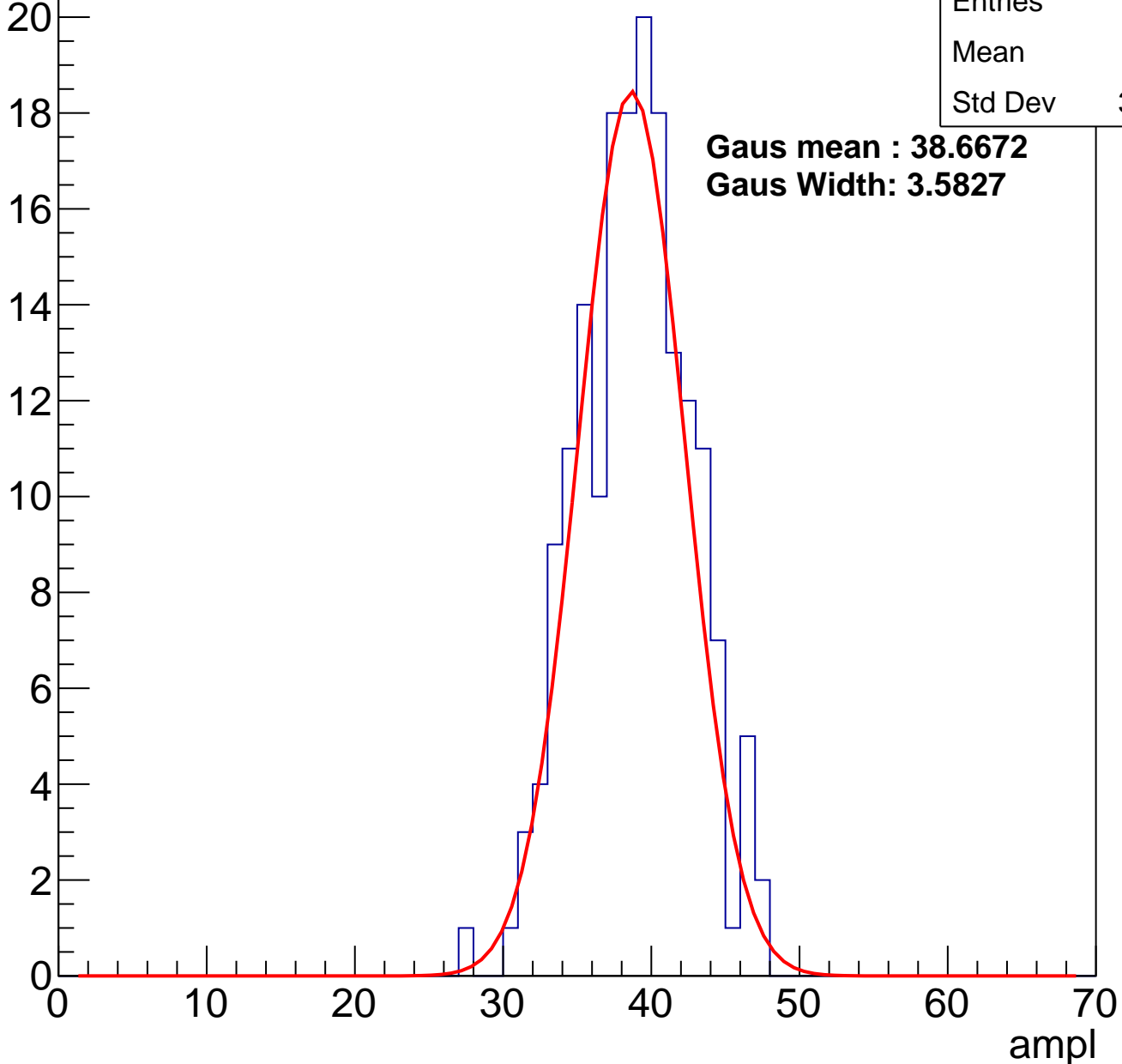
Entry



B0L101S, U22-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch65, adc2

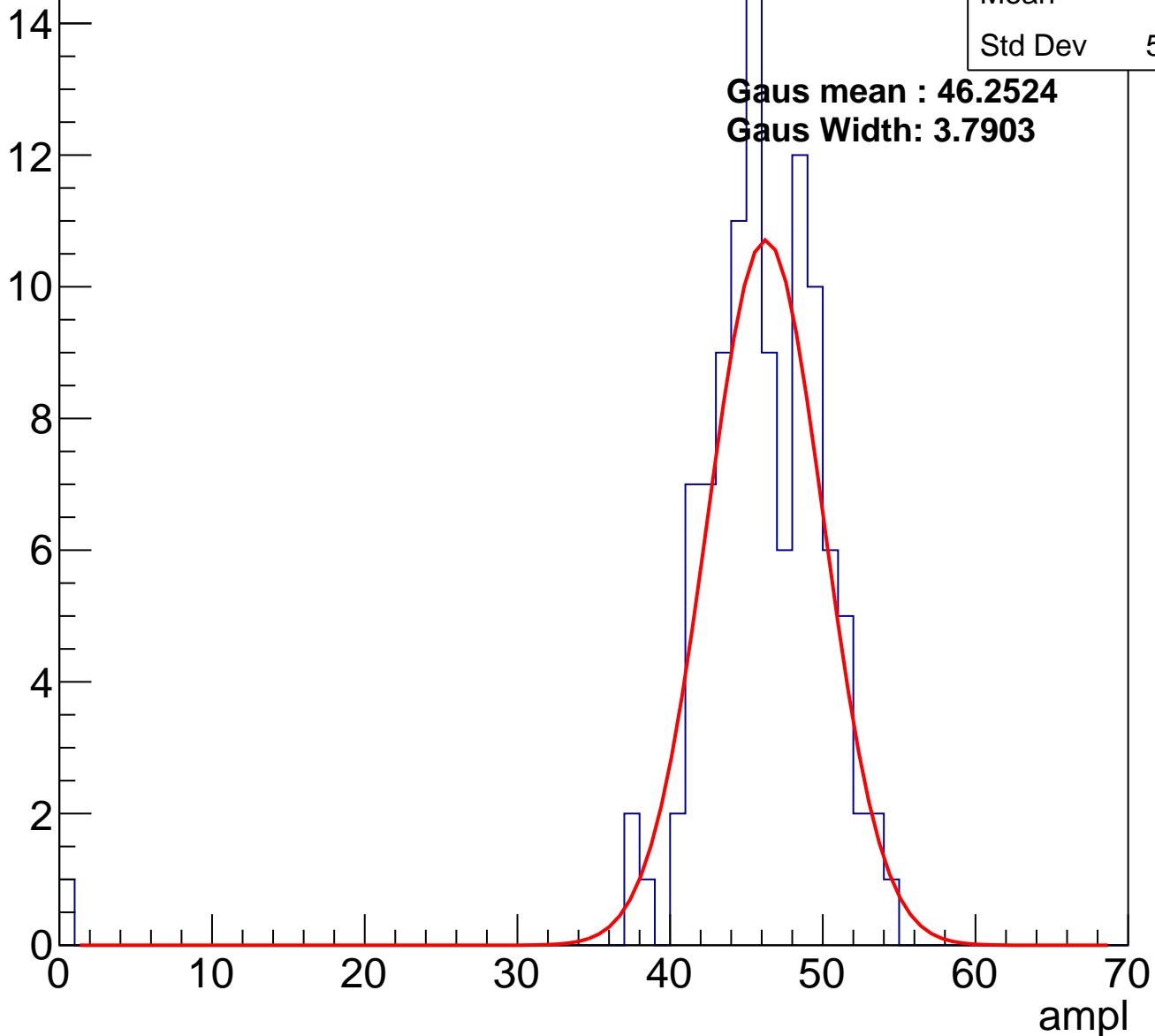
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 108 |
| Mean | 45.37 |
| Std Dev | 5.609 |

Gaus mean : 46.2524

Gaus Width: 3.7903

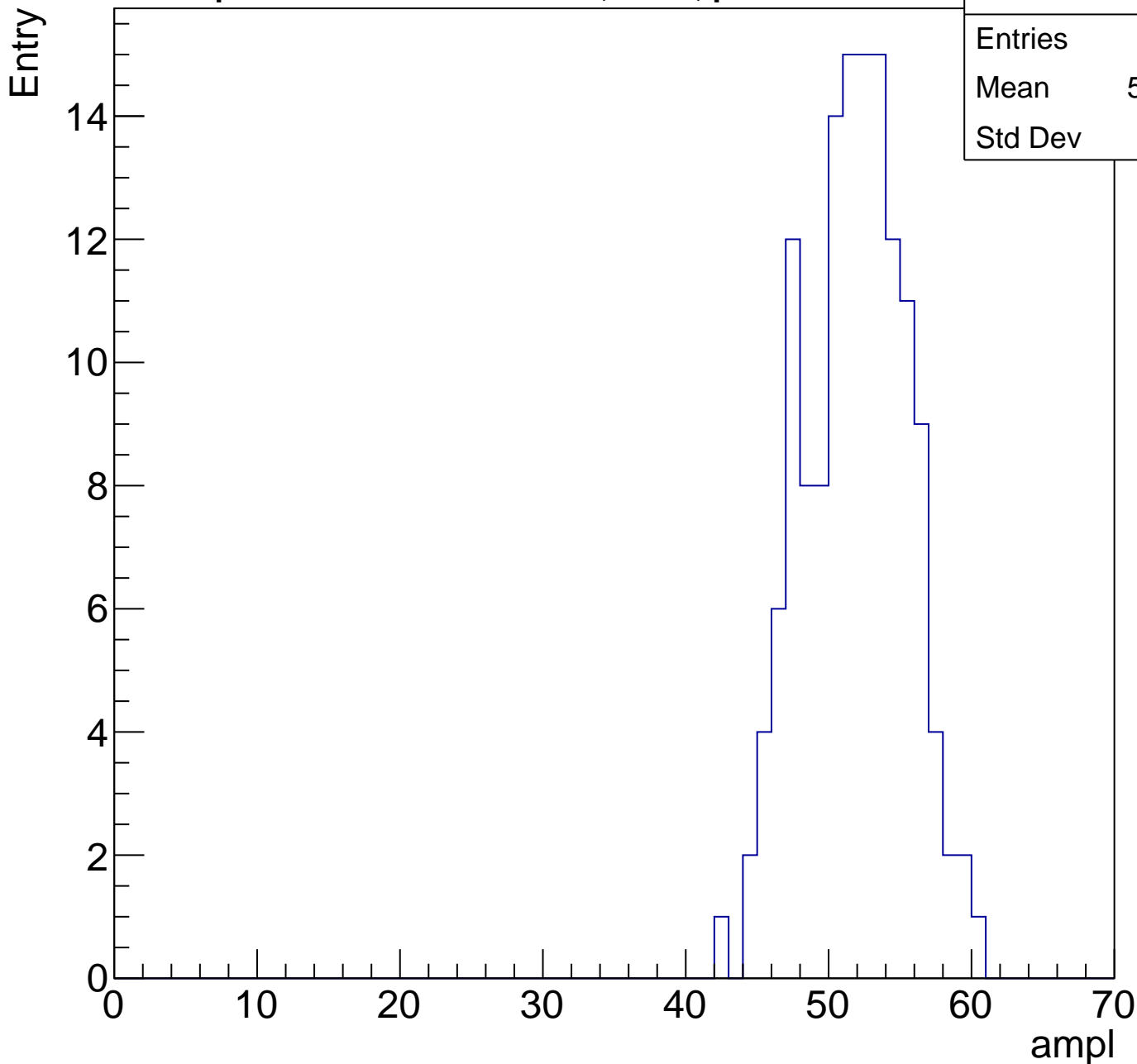
Entry



B0L101S, U22-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 51.38 |
| Std Dev | 3.57 |



B0L101S, U22-ch65, adc4

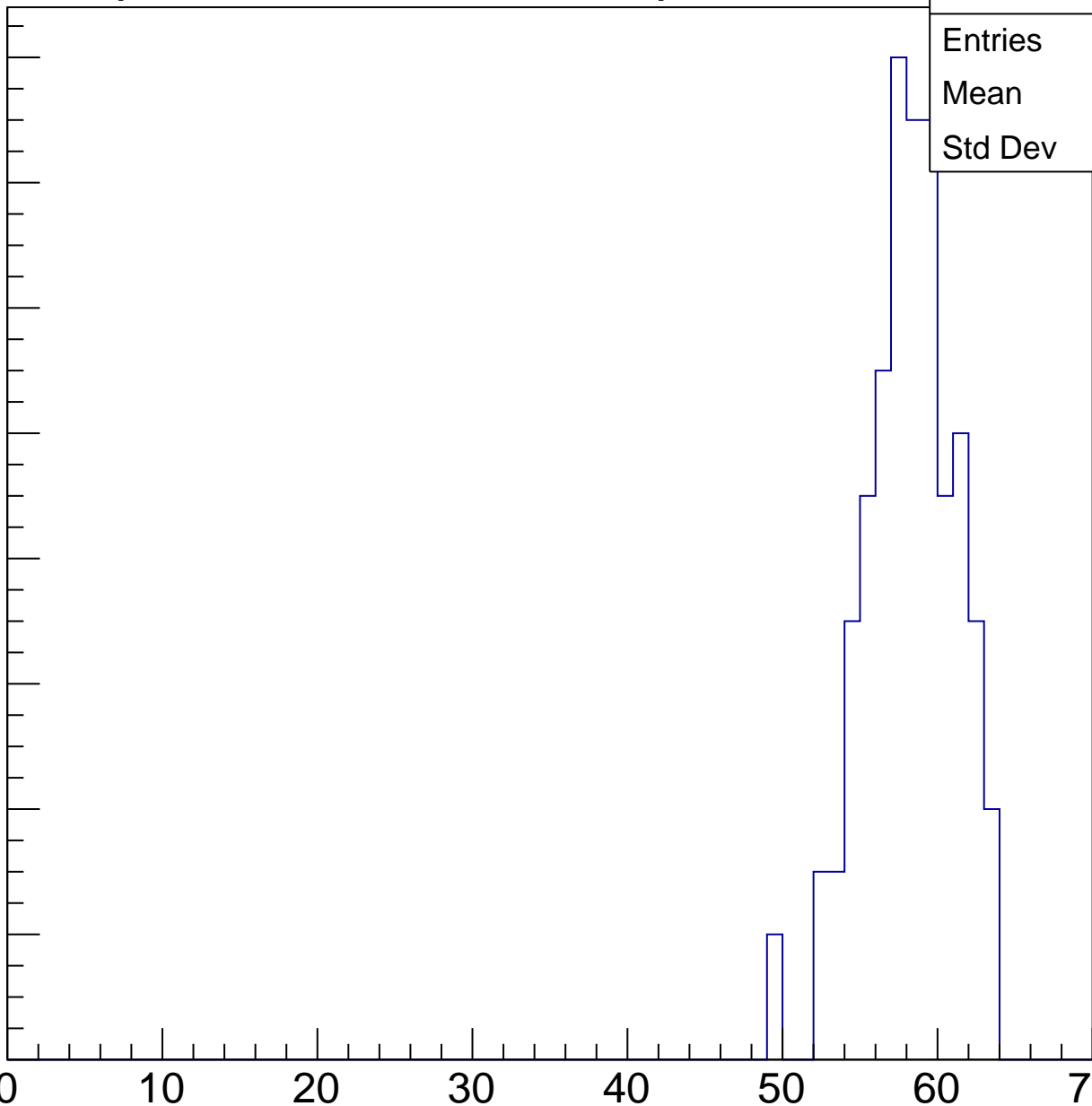
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 111 |
| Mean | 57.7 |
| Std Dev | 2.912 |

ampl



B0L101S, U22-ch65, adc5

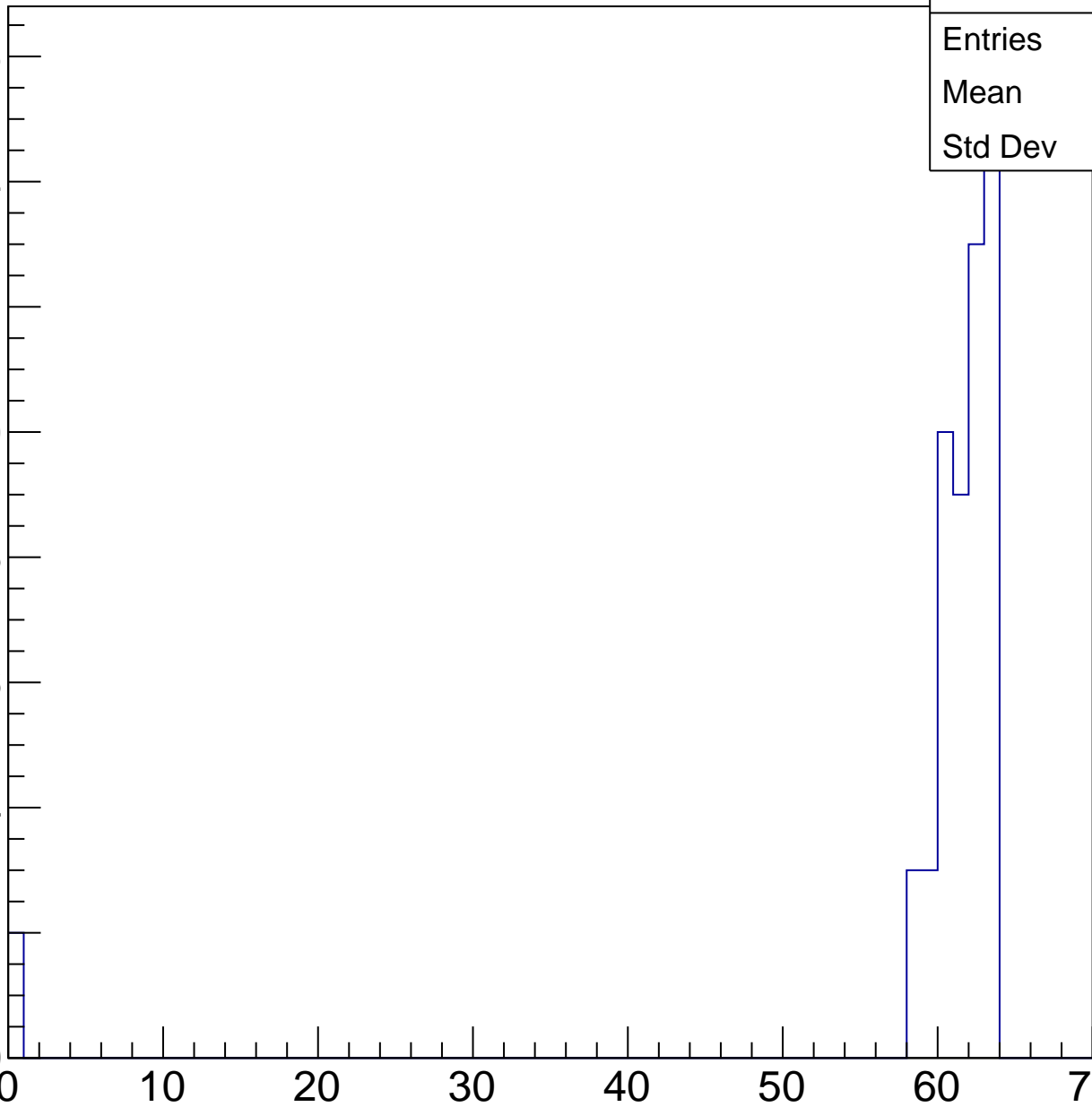
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.18 |
| Std Dev | 11.48 |

ampl



B0L101S, U22-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch66, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 29.32 |
| Std Dev | 3.76 |

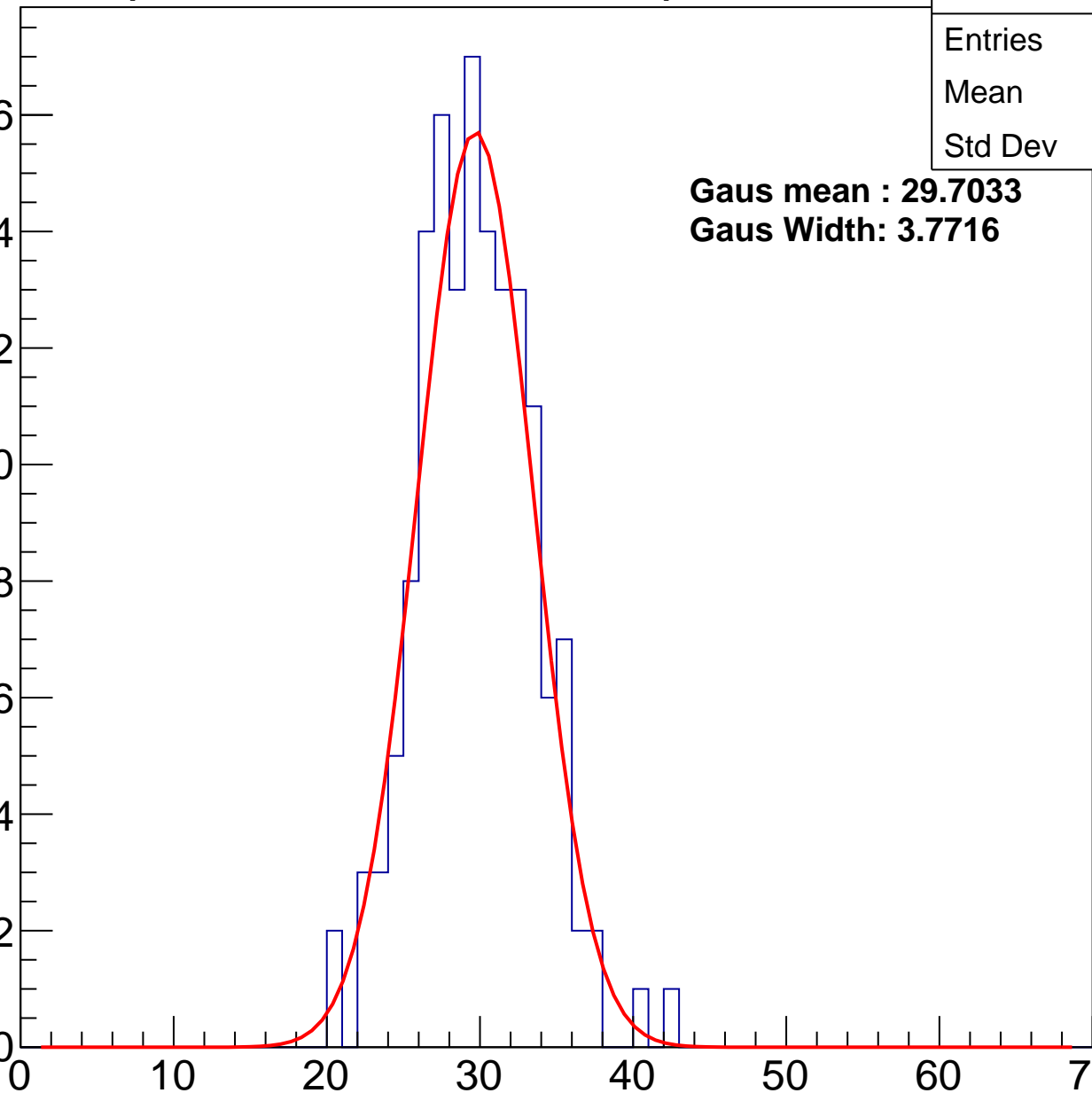
Gaus mean : 29.7033

Gaus Width: 3.7716

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch66, adc1

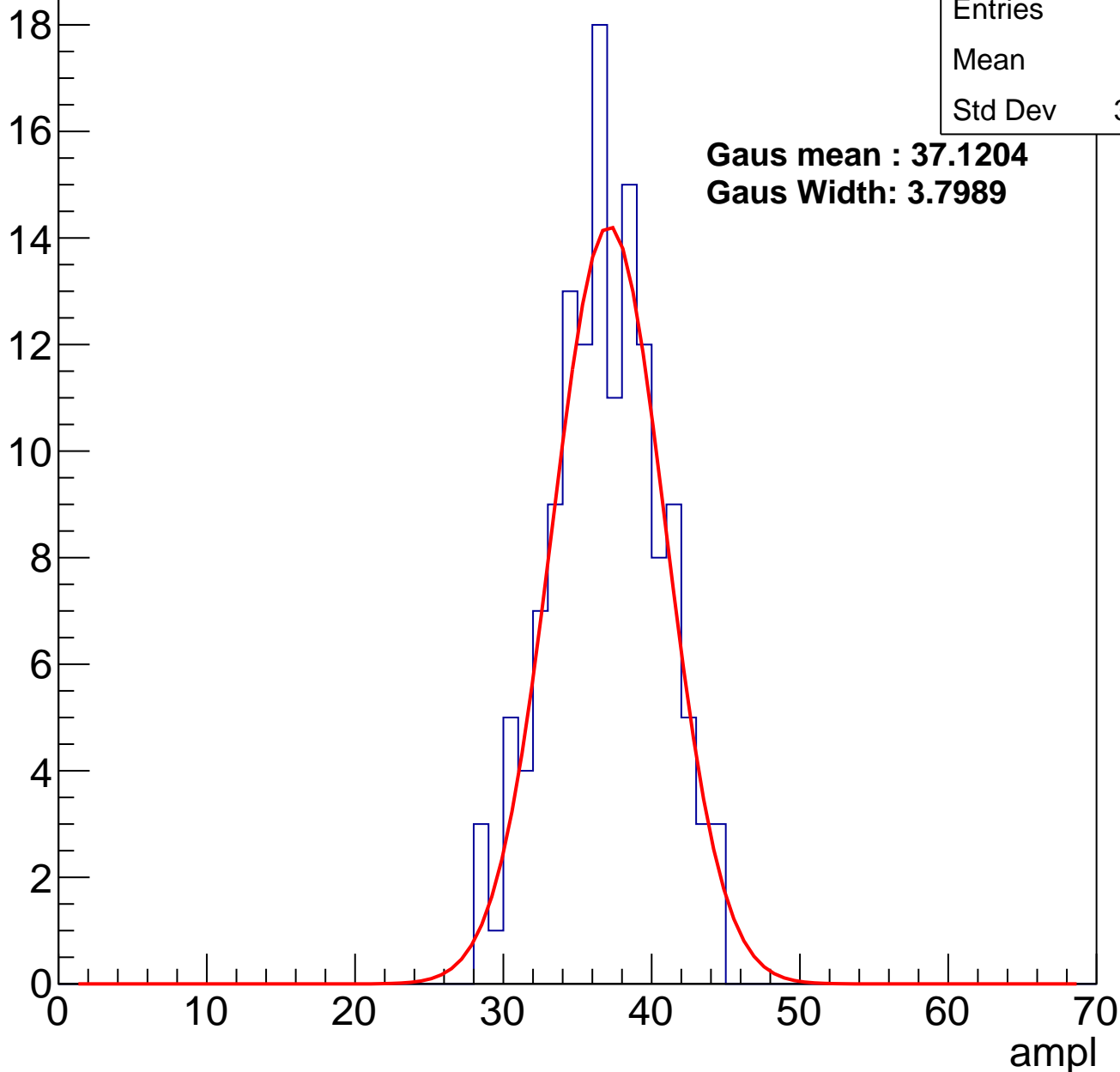
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 36.4 |
| Std Dev | 3.619 |

Gaus mean : 37.1204

Gaus Width: 3.7989

Entry



B0L101S, U22-ch66, adc2

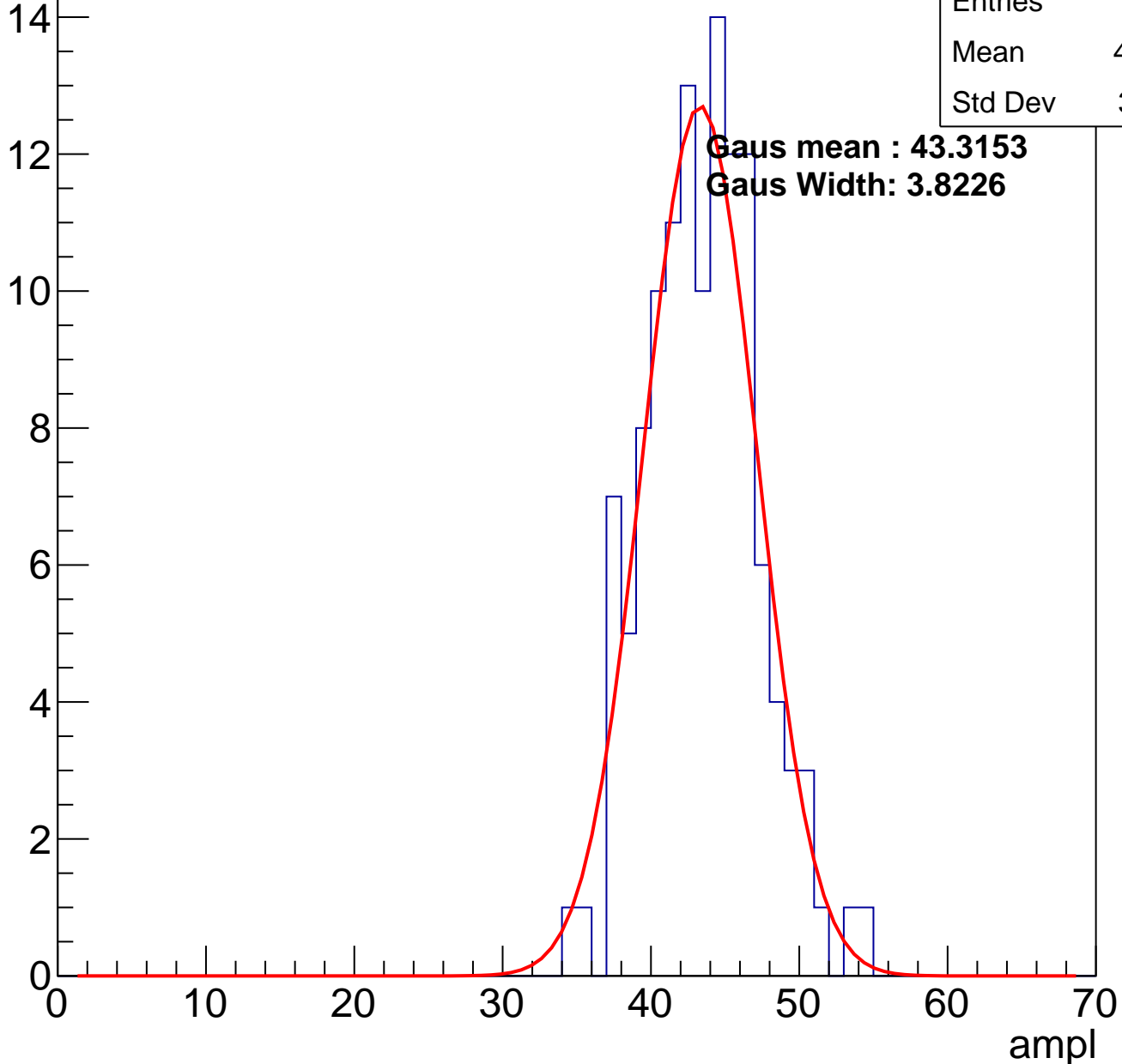
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 43.04 |
| Std Dev | 3.721 |

Gaus mean : 43.3153

Gaus Width: 3.8226

Entry



B0L101S, U22-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

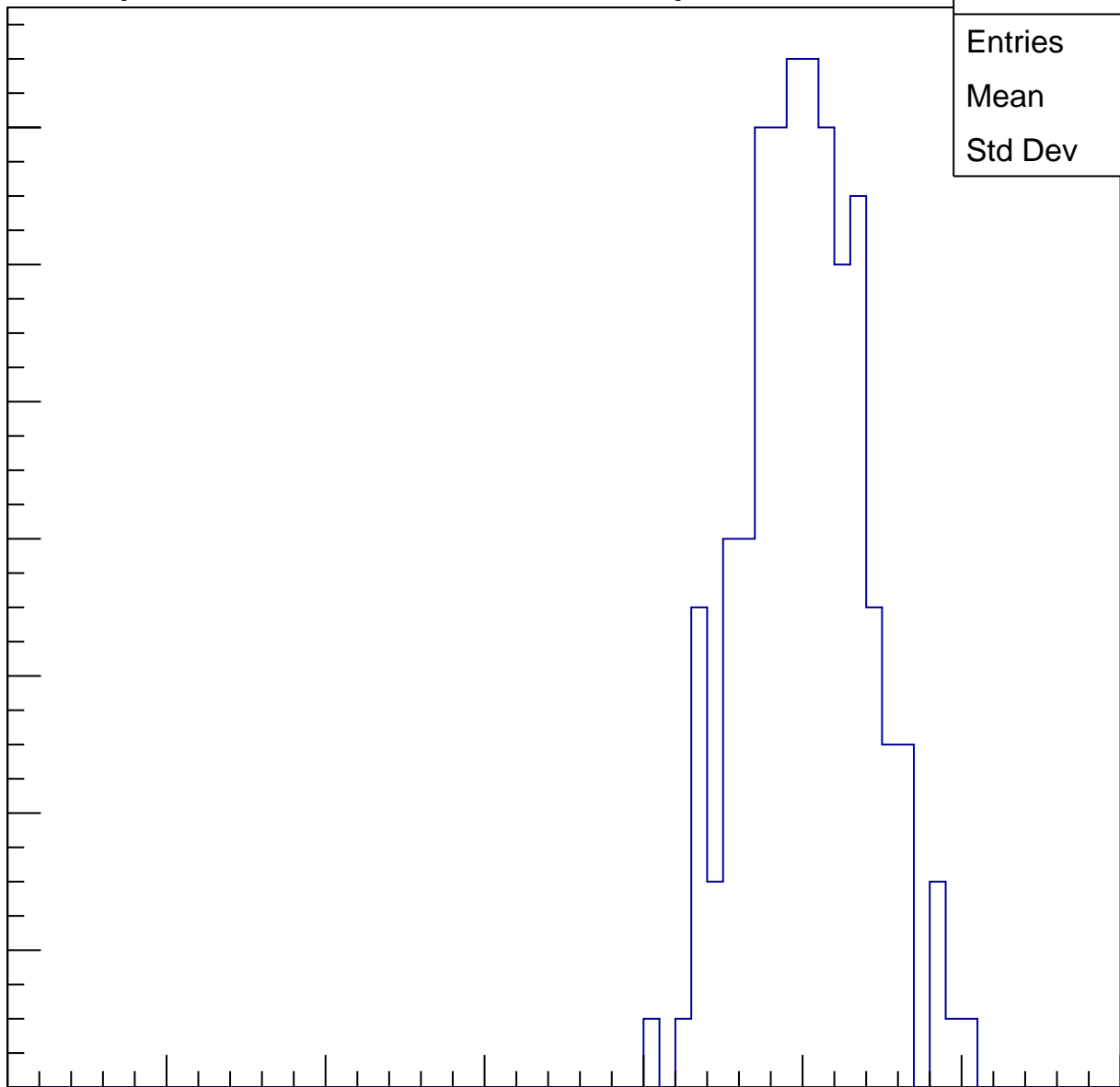
| | |
|---------|-------|
| Entries | 147 |
| Mean | 49.73 |
| Std Dev | 3.778 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch66, adc4

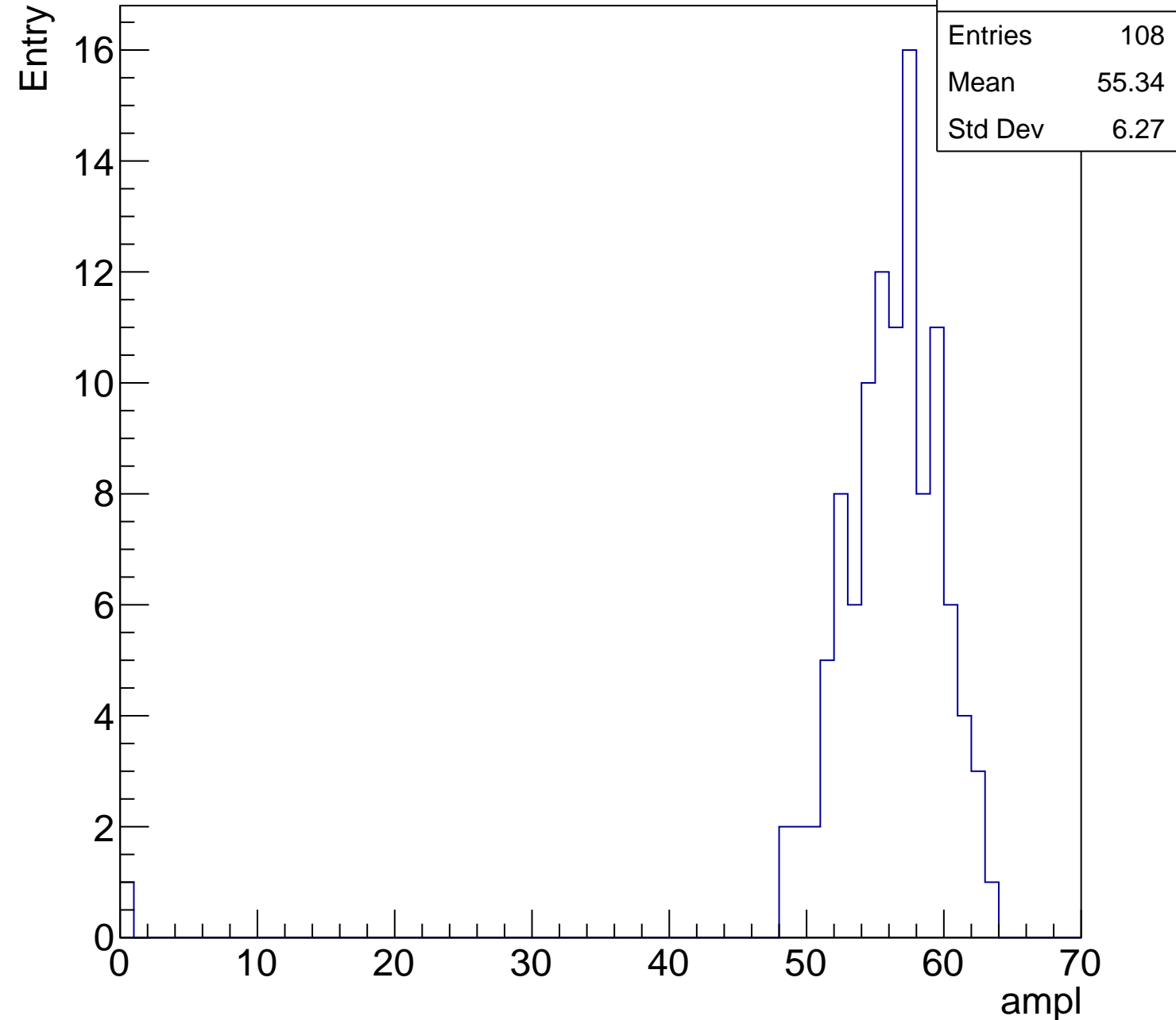
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 108 |
| Mean | 55.34 |
| Std Dev | 6.27 |

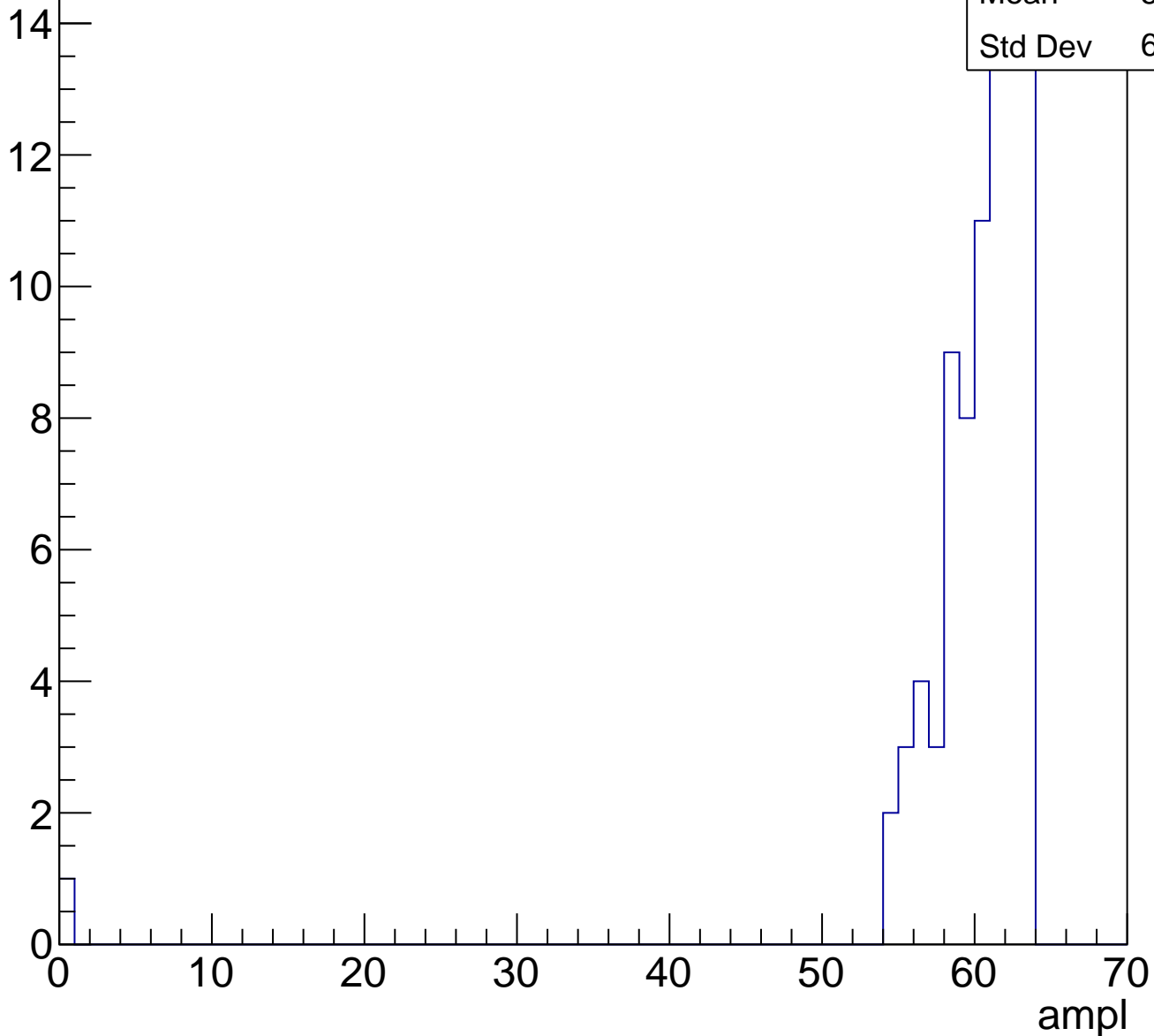
ampl



B0L101S, U22-ch66, adc5

calib_packv5_042523_0143.root, FC#1, port C1

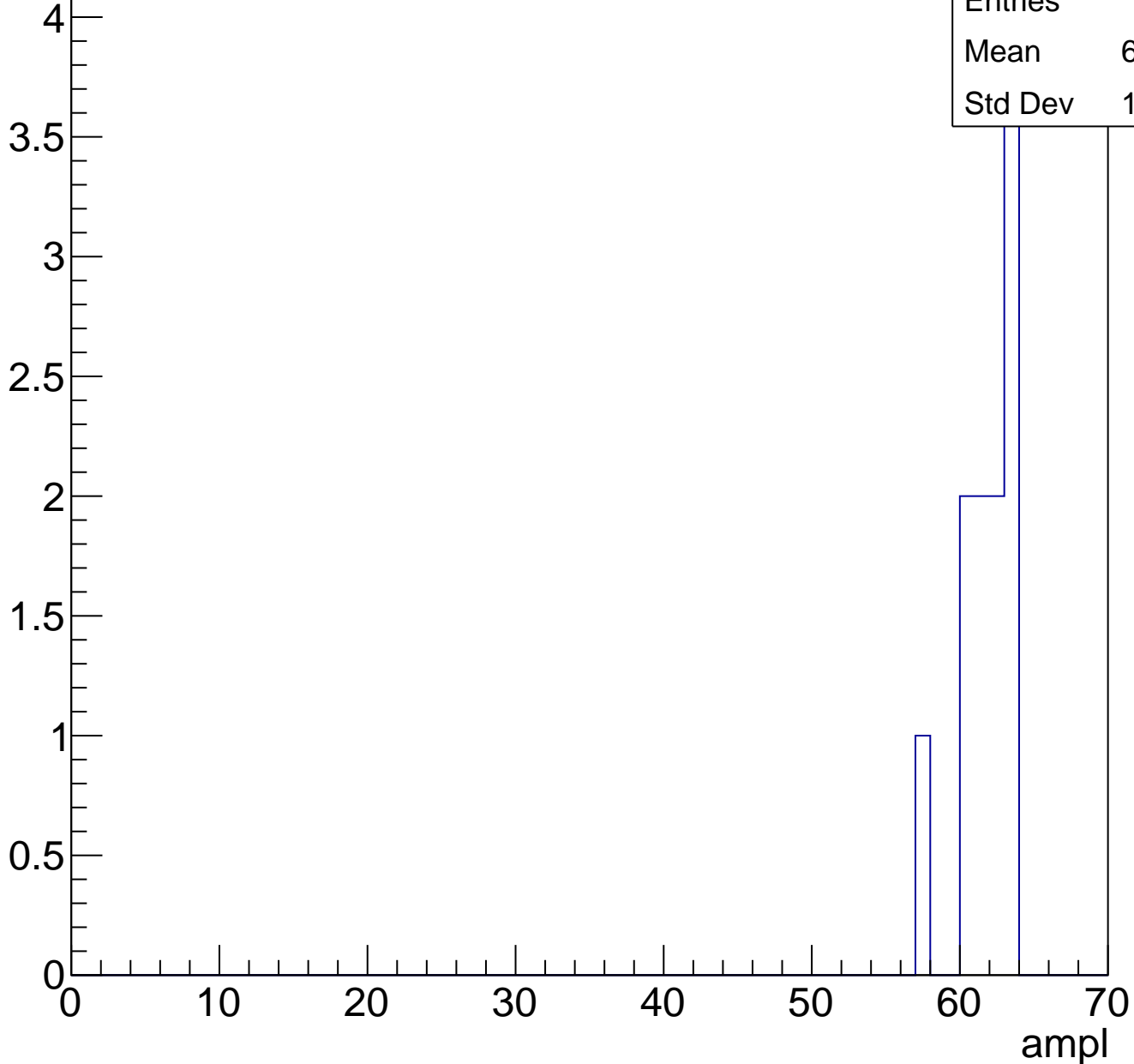
Entry



B0L101S, U22-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



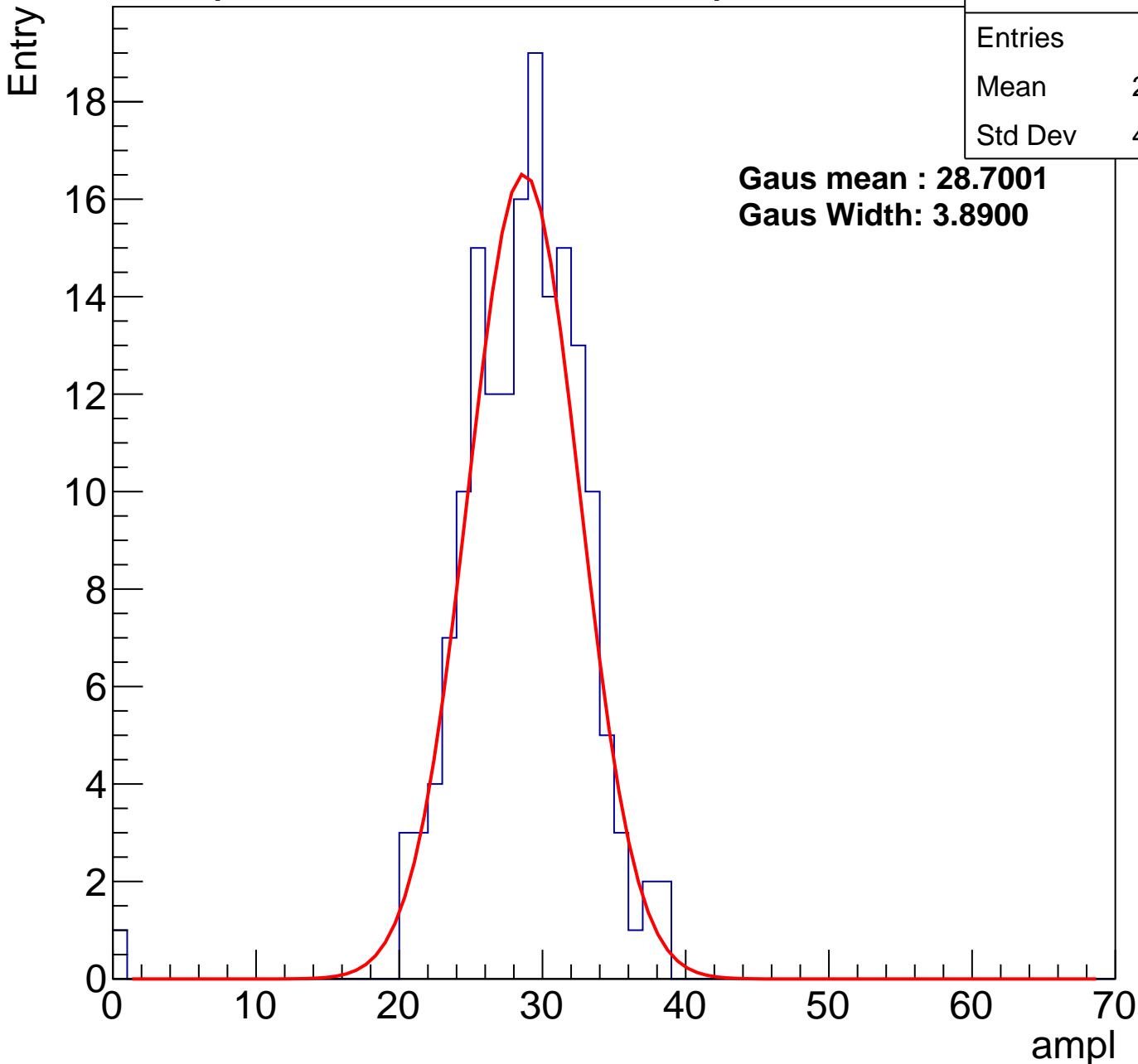
B0L101S, U22-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 167 |
| Mean | 28.23 |
| Std Dev | 4.384 |

Gaus mean : 28.7001

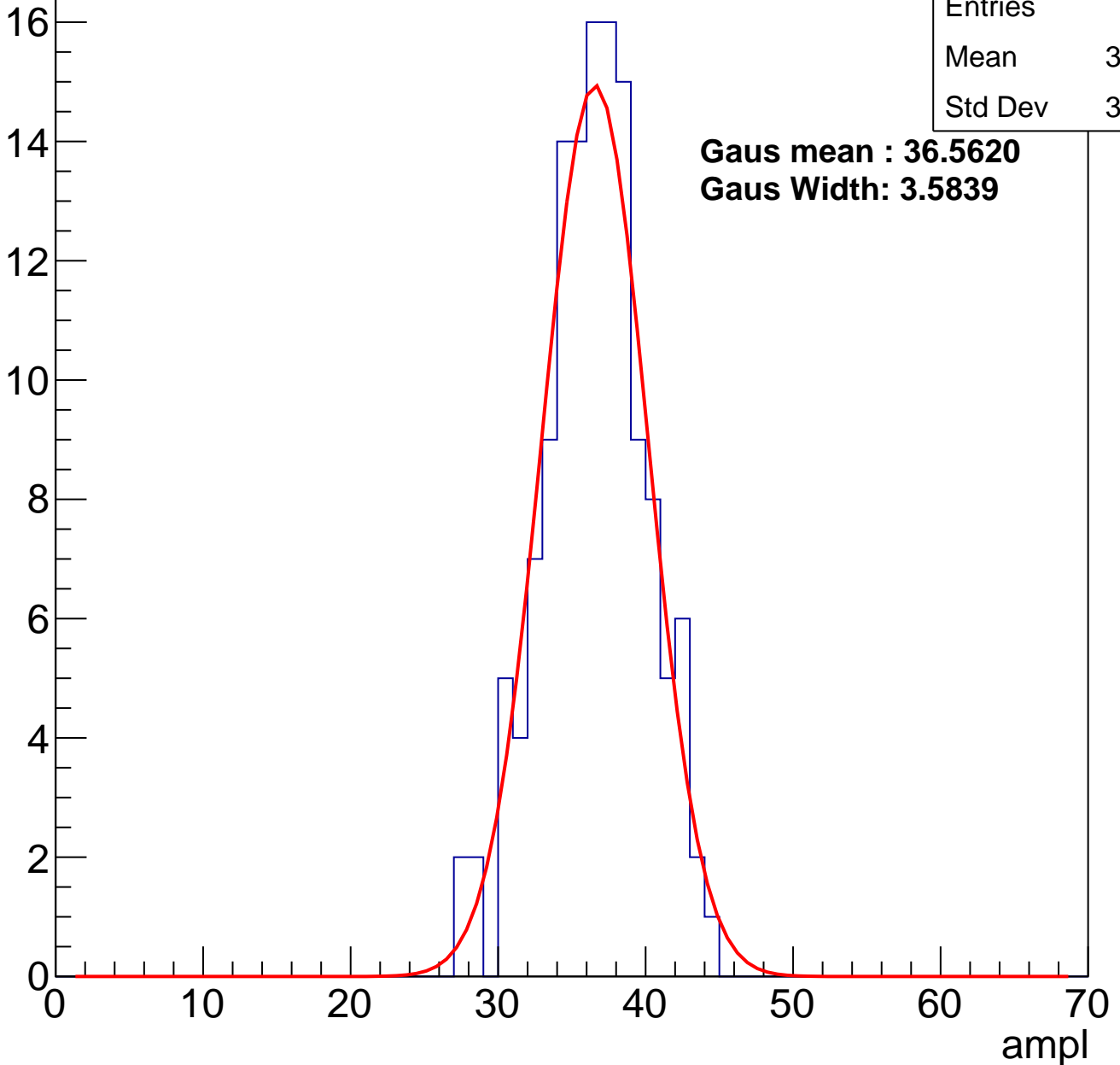
Gaus Width: 3.8900



B0L101S, U22-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch67, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

165

Mean

43.59

Std Dev

3.9

Gaus mean : 44.2962

Gaus Width: 3.7500

0

10

20

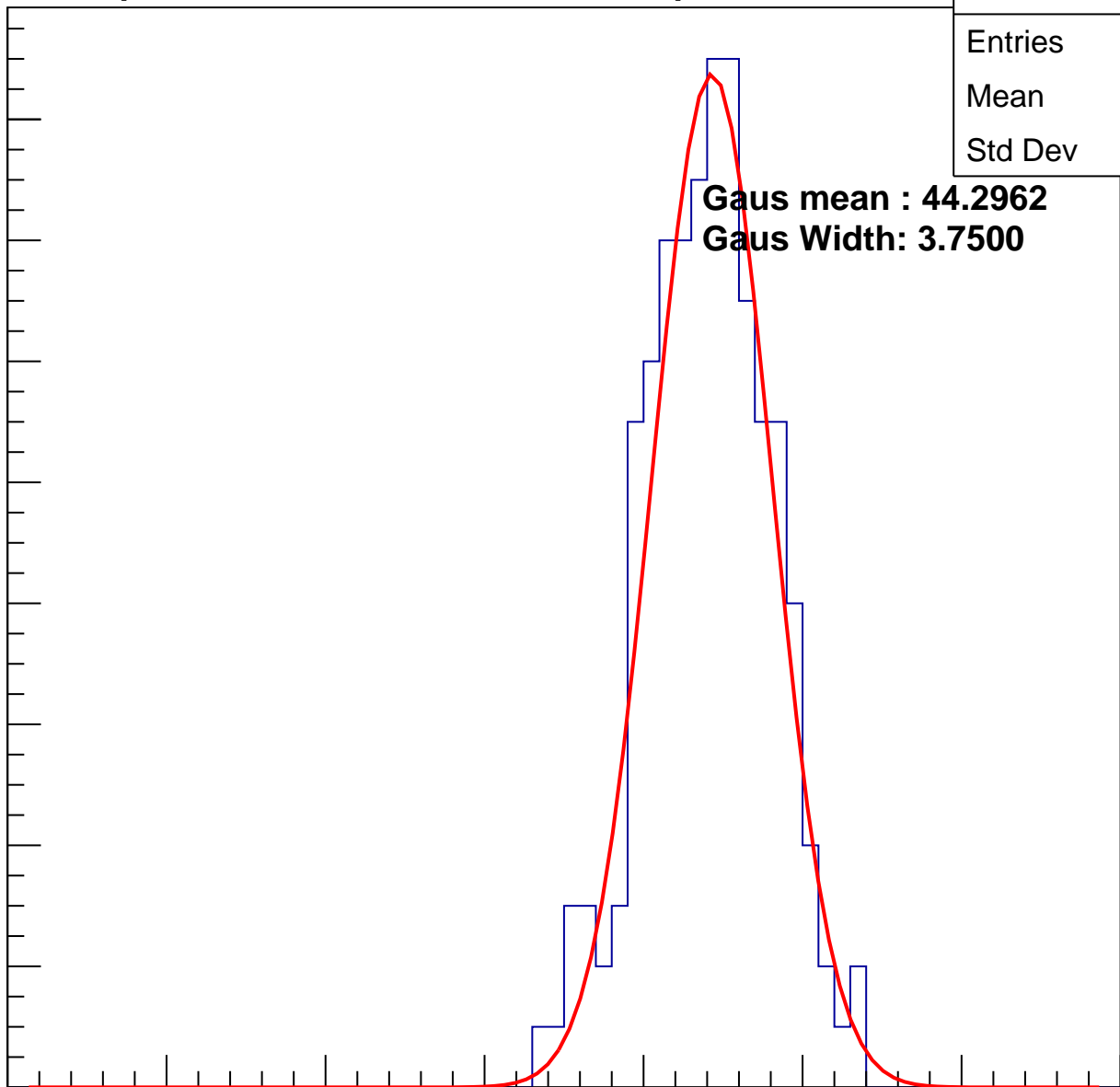
30

40

50

60

ampl



B0L101S, U22-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

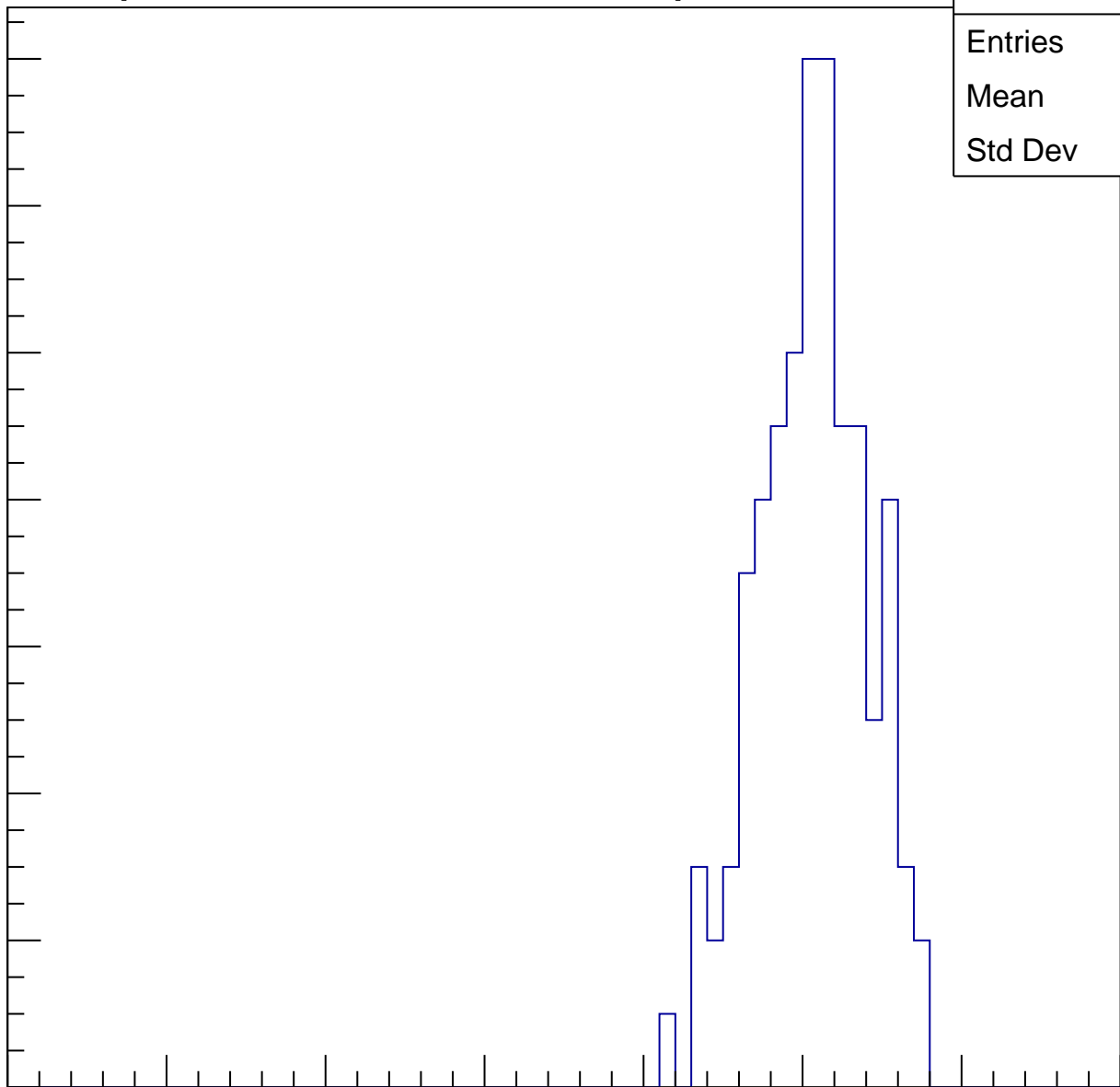
| | |
|---------|-------|
| Entries | 107 |
| Mean | 50.13 |
| Std Dev | 3.377 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch67, adc4

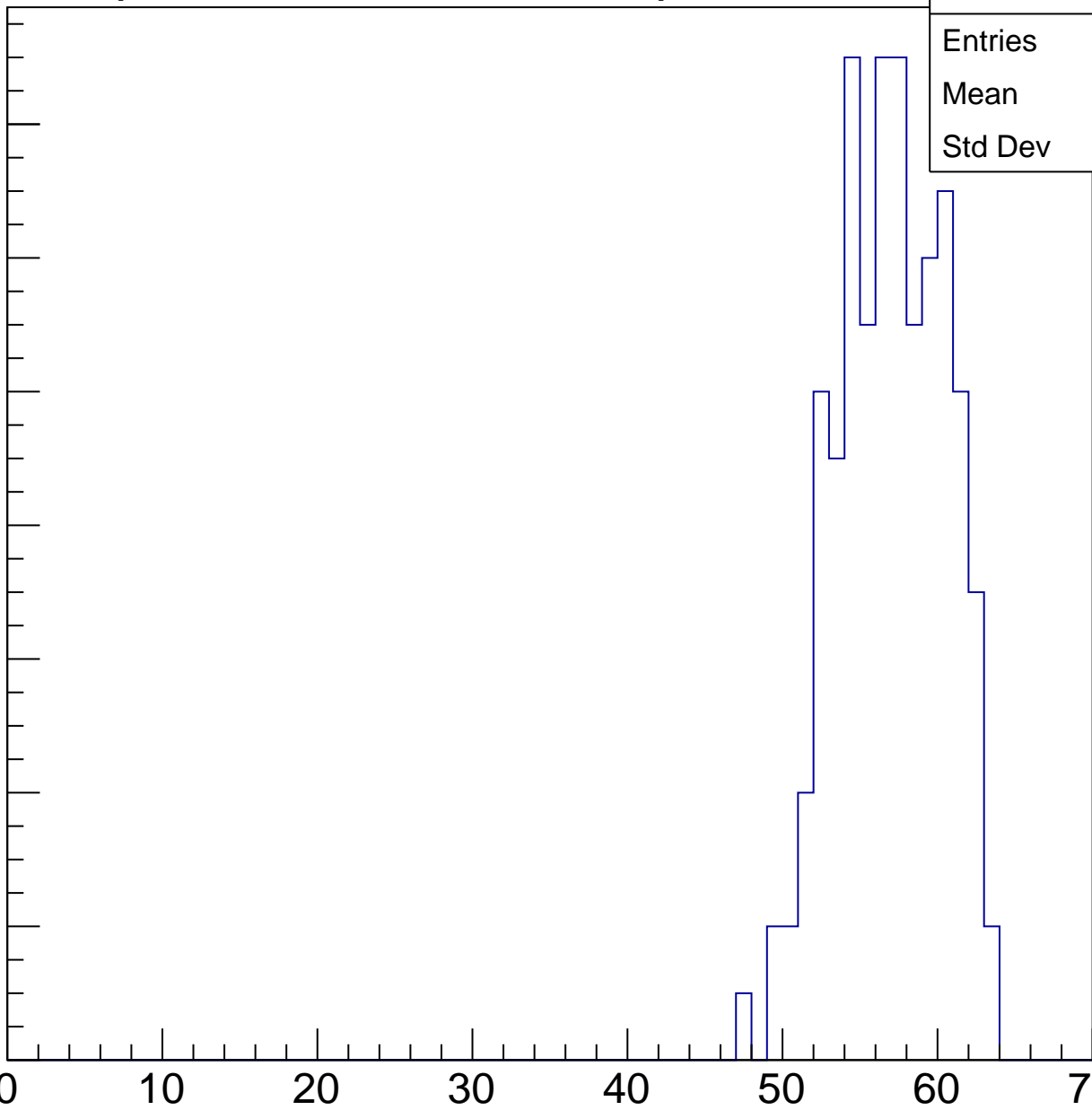
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 139 |
| Mean | 56.49 |
| Std Dev | 3.398 |

ampl



B0L101S, U22-ch67, adc5

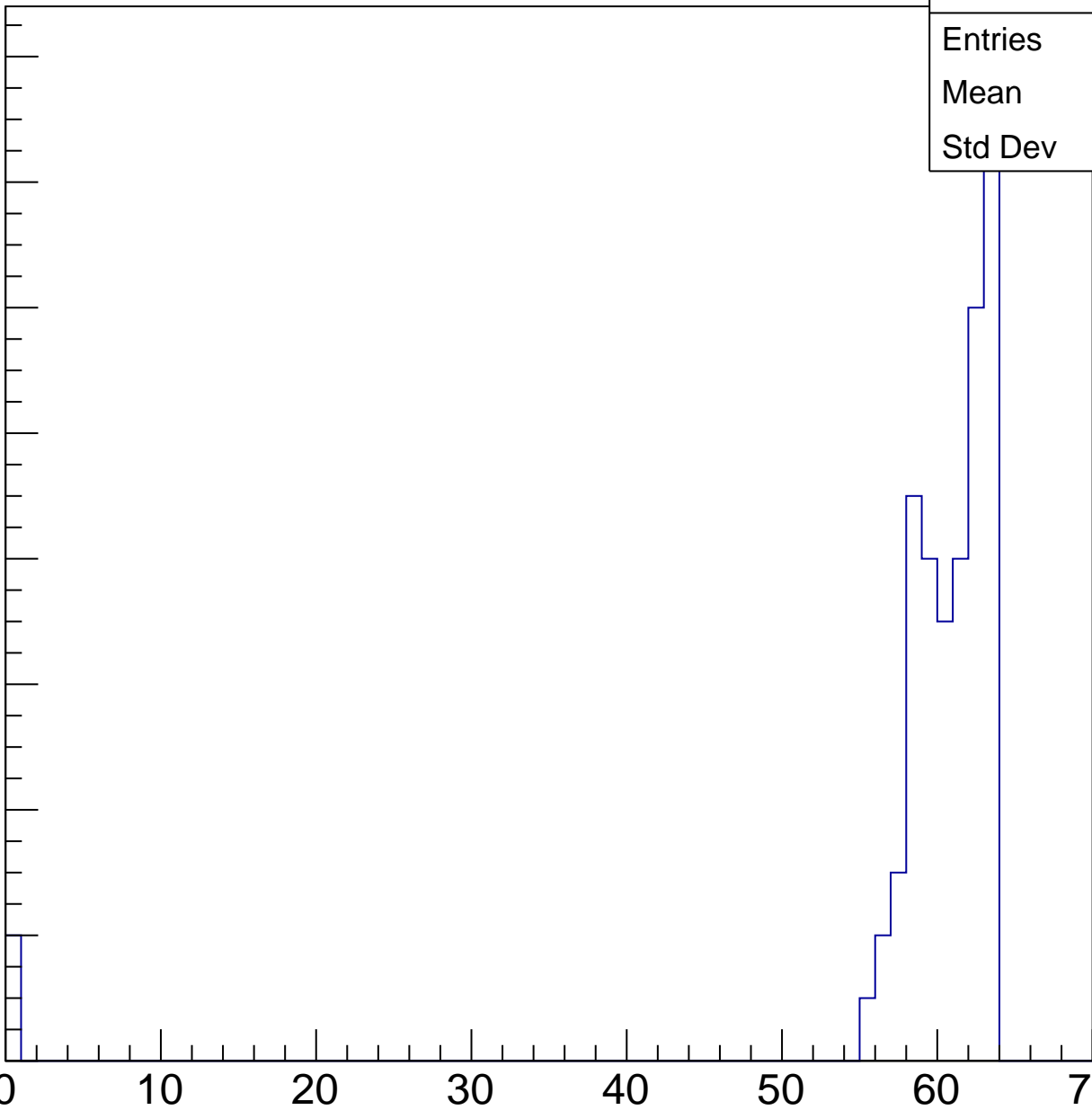
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 68 |
| Mean | 58.71 |
| Std Dev | 10.44 |

ampl



B0L101S, U22-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62 |
| Std Dev | 0.8944 |

ampl

0 10 20 30 40 50 60 70

B0L101S, U22-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 125 |
| Mean | 30.84 |
| Std Dev | 3.638 |

Gaus mean : 31.3856

Gaus Width: 3.7110

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

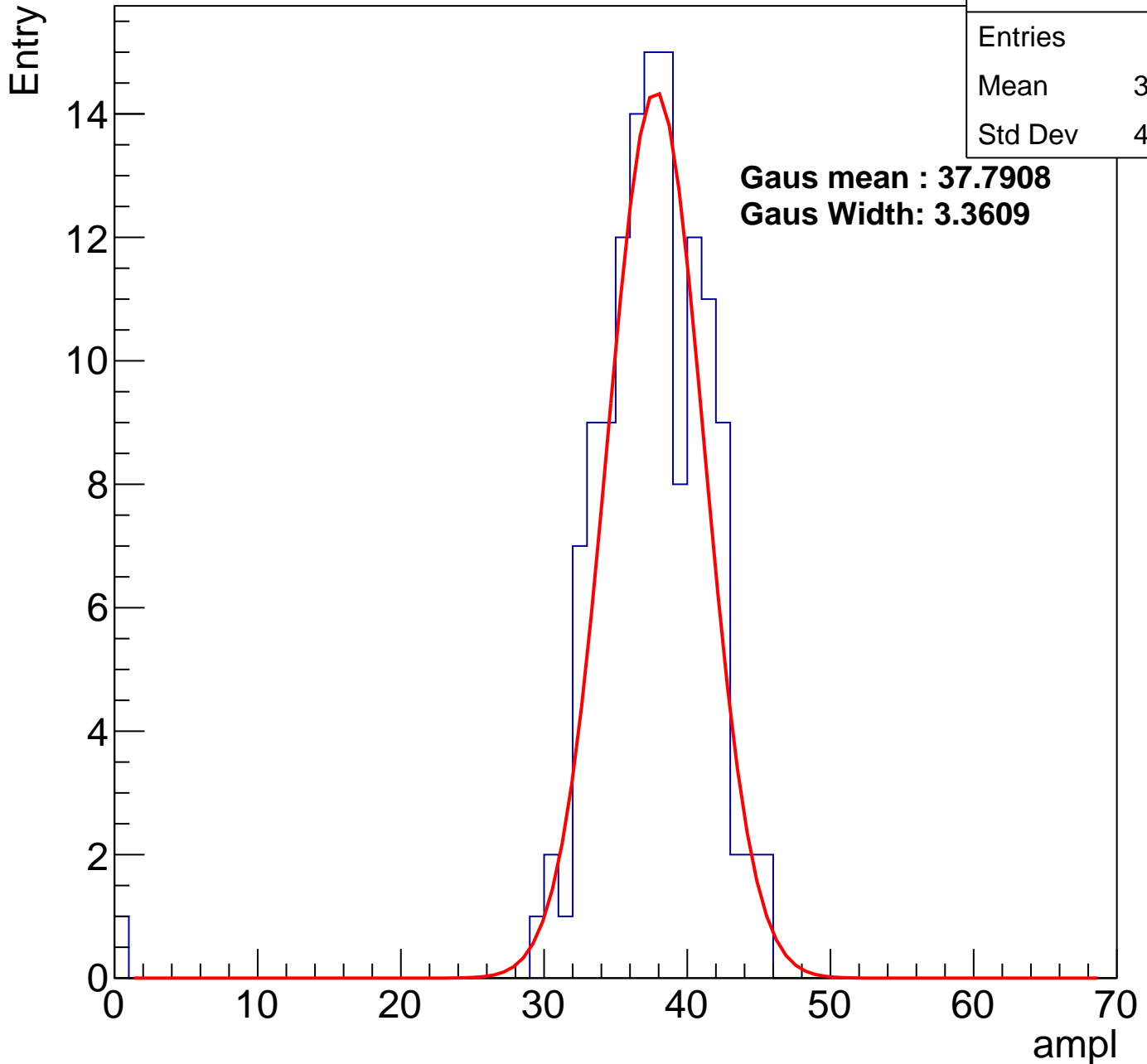
70

B0L101S, U22-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 36.98 |
| Std Dev | 4.673 |

Gaus mean : 37.7908
Gaus Width: 3.3609



B0L101S, U22-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 136 |
| Mean | 44.07 |
| Std Dev | 3.753 |

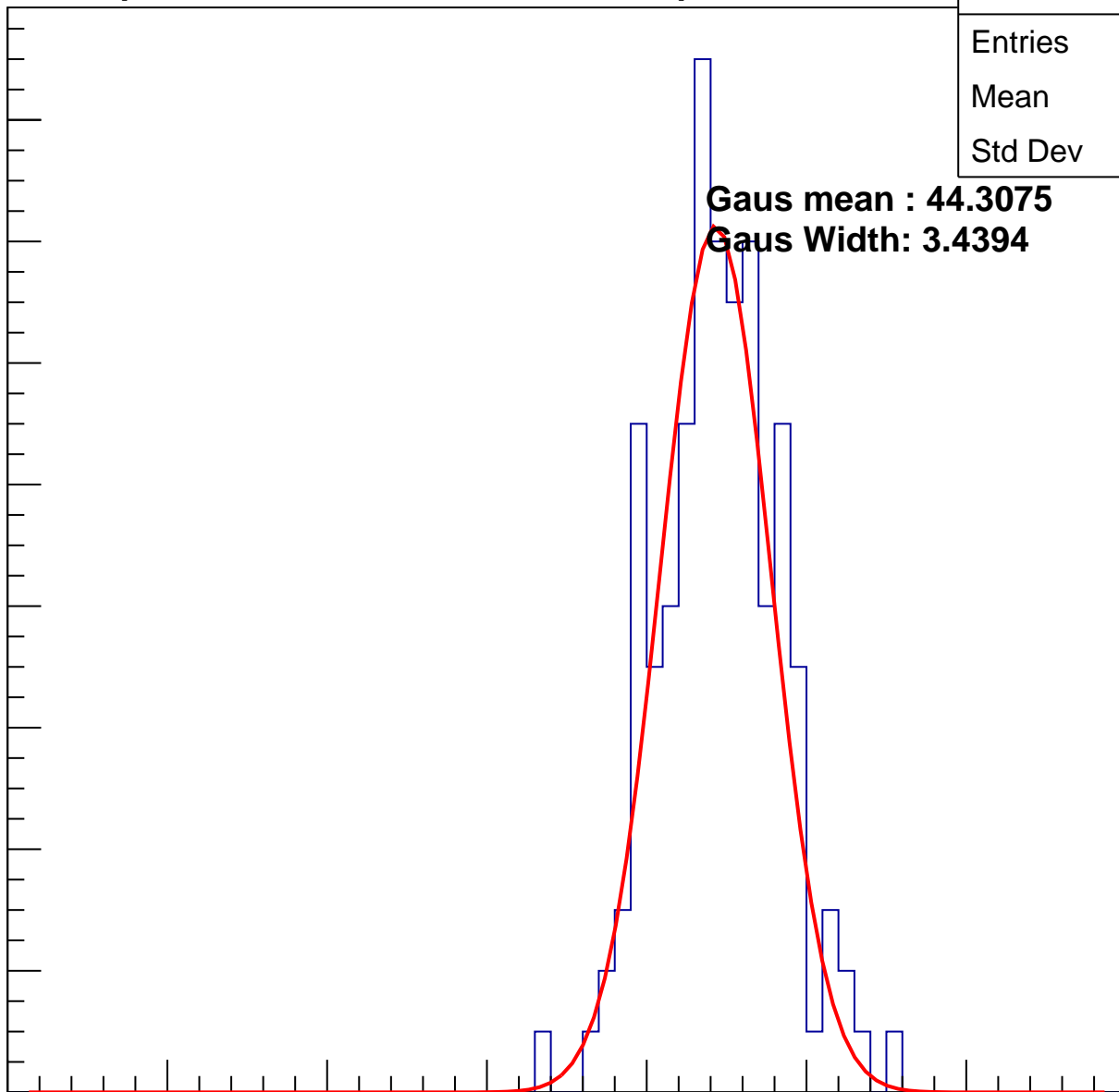
Gaus mean : 44.3075

Gaus Width: 3.4394

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

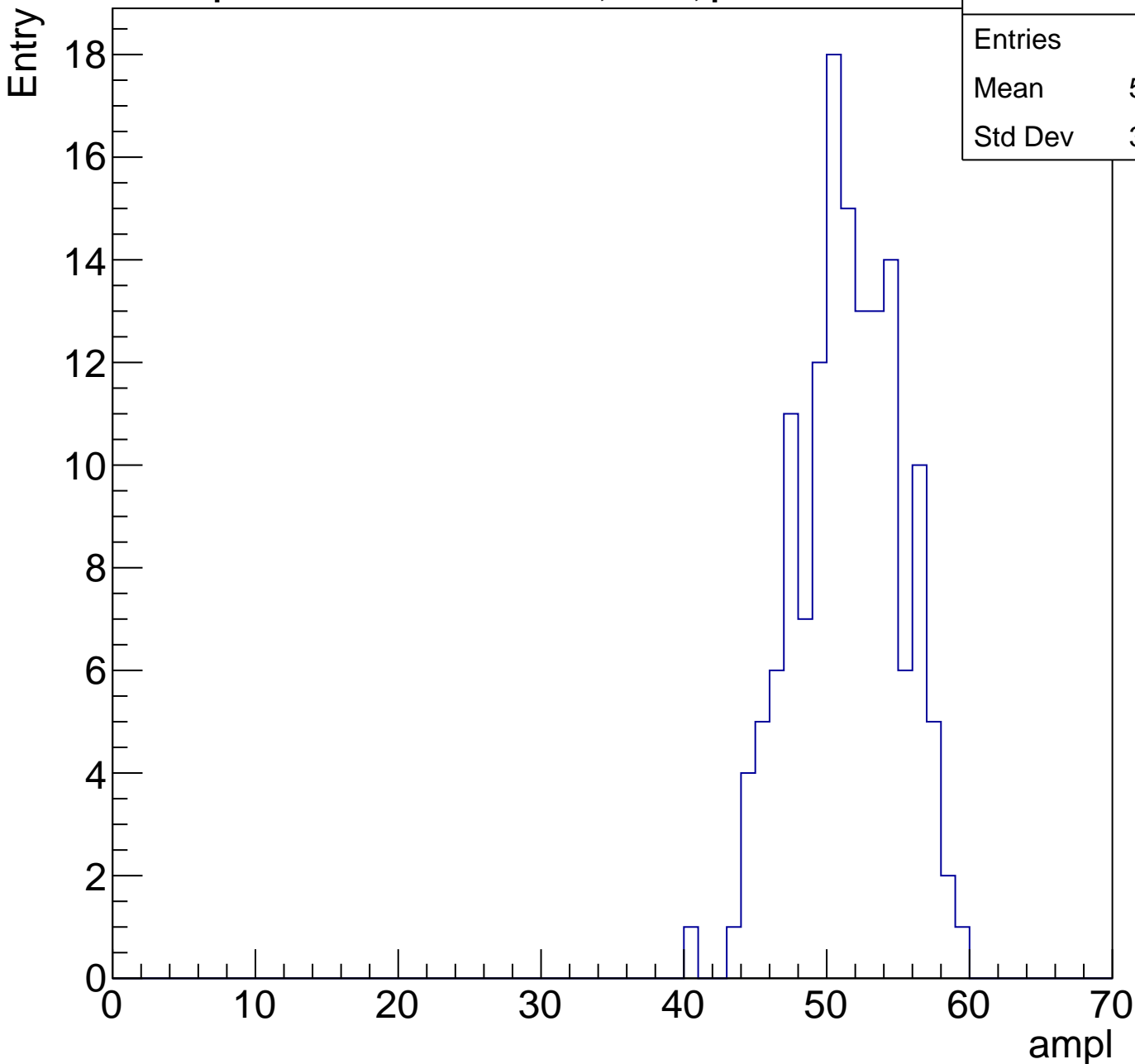
ampl



B0L101S, U22-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 50.95 |
| Std Dev | 3.627 |

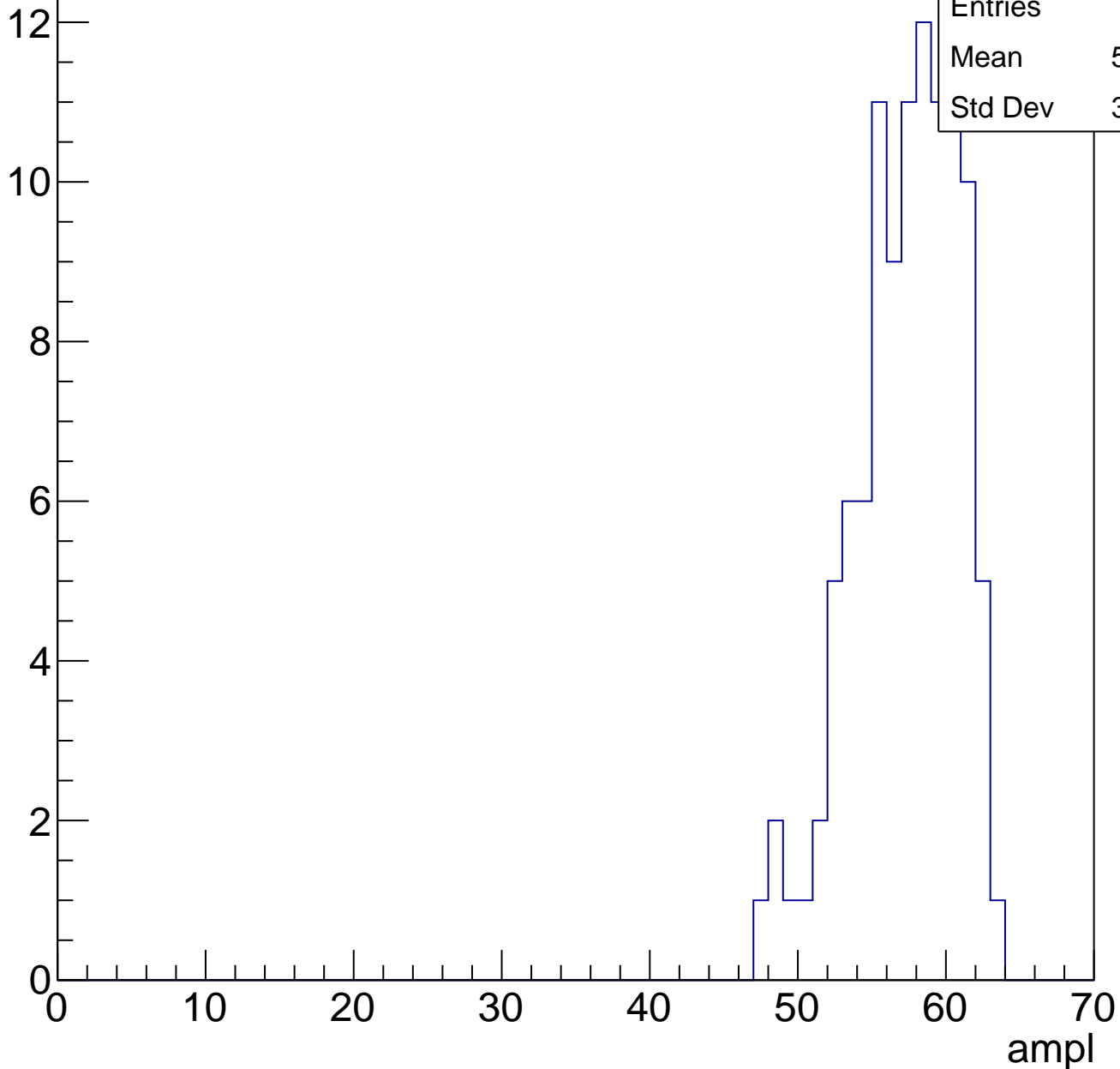


B0L101S, U22-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 105 |
| Mean | 56.86 |
| Std Dev | 3.449 |

Entry



B0L101S, U22-ch68, adc5

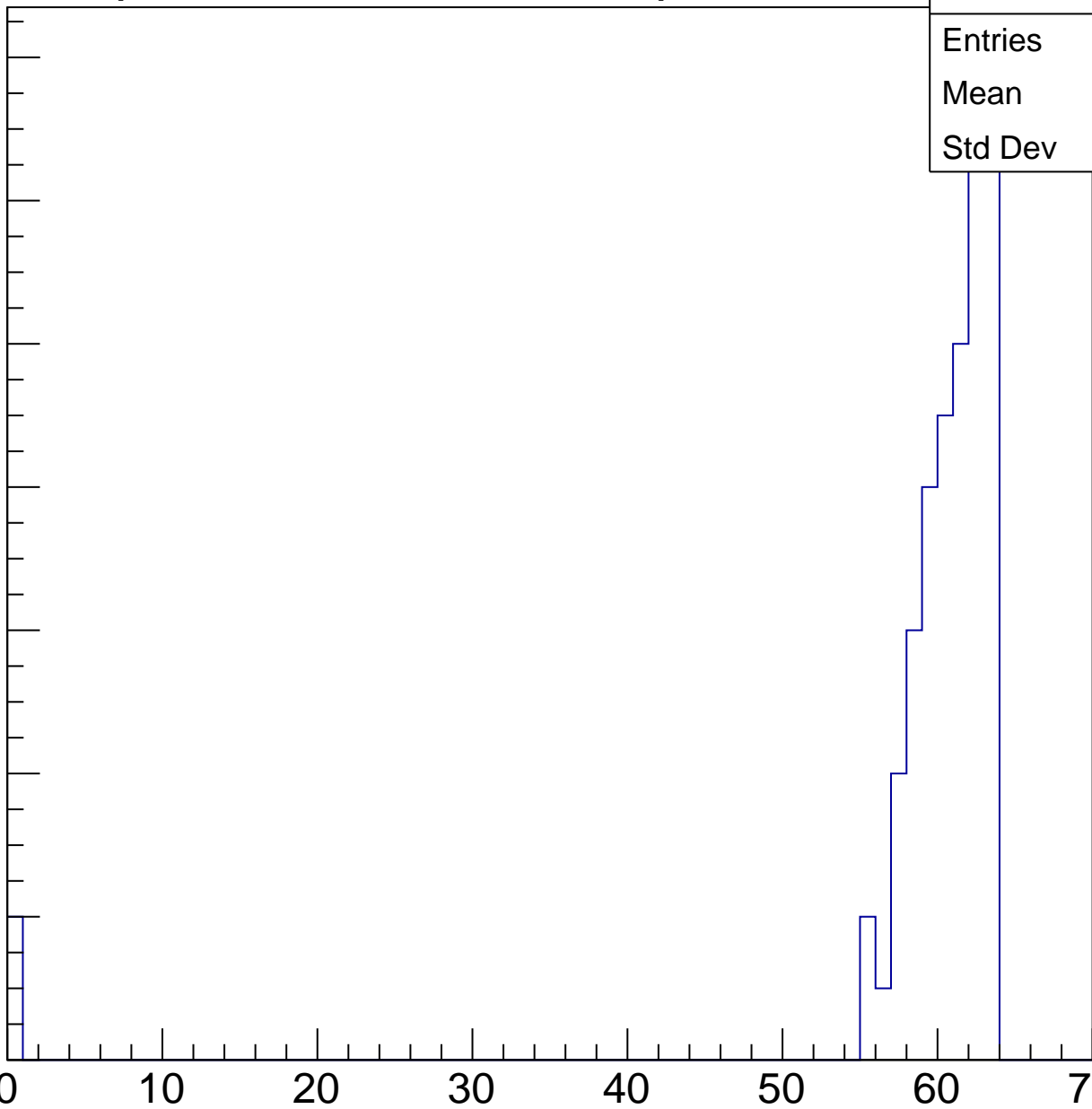
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.77 |
| Std Dev | 10.3 |

ampl

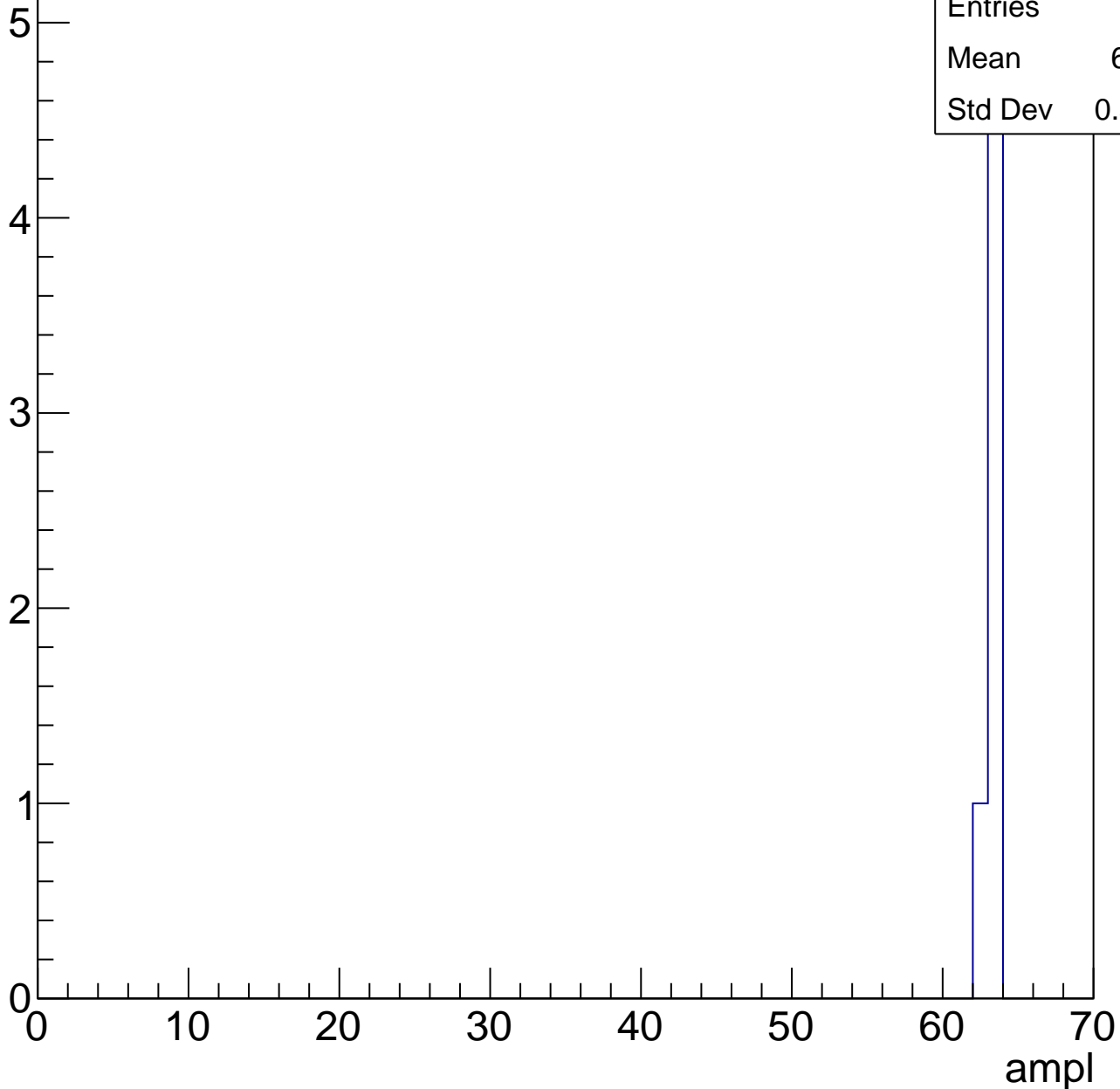


B0L101S, U22-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|--------|
| Entries | 6 |
| Mean | 62.83 |
| Std Dev | 0.3727 |



B0L101S, U22-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch69, adc0

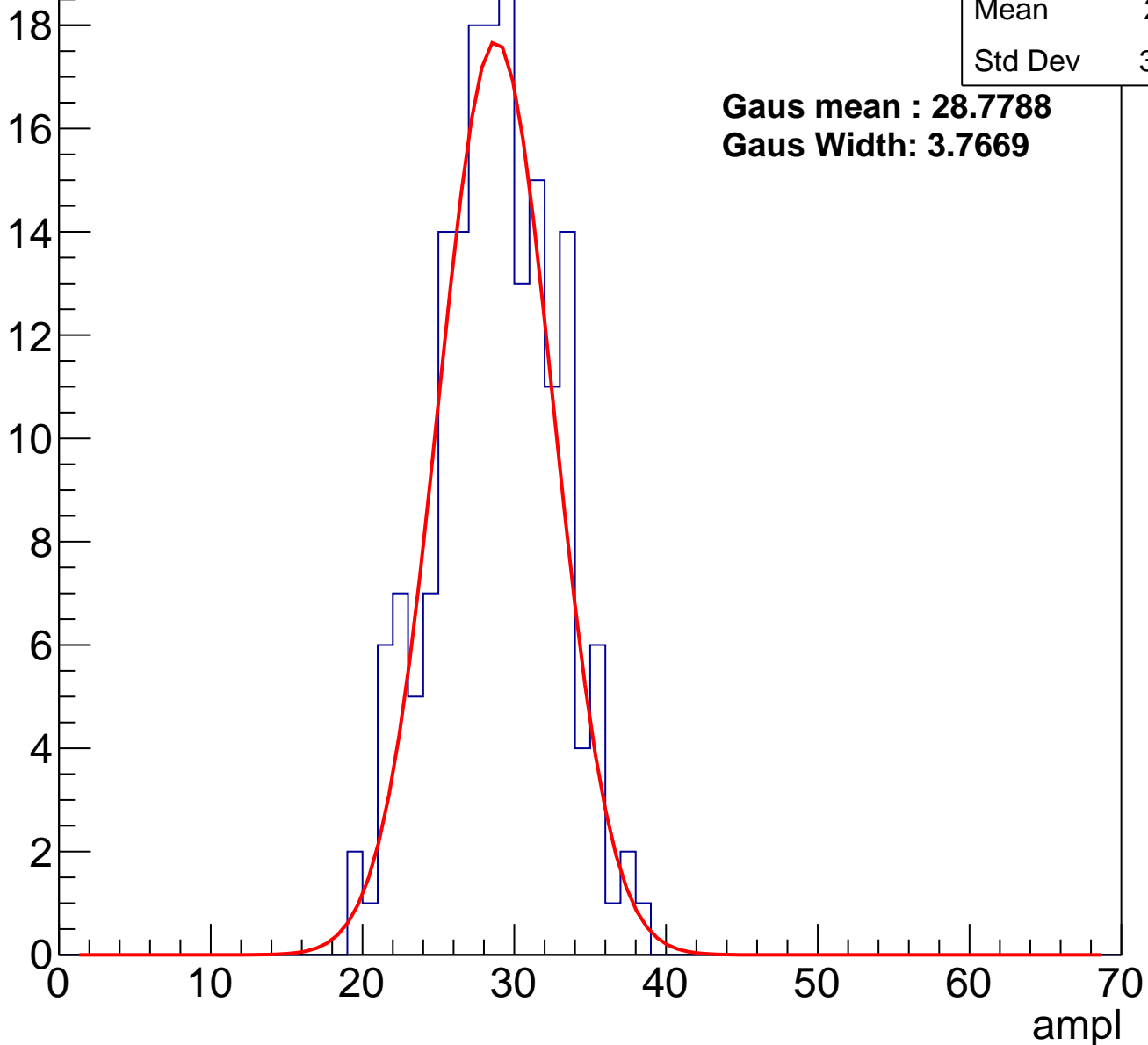
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 178 |
| Mean | 28.31 |
| Std Dev | 3.886 |

Gaus mean : 28.7788

Gaus Width: 3.7669

Entry



B0L101S, U22-ch69, adc1

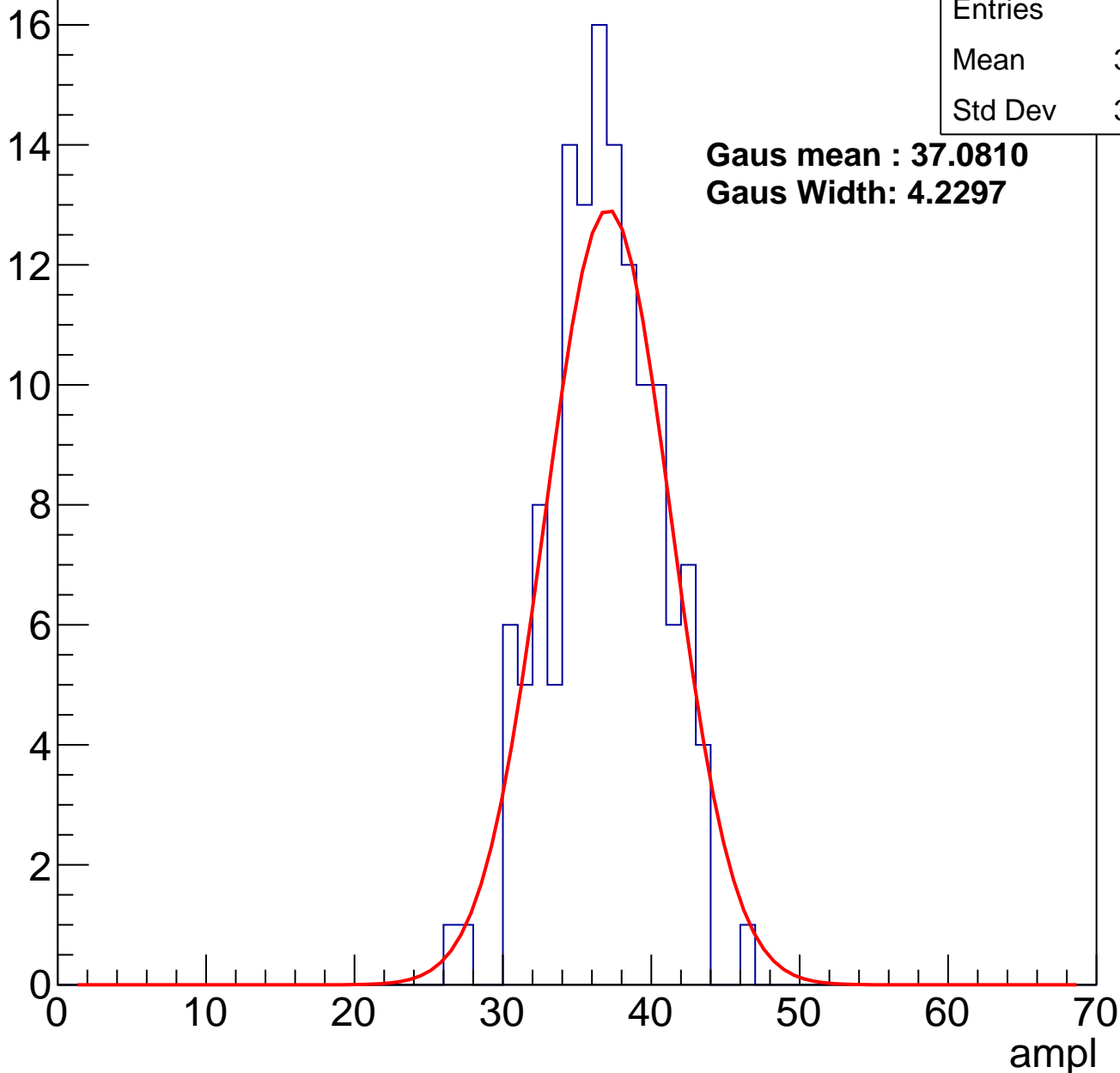
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 36.38 |
| Std Dev | 3.633 |

Gaus mean : 37.0810

Gaus Width: 4.2297

Entry



B0L101S, U22-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

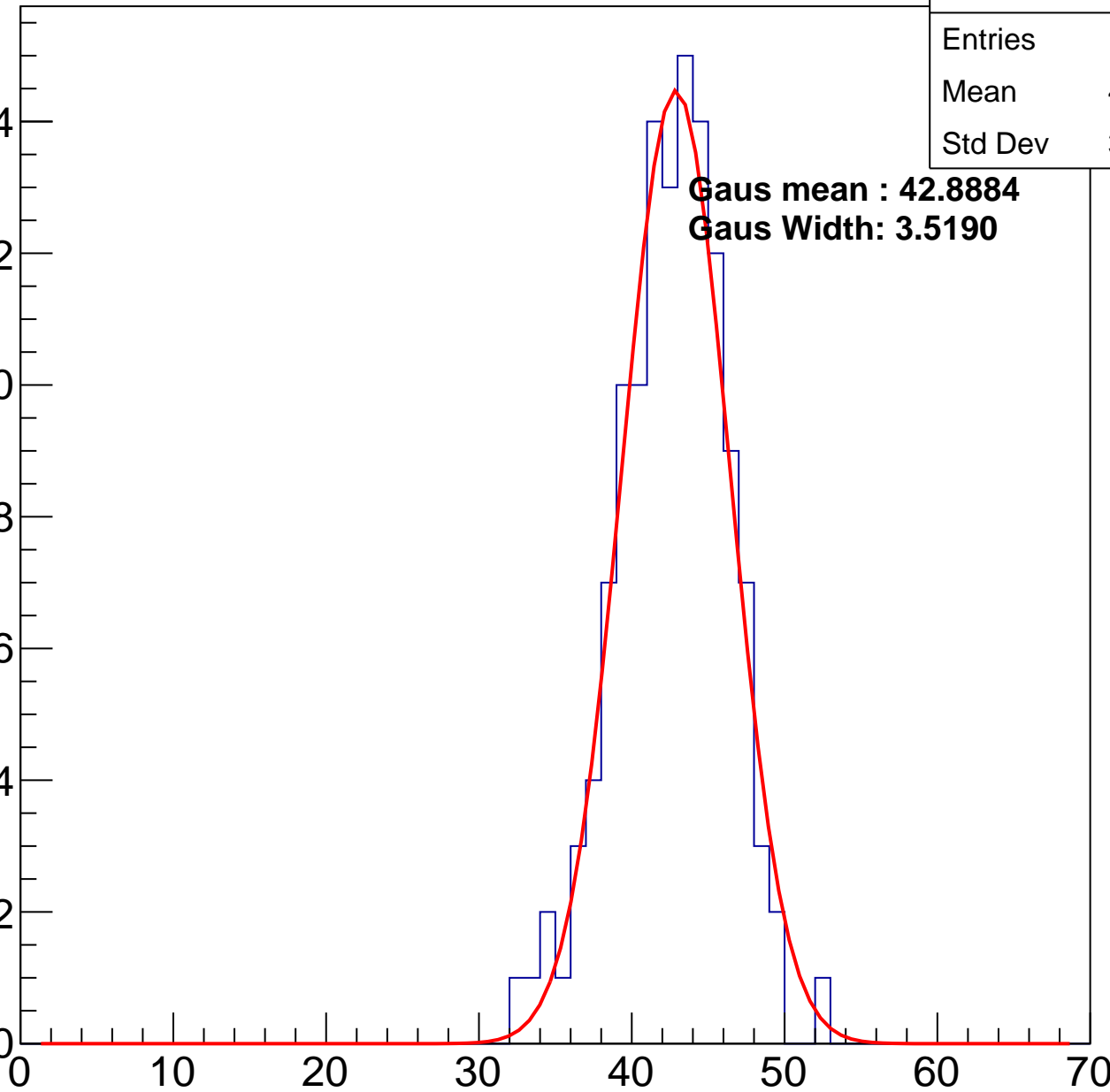
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 129 |
| Mean | 42.16 |
| Std Dev | 3.547 |

Gaus mean : 42.8884

Gaus Width: 3.5190

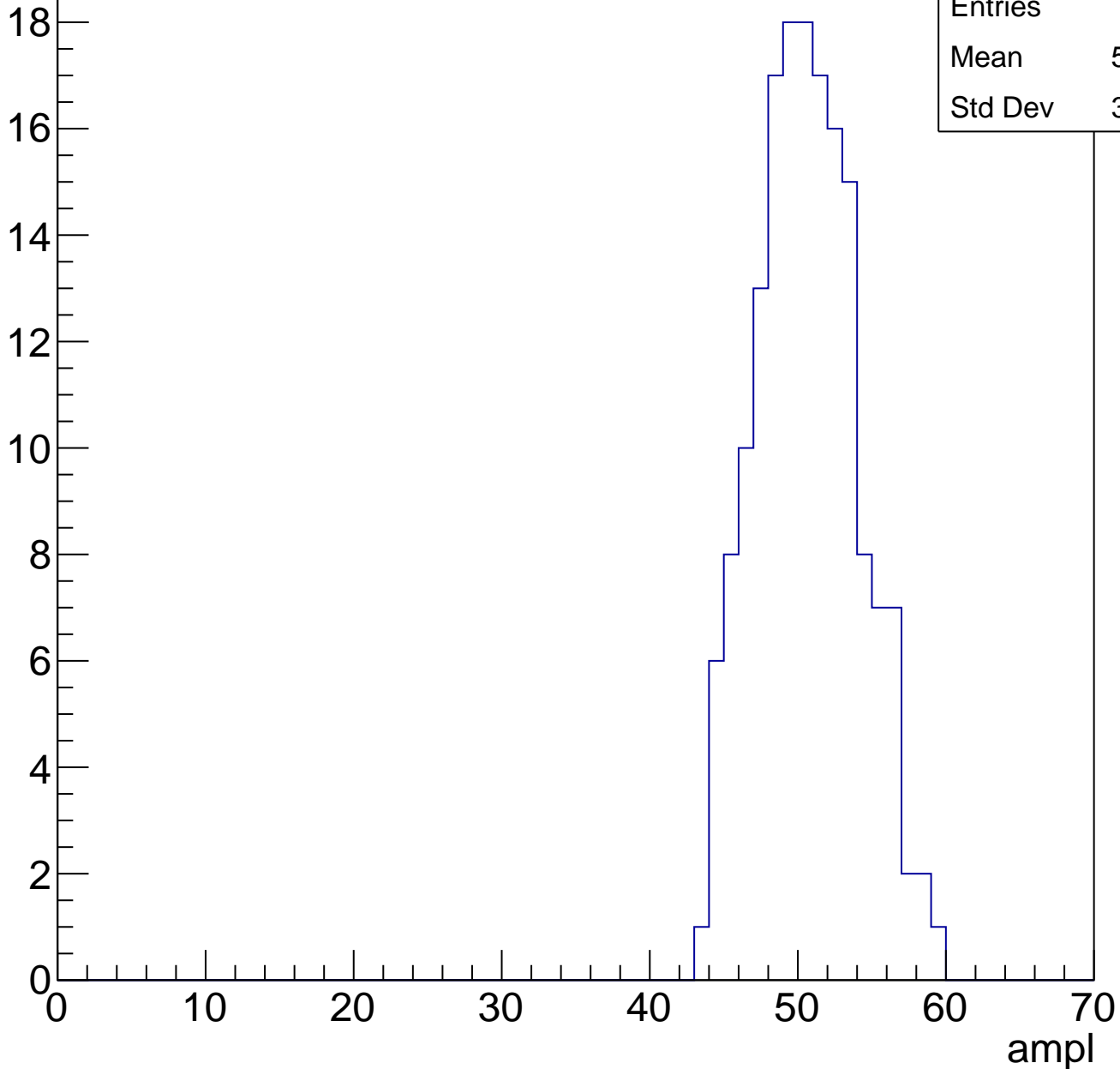
ampl



B0L101S, U22-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



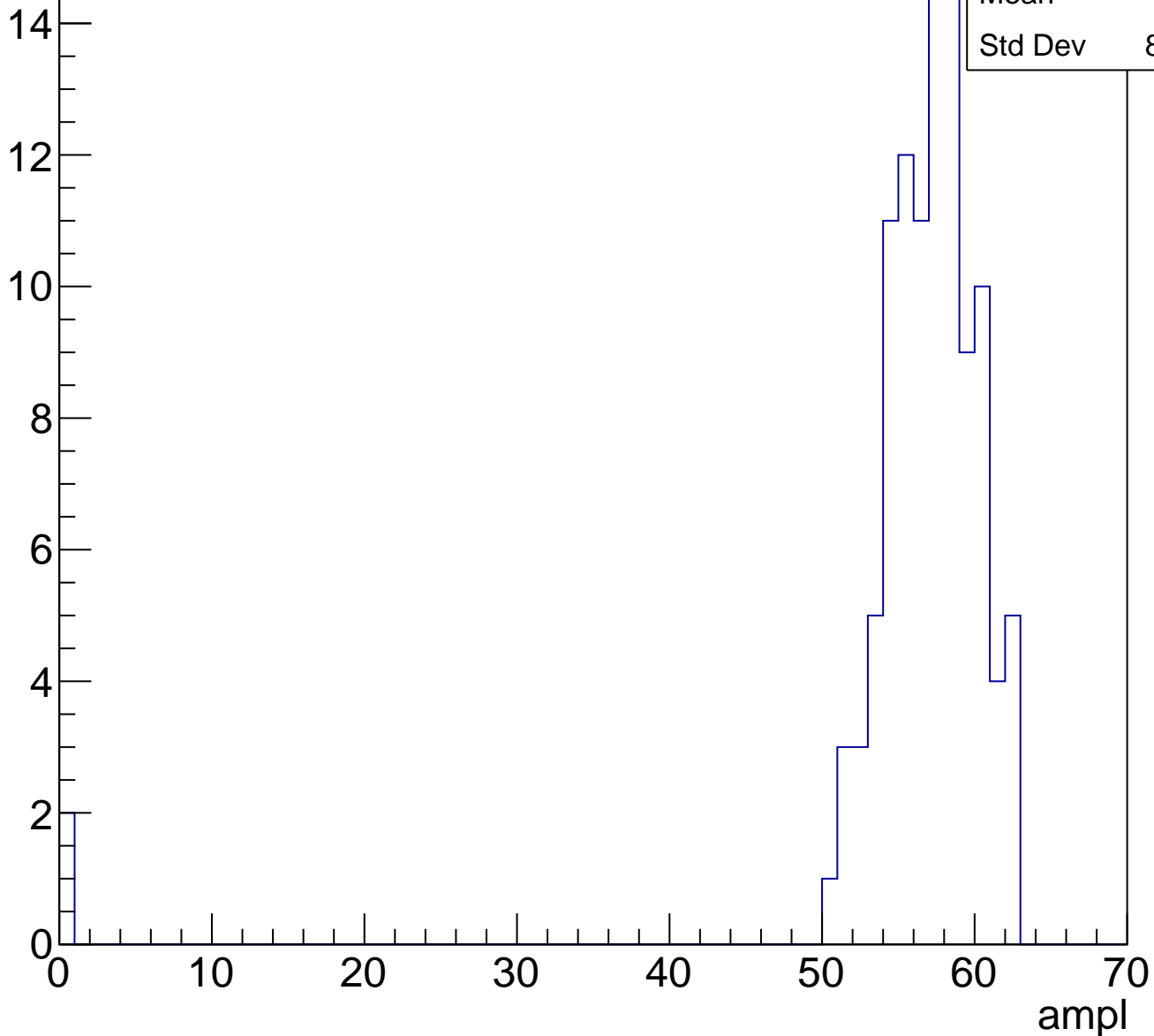
| | |
|---------|-------|
| Entries | 166 |
| Mean | 50.17 |
| Std Dev | 3.384 |

B0L101S, U22-ch69, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 106 |
| Mean | 55.7 |
| Std Dev | 8.195 |

Entry



B0L101S, U22-ch69, adc5

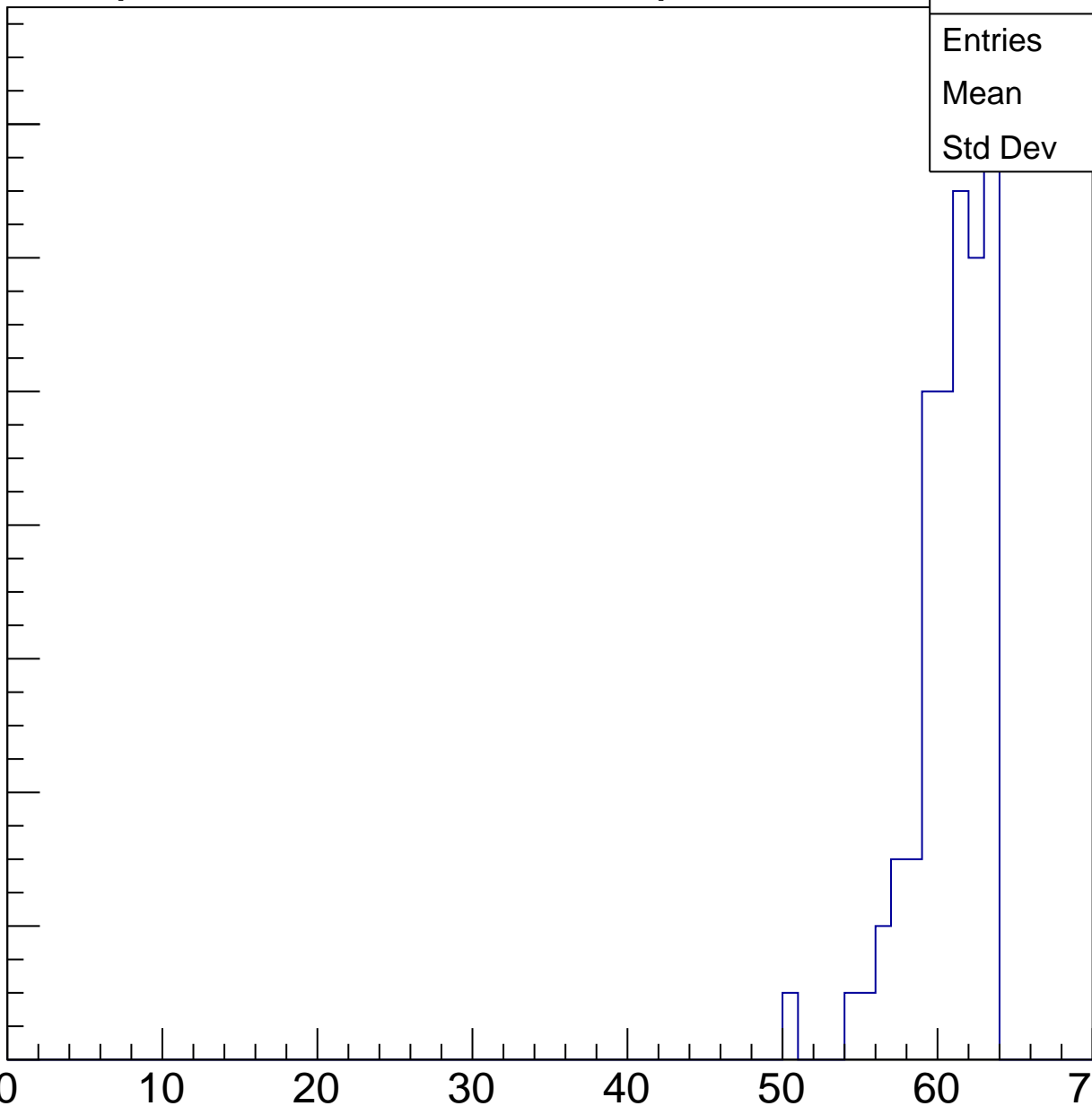
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 60.39 |
| Std Dev | 2.452 |

ampl

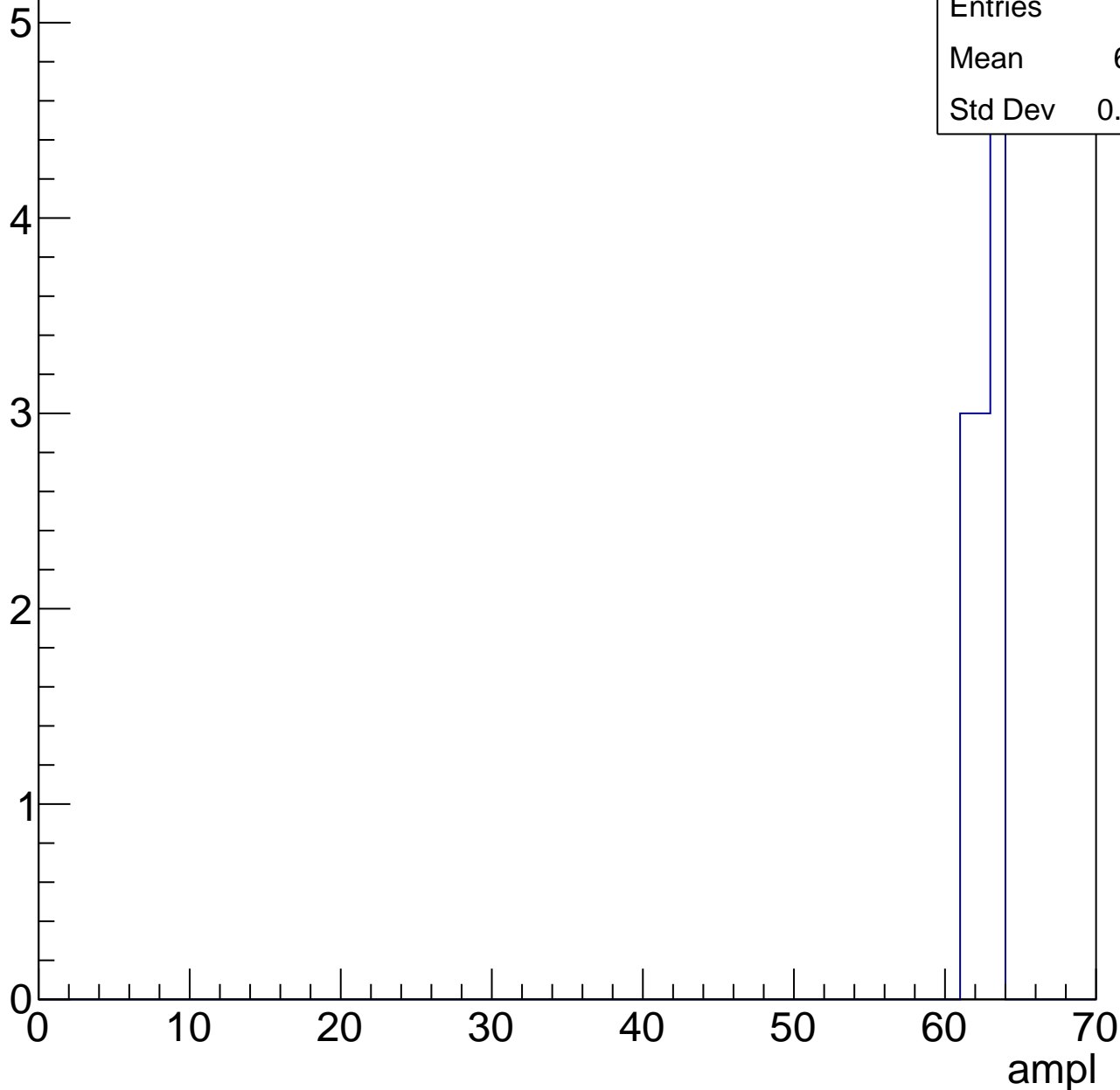


B0L101S, U22-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|--------|
| Entries | 11 |
| Mean | 62.18 |
| Std Dev | 0.8332 |



B0L101S, U22-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch70, adc0

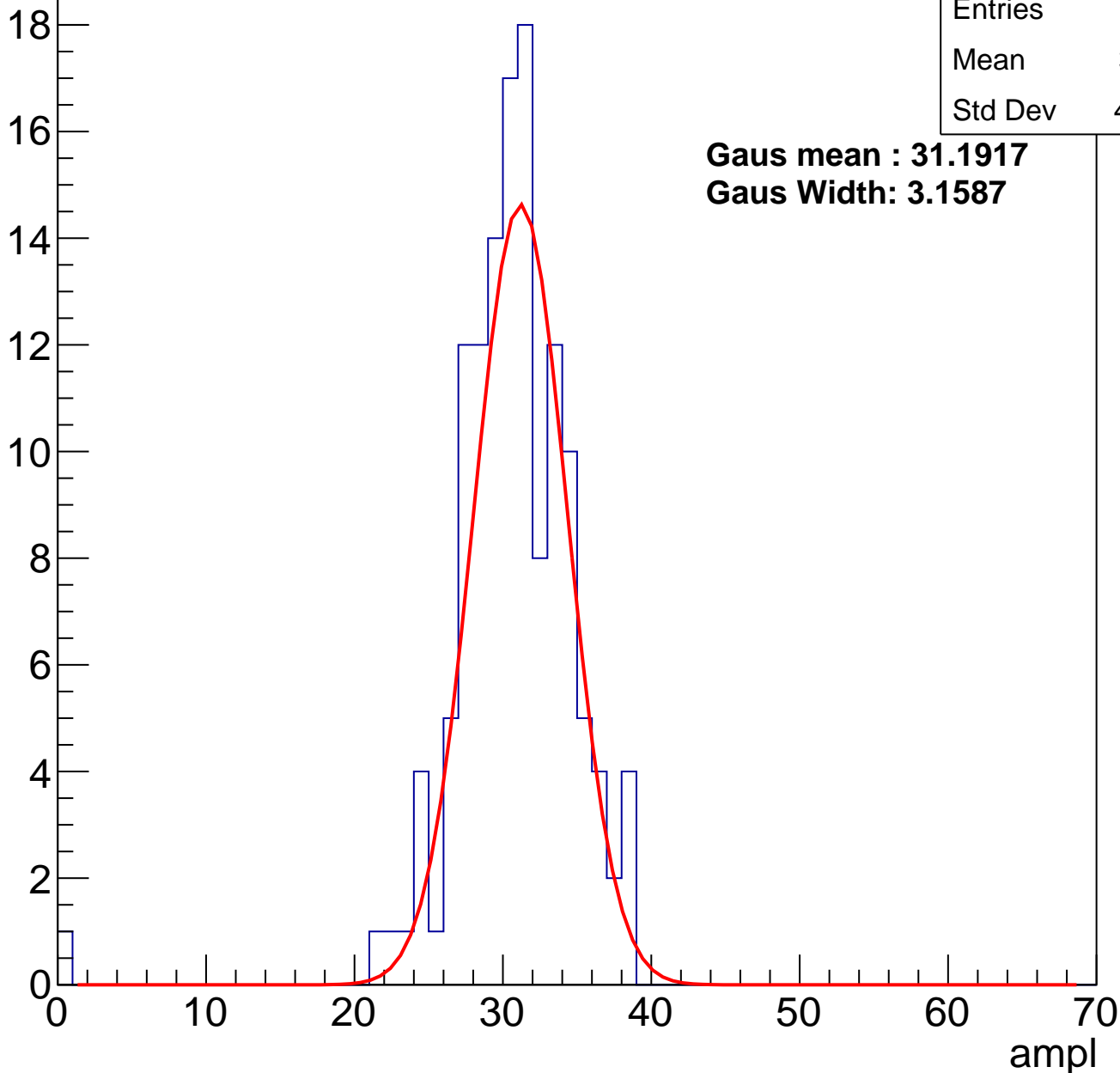
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 30.21 |
| Std Dev | 4.306 |

Gaus mean : 31.1917

Gaus Width: 3.1587

Entry



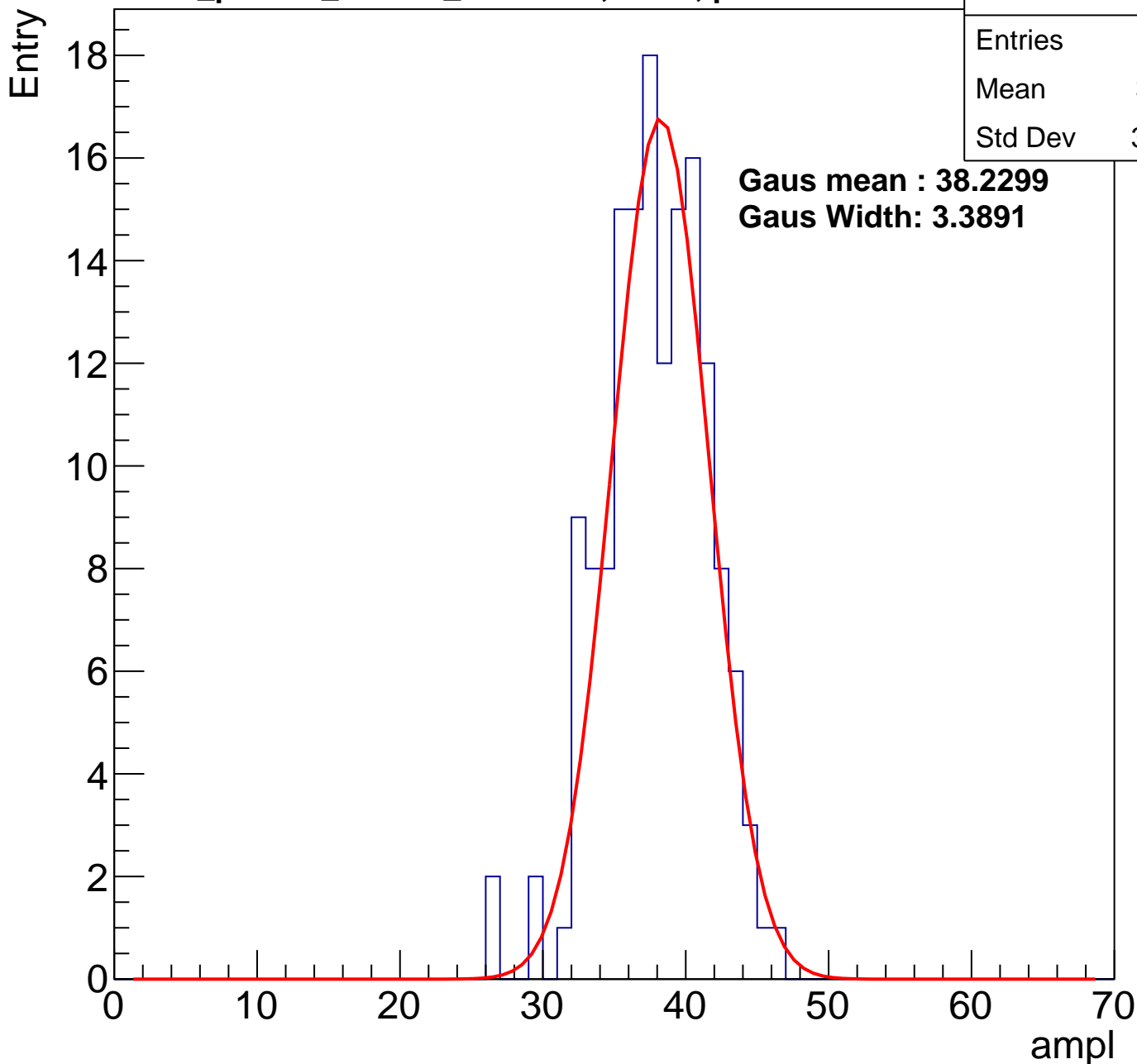
B0L101S, U22-ch70, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 152 |
| Mean | 37.41 |
| Std Dev | 3.624 |

Gaus mean : 38.2299

Gaus Width: 3.3891



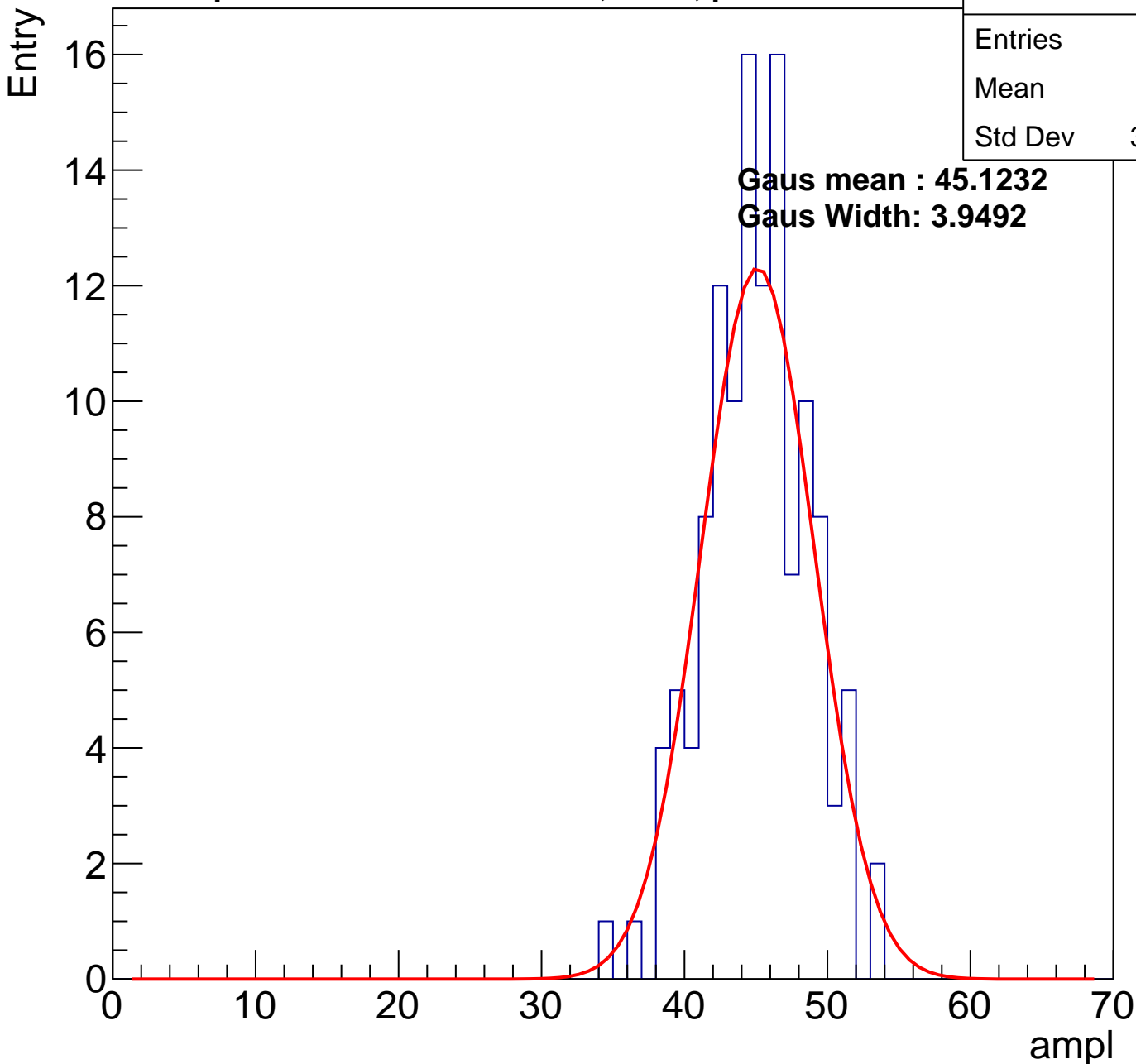
B0L101S, U22-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 44.6 |
| Std Dev | 3.587 |

Gaus mean : 45.1232

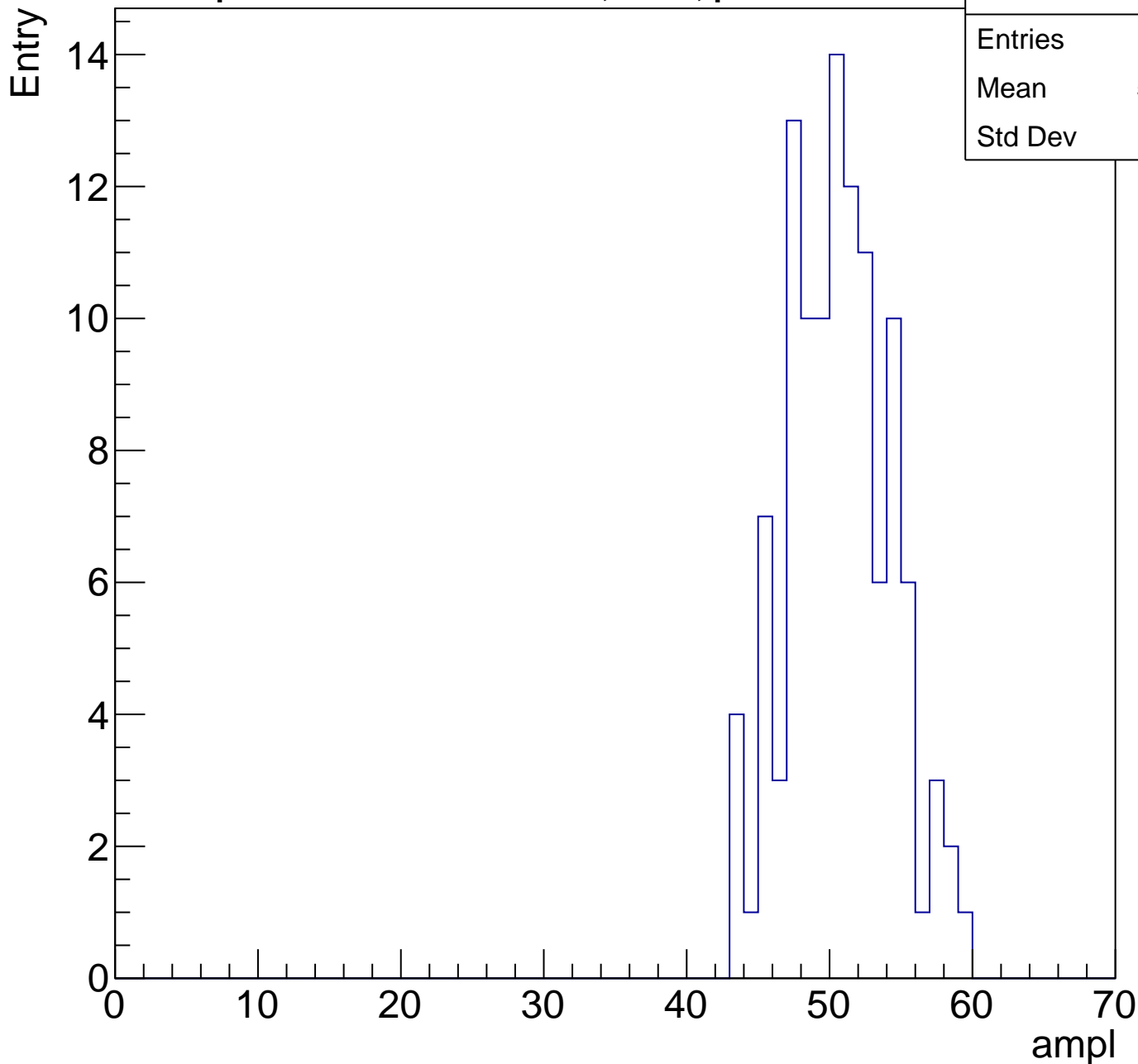
Gaus Width: 3.9492



B0L101S, U22-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 114 |
| Mean | 50.21 |
| Std Dev | 3.54 |



B0L101S, U22-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 55.97 |
| Std Dev | 5.892 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

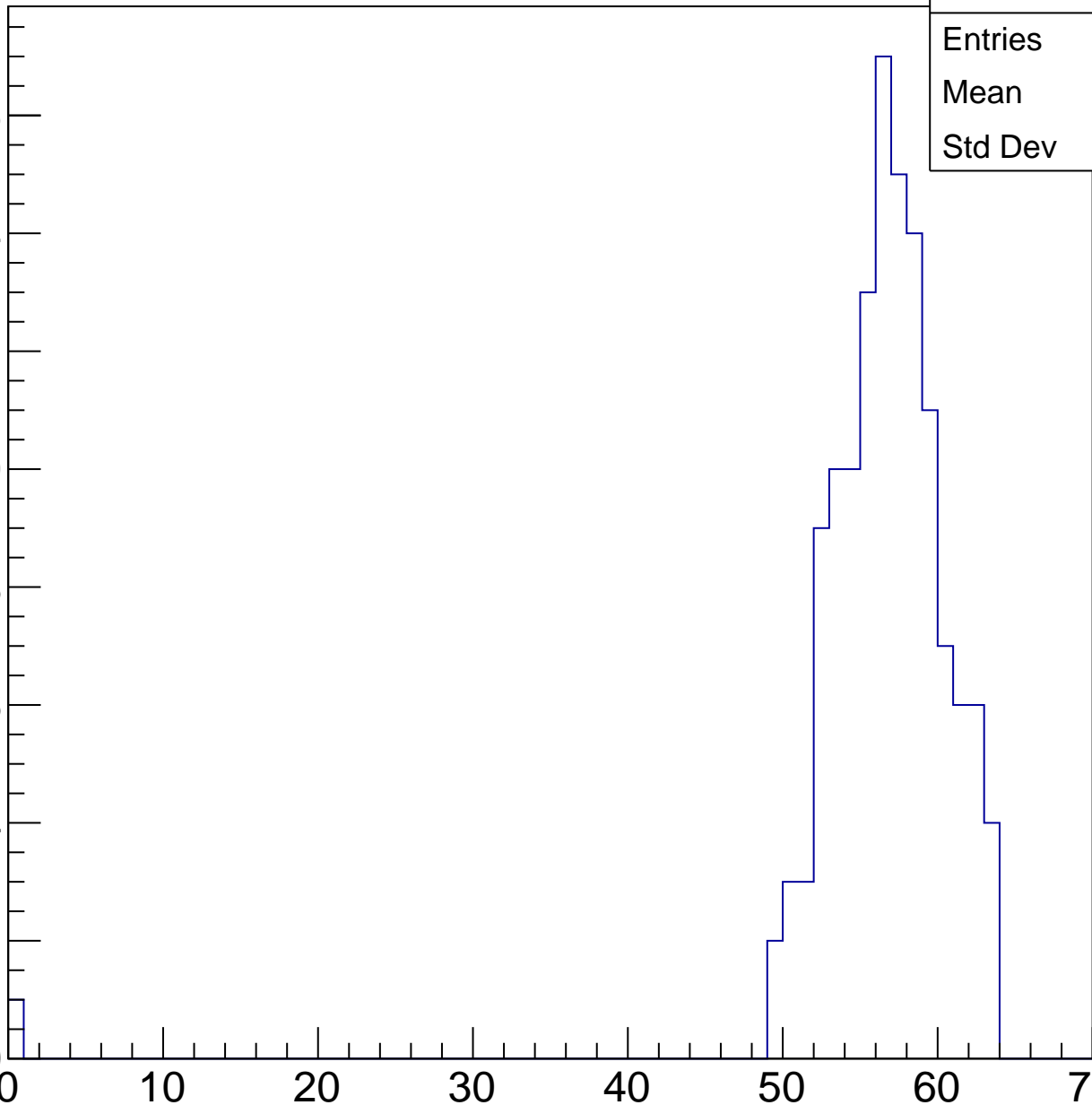
40

50

60

70

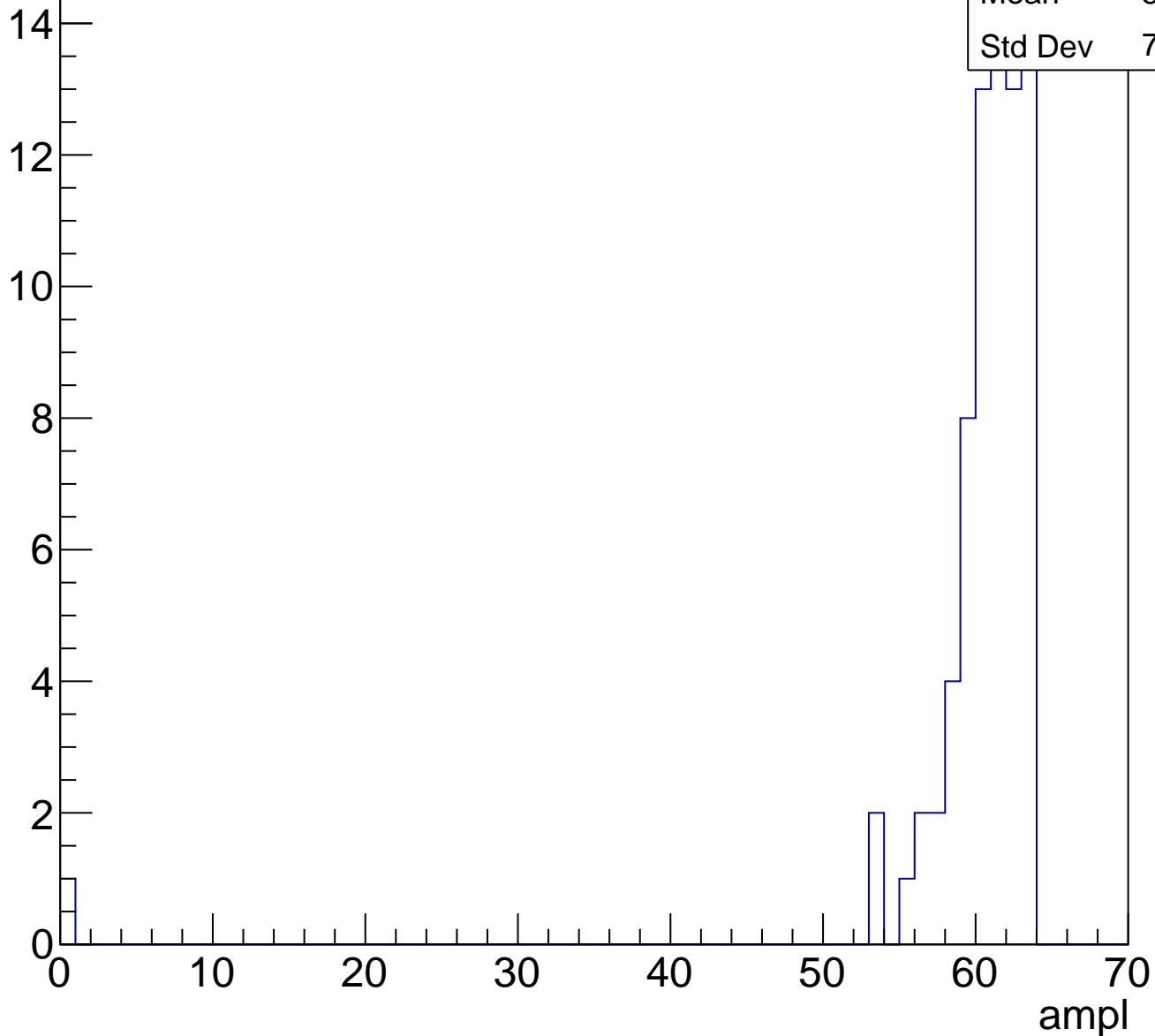
ampl



B0L101S, U22-ch70, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch71, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 129 |
| Mean | 29.96 |
| Std Dev | 4.919 |

Gaus mean : 31.0303

Gaus Width: 3.2153

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

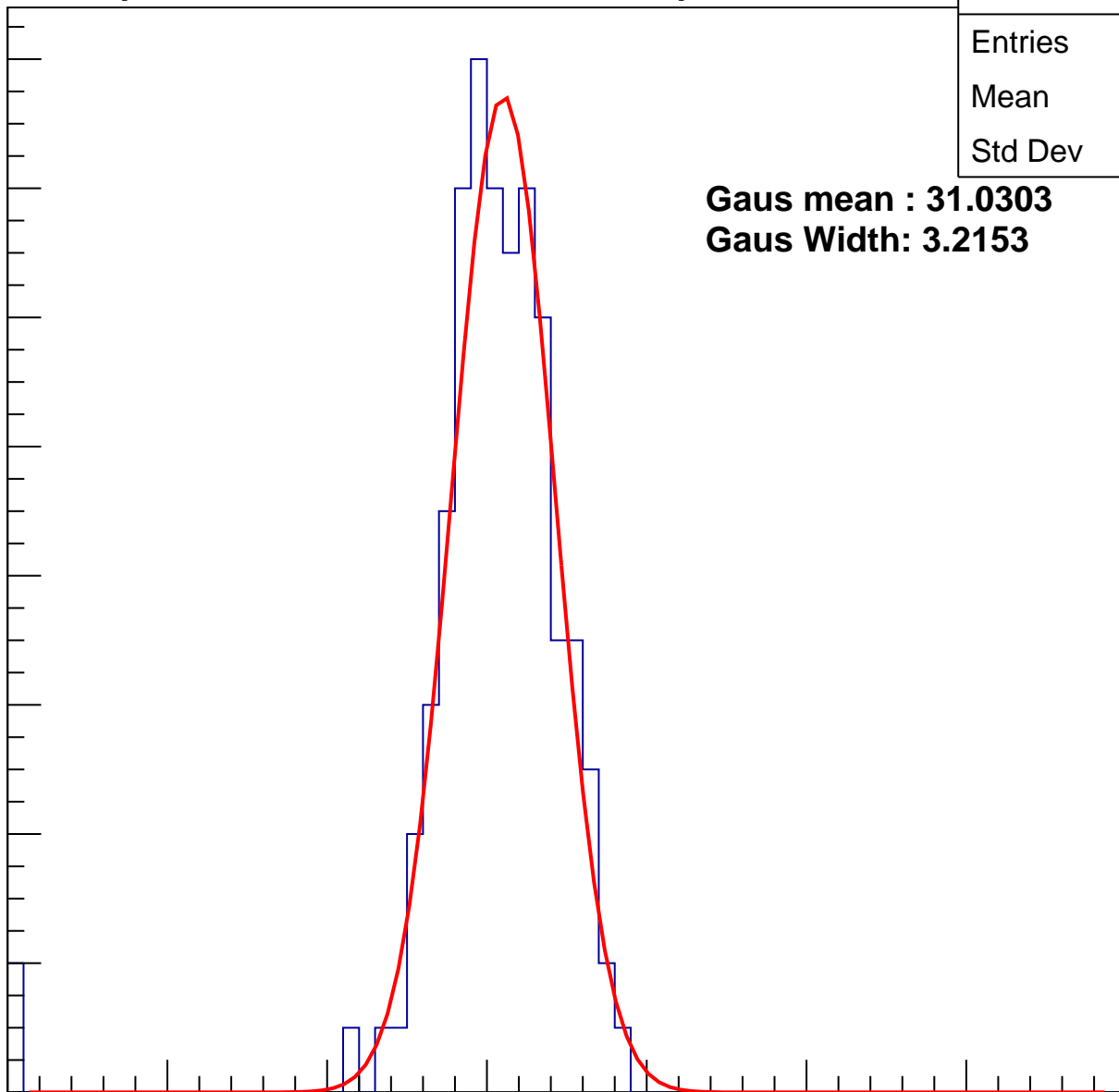
40

50

60

70

ampl



B0L101S, U22-ch71, adc1

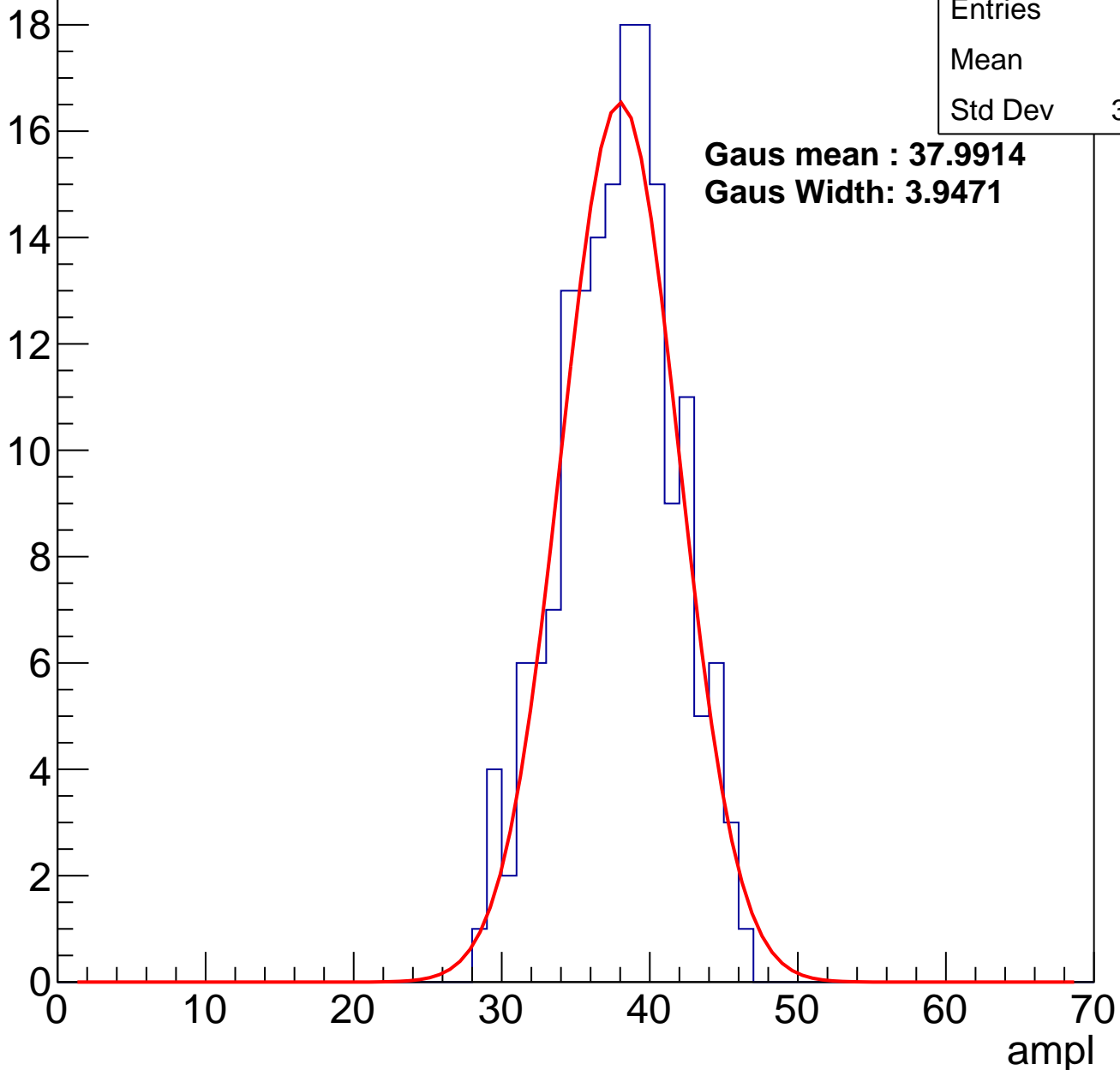
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 167 |
| Mean | 37.4 |
| Std Dev | 3.838 |

Gaus mean : 37.9914

Gaus Width: 3.9471

Entry



B0L101S, U22-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

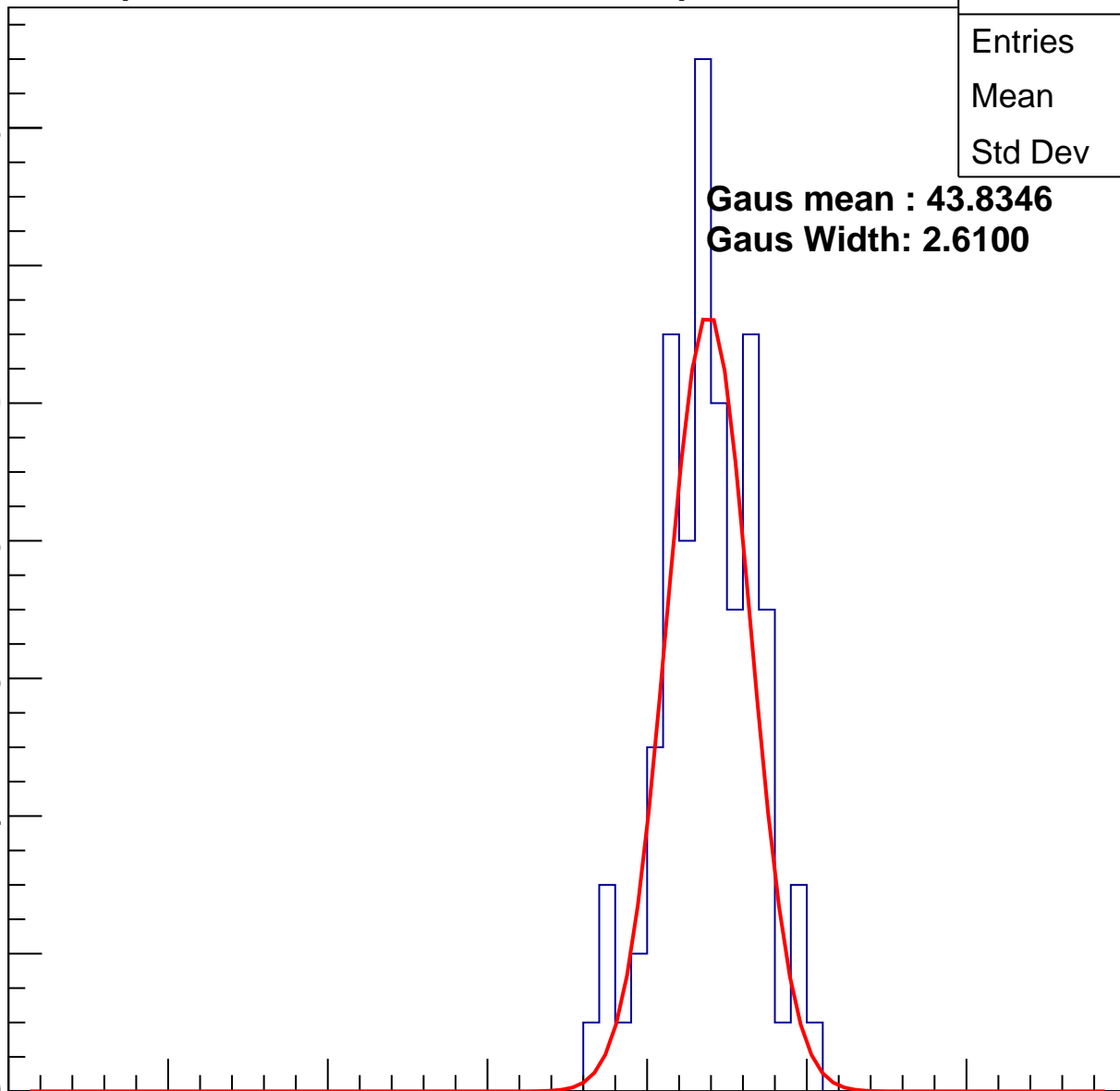
| | |
|---------|-------|
| Entries | 86 |
| Mean | 43.37 |
| Std Dev | 2.914 |

Gaus mean : 43.8346

Gaus Width: 2.6100

0 10 20 30 40 50 60 70

ampl

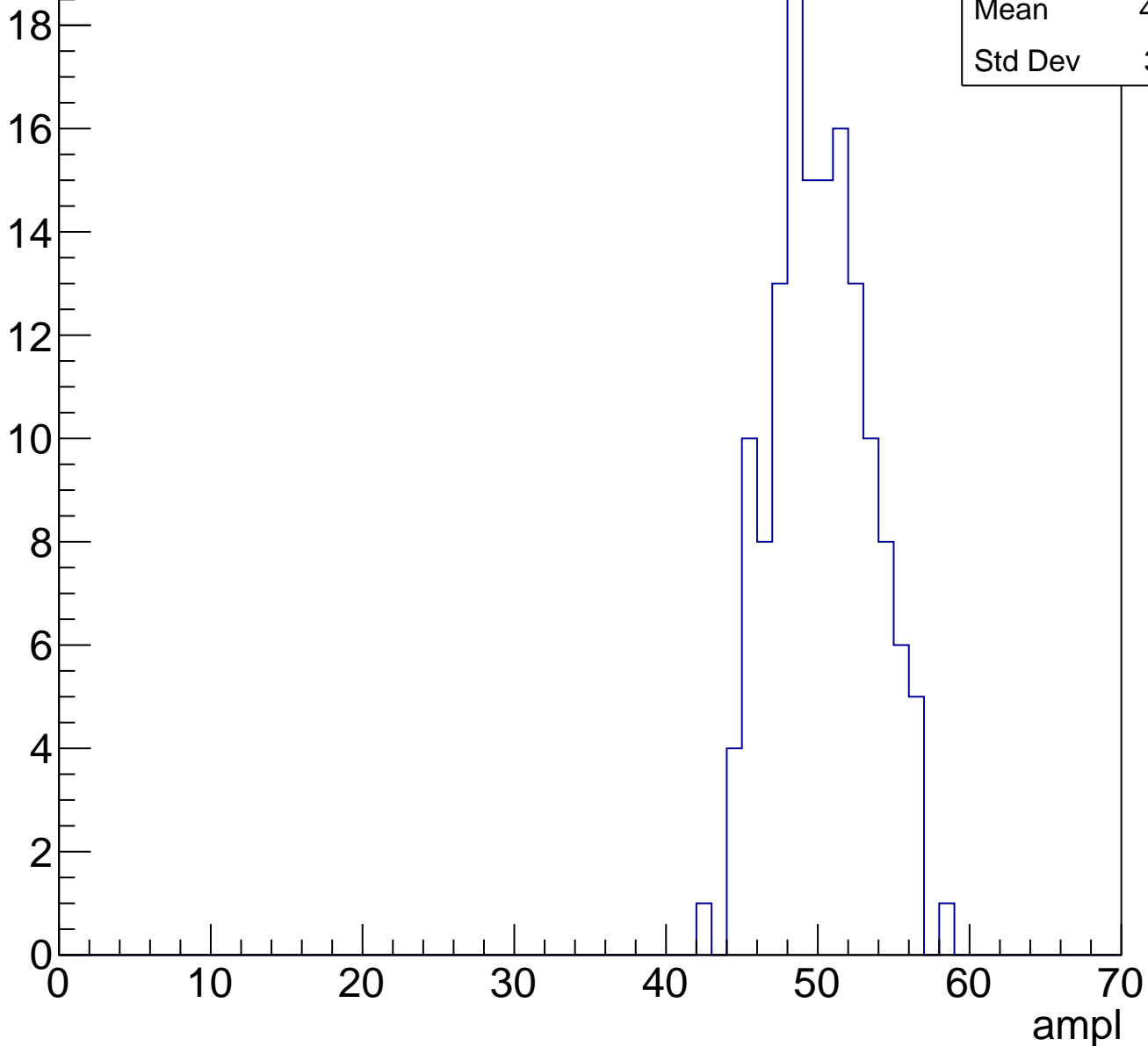


B0L101S, U22-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 49.76 |
| Std Dev | 3.191 |

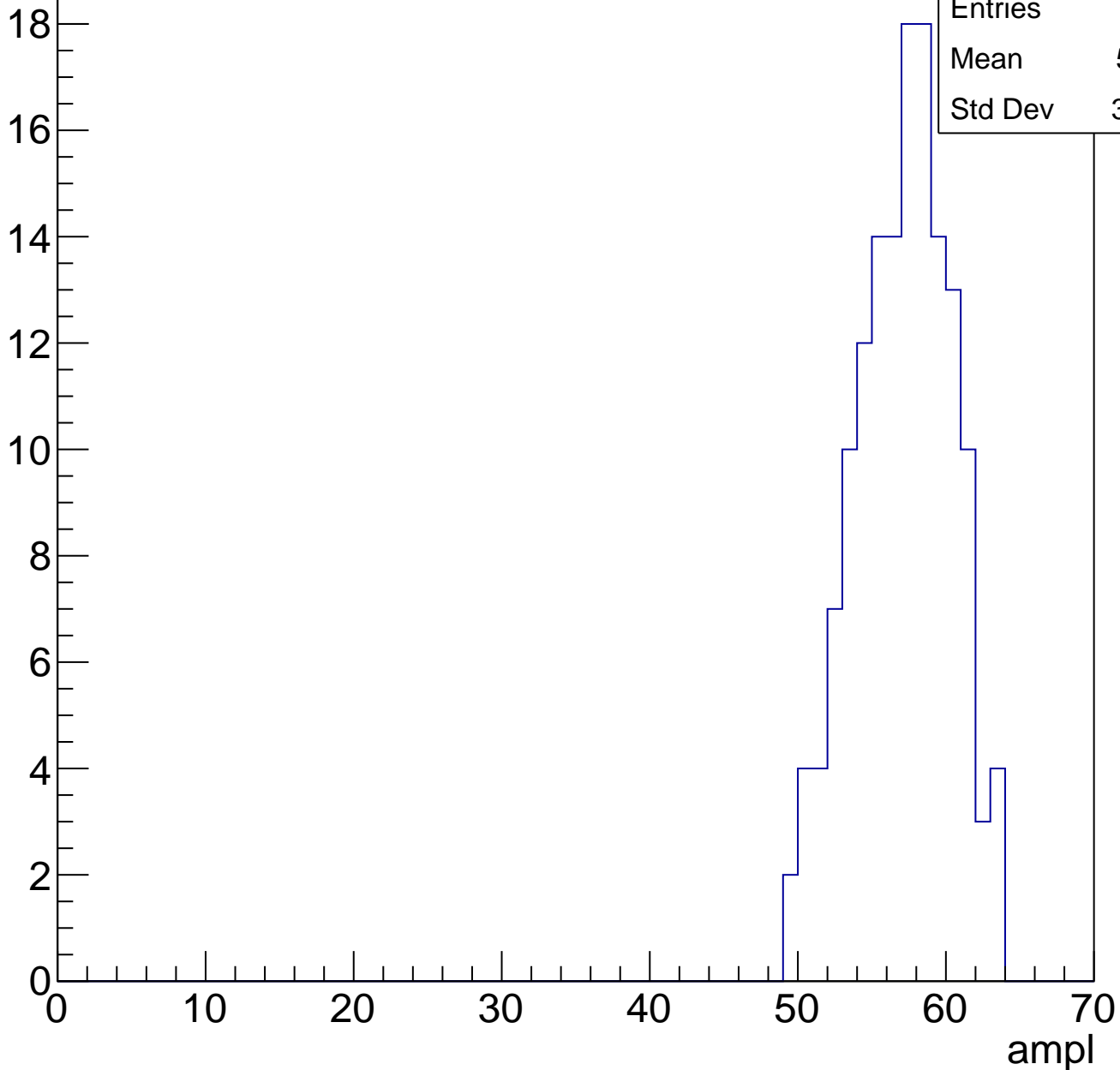
Entry



B0L101S, U22-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

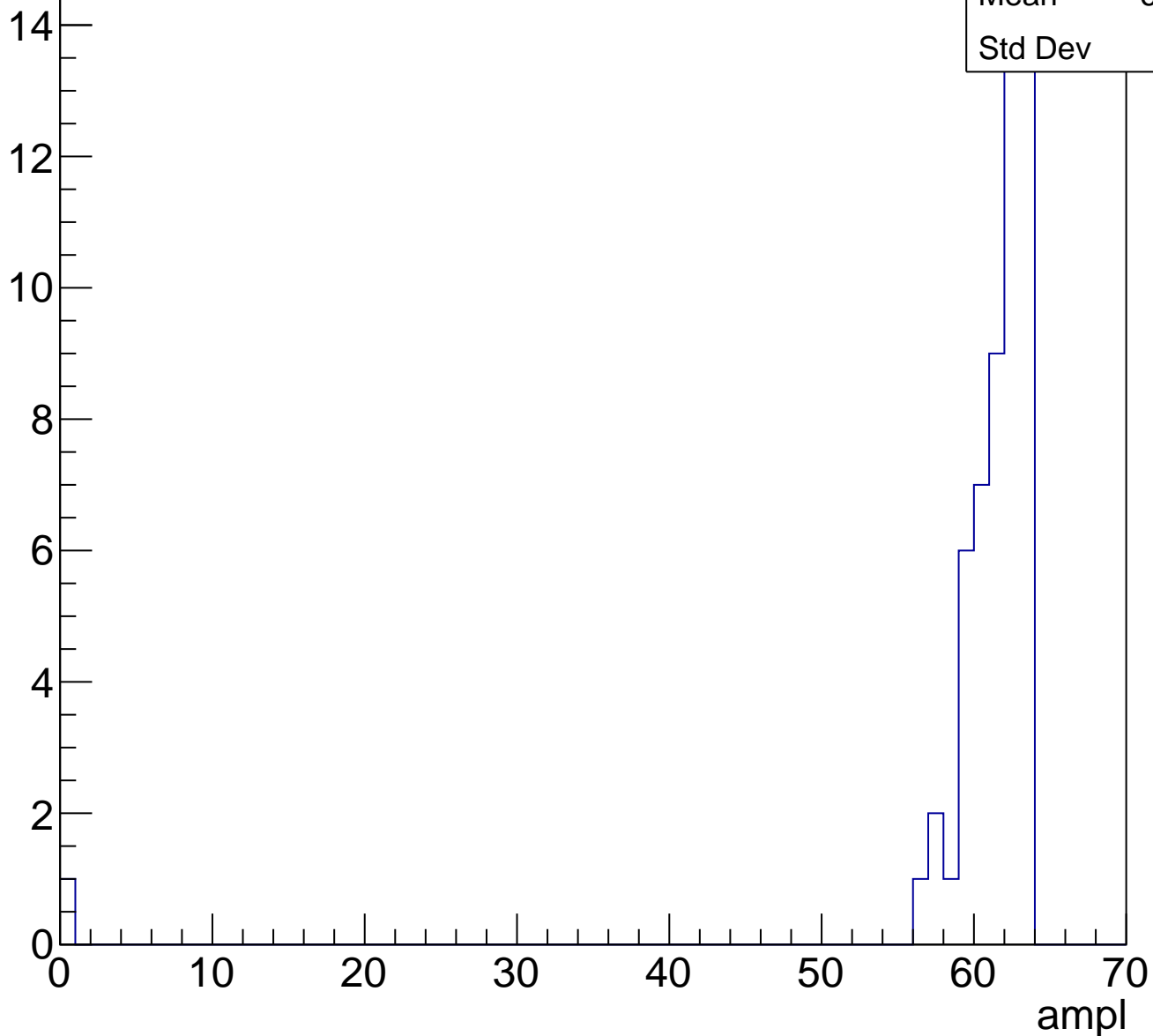


B0L101S, U22-ch71, adc5

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 56 |
| Mean | 60.05 |
| Std Dev | 8.28 |

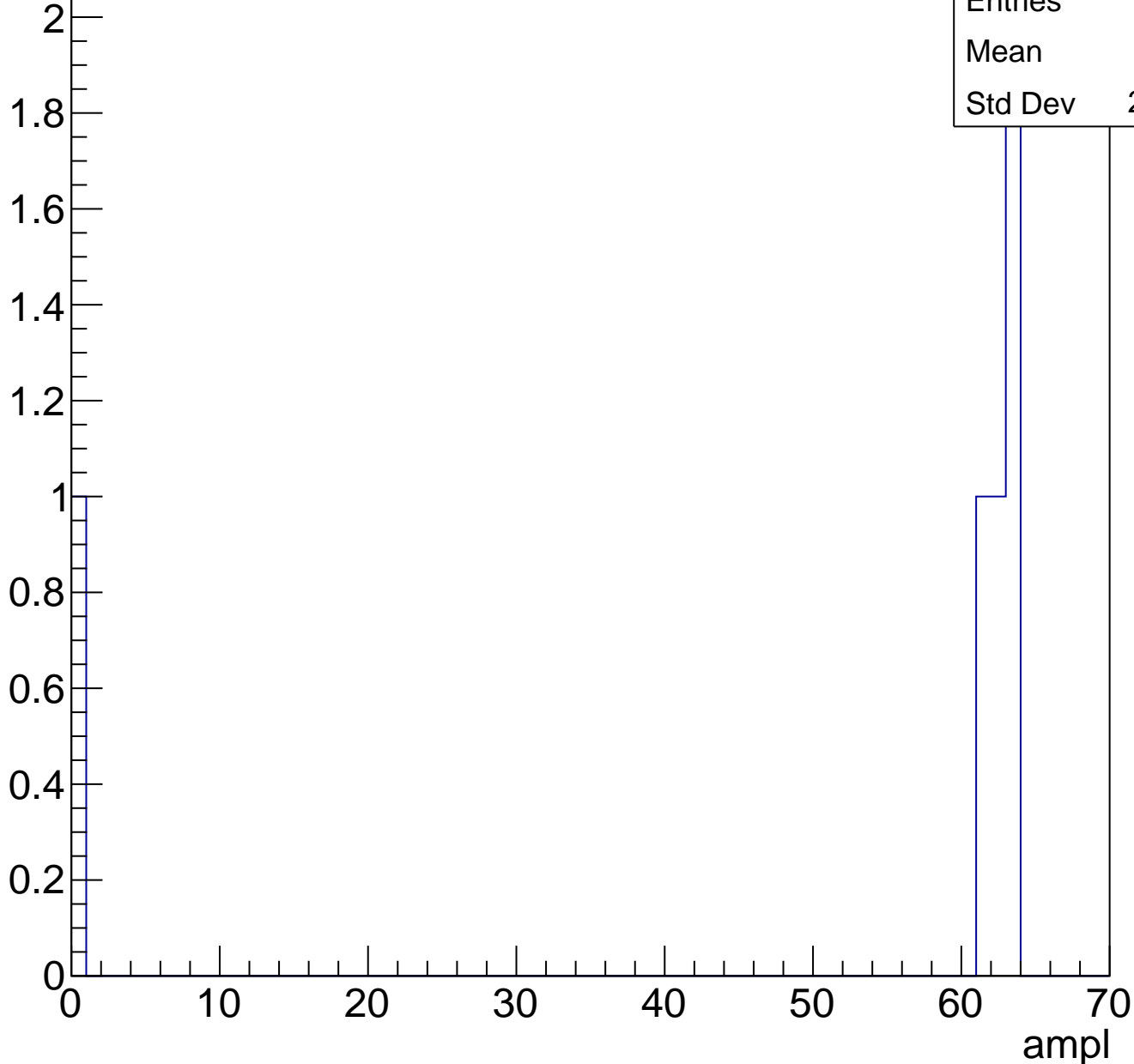
Entry



B0L101S, U22-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

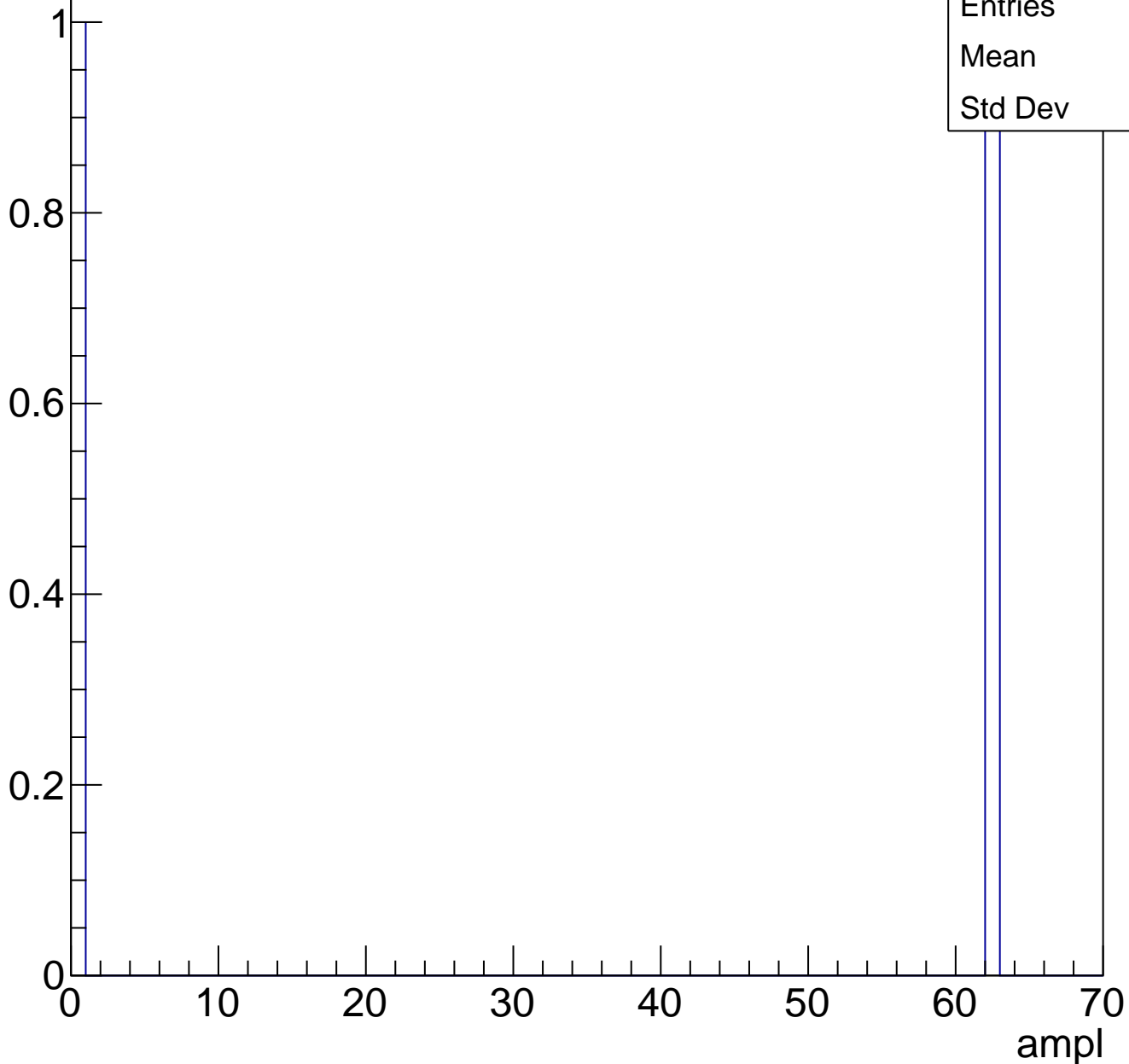
Entry



B0L101S, U22-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 29.44 |
| Std Dev | 3.635 |

Gaus mean : 29.6234

Gaus Width: 3.7970

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 37.05 |
| Std Dev | 3.86 |

Gaus mean : 36.9936

Gaus Width: 3.2836

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

145

Mean

44.51

Std Dev

3.815

Gaus mean : 45.0650

Gaus Width: 3.9035

0

10

20

30

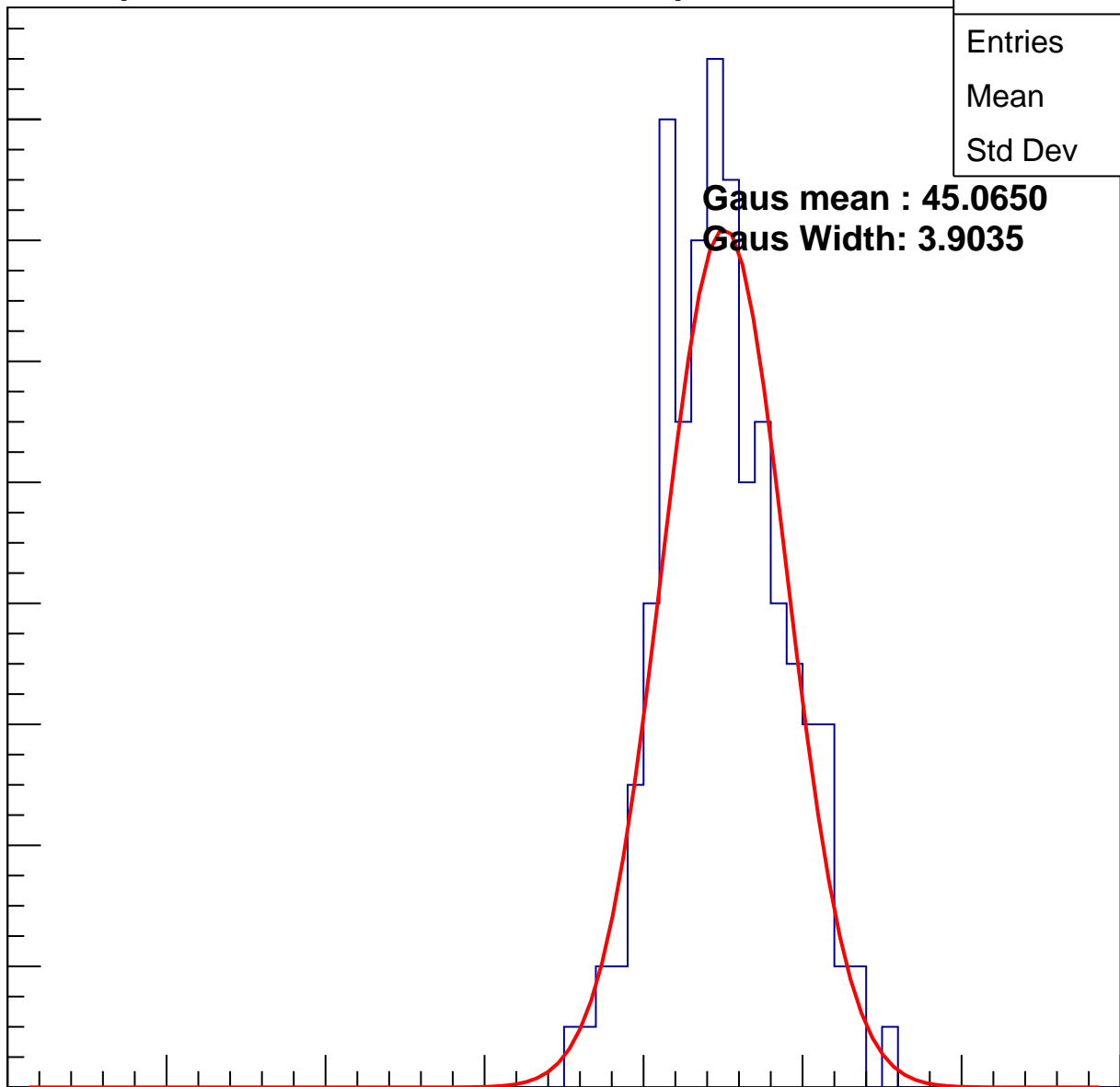
40

50

60

70

ampl



B0L101S, U22-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

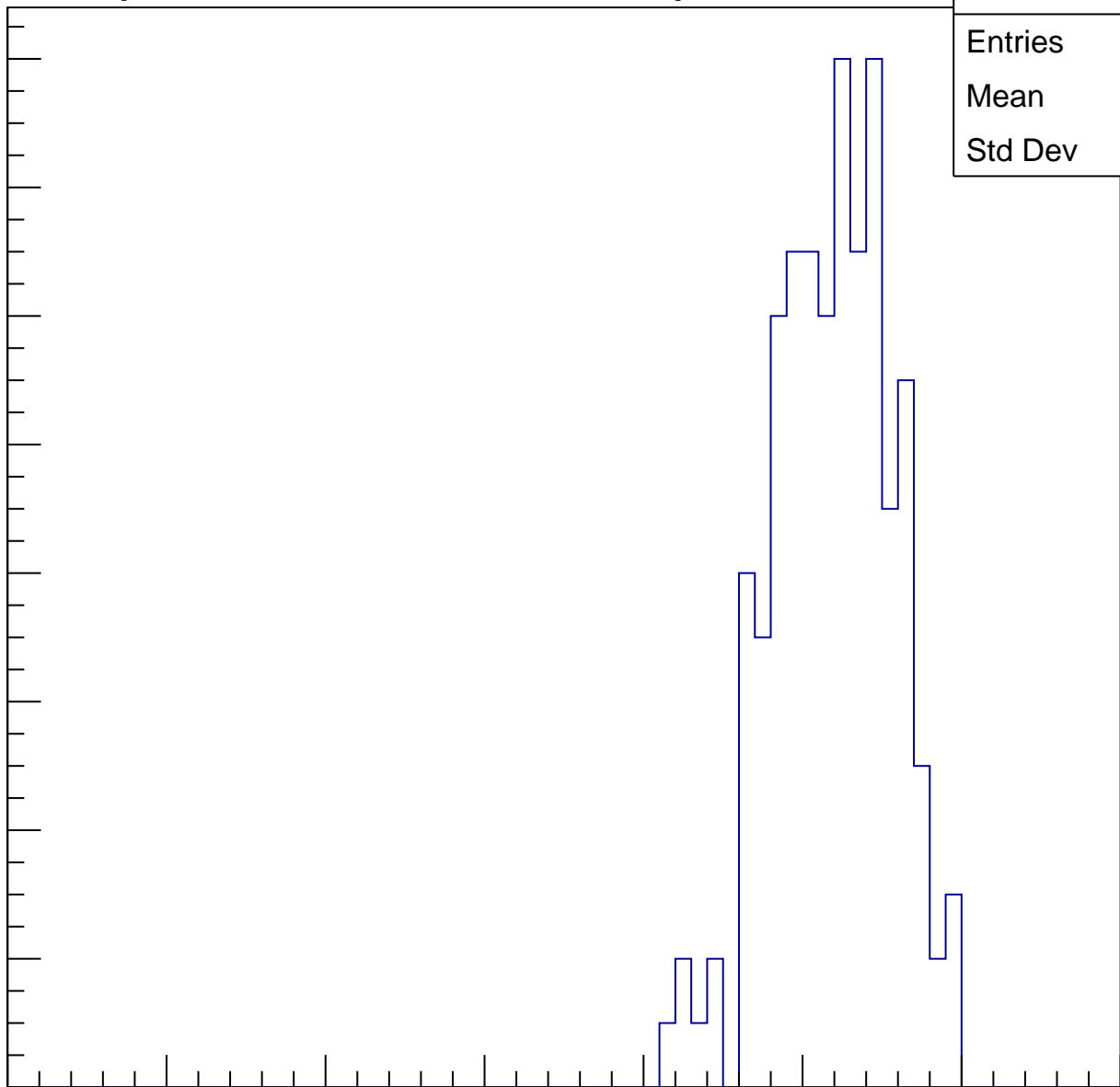
| | |
|---------|-------|
| Entries | 146 |
| Mean | 51.38 |
| Std Dev | 3.691 |

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

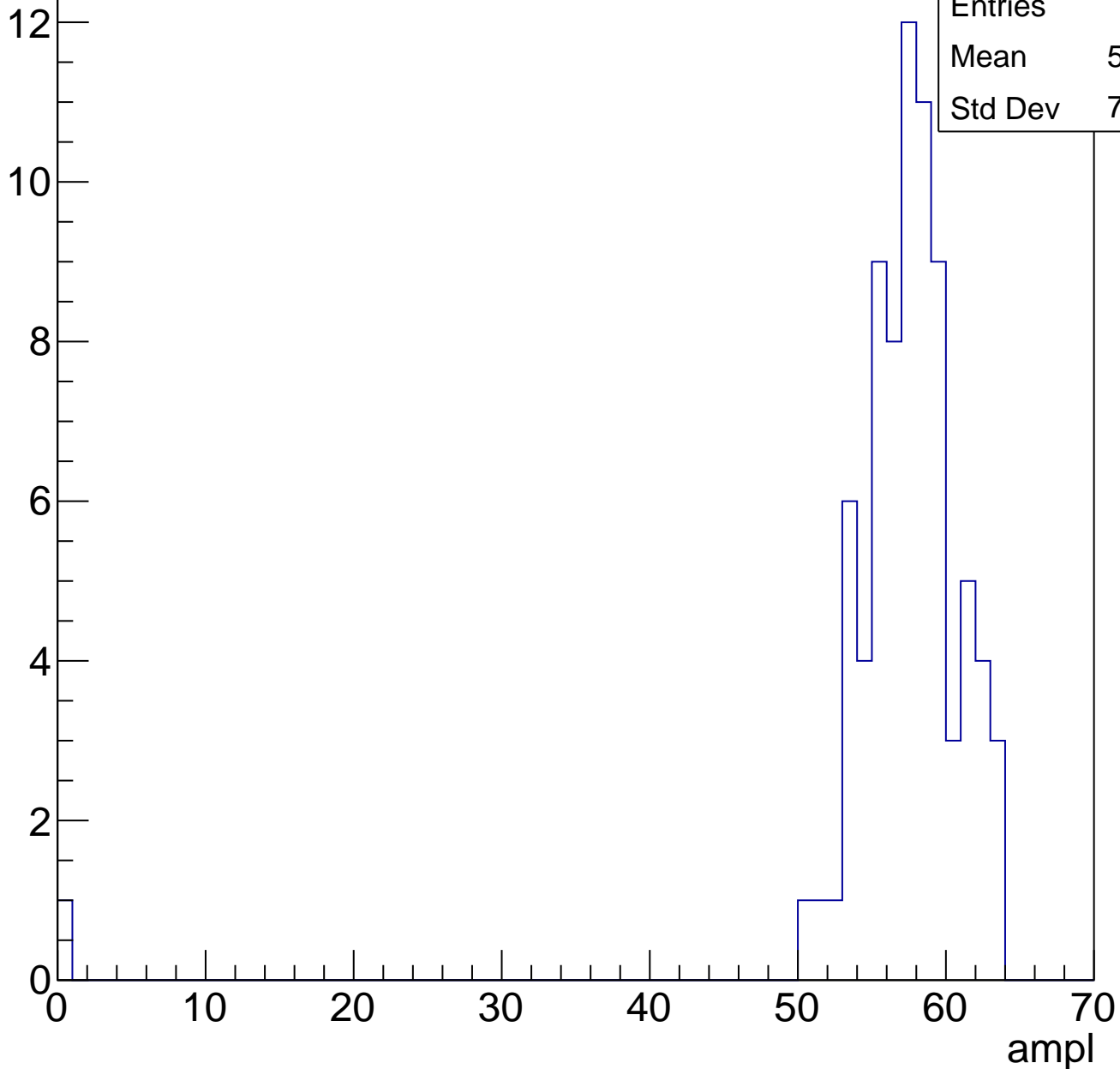


B0L101S, U22-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 78 |
| Mean | 56.47 |
| Std Dev | 7.049 |

Entry



B0L101S, U22-ch72, adc5

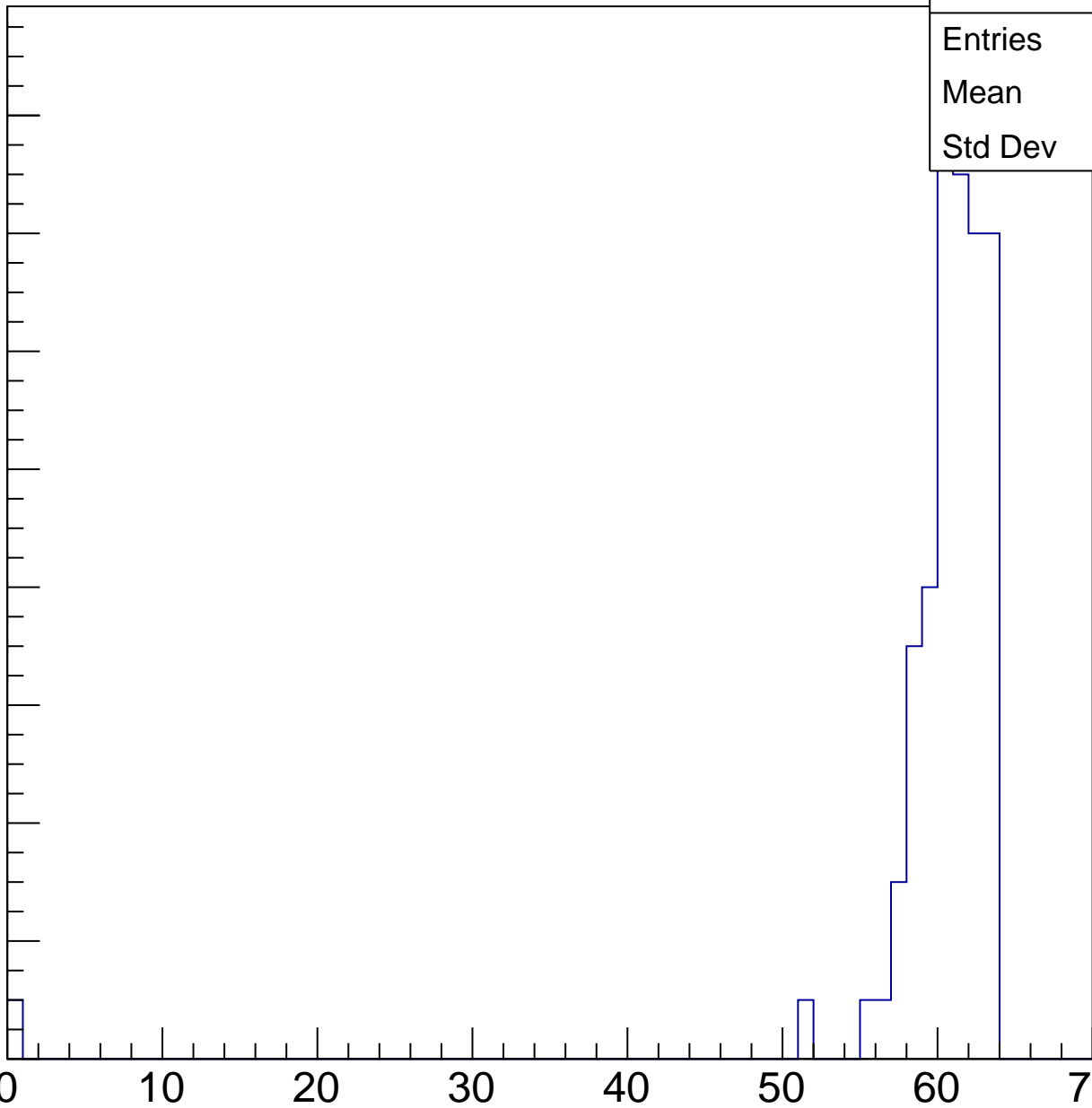
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 82 |
| Mean | 59.71 |
| Std Dev | 6.962 |

ampl



B0L101S, U22-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L101S, U22-ch73, adc0

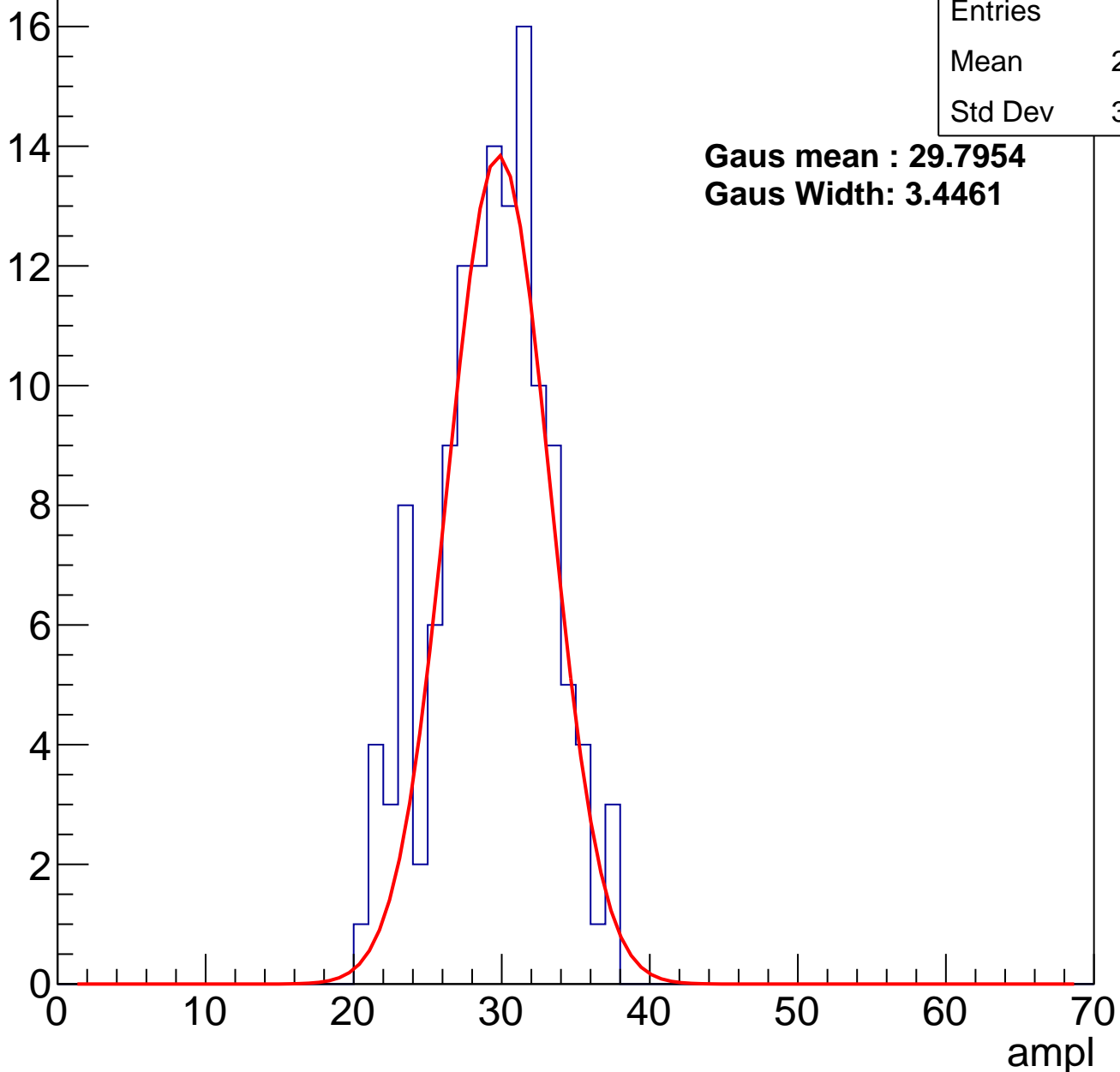
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 28.88 |
| Std Dev | 3.772 |

Gaus mean : 29.7954

Gaus Width: 3.4461

Entry



B0L101S, U22-ch73, adc1

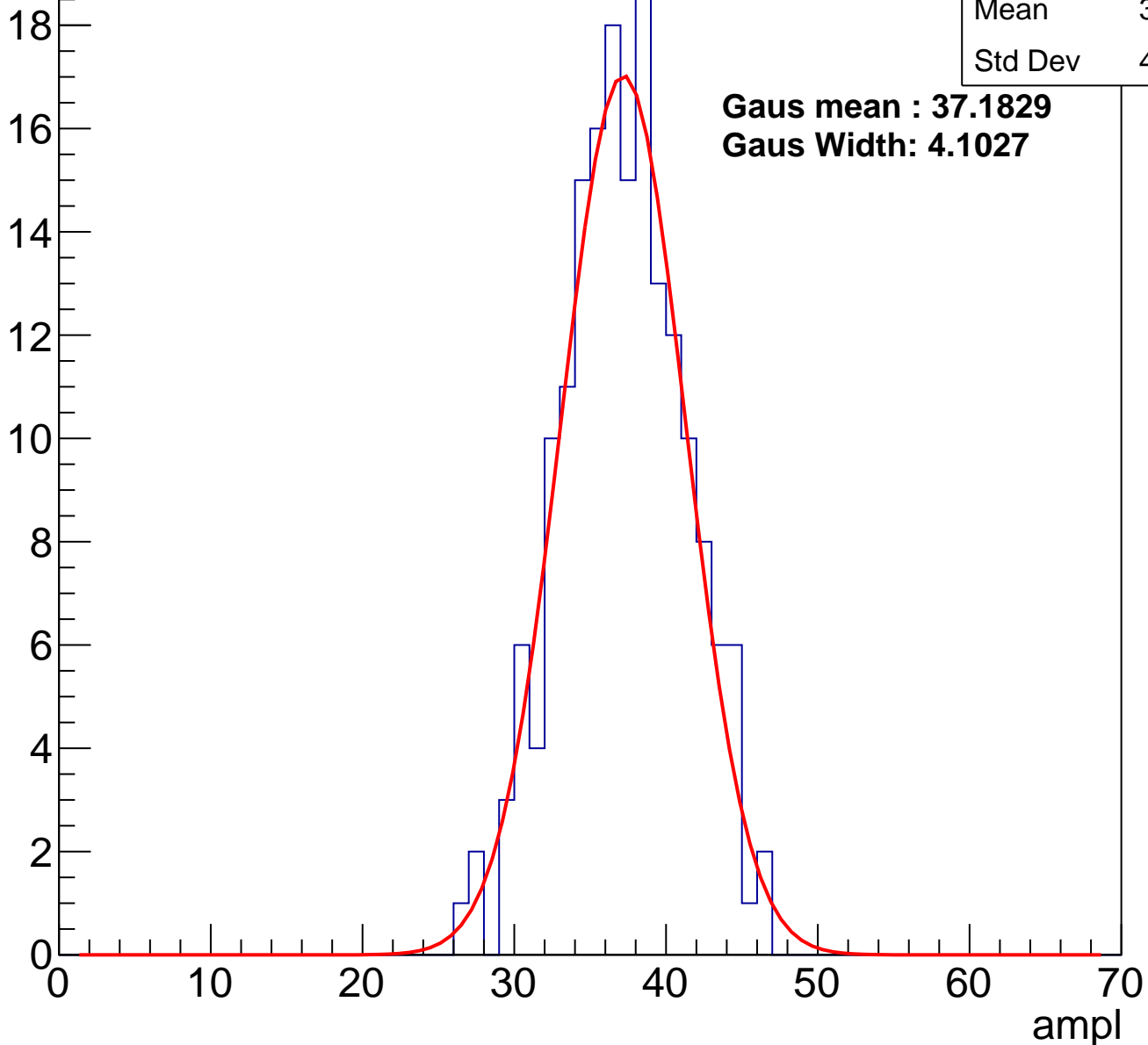
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 178 |
| Mean | 36.75 |
| Std Dev | 4.007 |

Gaus mean : 37.1829

Gaus Width: 4.1027

Entry



B0L101S, U22-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

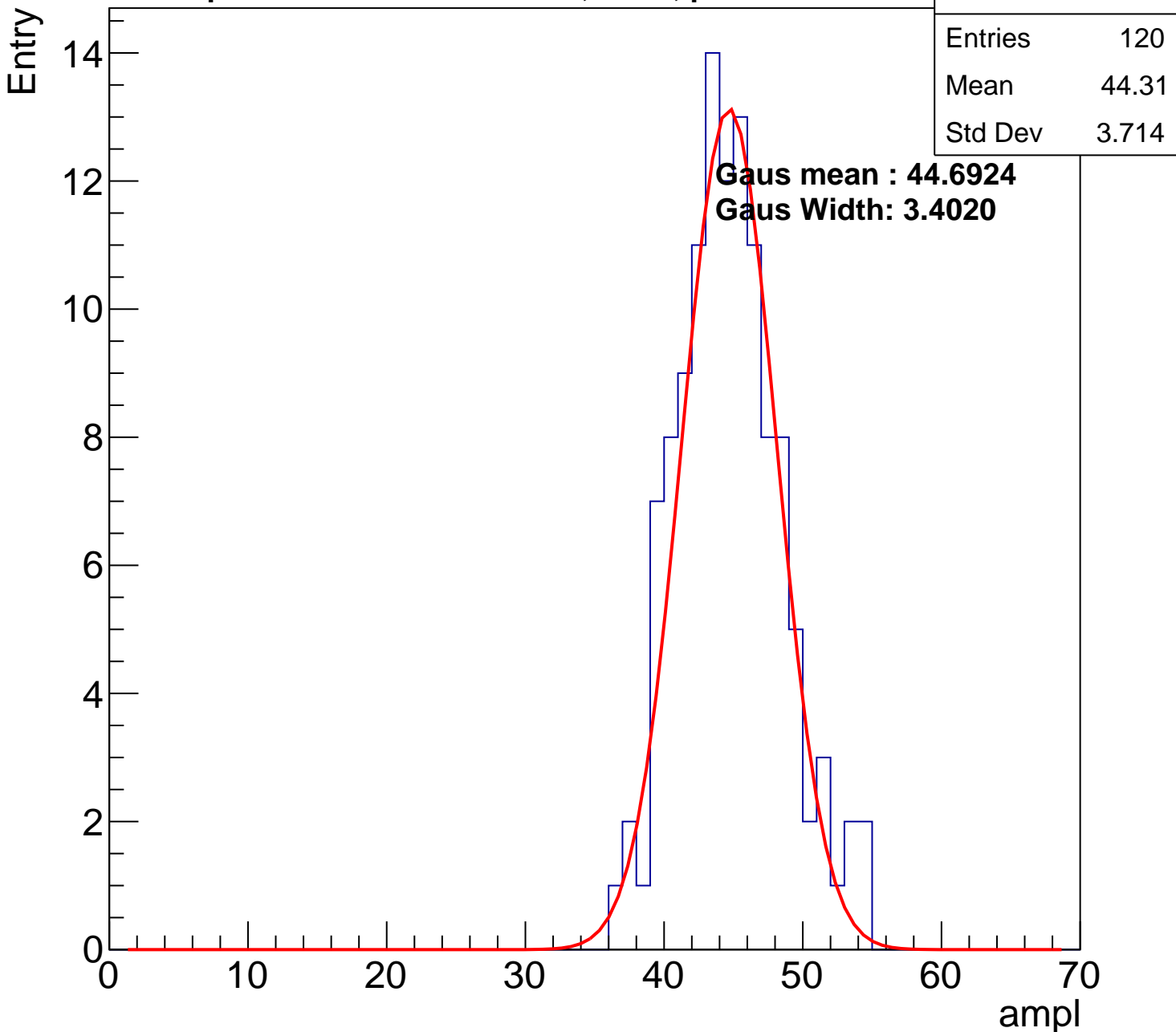
| | |
|---------|-------|
| Entries | 120 |
| Mean | 44.31 |
| Std Dev | 3.714 |

Gaus mean : 44.6924

Gaus Width: 3.4020

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 50.63 |
| Std Dev | 3.564 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

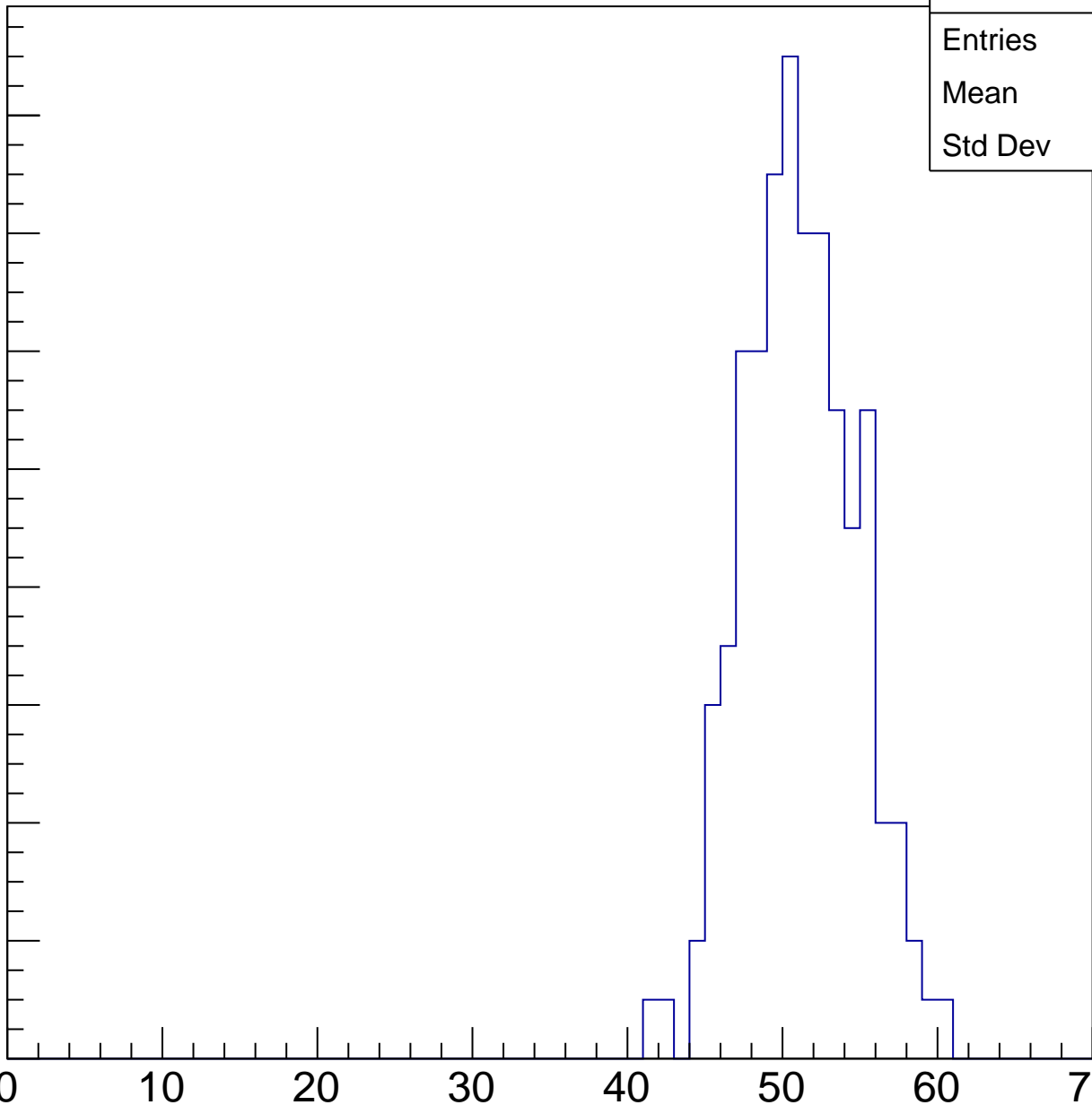
30

40

50

60

ampl



B0L101S, U22-ch73, adc4

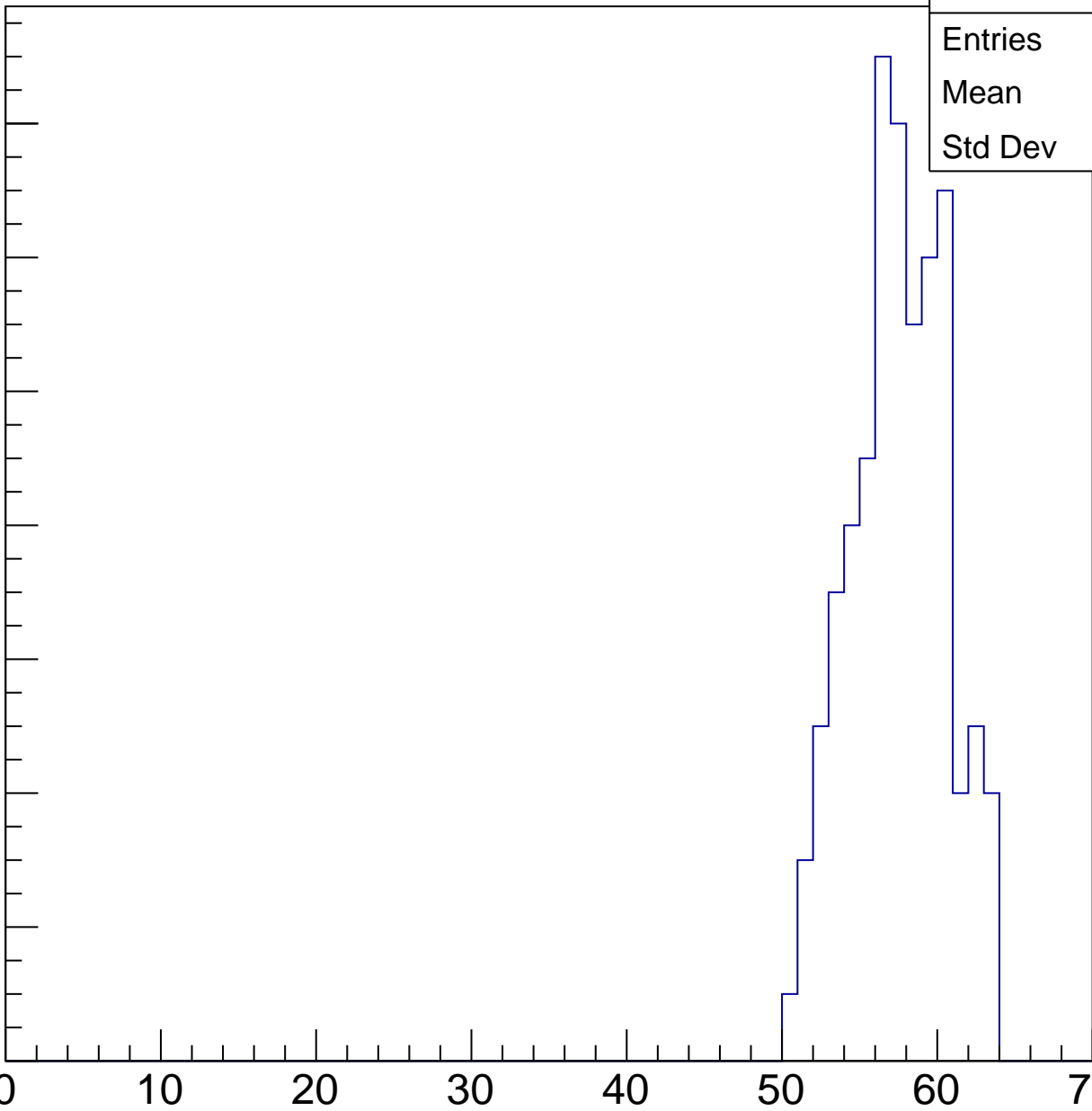
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 111 |
| Mean | 57.04 |
| Std Dev | 3.04 |

ampl



B0L101S, U22-ch73, adc5

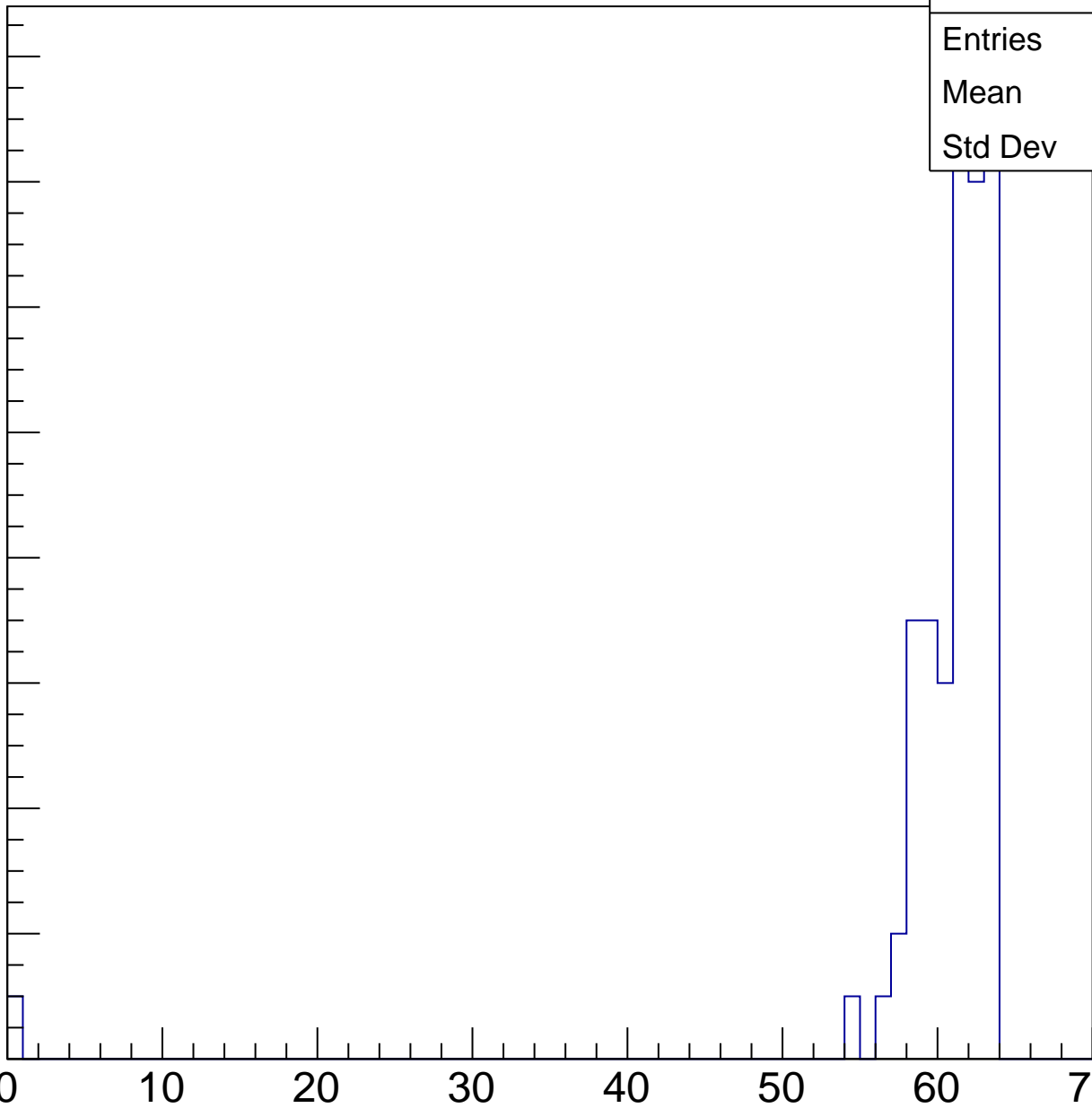
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 59.89 |
| Std Dev | 7.475 |

ampl



B0L101S, U22-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch74, adc0

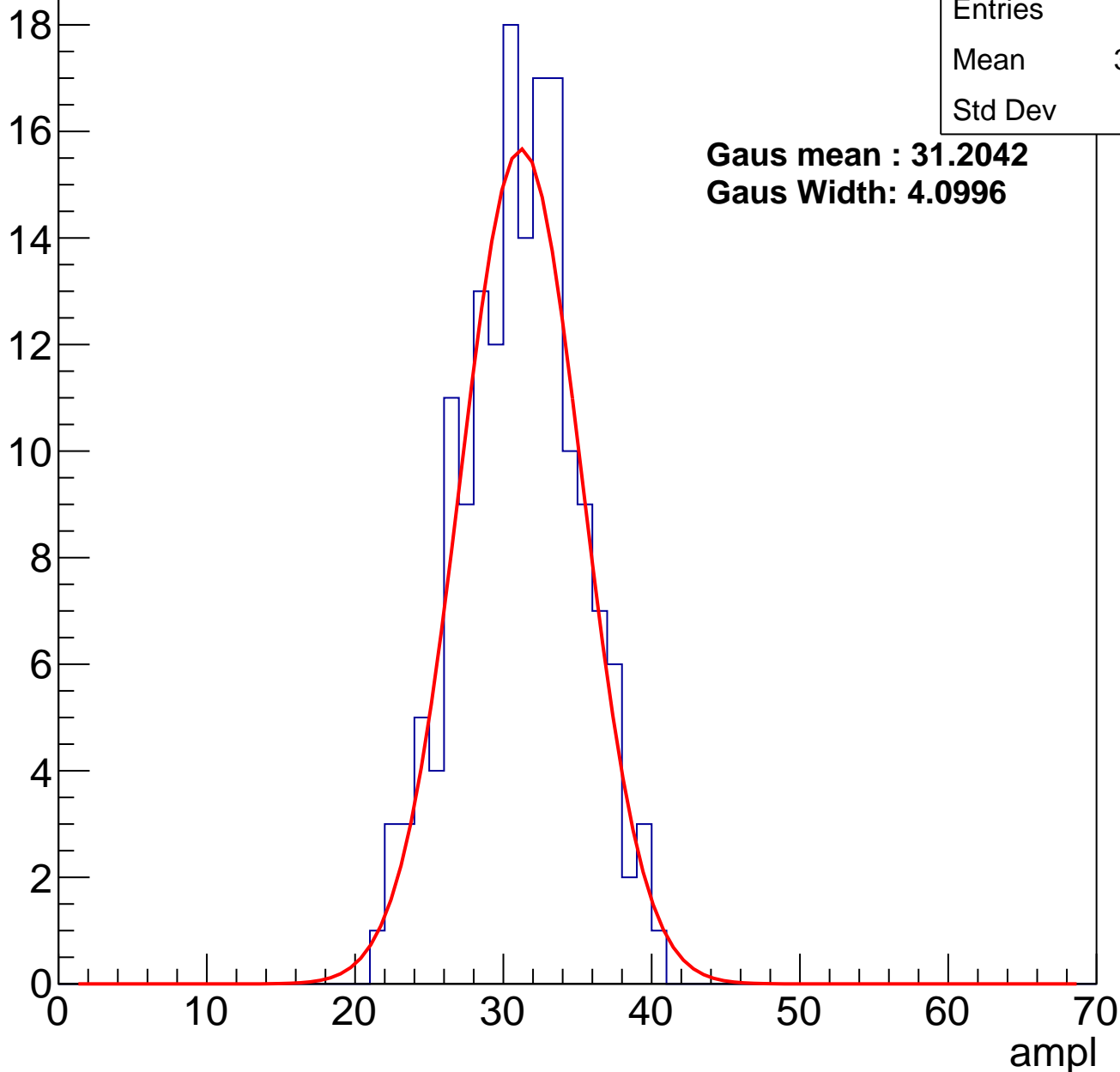
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 165 |
| Mean | 30.65 |
| Std Dev | 3.96 |

Gaus mean : 31.2042

Gaus Width: 4.0996

Entry

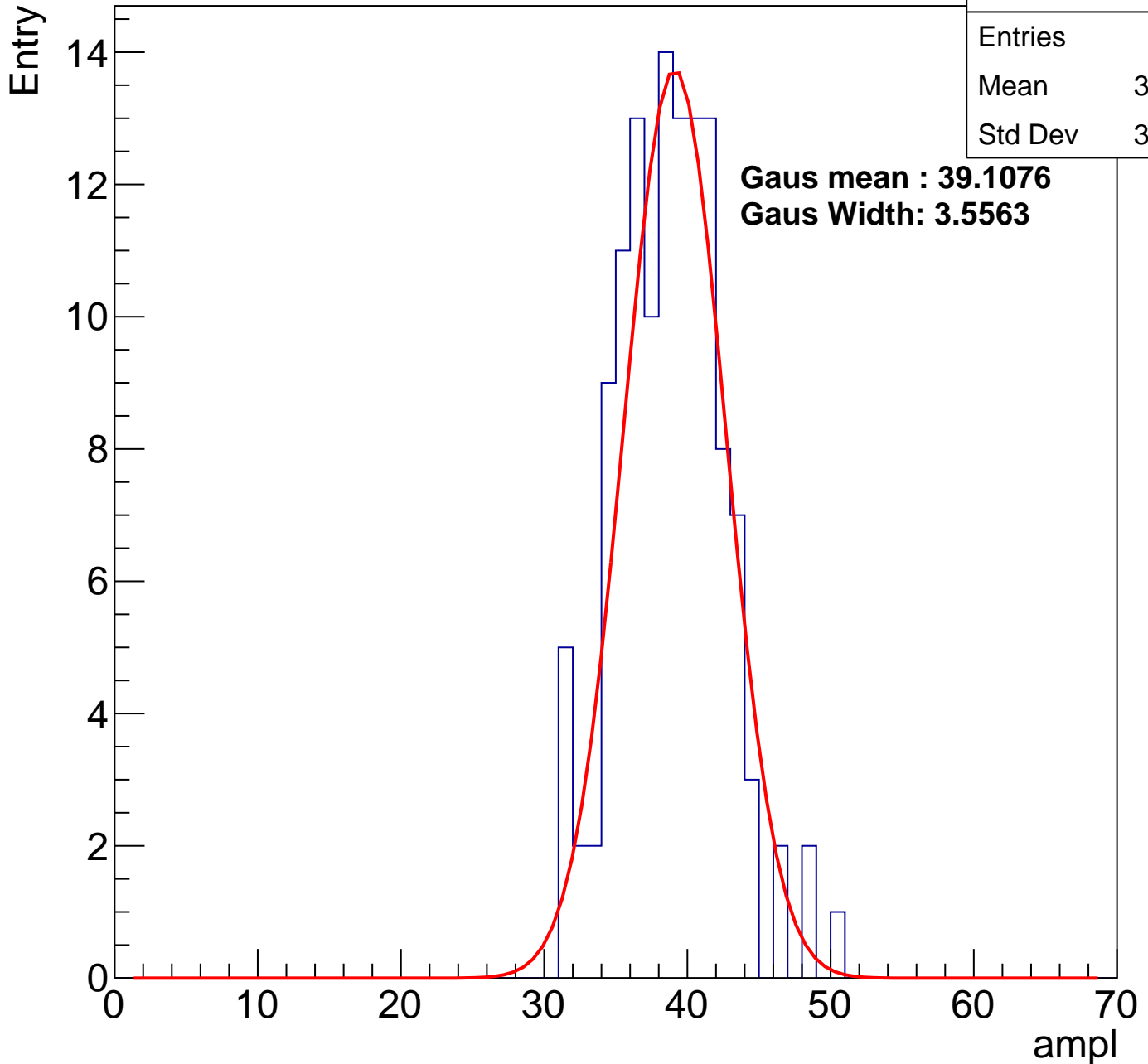


B0L101S, U22-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 128 |
| Mean | 38.38 |
| Std Dev | 3.664 |

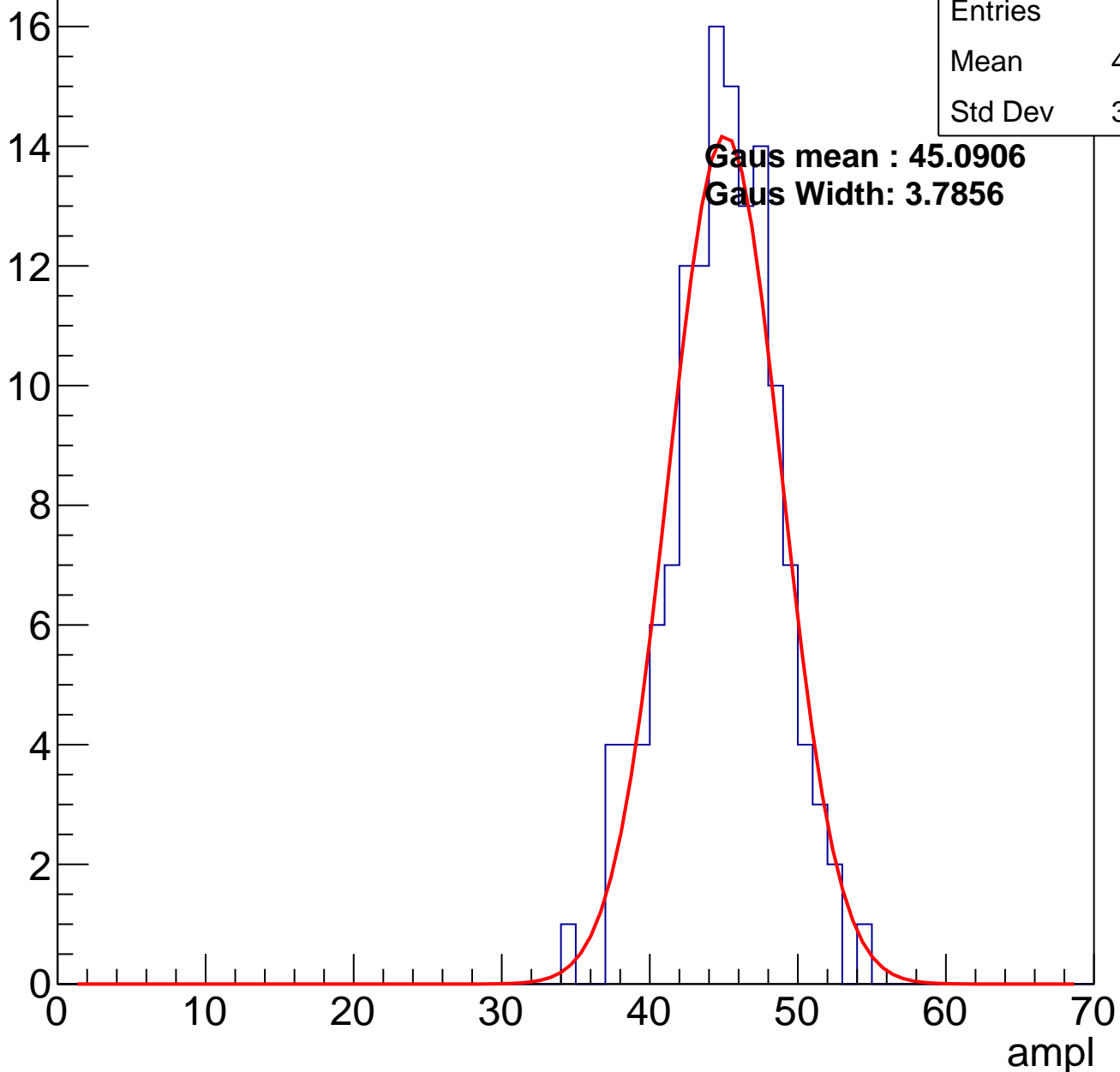
Gaus mean : 39.1076
Gaus Width: 3.5563



B0L101S, U22-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

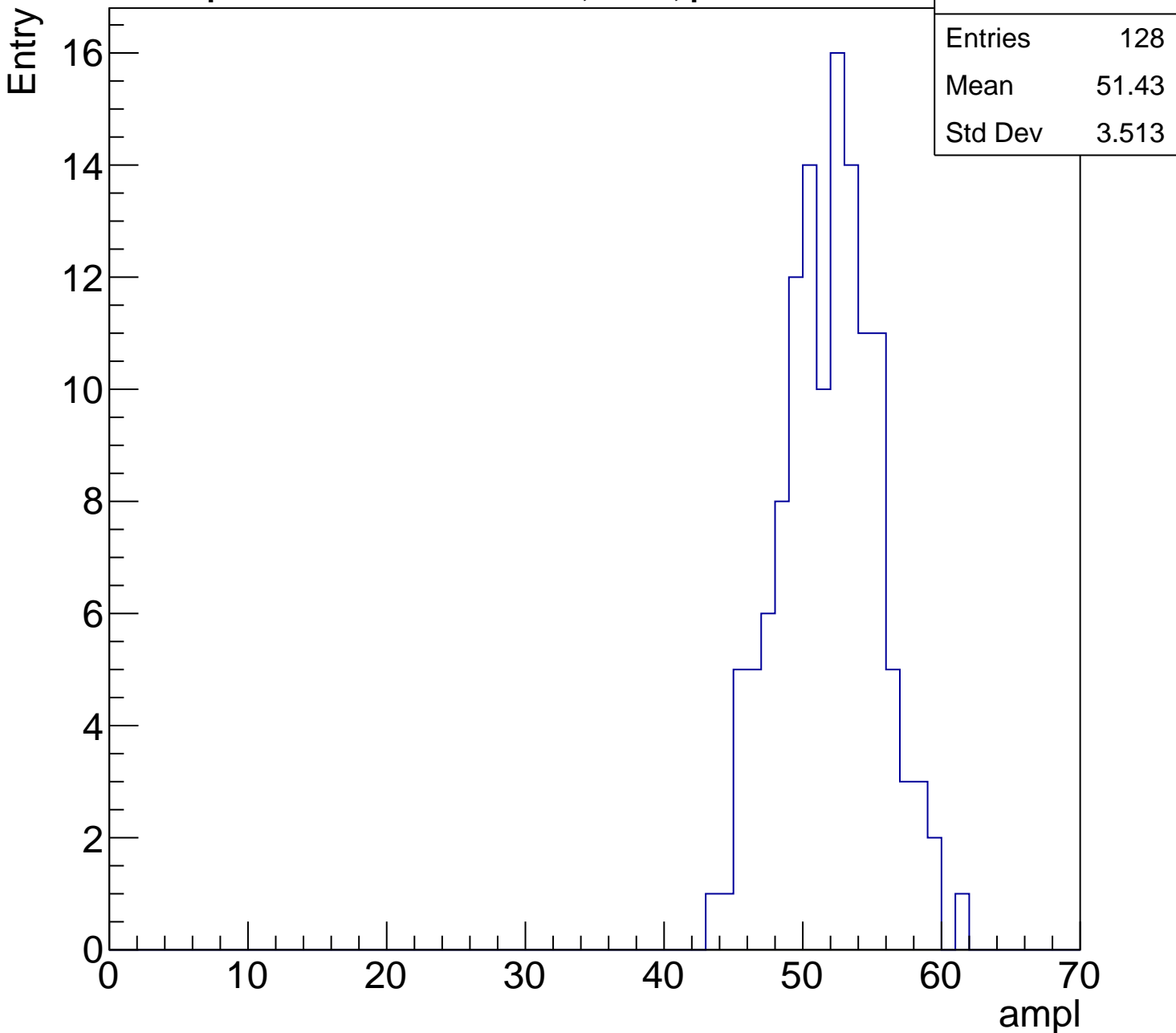
| | |
|---------|-------|
| Entries | 128 |
| Mean | 51.43 |
| Std Dev | 3.513 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch74, adc4

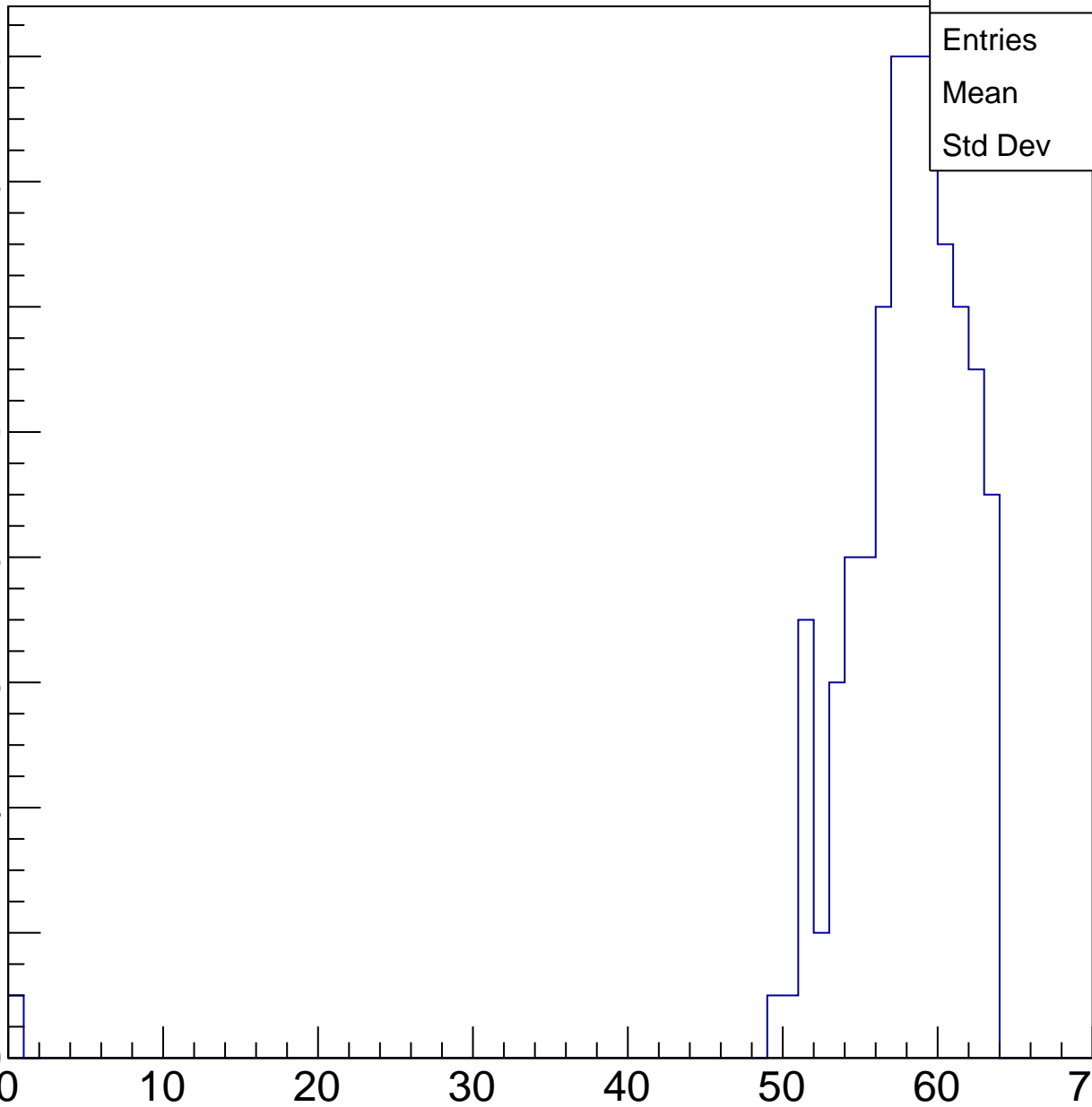
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 139 |
| Mean | 57.32 |
| Std Dev | 5.907 |

ampl

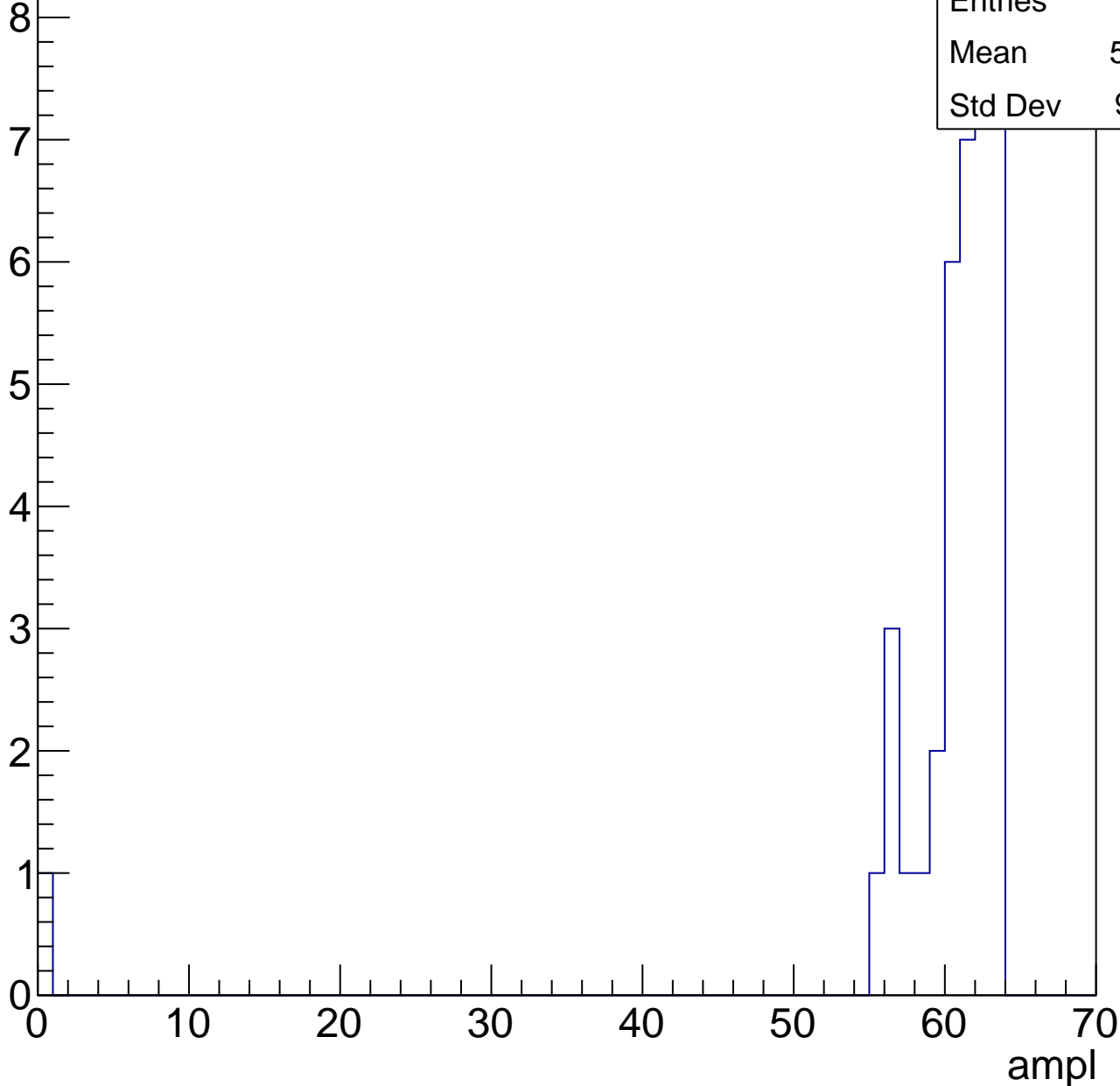


B0L101S, U22-ch74, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 59.03 |
| Std Dev | 9.951 |



B0L101S, U22-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



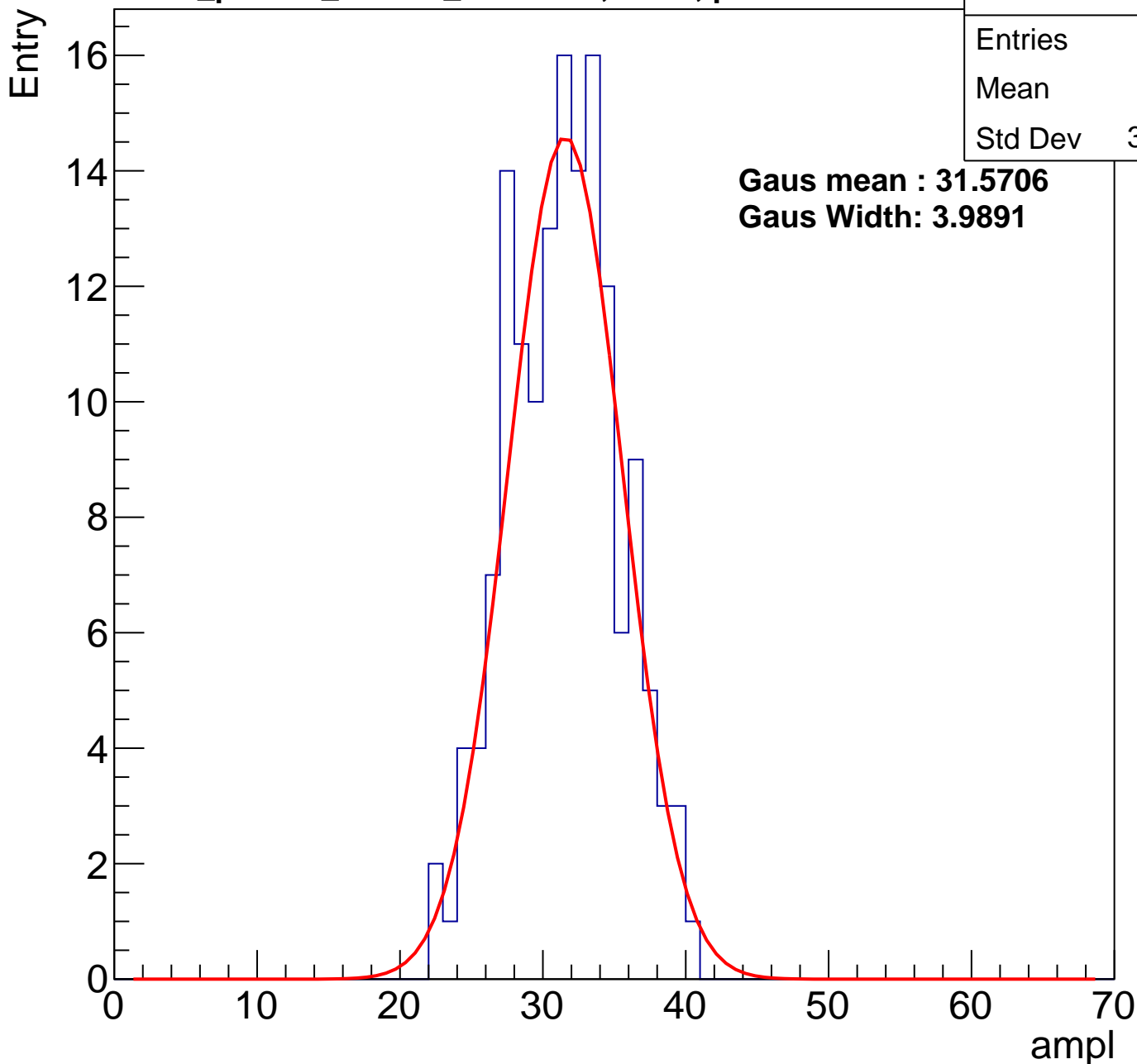
B0L101S, U22-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 31 |
| Std Dev | 3.838 |

Gaus mean : 31.5706

Gaus Width: 3.9891



B0L101S, U22-ch75, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 38.17 |
| Std Dev | 3.463 |

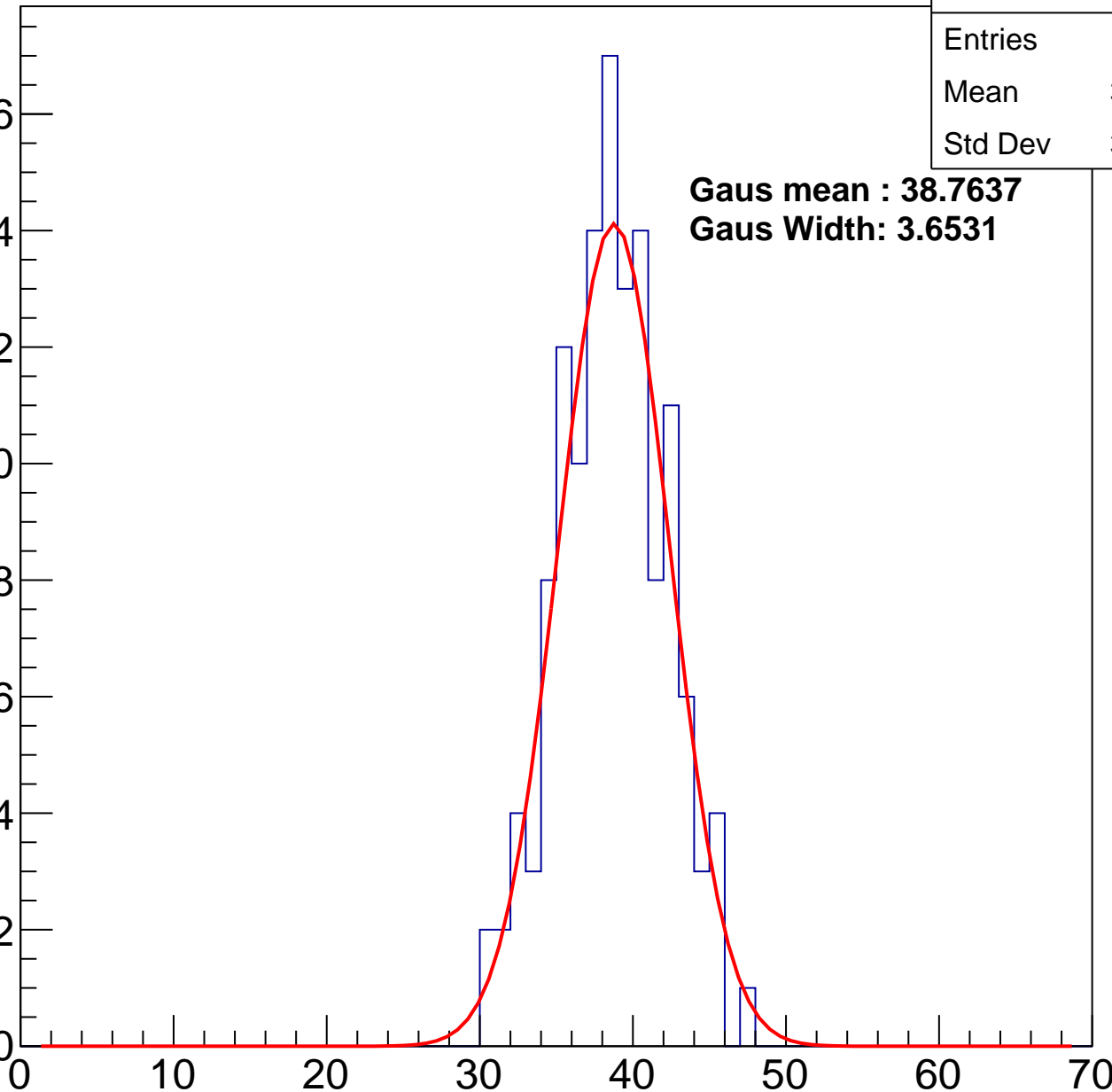
Gaus mean : 38.7637

Gaus Width: 3.6531

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch75, adc2

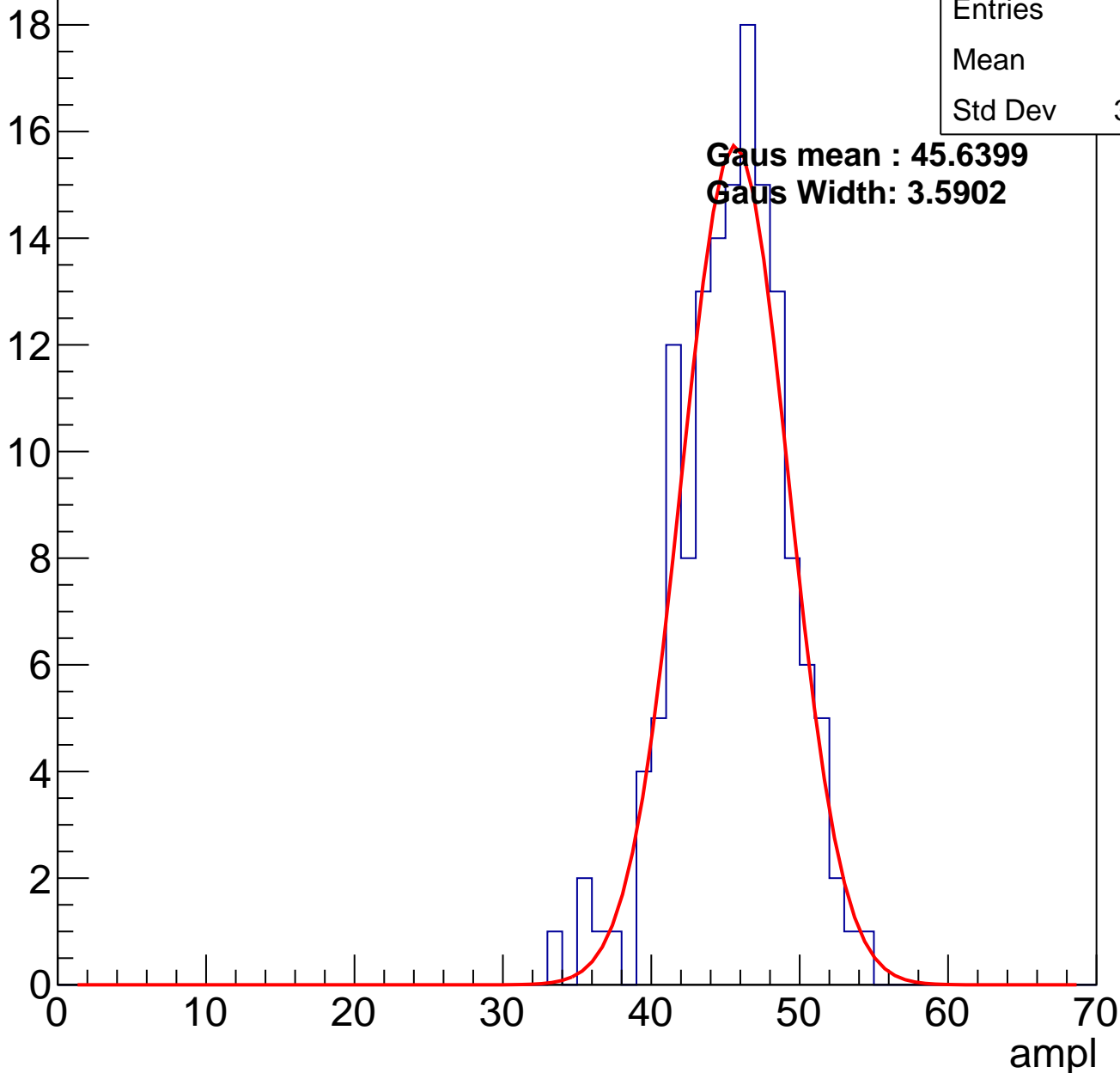
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 145 |
| Mean | 45 |
| Std Dev | 3.699 |

Gaus mean : 45.6399

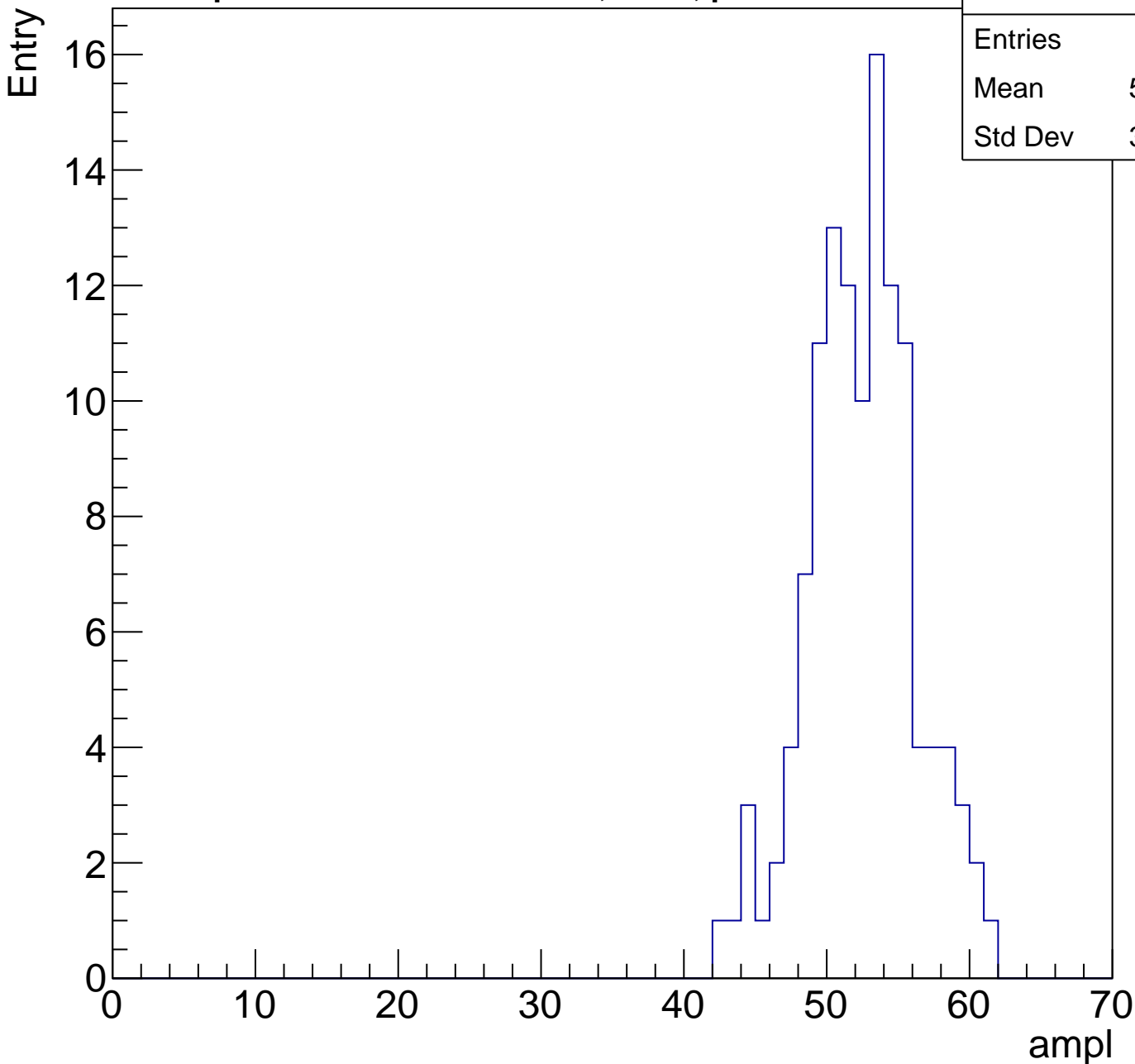
Gaus Width: 3.5902



B0L101S, U22-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 51.98 |
| Std Dev | 3.728 |

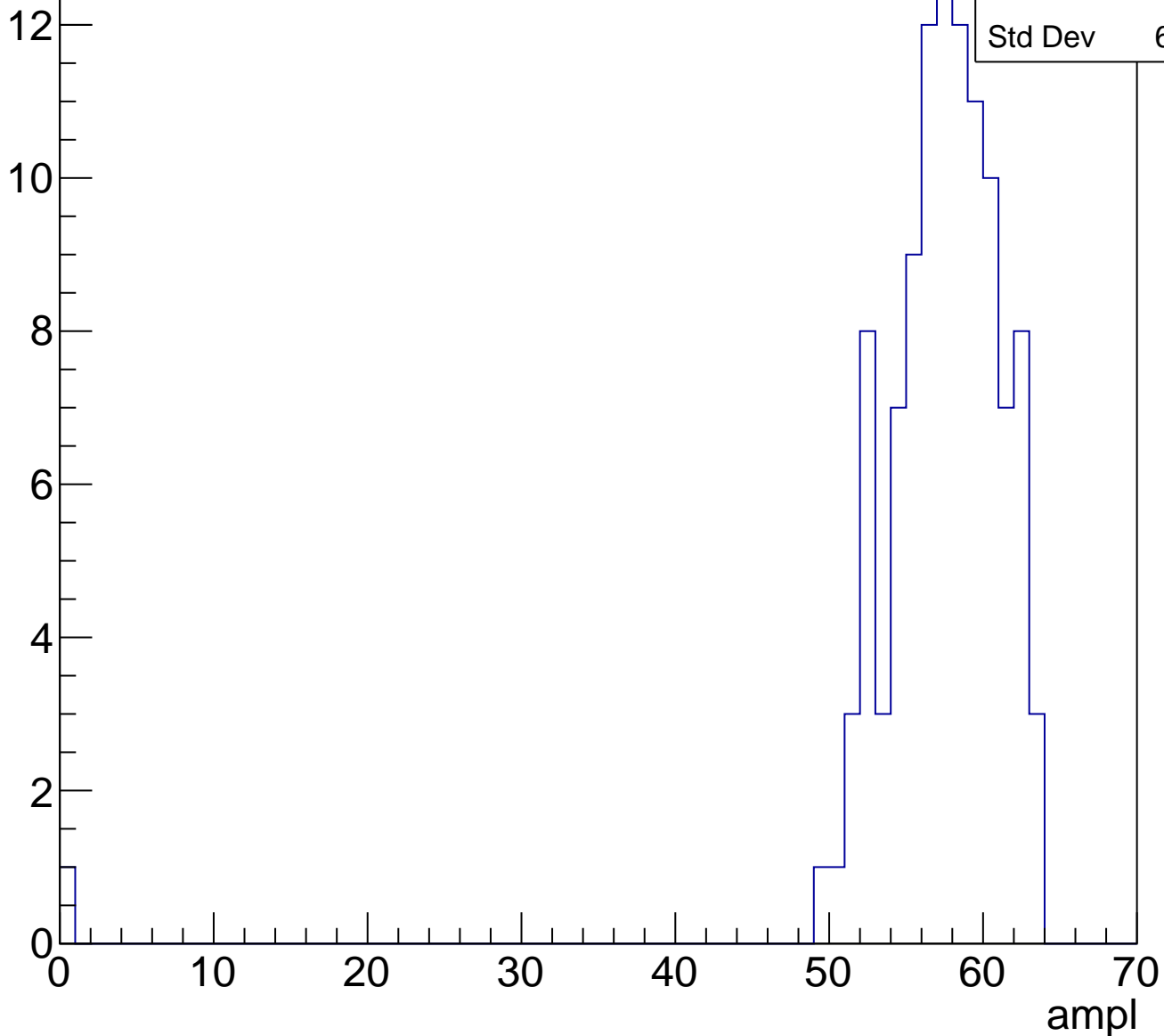


B0L101S, U22-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 109 |
| Mean | 56.61 |
| Std Dev | 6.338 |

Entry



B0L101S, U22-ch75, adc5

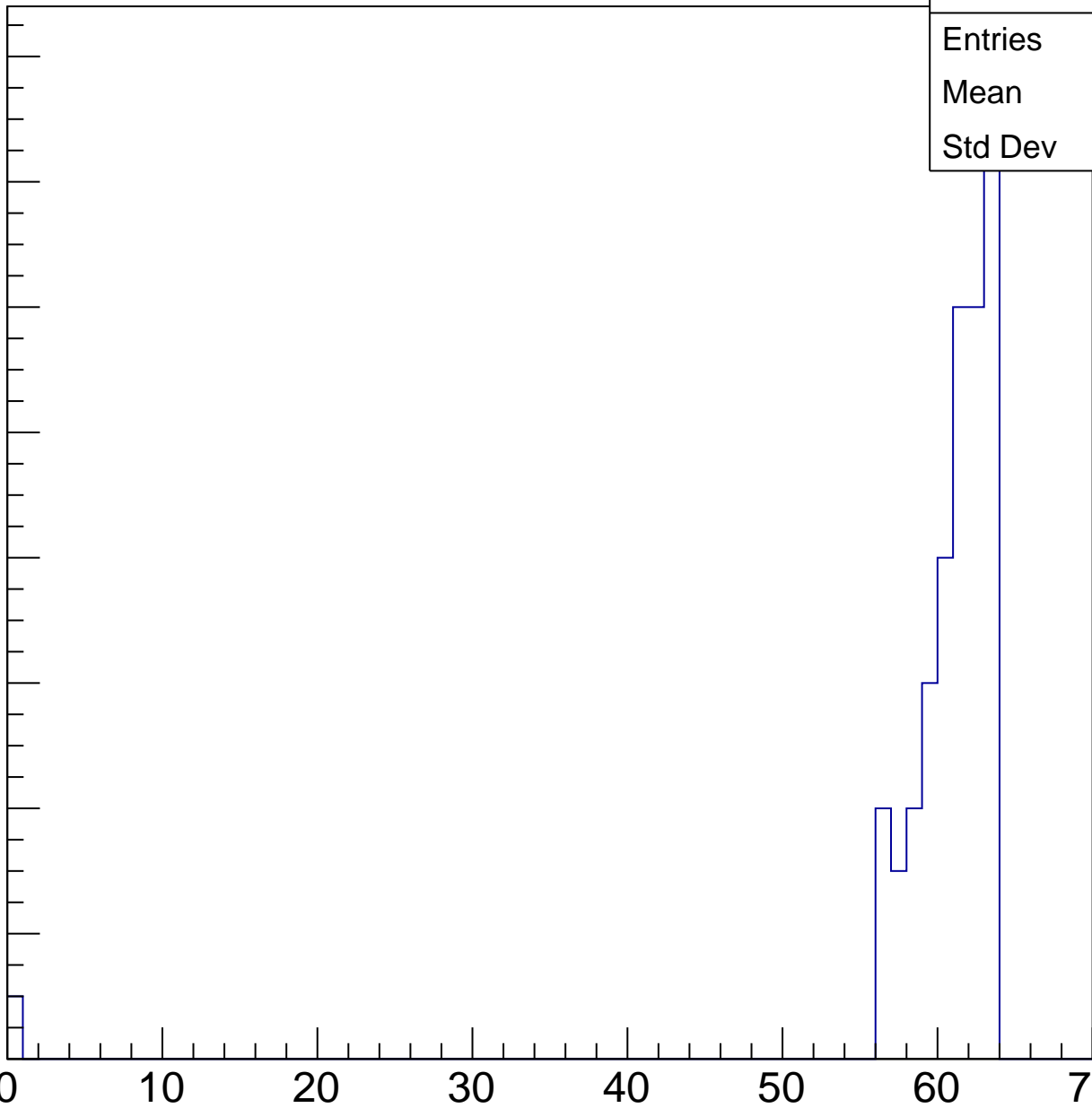
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 66 |
| Mean | 59.77 |
| Std Dev | 7.7 |

ampl



B0L101S, U22-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

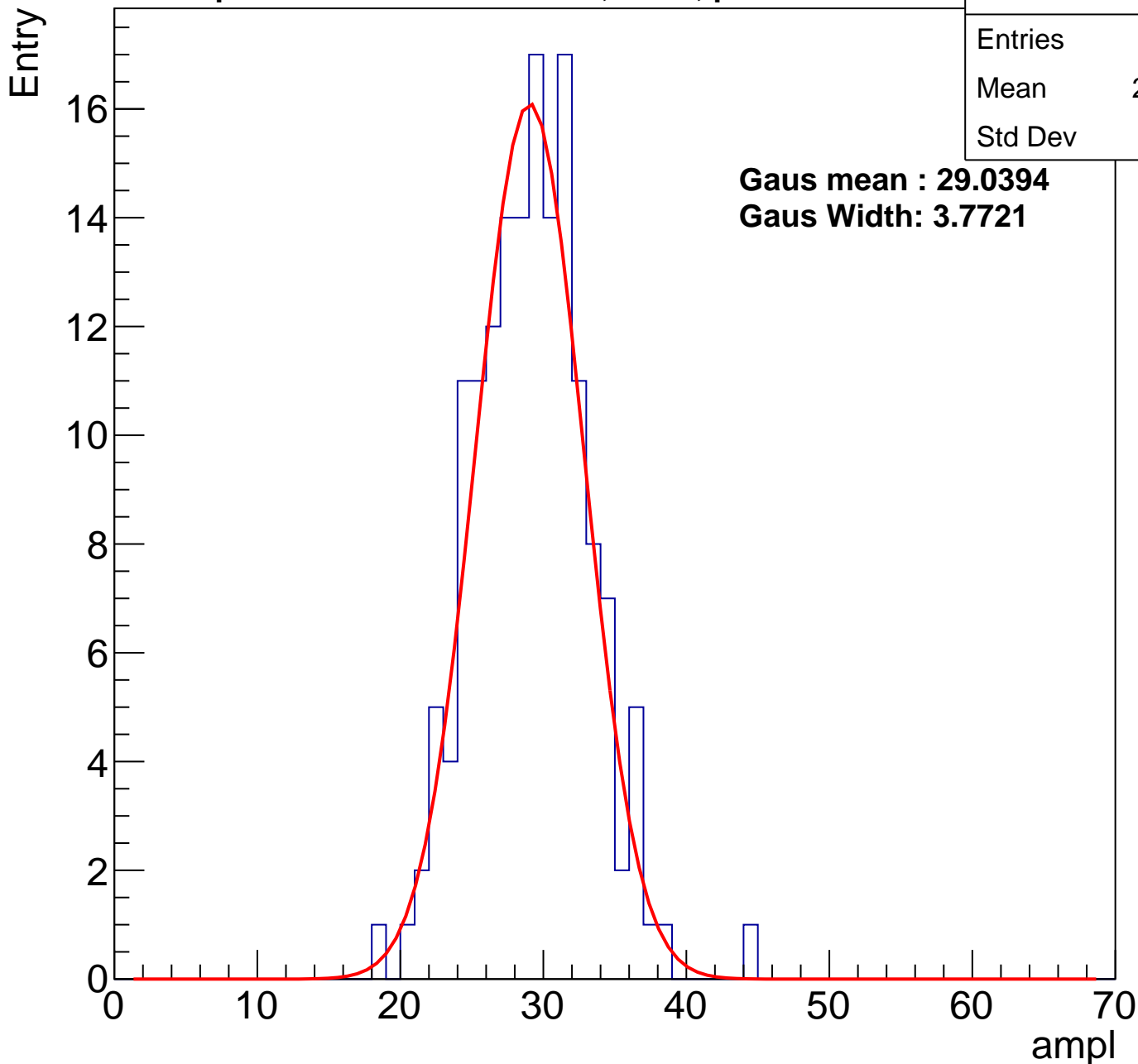
B0L101S, U22-ch76, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 28.72 |
| Std Dev | 3.97 |

Gaus mean : 29.0394

Gaus Width: 3.7721



B0L101S, U22-ch76, adc1

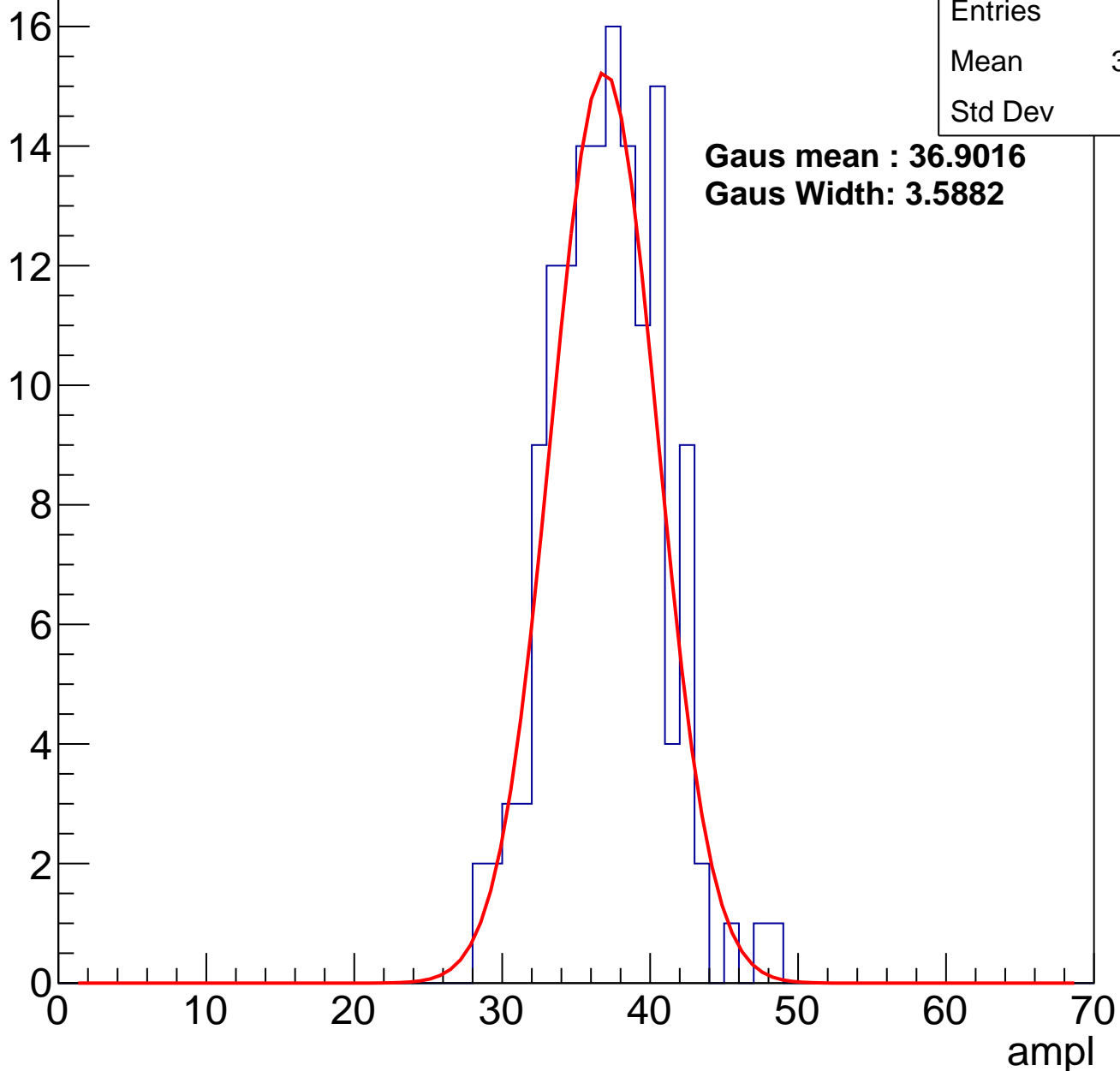
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 36.58 |
| Std Dev | 3.67 |

Gaus mean : 36.9016

Gaus Width: 3.5882

Entry



B0L101S, U22-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

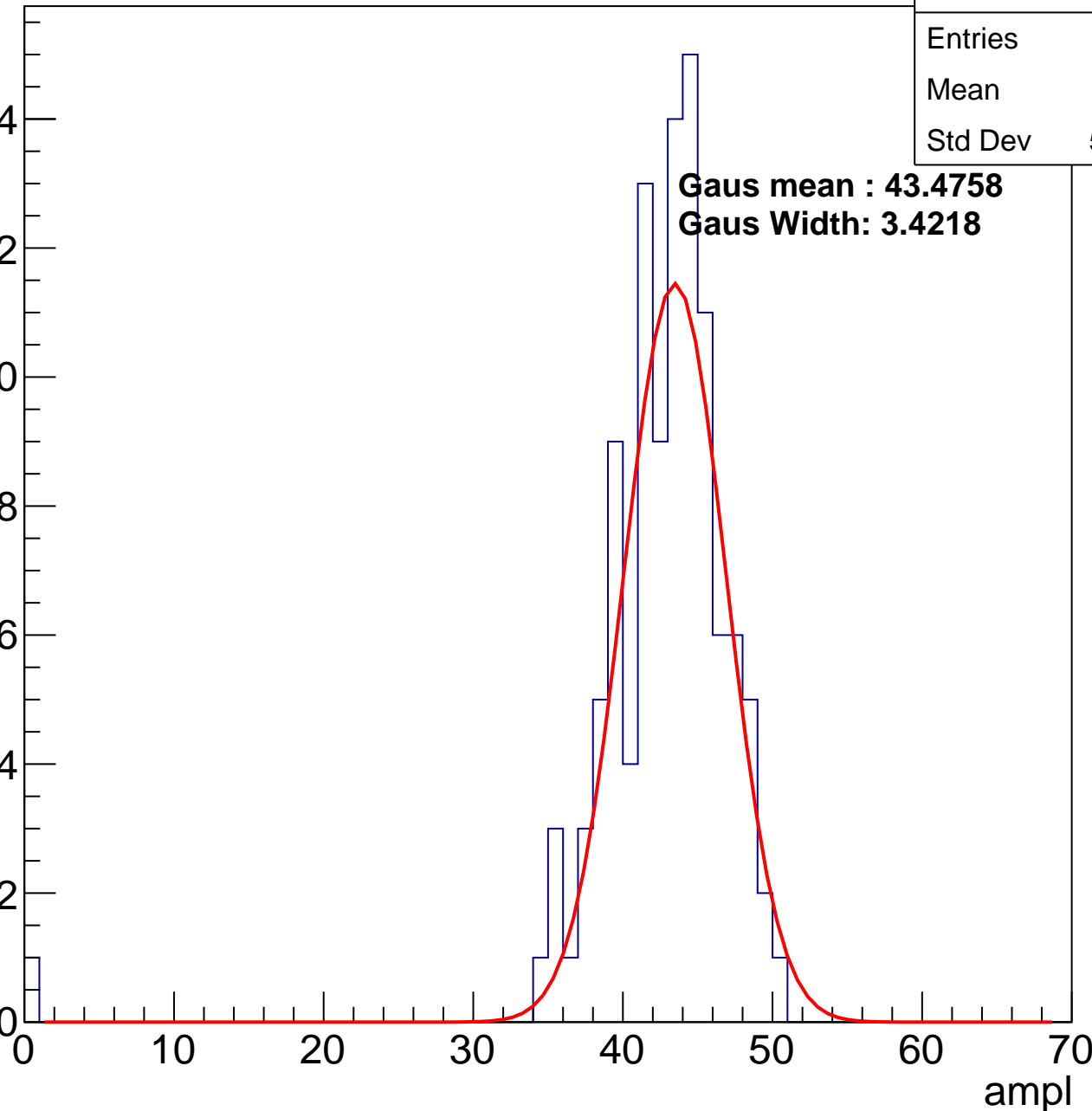
Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 109 |
| Mean | 42.21 |
| Std Dev | 5.274 |

Gaus mean : 43.4758

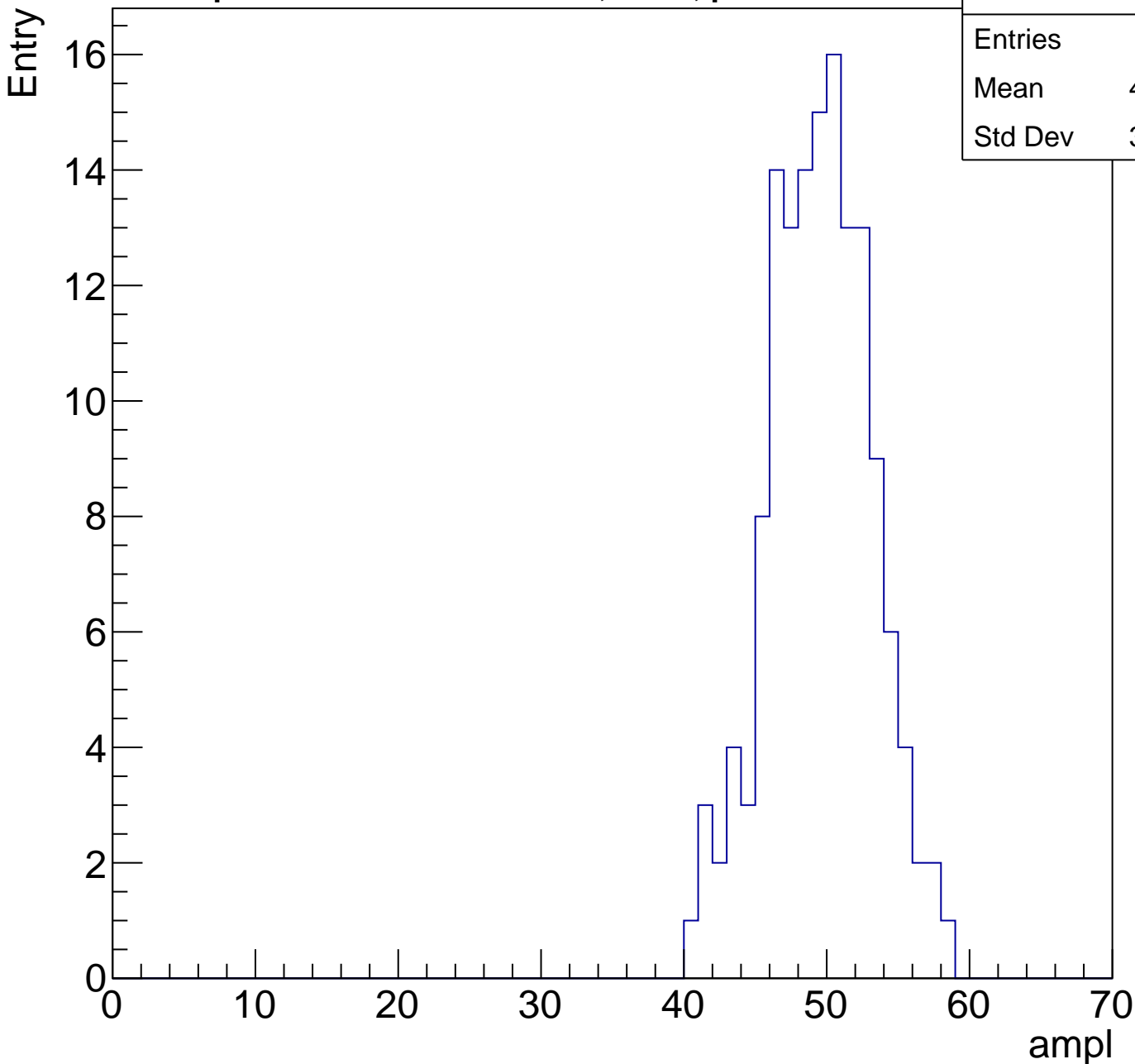
Gaus Width: 3.4218



B0L101S, U22-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

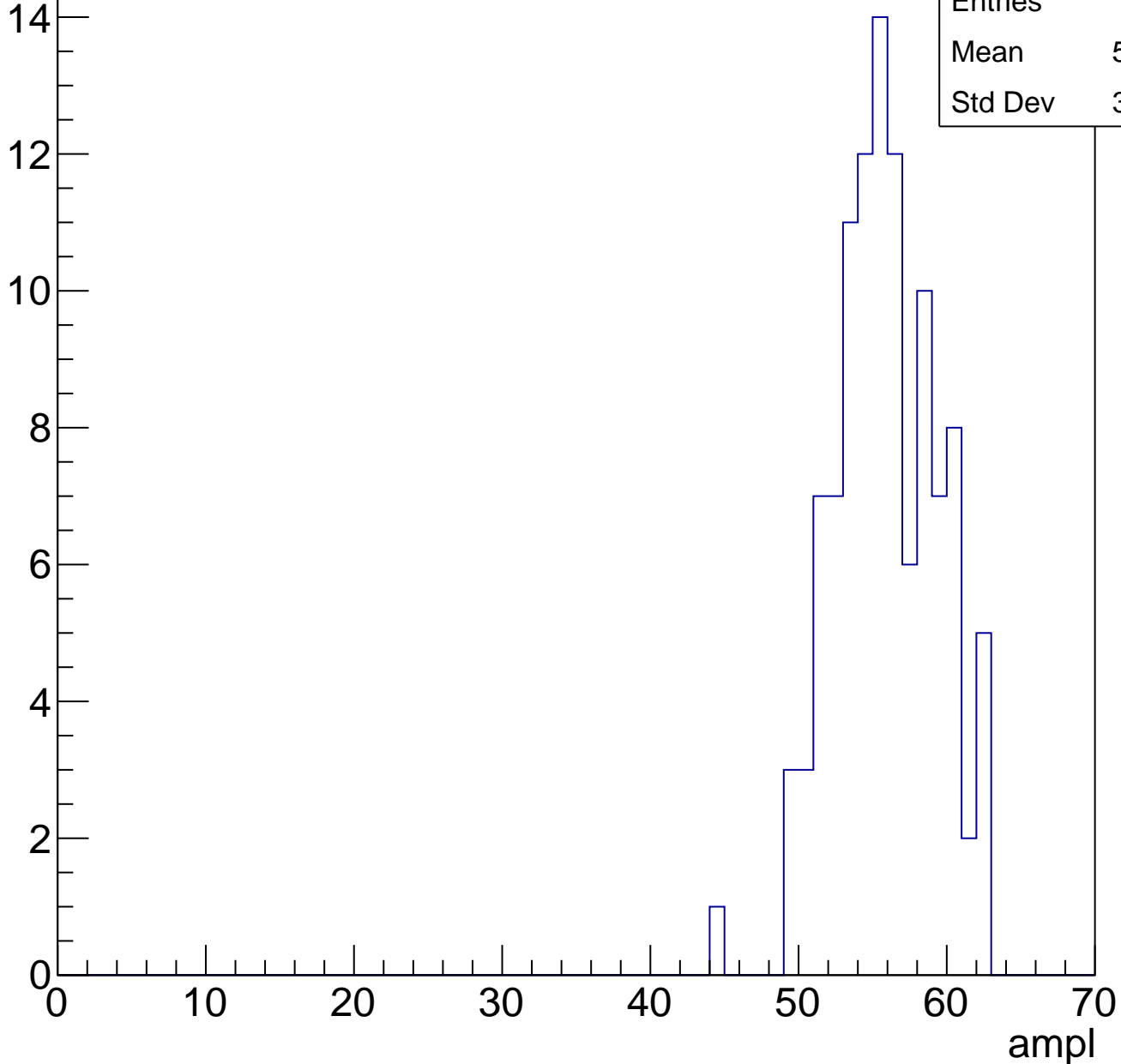
| | |
|---------|-------|
| Entries | 143 |
| Mean | 49.07 |
| Std Dev | 3.586 |



B0L101S, U22-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

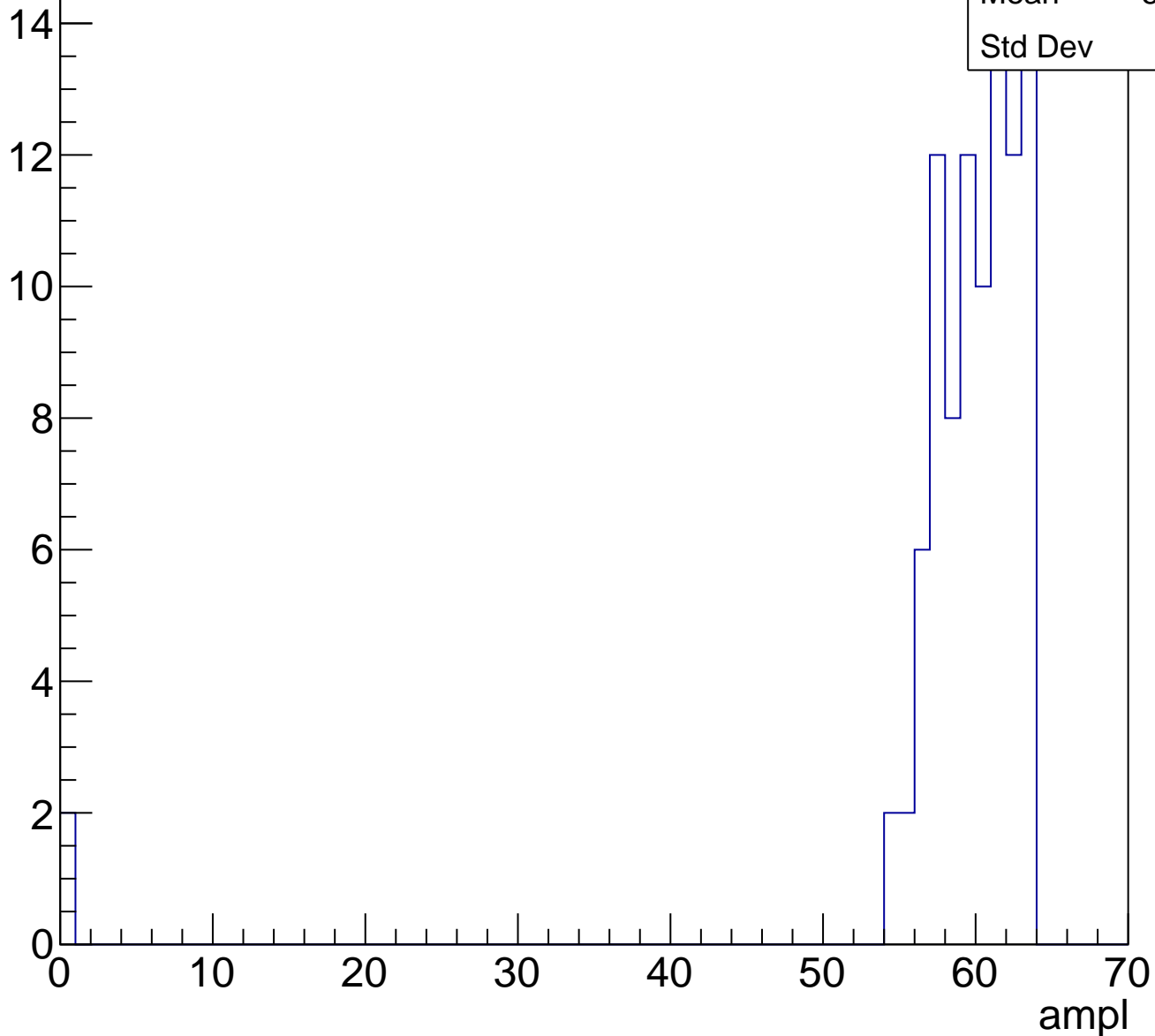


| | |
|---------|-------|
| Entries | 108 |
| Mean | 55.39 |
| Std Dev | 3.434 |

B0L101S, U22-ch76, adc5

calib_packv5_042523_0143.root, FC#1, port C1

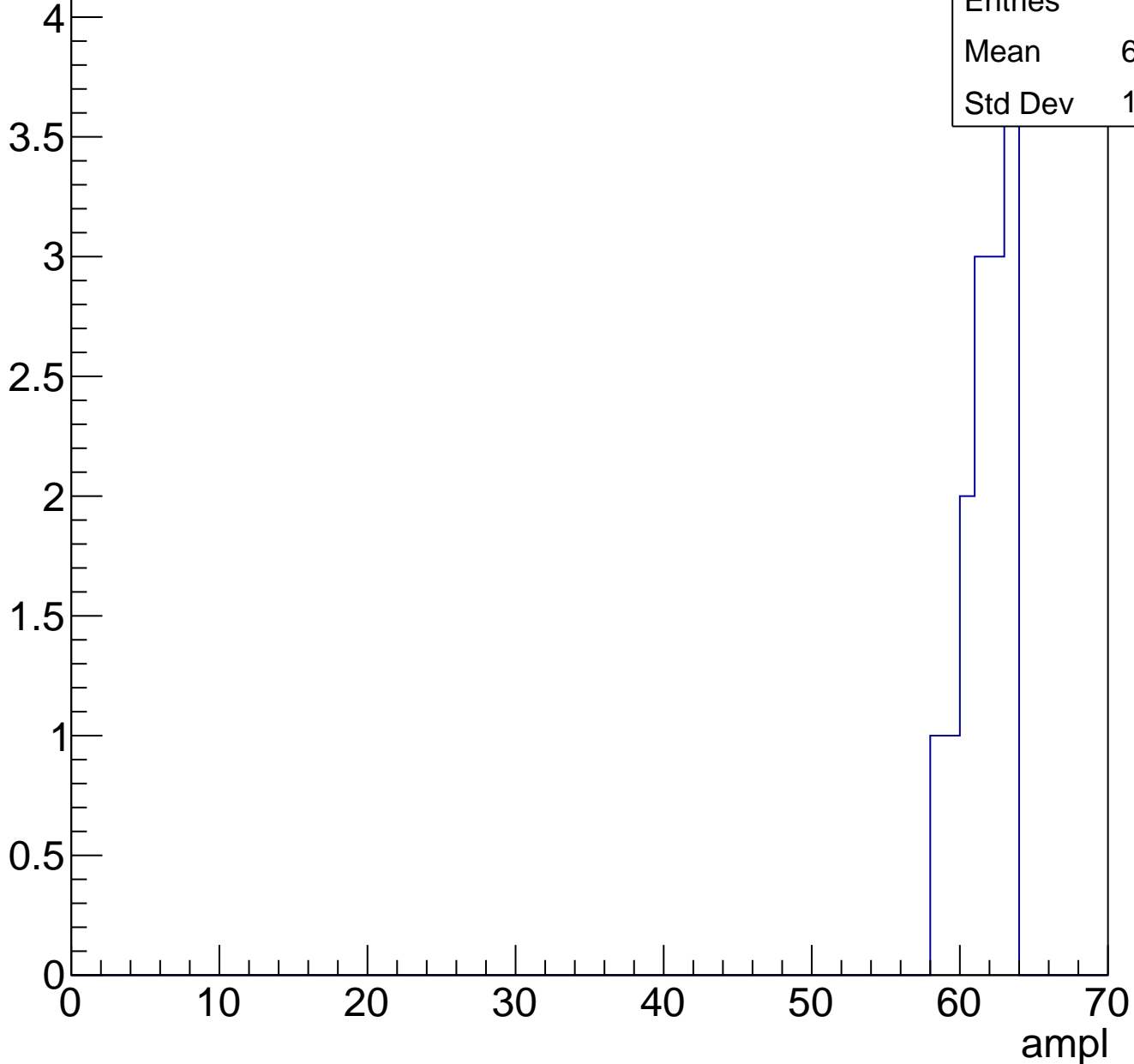
Entry



B0L101S, U22-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 30.65 |
| Std Dev | 4.217 |

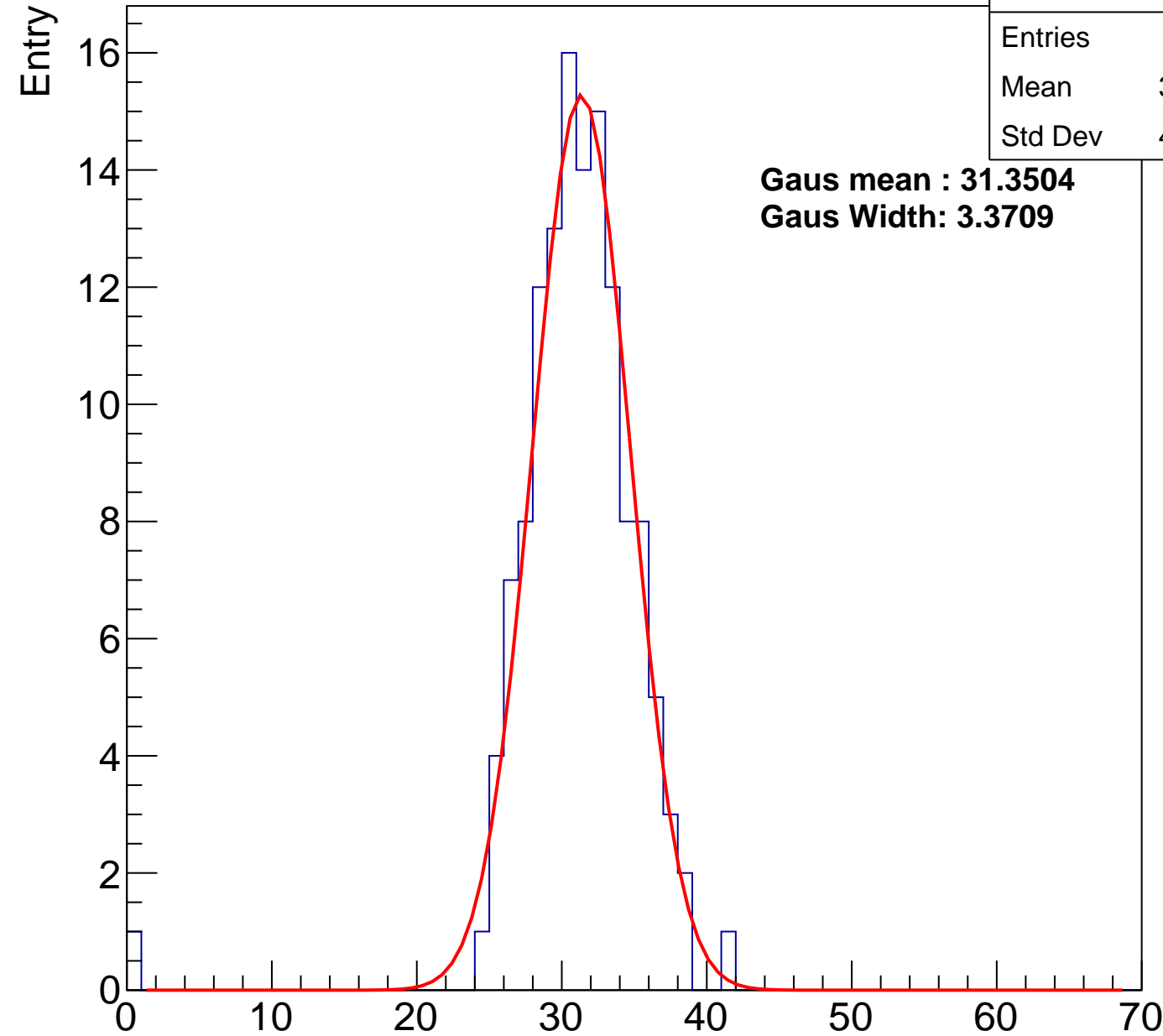
Gaus mean : 31.3504

Gaus Width: 3.3709

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch77, adc1

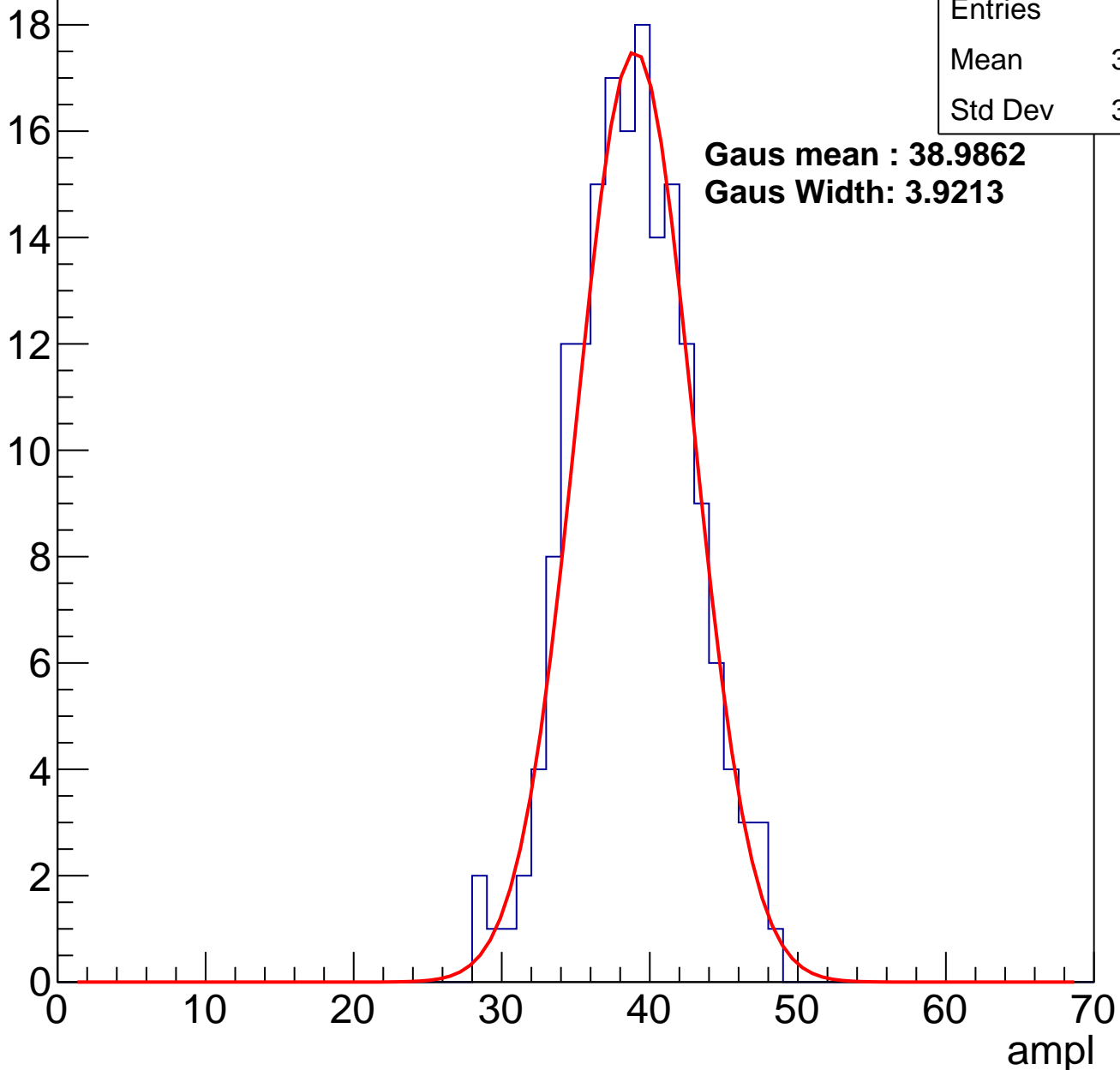
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 175 |
| Mean | 38.36 |
| Std Dev | 3.938 |

Gaus mean : 38.9862

Gaus Width: 3.9213

Entry



B0L101S, U22-ch77, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 45.96 |
| Std Dev | 3.467 |

Gaus mean : 46.2582

Gaus Width: 3.4717

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

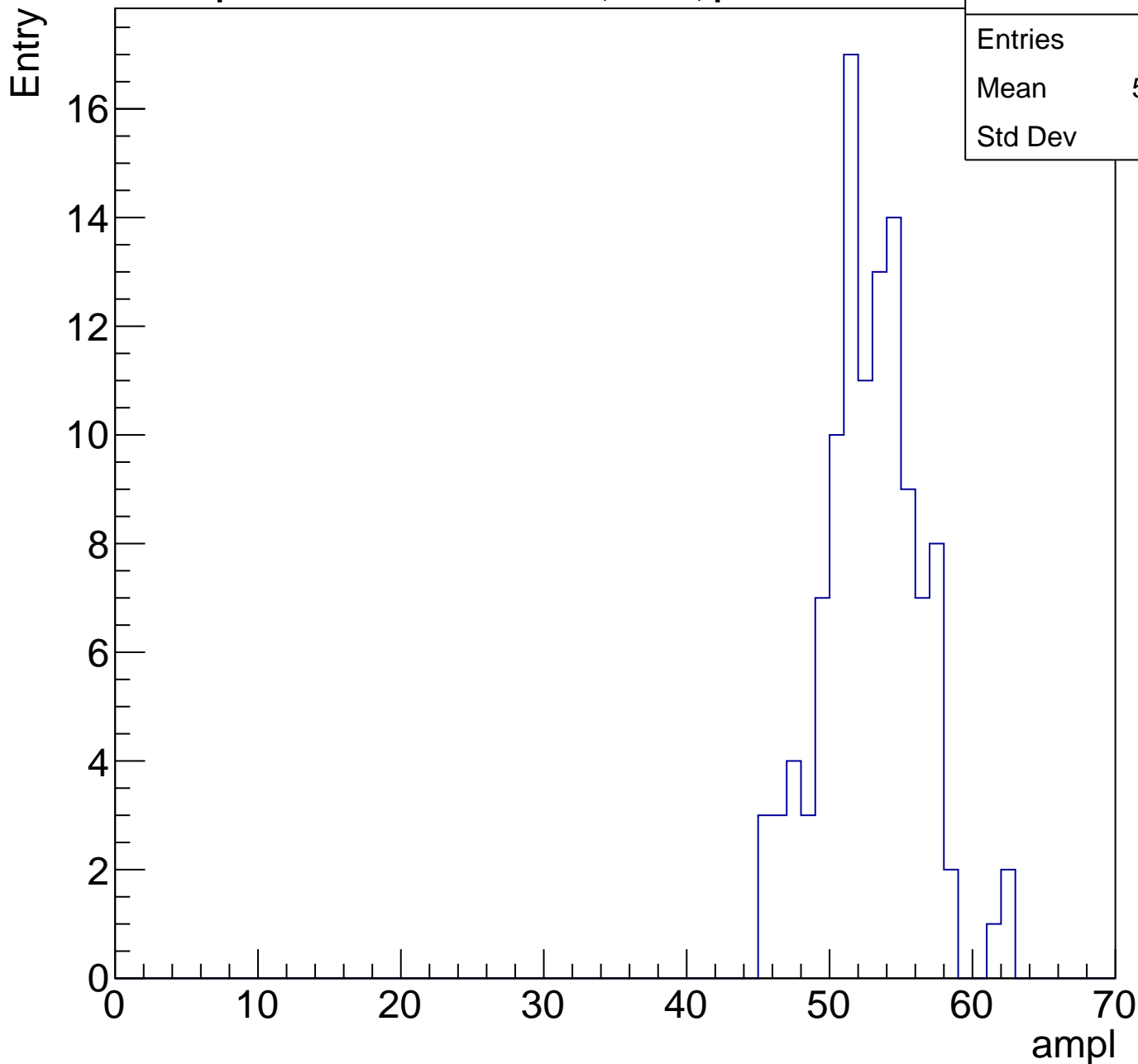
60

70

B0L101S, U22-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 114 |
| Mean | 52.42 |
| Std Dev | 3.4 |



B0L101S, U22-ch77, adc4

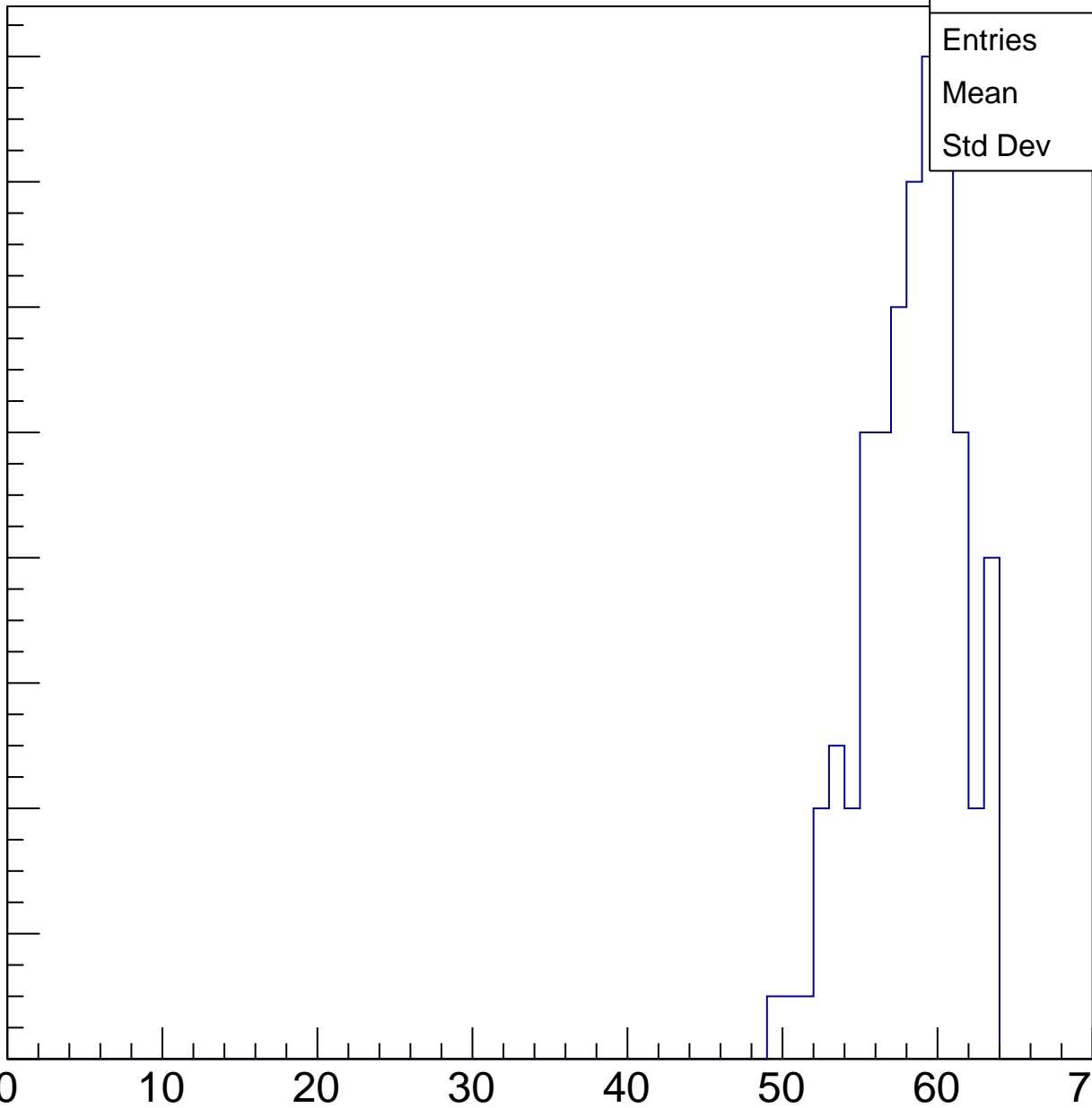
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 115 |
| Mean | 57.83 |
| Std Dev | 3.087 |

ampl



B0L101S, U22-ch77, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

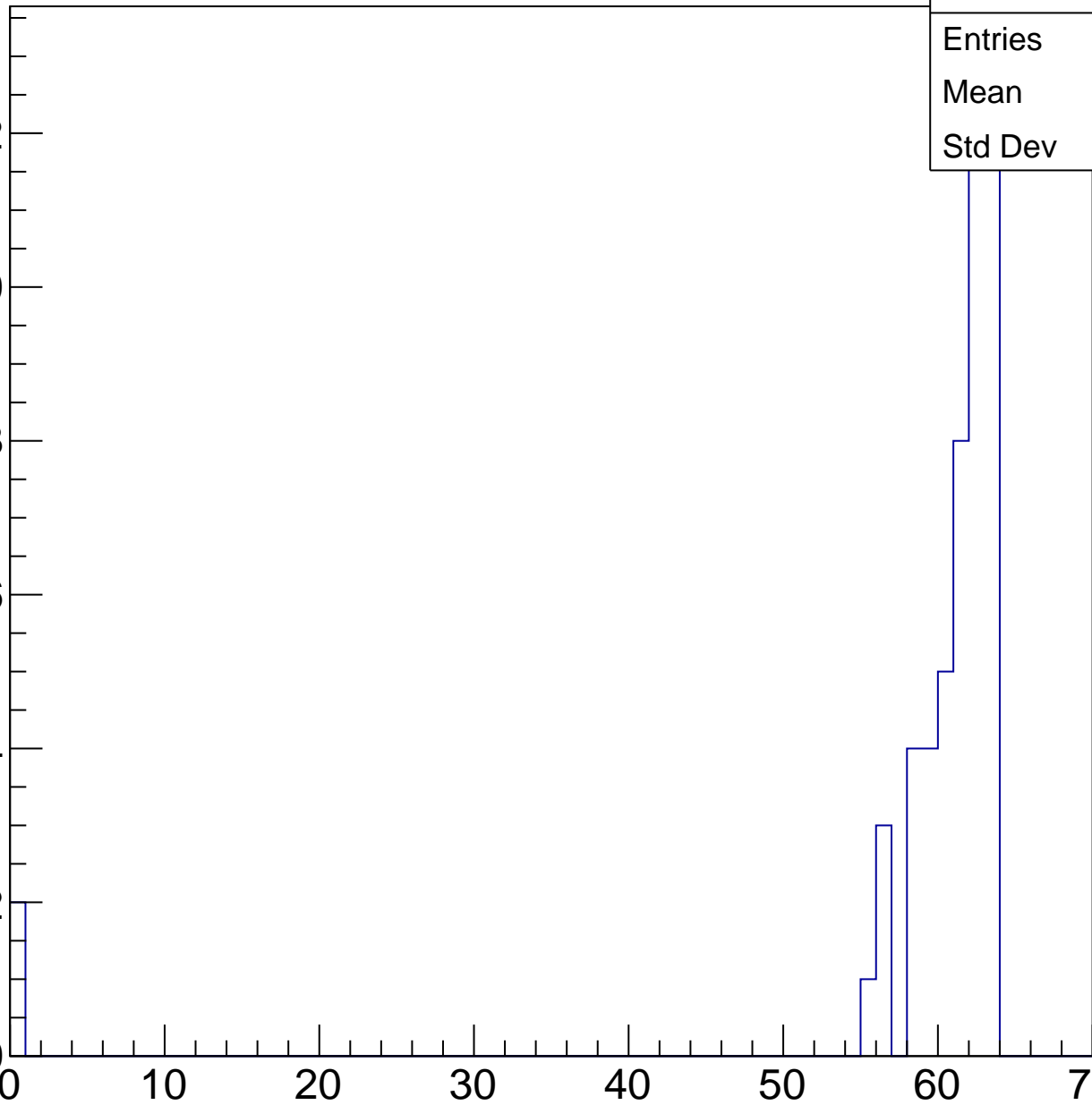
4

2

0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.48 |
| Std Dev | 11.88 |

ampl



B0L101S, U22-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch78, adc0

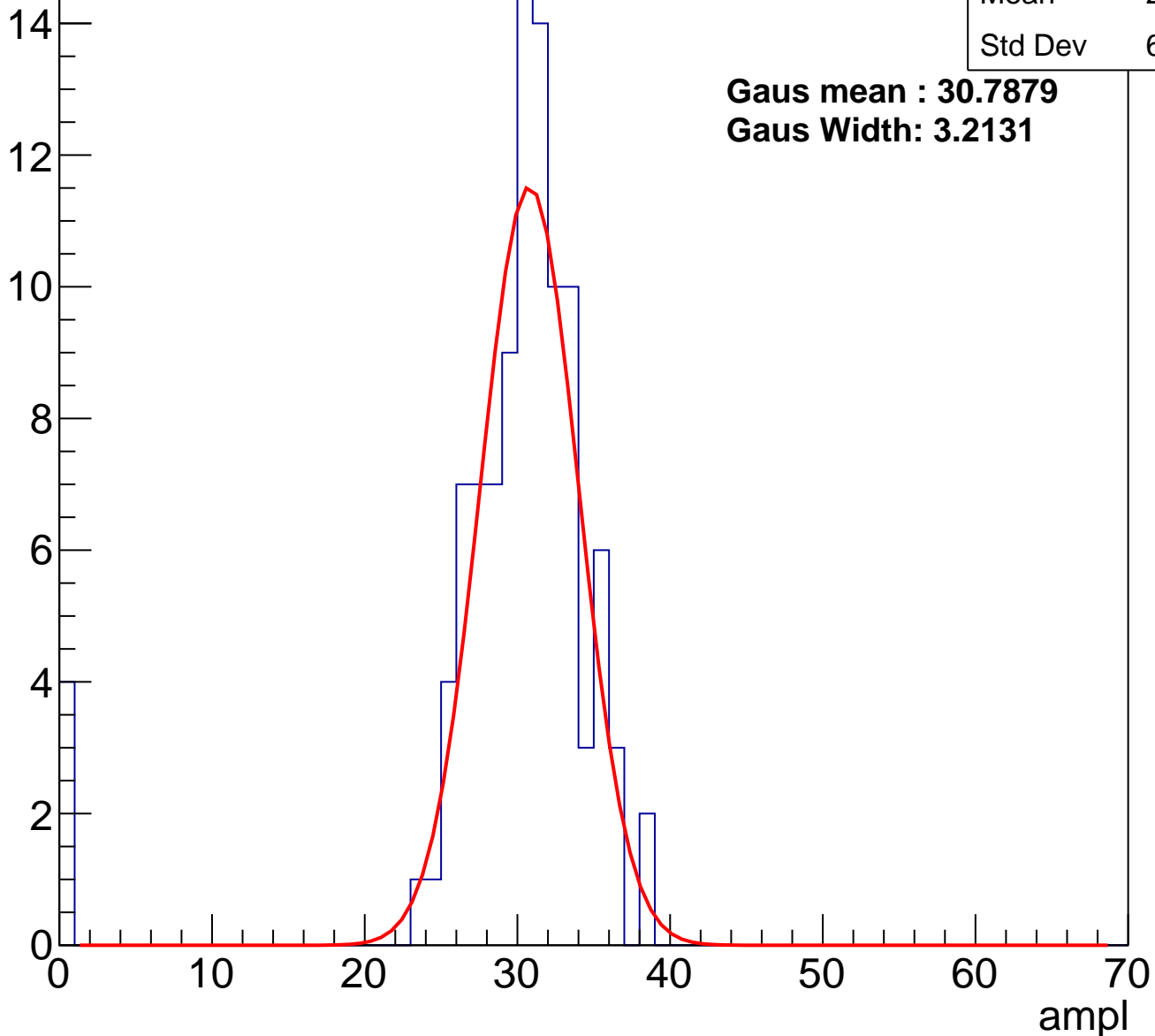
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 103 |
| Mean | 29.17 |
| Std Dev | 6.613 |

Gaus mean : 30.7879

Gaus Width: 3.2131

Entry



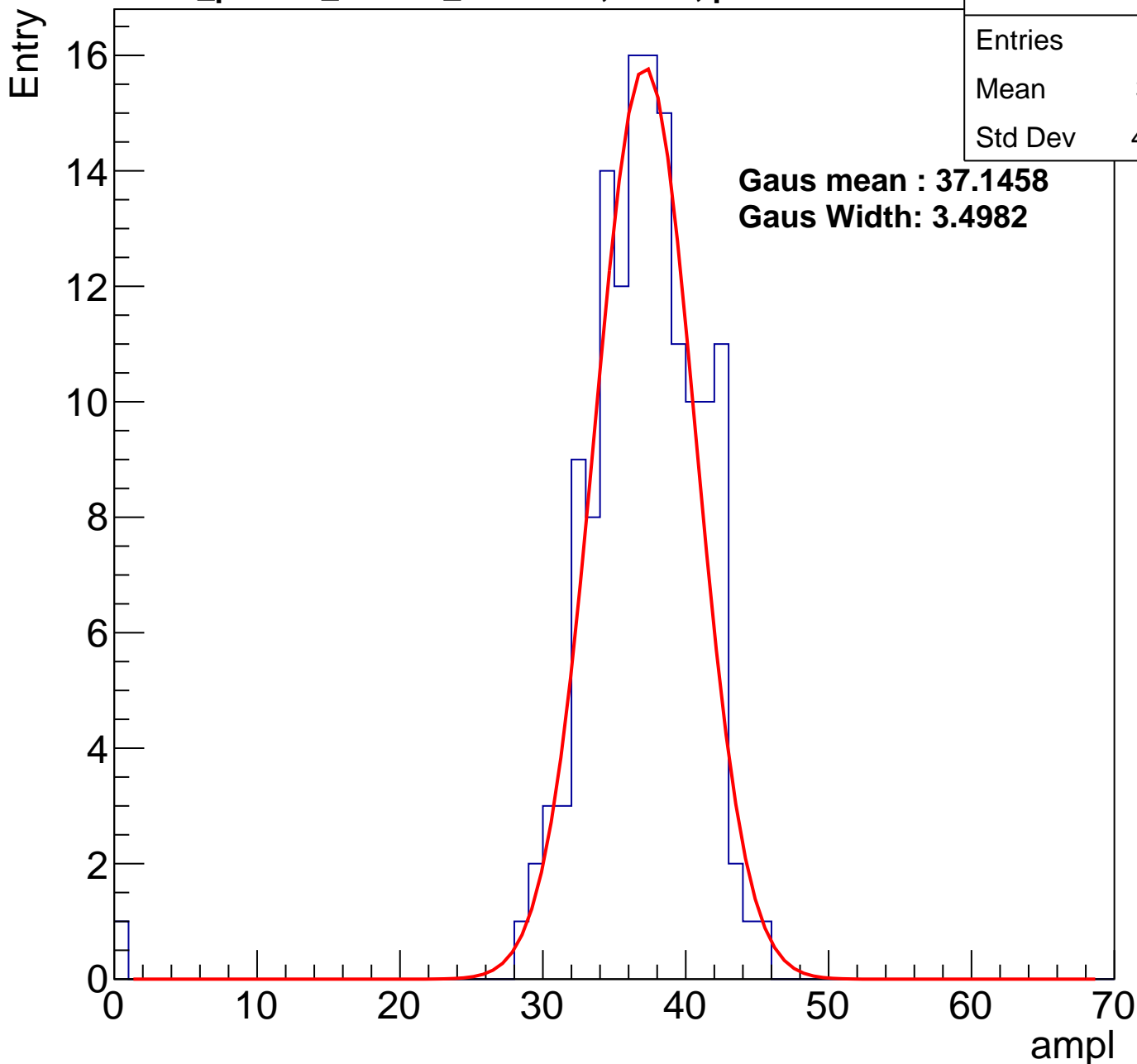
B0L101S, U22-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 36.51 |
| Std Dev | 4.616 |

Gaus mean : 37.1458

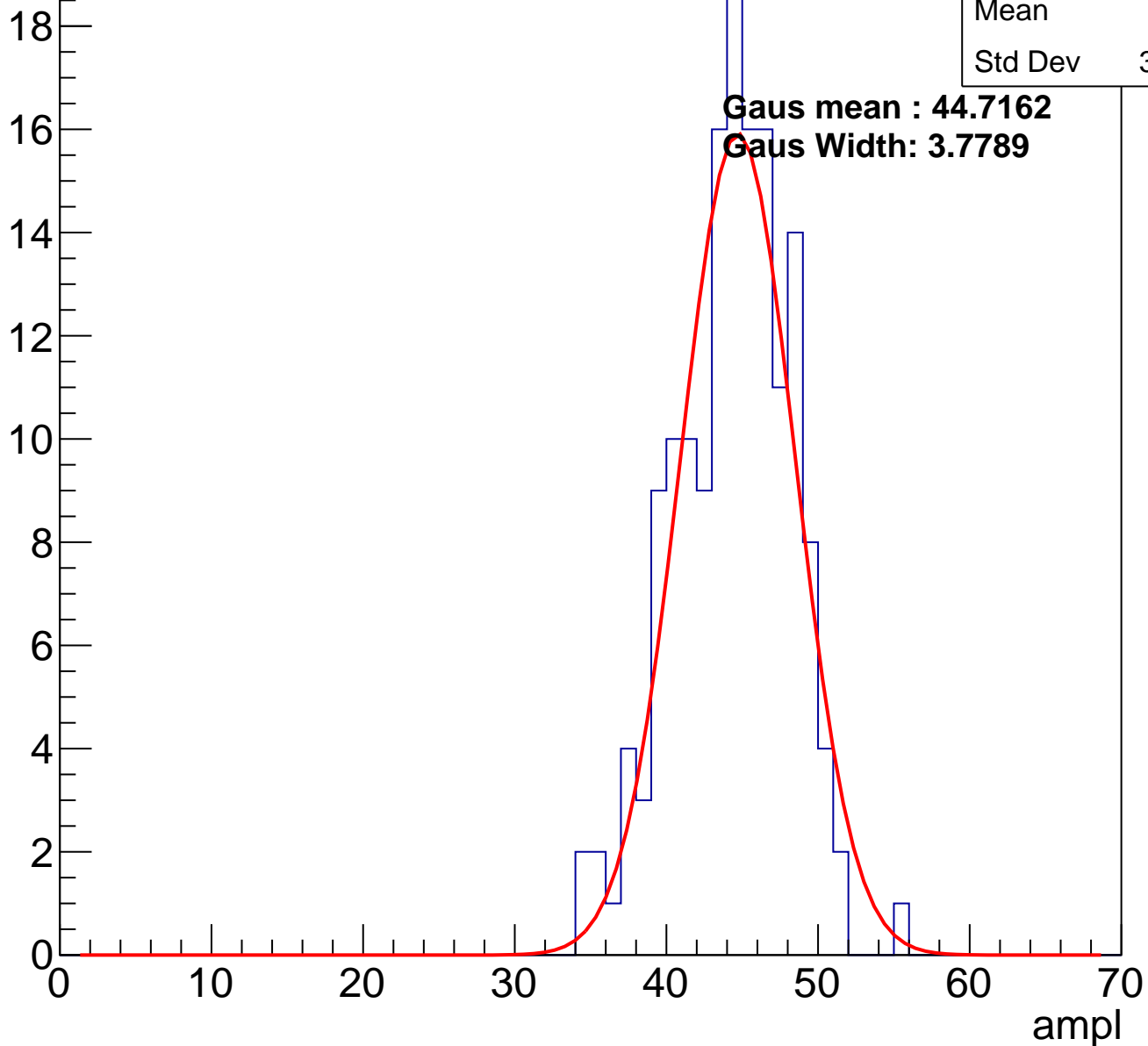
Gaus Width: 3.4982



B0L101S, U22-ch78, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 116 |
| Mean | 51.01 |
| Std Dev | 3.215 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

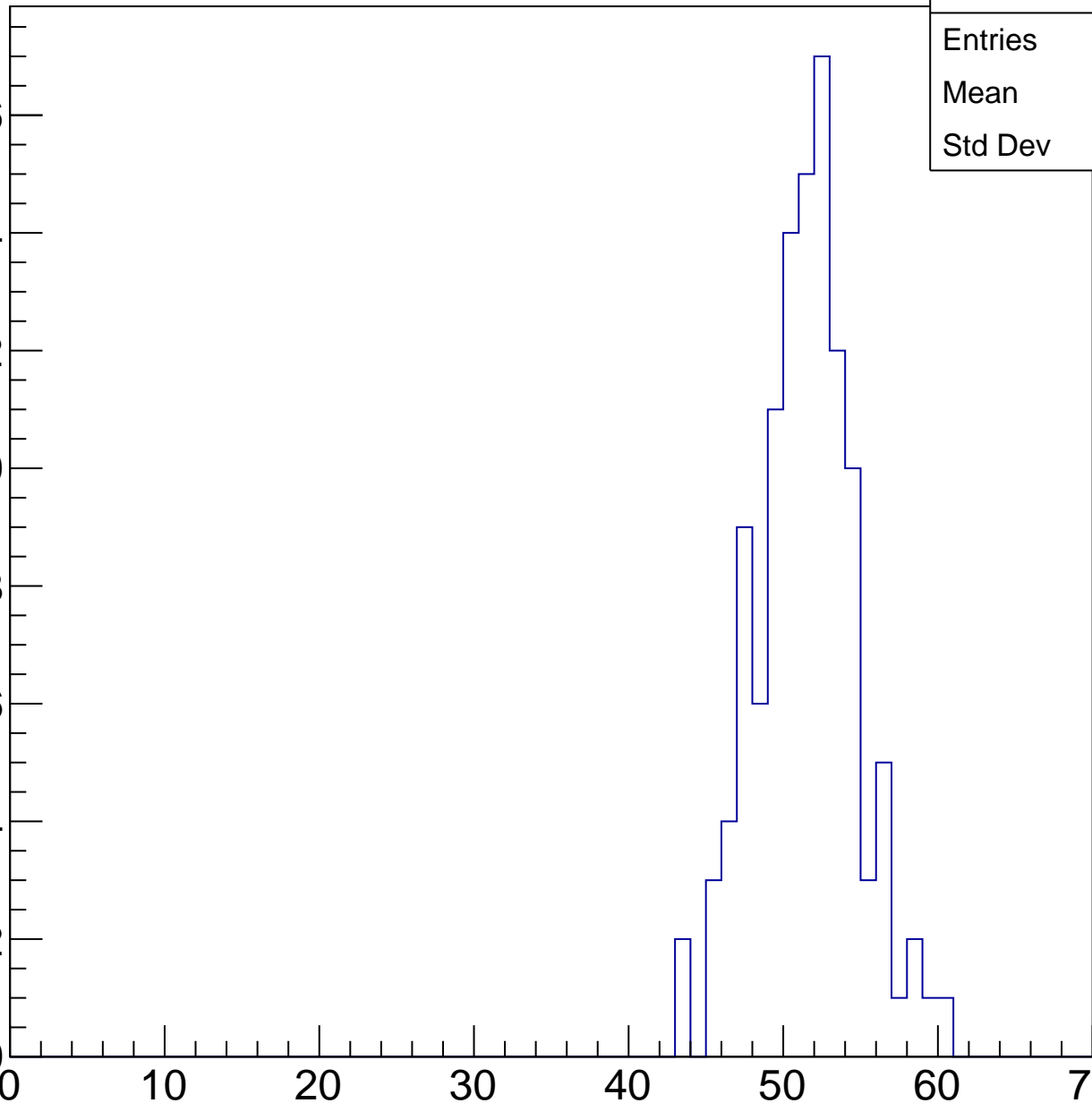
30

40

50

60

ampl



B0L101S, U22-ch78, adc4

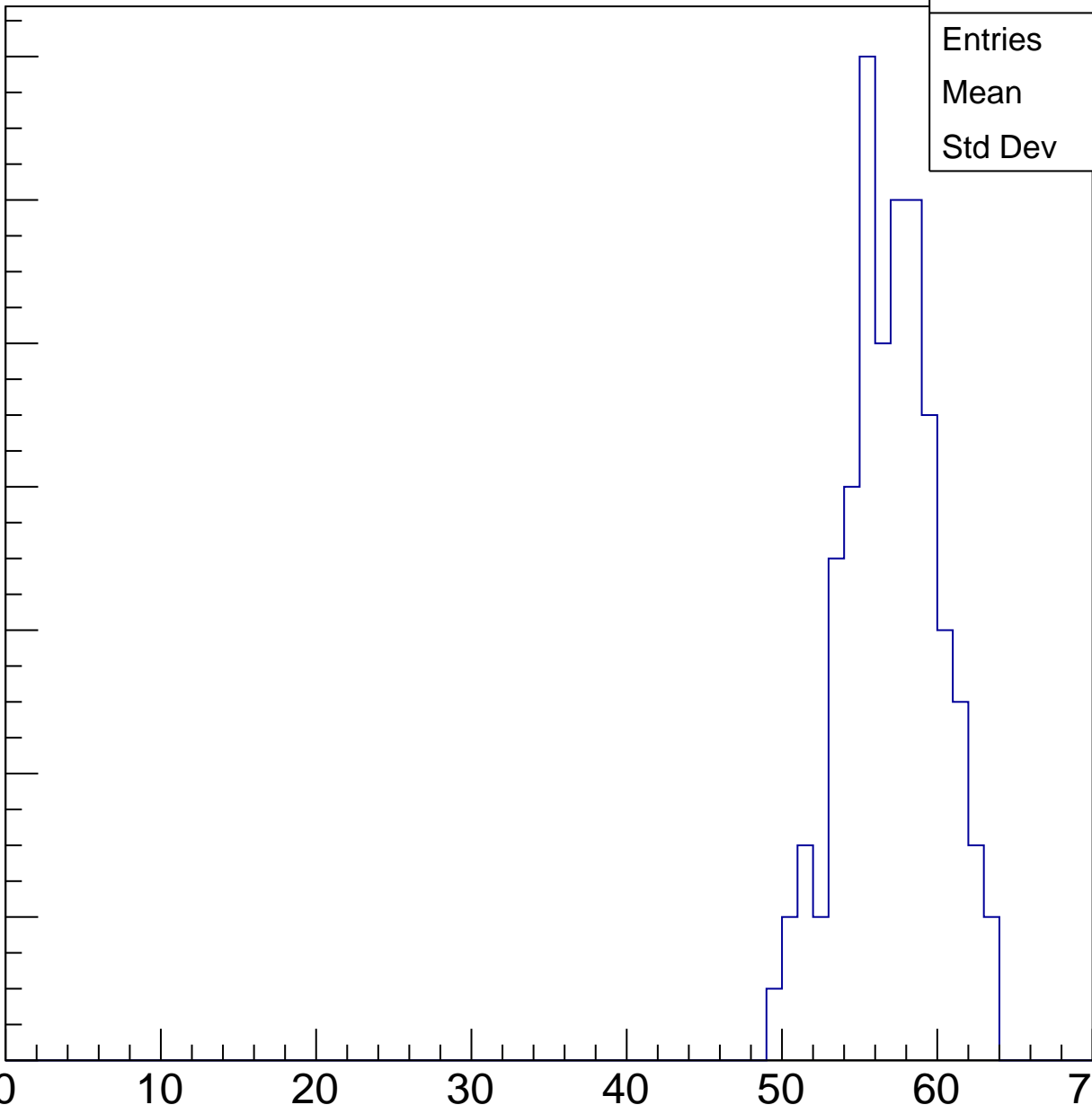
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 96 |
| Mean | 56.53 |
| Std Dev | 3.024 |

ampl



B0L101S, U22-ch78, adc5

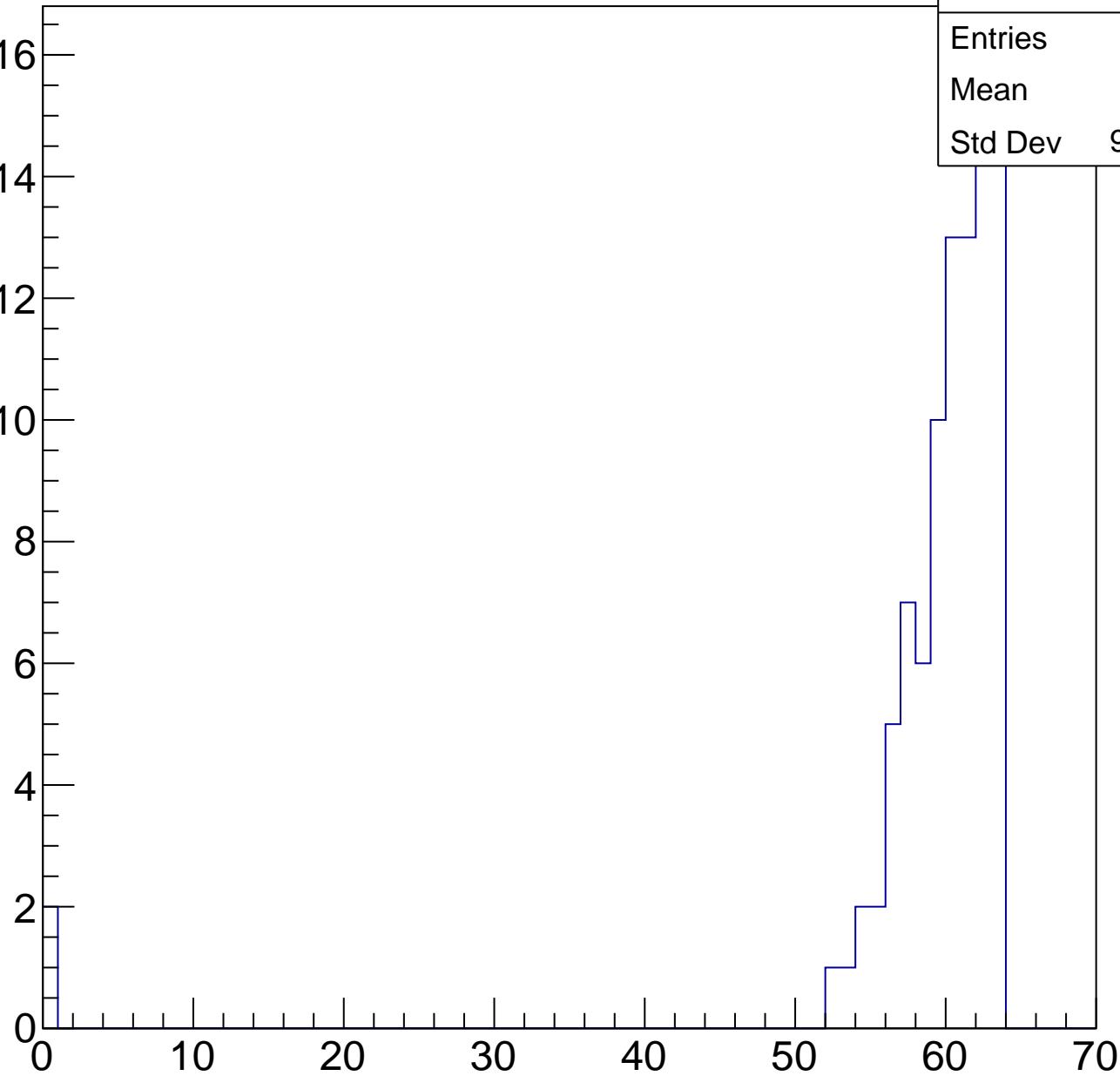
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 93 |
| Mean | 58.6 |
| Std Dev | 9.065 |

ampl



B0L101S, U22-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 130 |
| Mean | 30.32 |
| Std Dev | 4.441 |

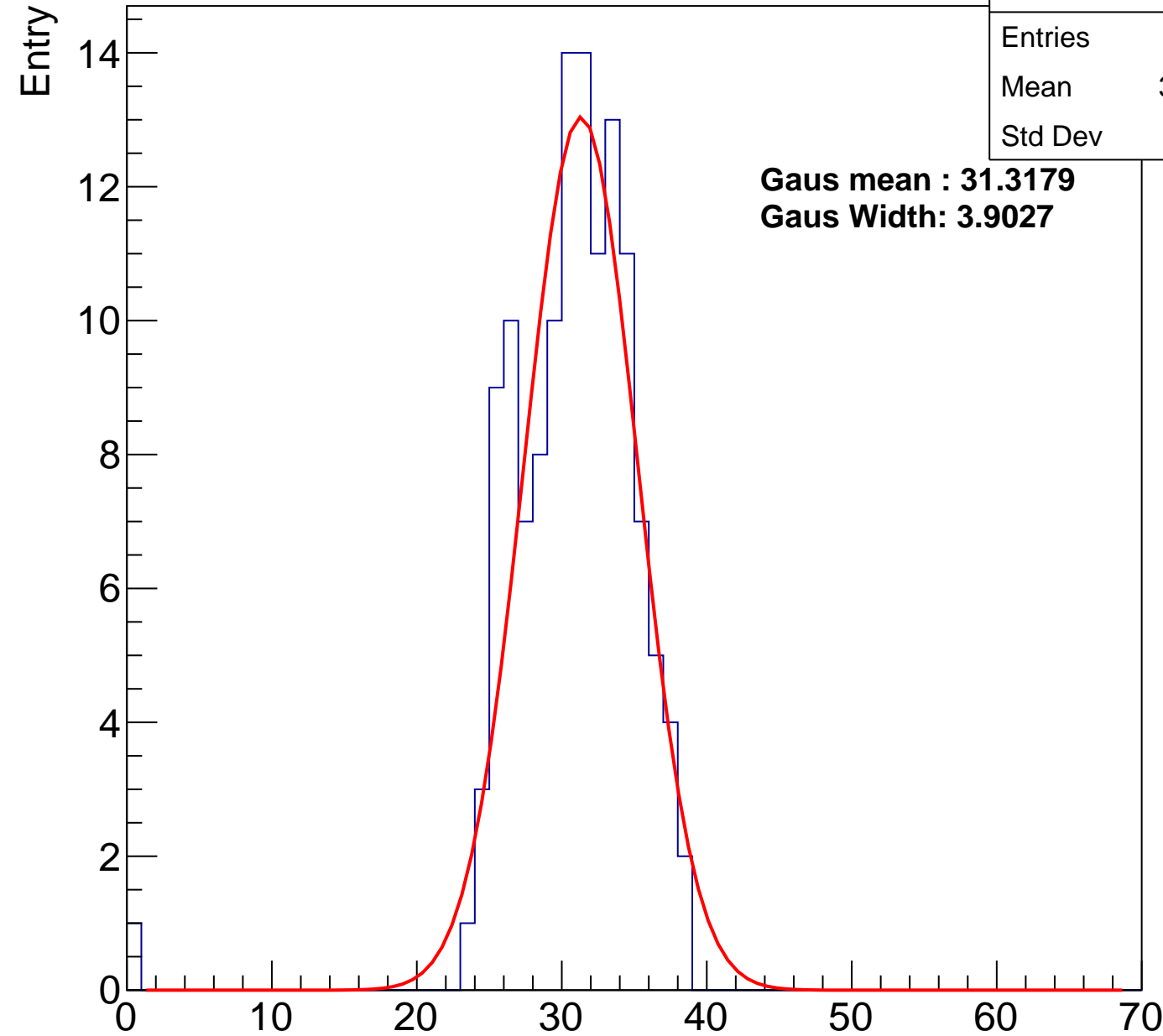
Gaus mean : 31.3179

Gaus Width: 3.9027

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch79, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 37.3 |
| Std Dev | 3.519 |

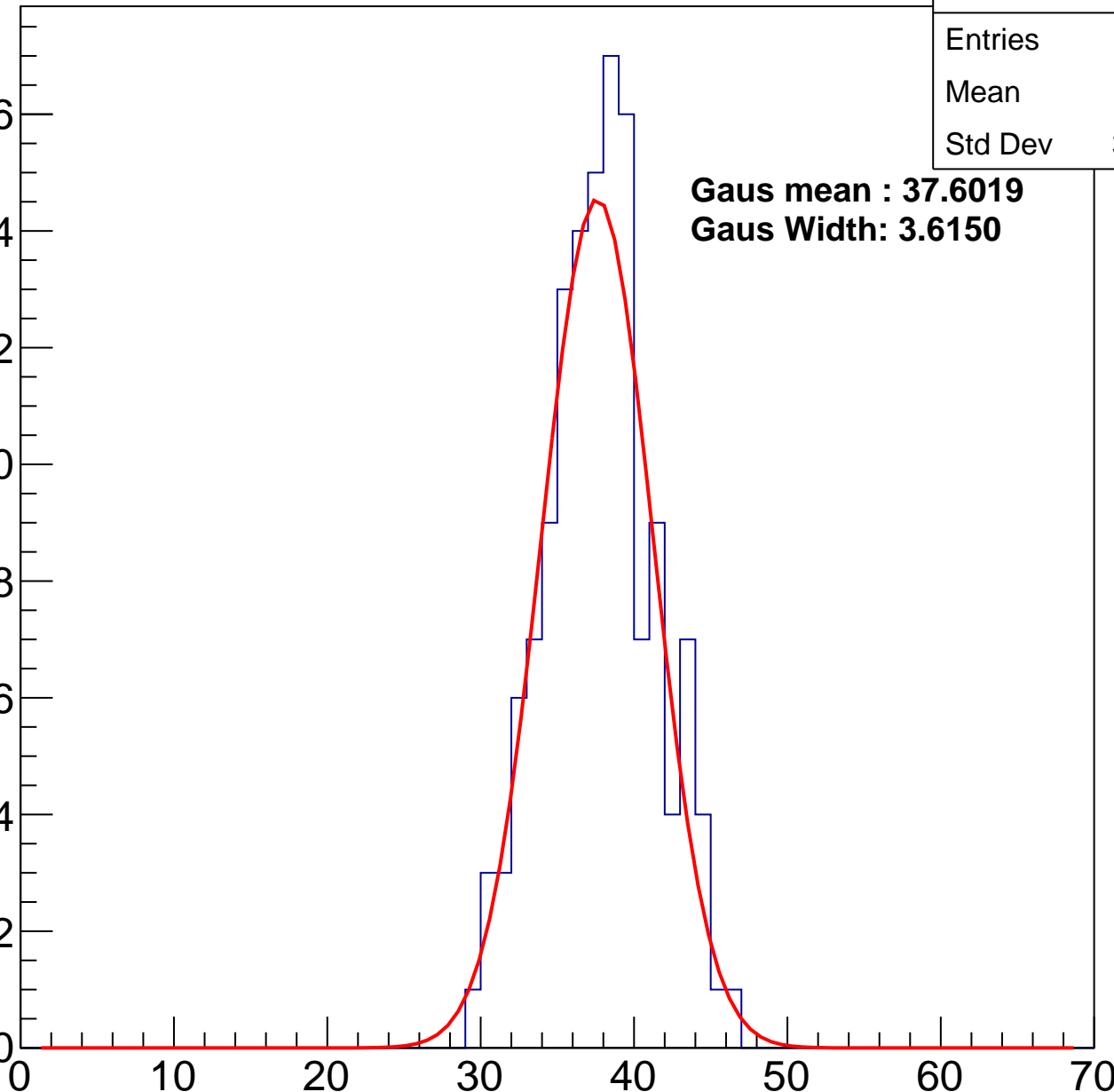
Gaus mean : 37.6019

Gaus Width: 3.6150

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch79, adc2

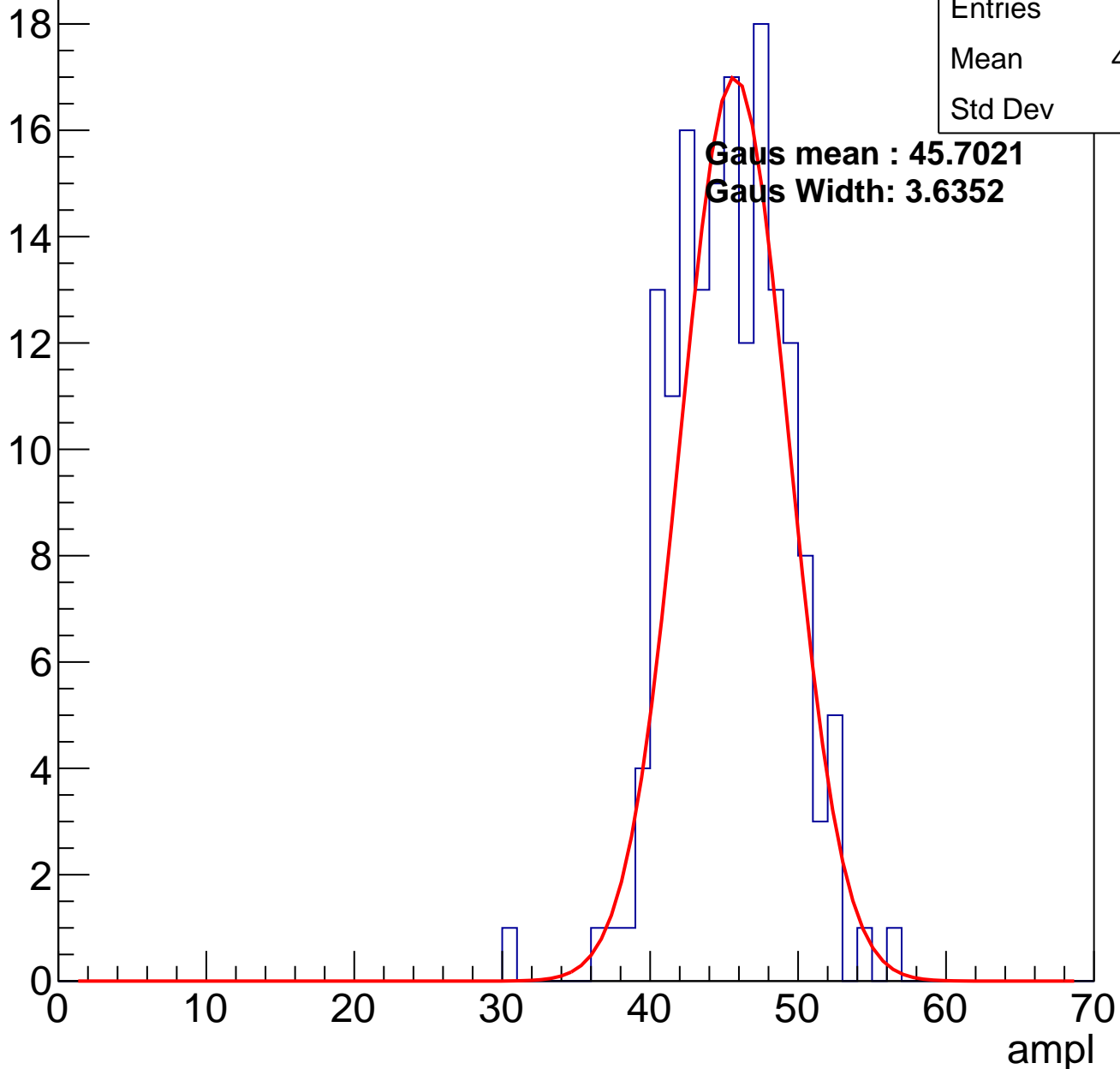
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 44.92 |
| Std Dev | 3.82 |

Gaus mean : 45.7021

Gaus Width: 3.6352

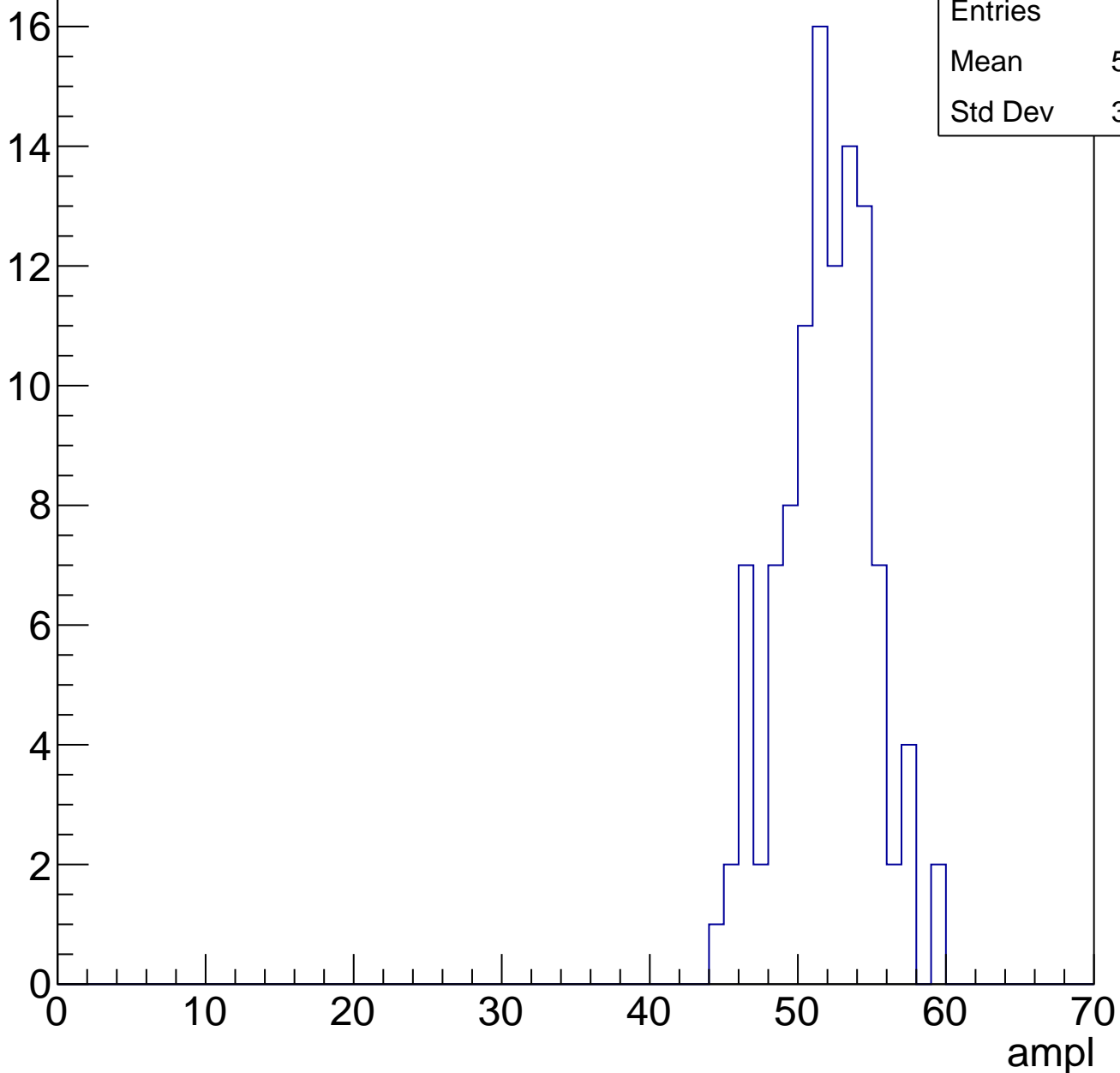
Entry



B0L101S, U22-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch79, adc4

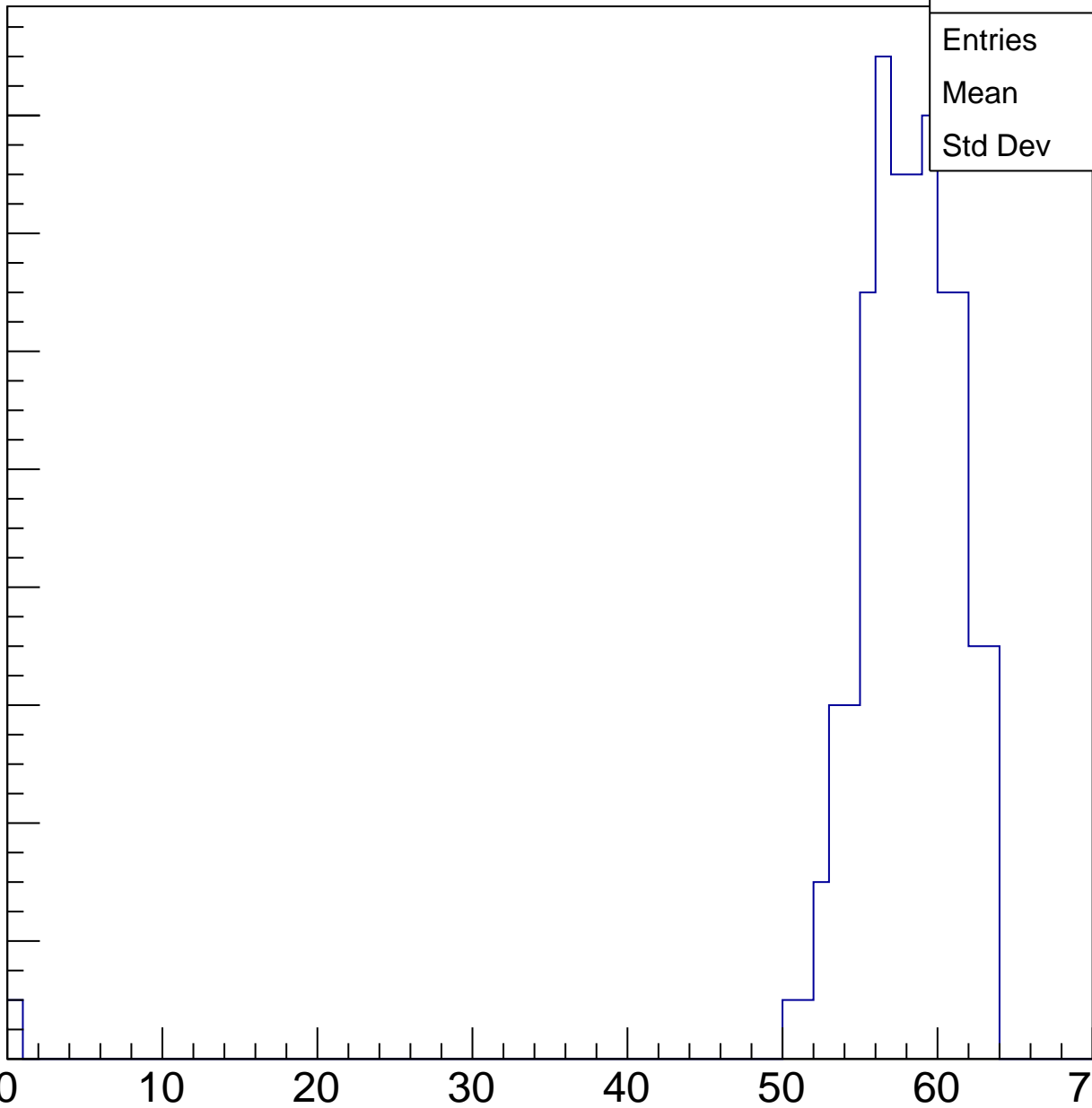
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 57.34 |
| Std Dev | 5.759 |

ampl



B0L101S, U22-ch79, adc5

calib_packv5_042523_0143.root, FC#1, port C1

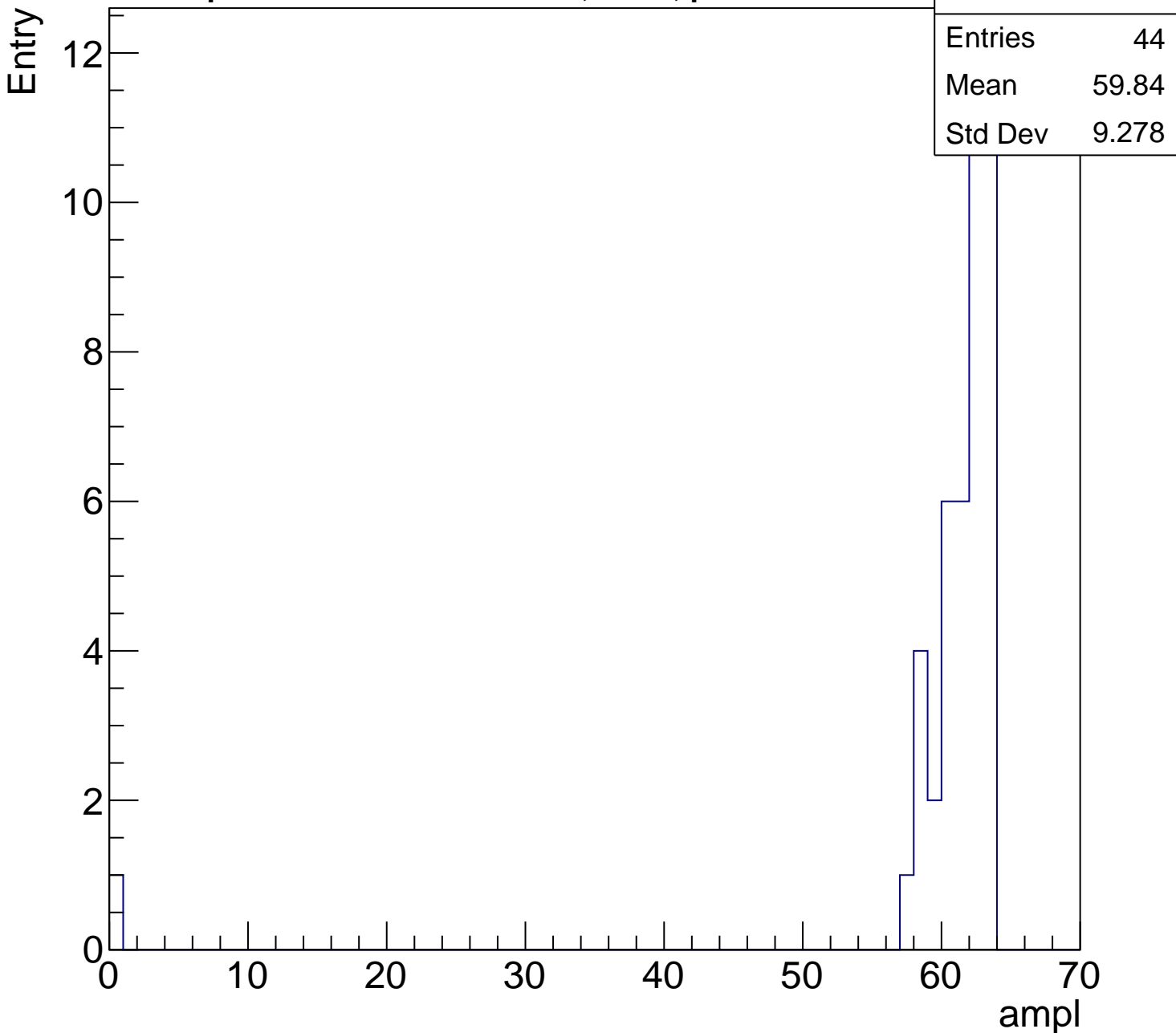
Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.84 |
| Std Dev | 9.278 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

60.8

Std Dev

1.72

B0L101S, U22-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch80, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 162 |
| Mean | 29.16 |
| Std Dev | 3.68 |

Gaus mean : 29.9458

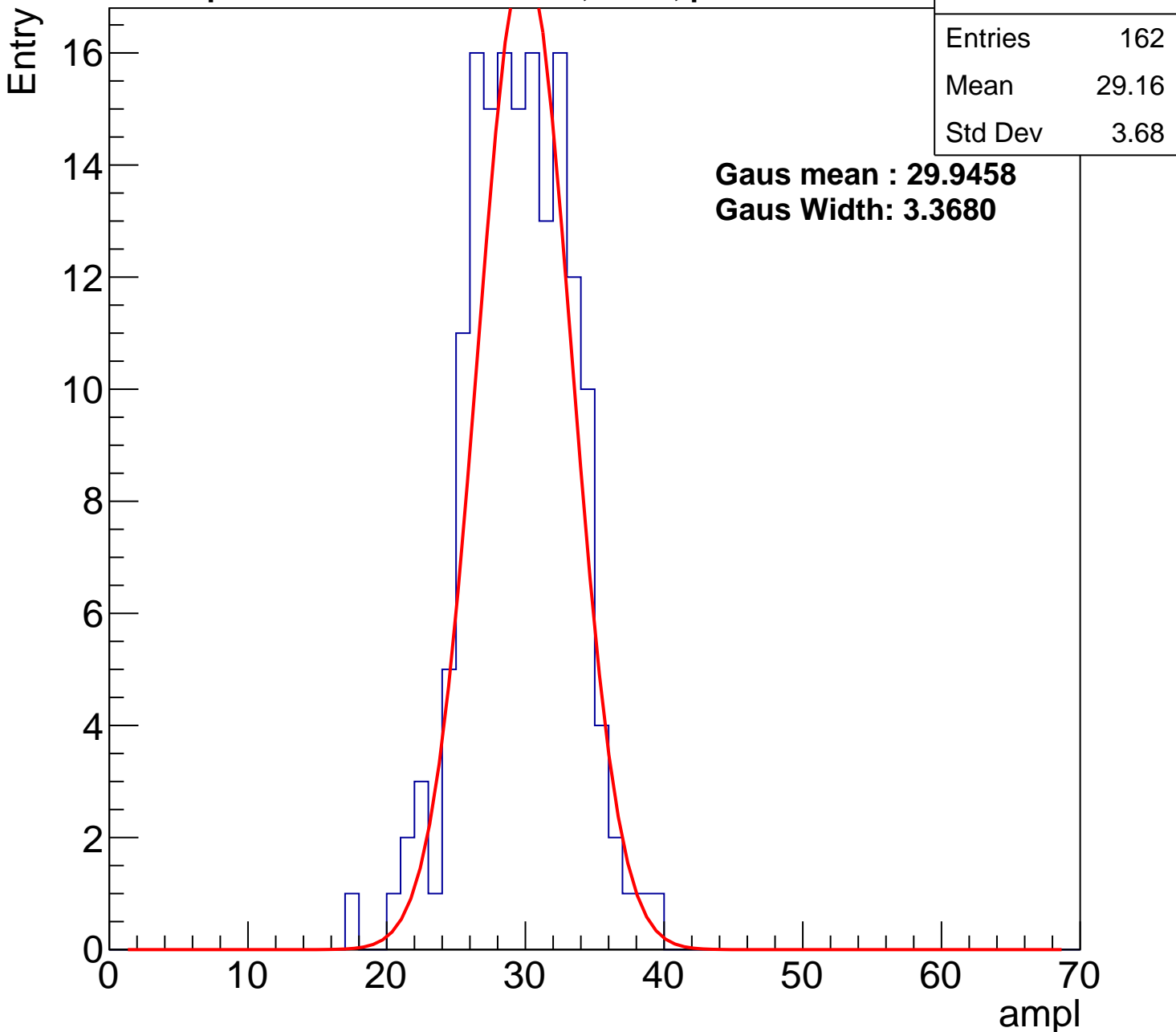
Gaus Width: 3.3680

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

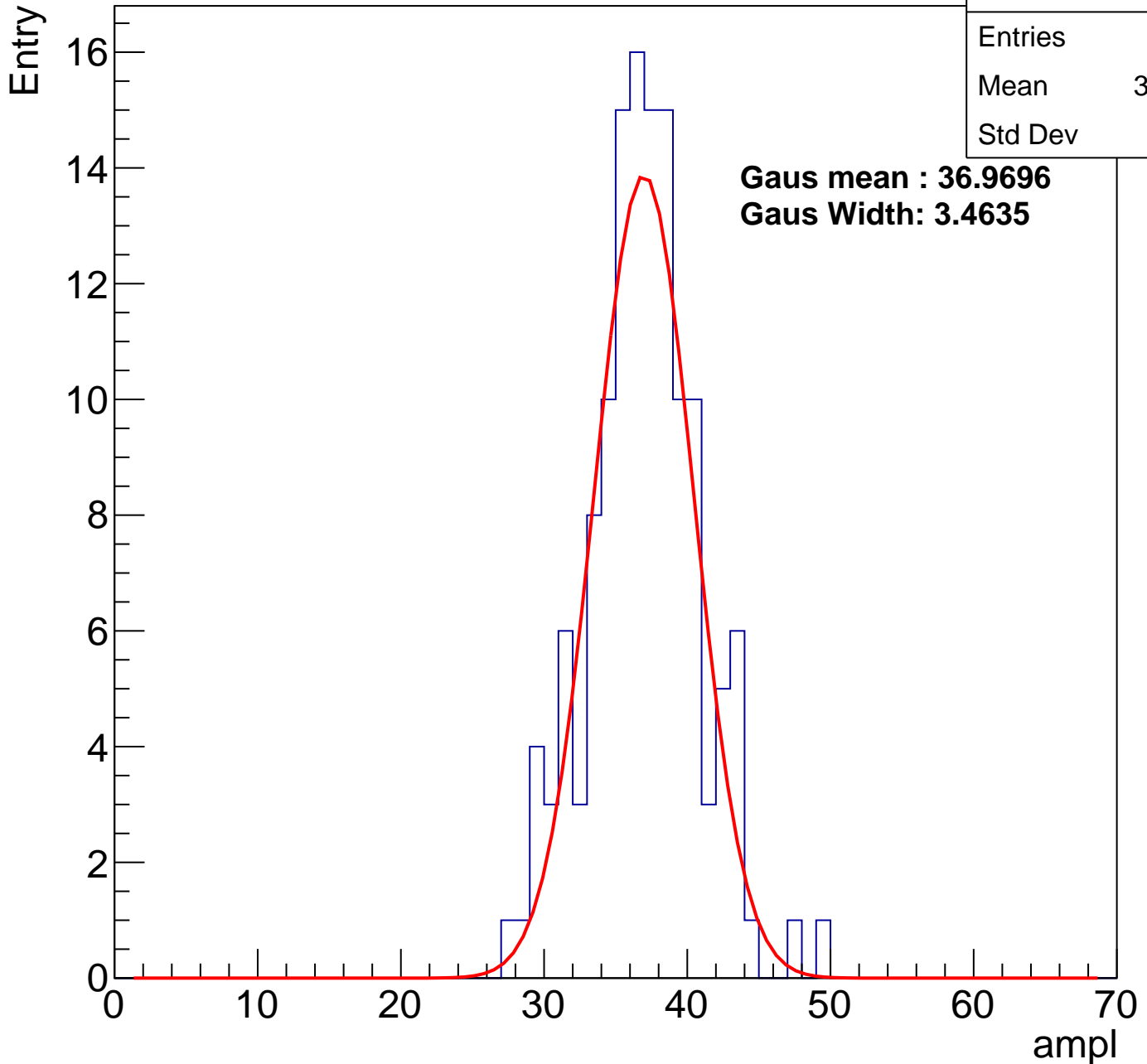


B0L101S, U22-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 36.52 |
| Std Dev | 3.83 |

Gaus mean : 36.9696
Gaus Width: 3.4635



B0L101S, U22-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

141

Mean

43.22

Std Dev

3.47

Gaus mean : 43.6384

Gaus Width: 3.5261

0

10

20

30

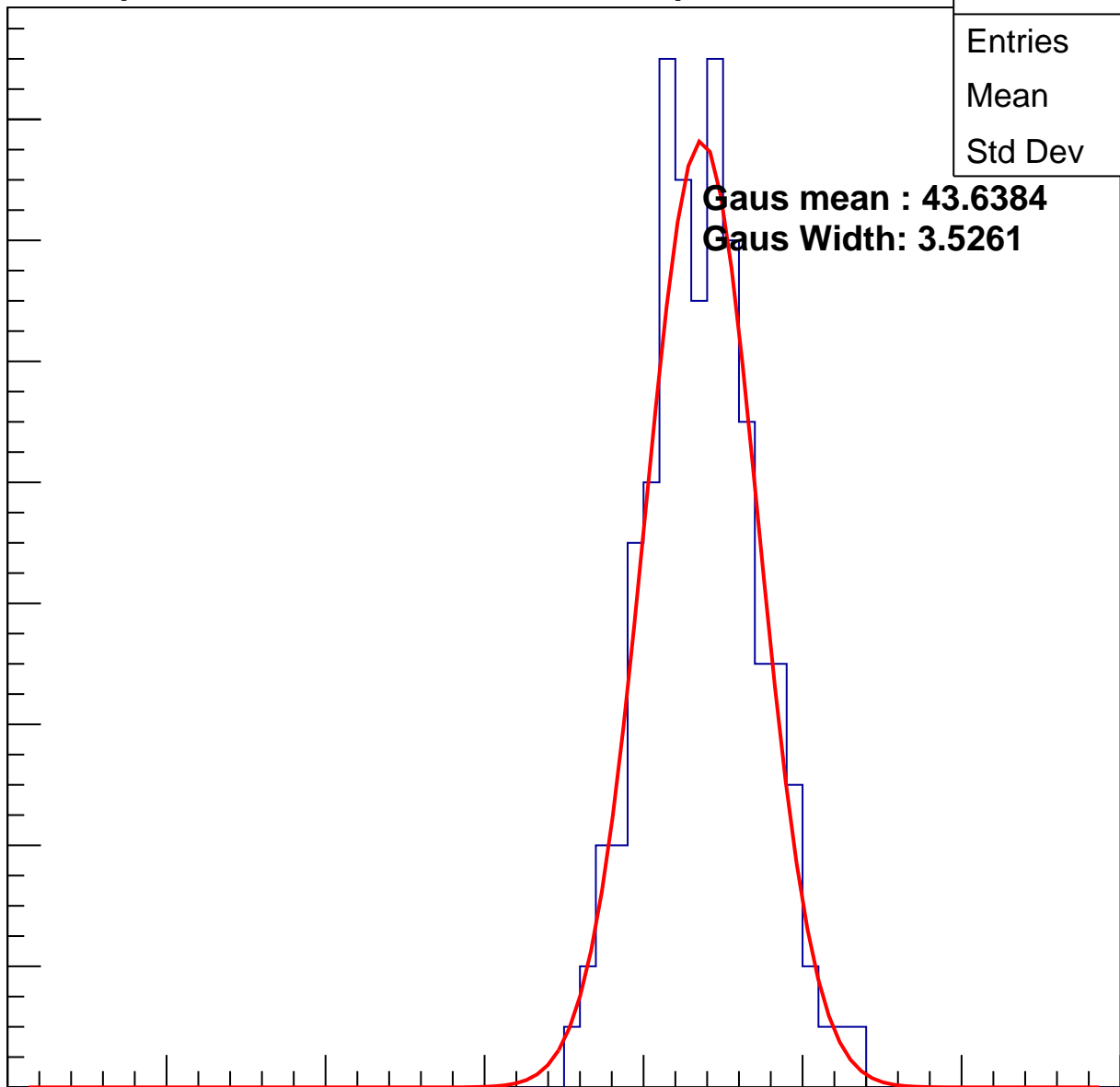
40

50

60

70

ampl



B0L101S, U22-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

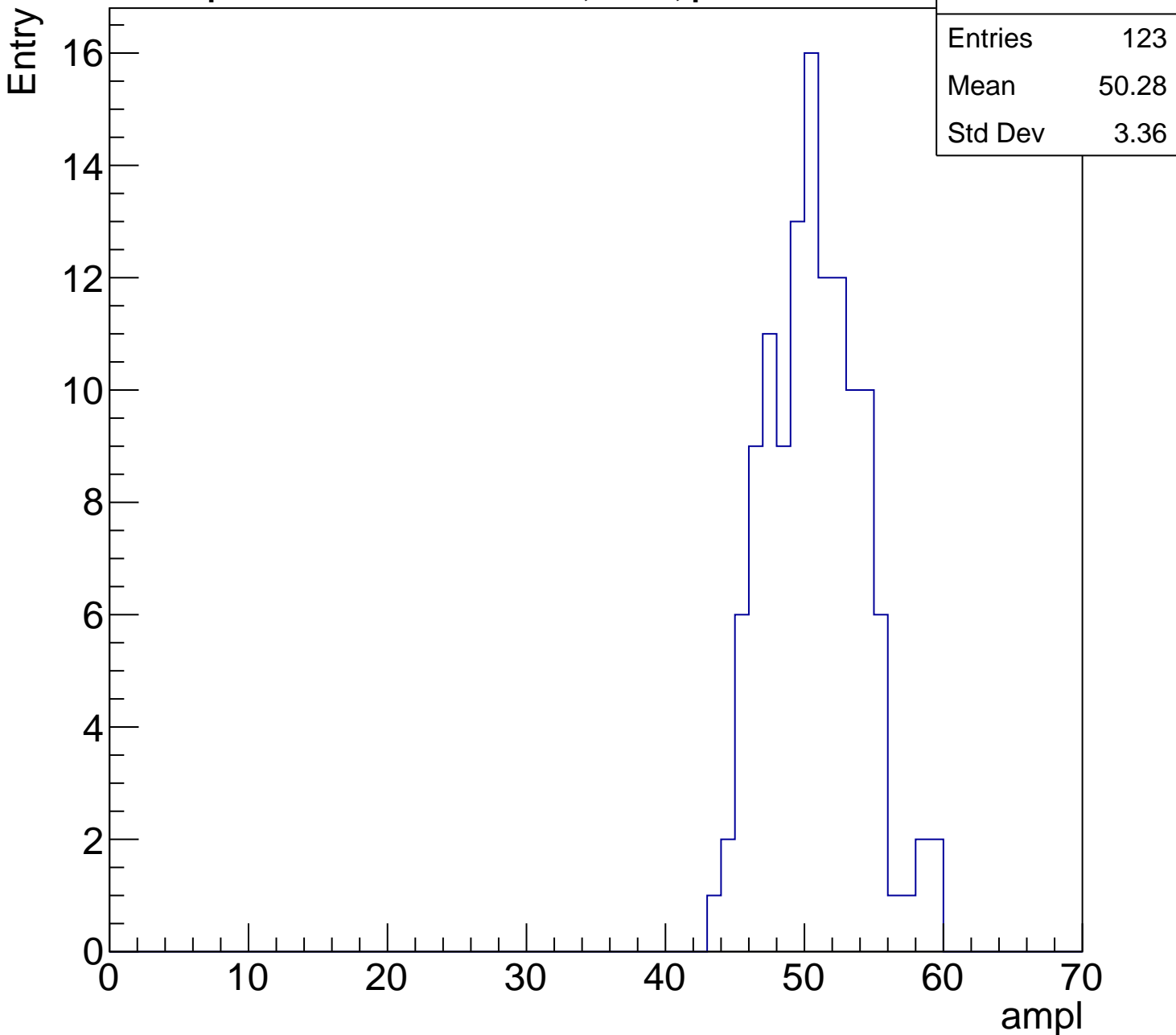
| | |
|---------|-------|
| Entries | 123 |
| Mean | 50.28 |
| Std Dev | 3.36 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

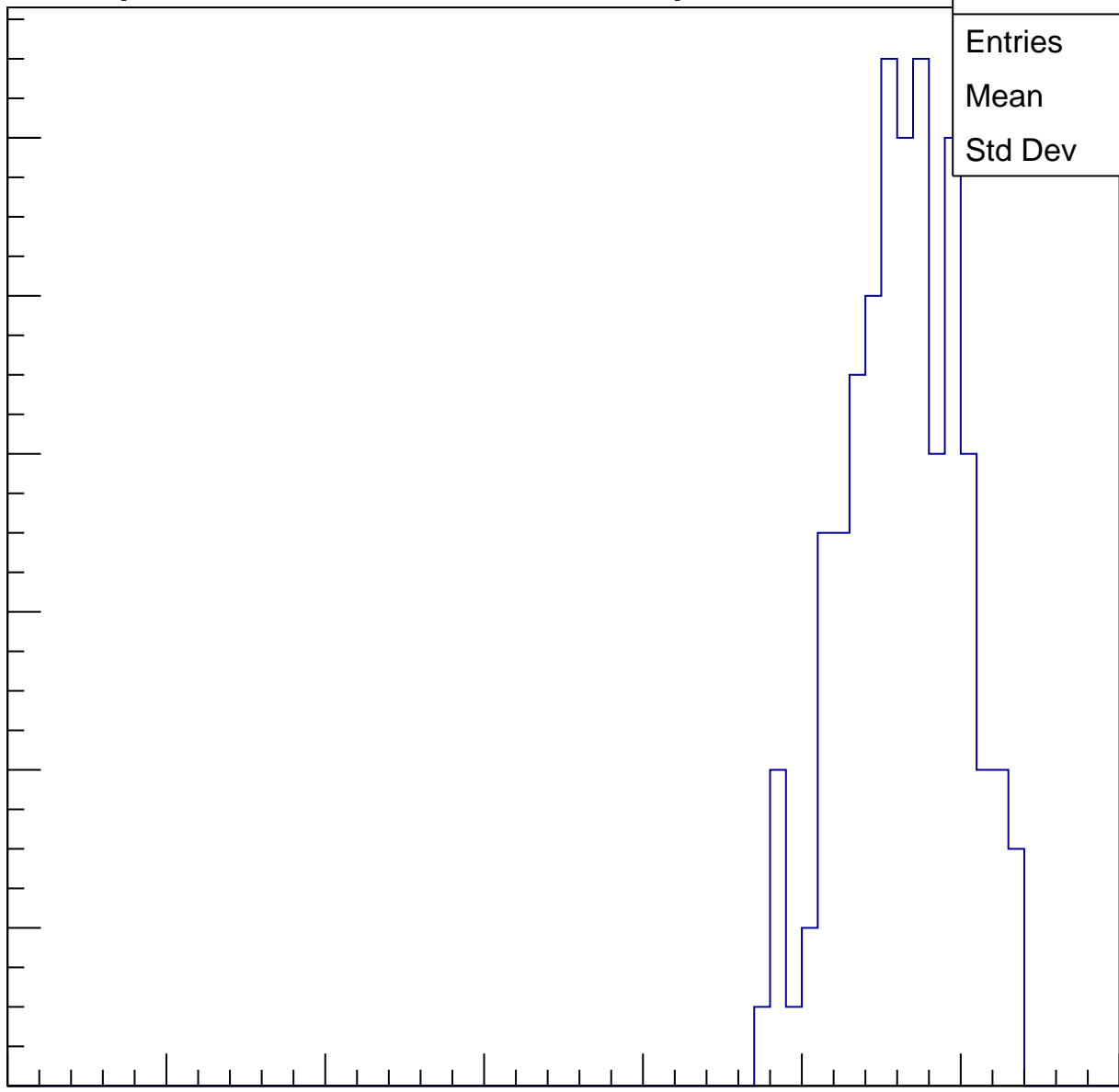
40

50

60

ampl

| | |
|---------|-------|
| Entries | 118 |
| Mean | 55.82 |
| Std Dev | 3.621 |



B0L101S, U22-ch80, adc5

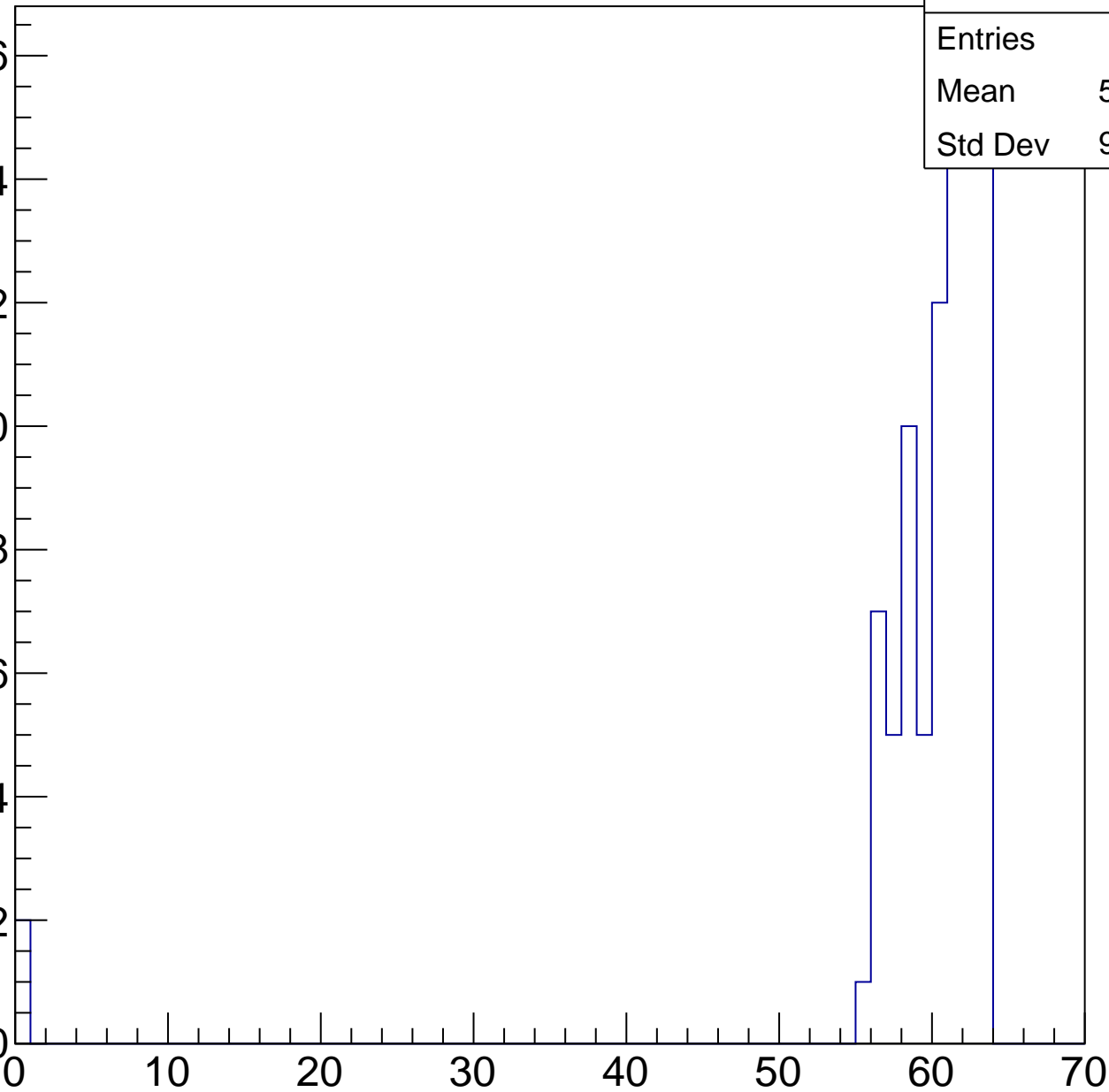
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 58.89 |
| Std Dev | 9.202 |

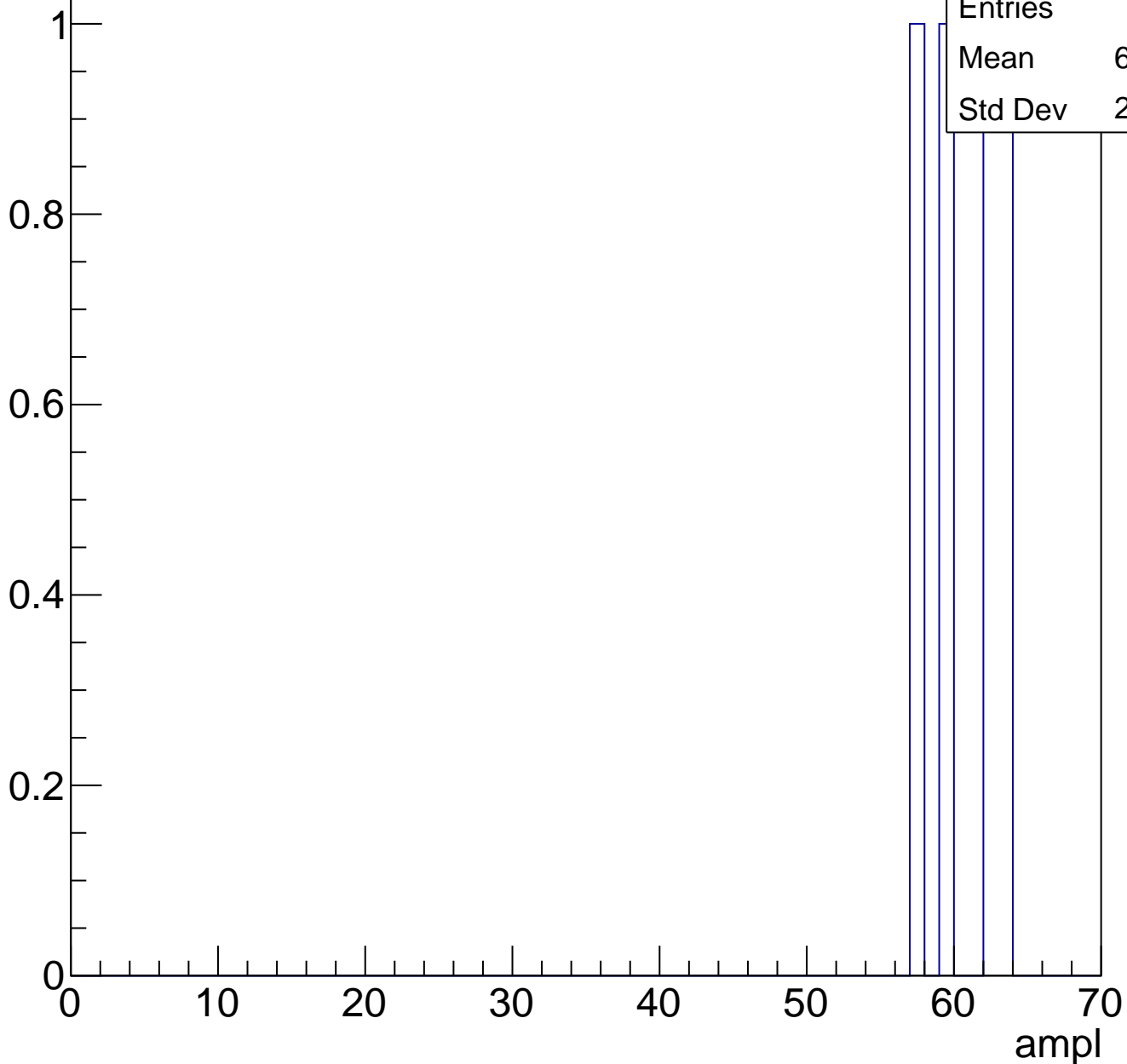
ampl



B0L101S, U22-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch81, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 116 |
| Mean | 29.35 |
| Std Dev | 3.23 |

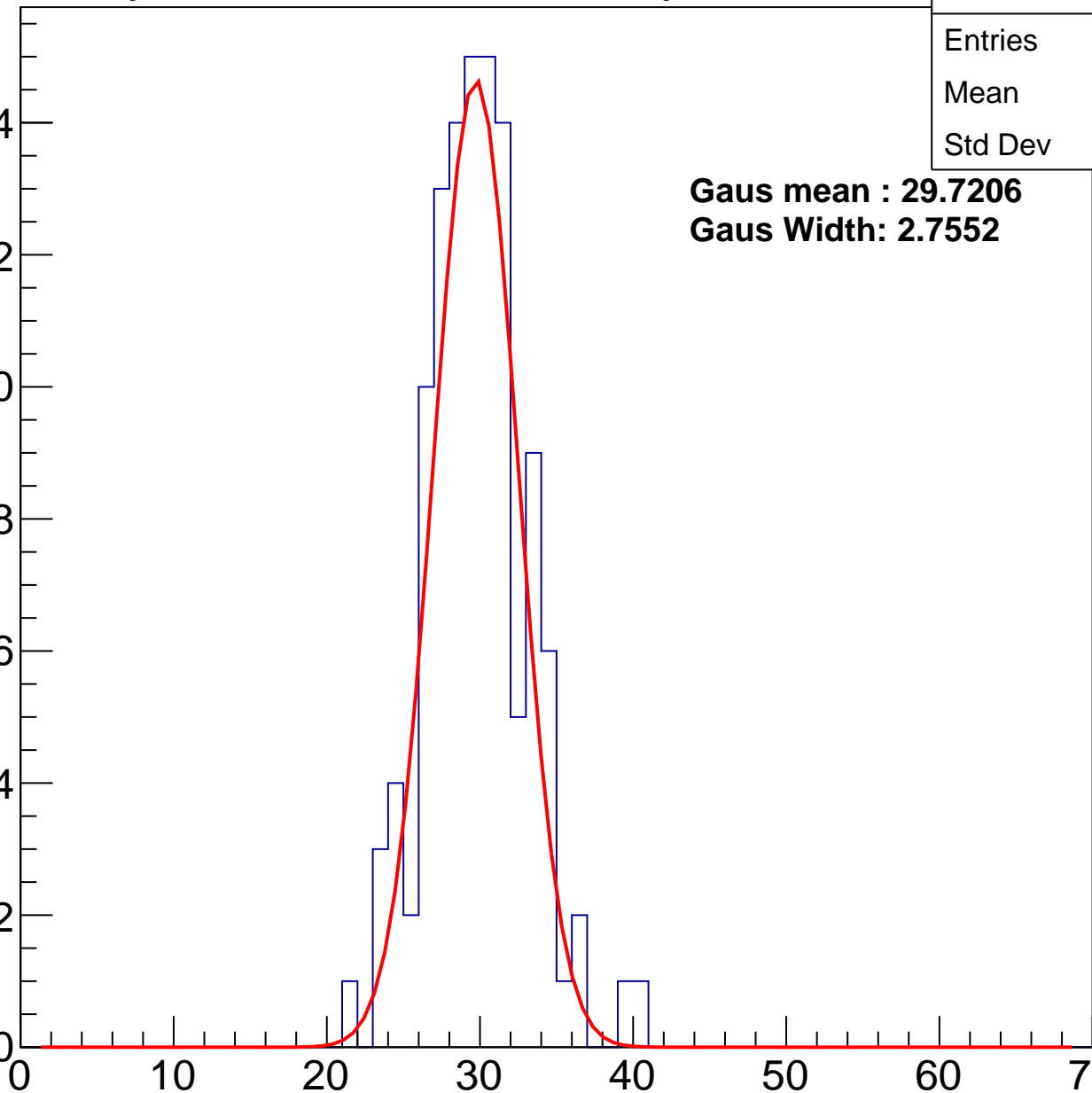
Gaus mean : 29.7206

Gaus Width: 2.7552

Entry

14
12
10
8
6
4
2
0

ampl



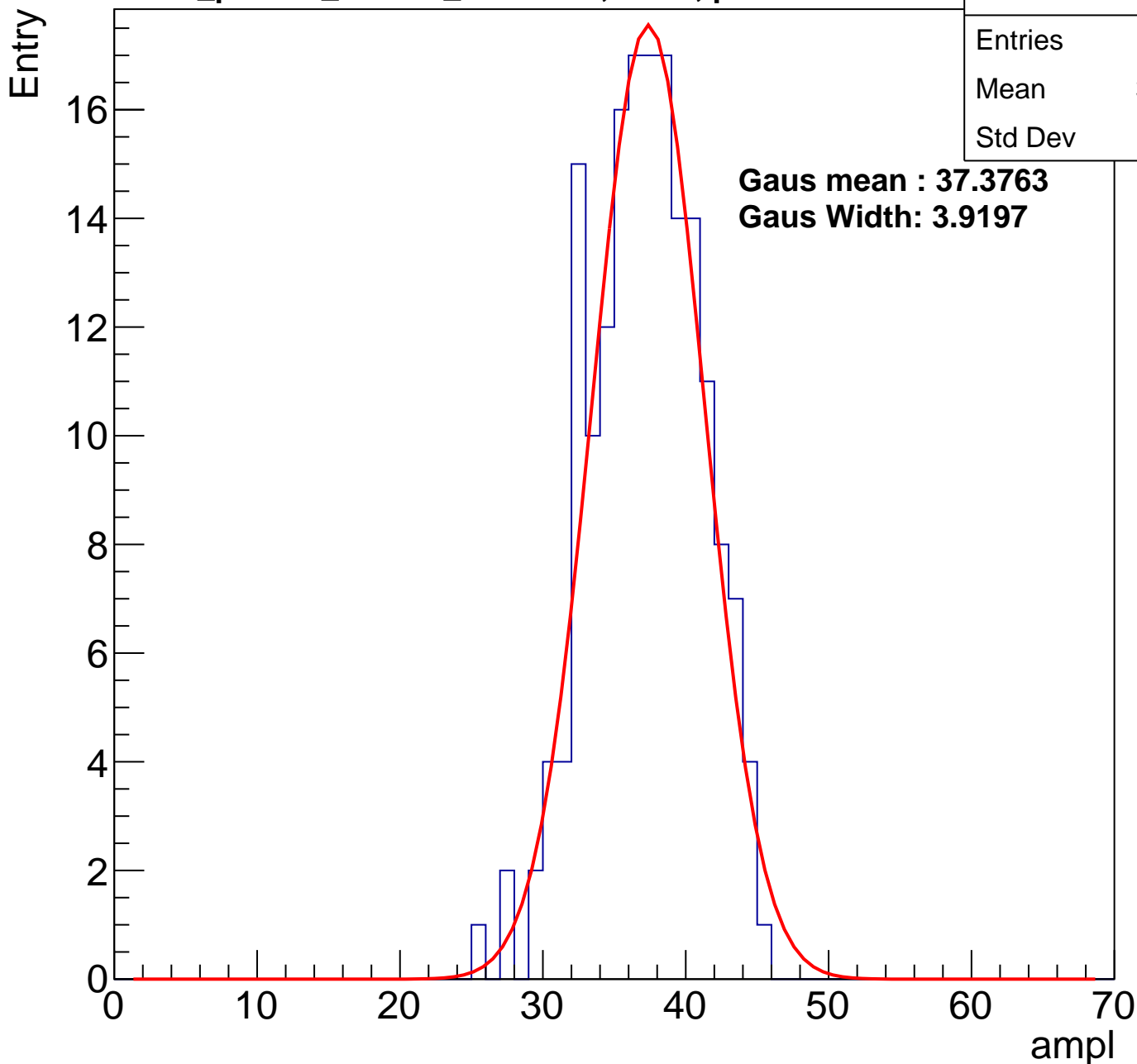
B0L101S, U22-ch81, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 176 |
| Mean | 36.71 |
| Std Dev | 3.85 |

Gaus mean : 37.3763

Gaus Width: 3.9197



B0L101S, U22-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

148

Mean

44.28

Std Dev

4.075

Gaus mean : 45.0376

Gaus Width: 3.7010

0

10

20

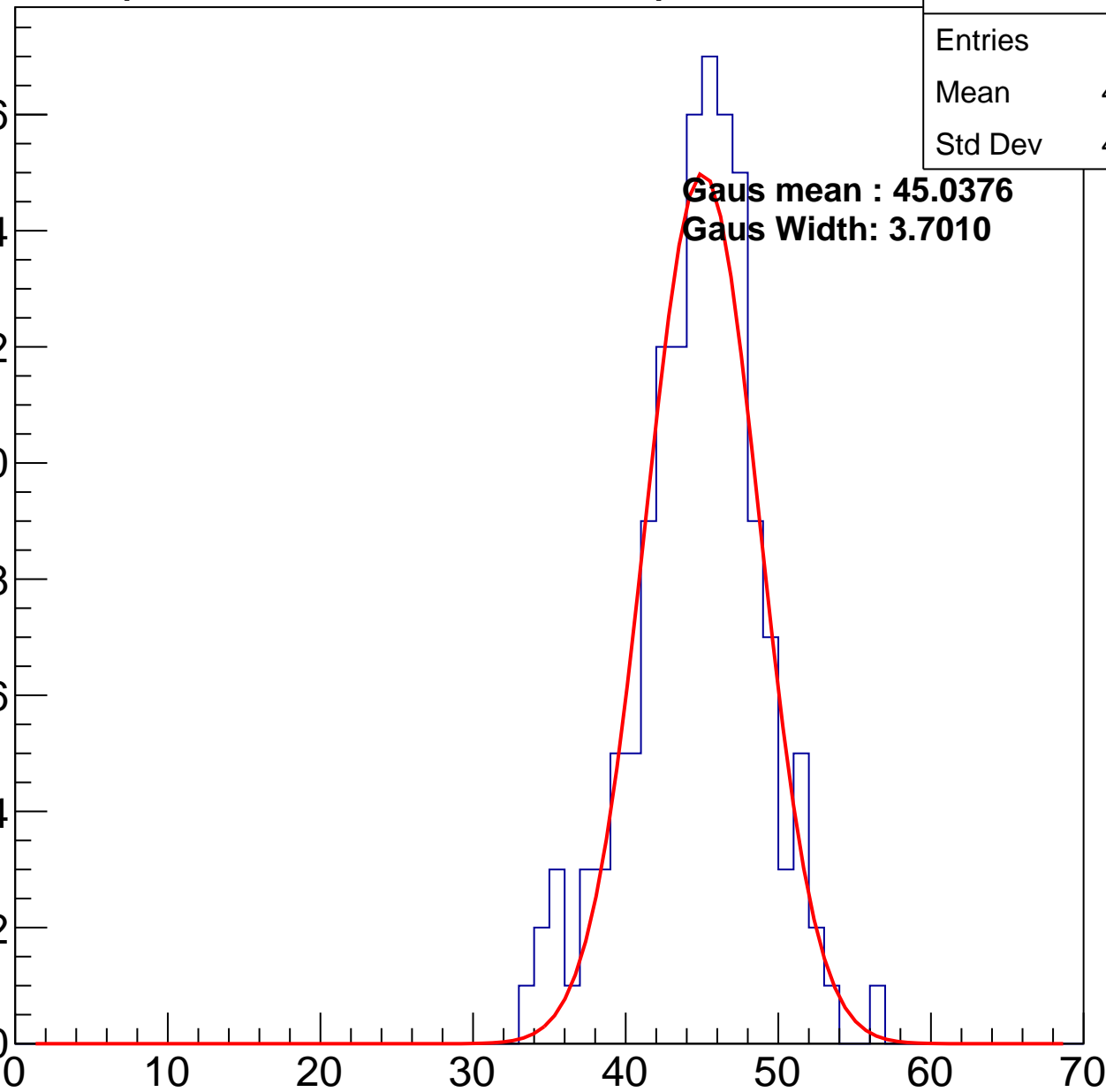
30

40

50

60

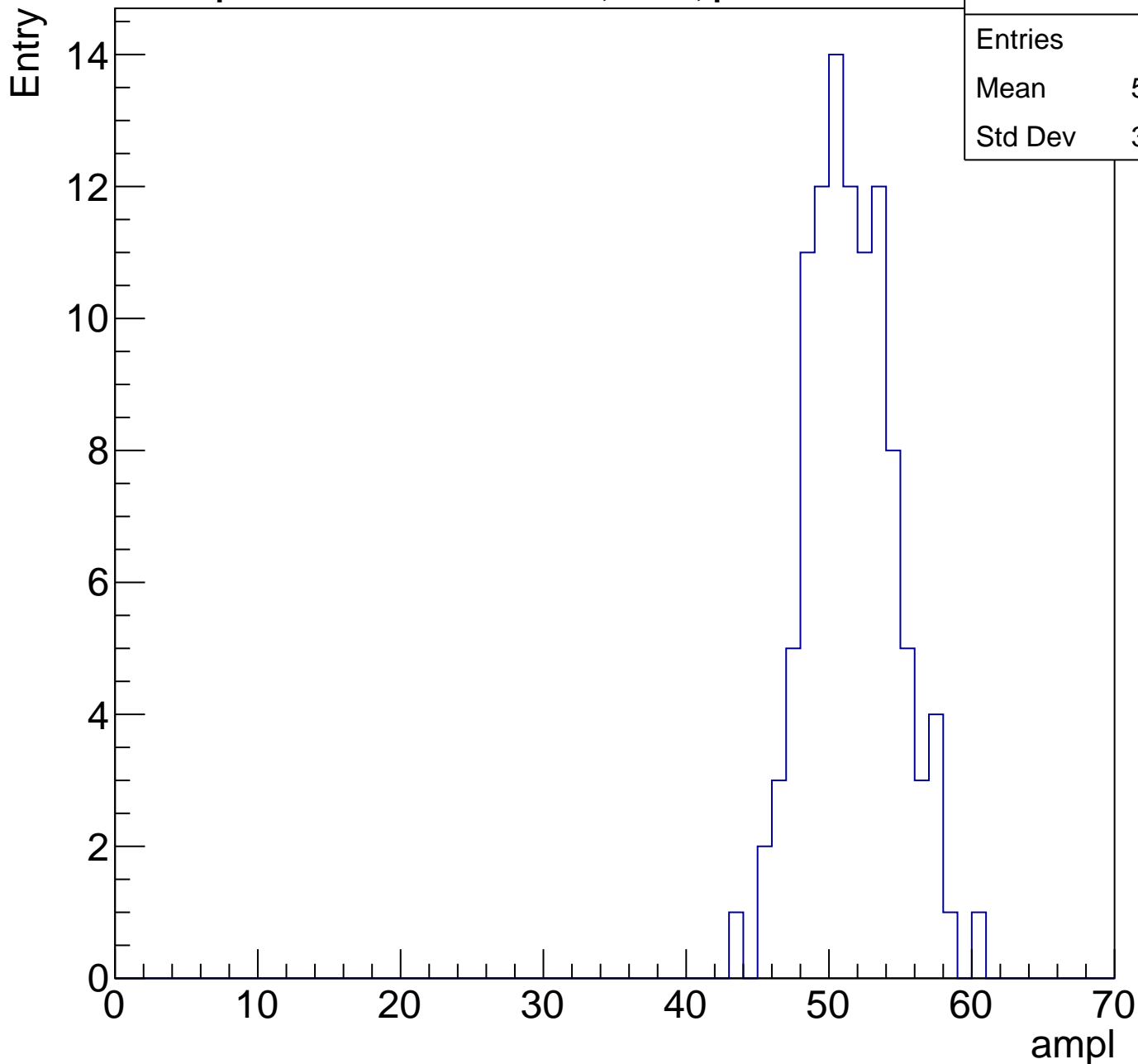
ampl



B0L101S, U22-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

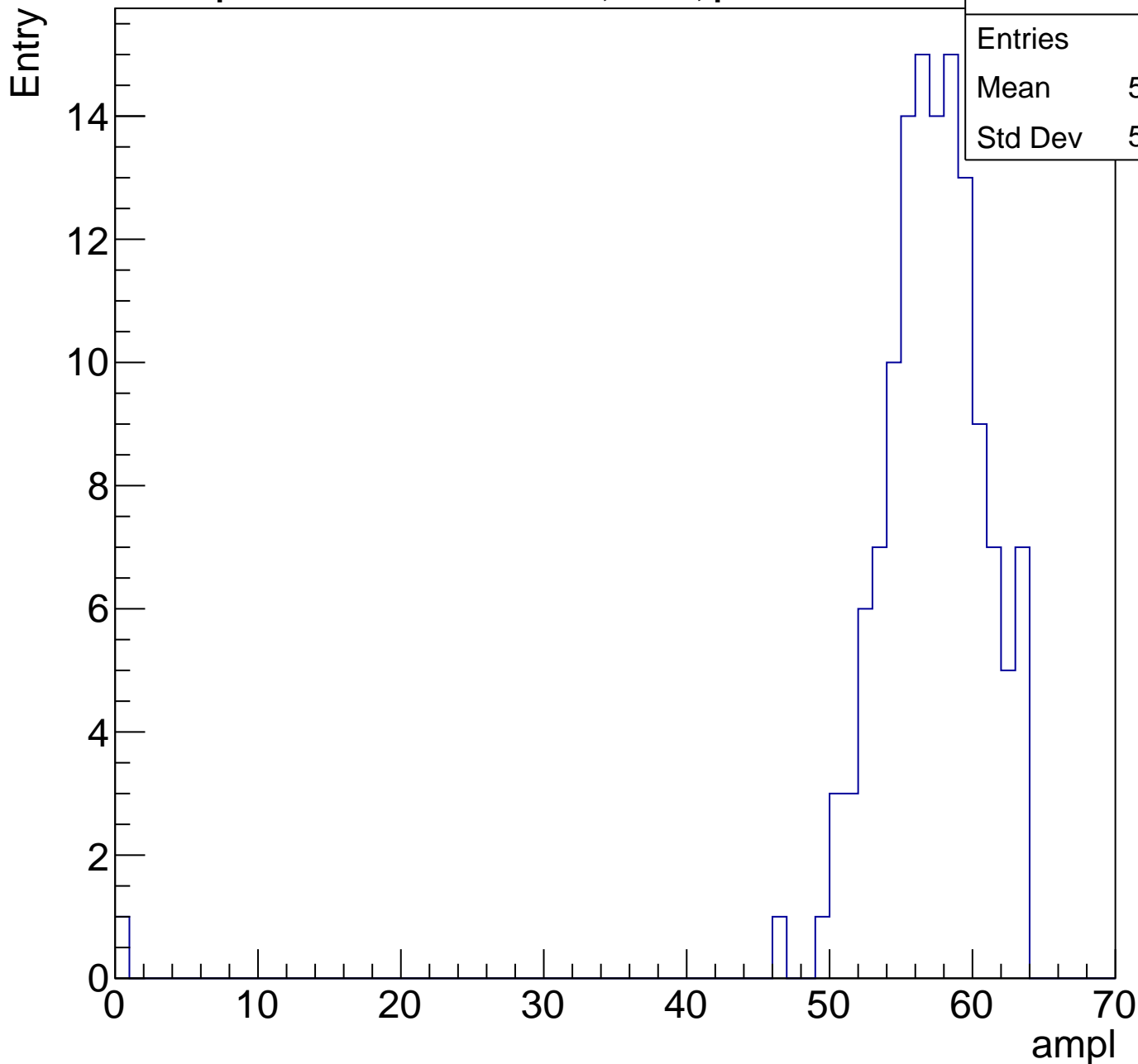
| | |
|---------|-------|
| Entries | 105 |
| Mean | 51.08 |
| Std Dev | 3.104 |



B0L101S, U22-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 56.37 |
| Std Dev | 5.988 |



B0L101S, U22-ch81, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.27 |
| Std Dev | 7.966 |

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62.12

Std Dev

0.7806

B0L101S, U22-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

115

Mean

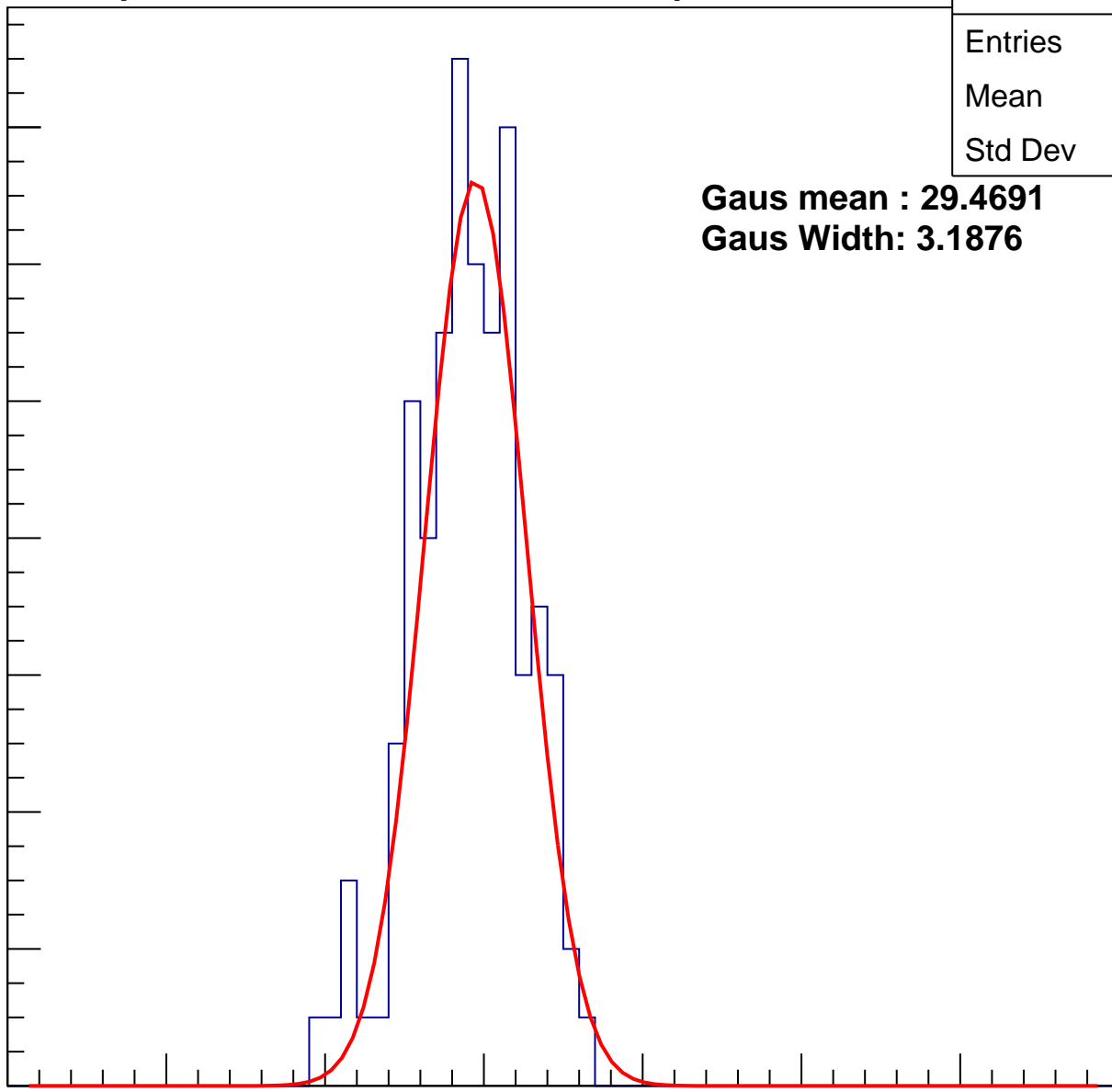
28.58

Std Dev

3.436

Gaus mean : 29.4691

Gaus Width: 3.1876



B0L101S, U22-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 154 |
| Mean | 35.21 |
| Std Dev | 3.721 |

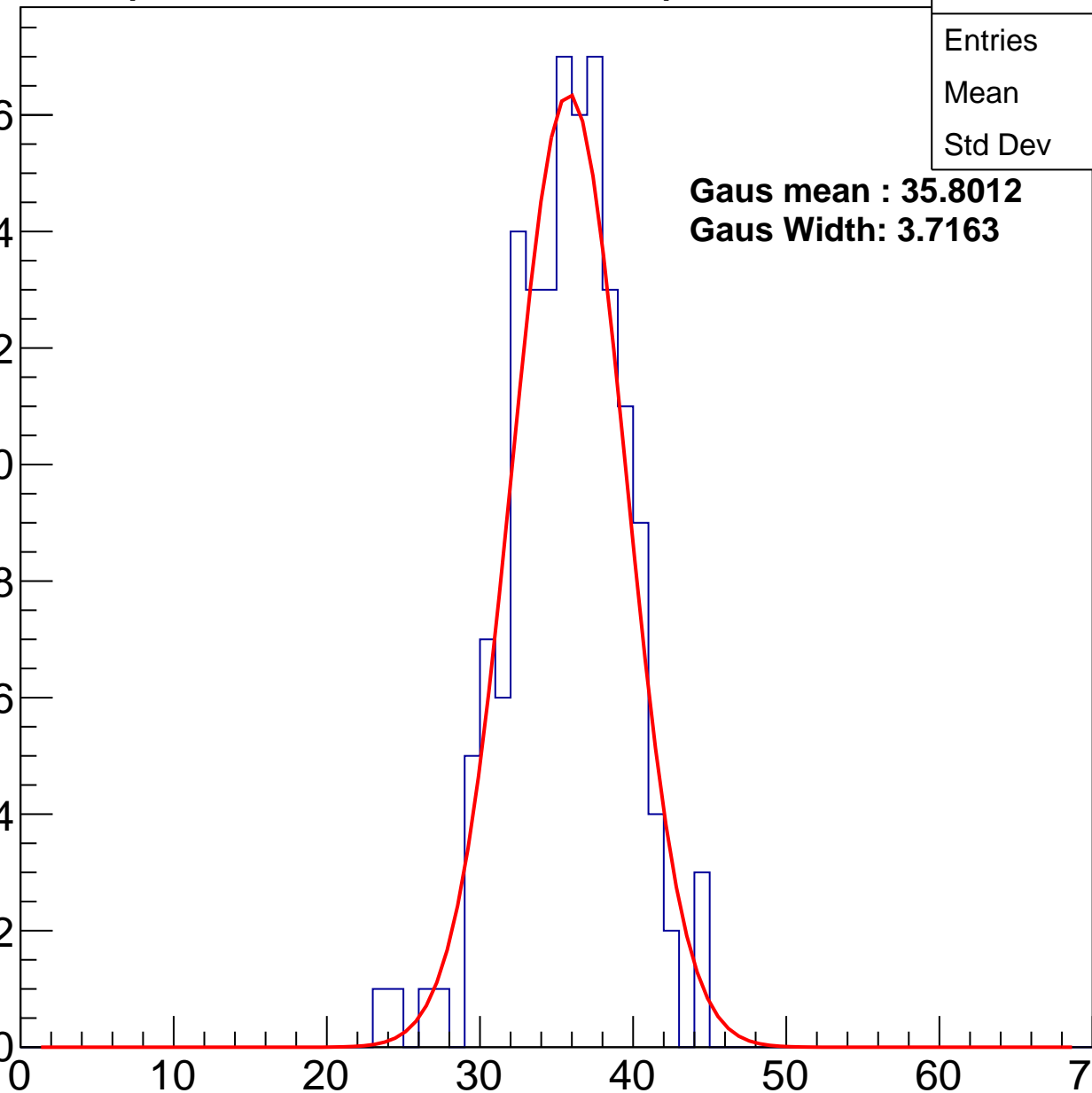
Gaus mean : 35.8012

Gaus Width: 3.7163

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

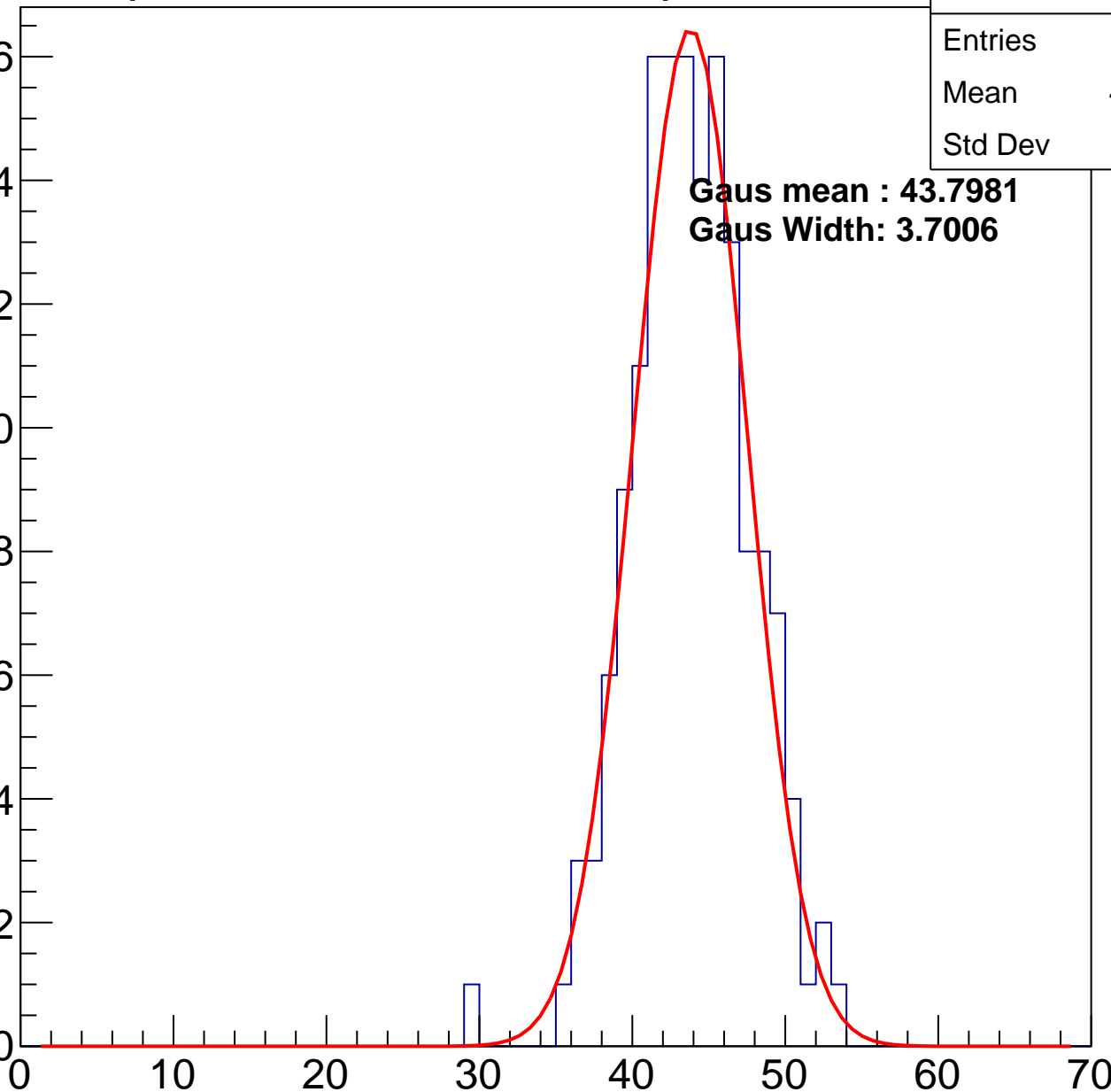
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 156 |
| Mean | 43.35 |
| Std Dev | 3.811 |

Gaus mean : 43.7981

Gaus Width: 3.7006

ampl

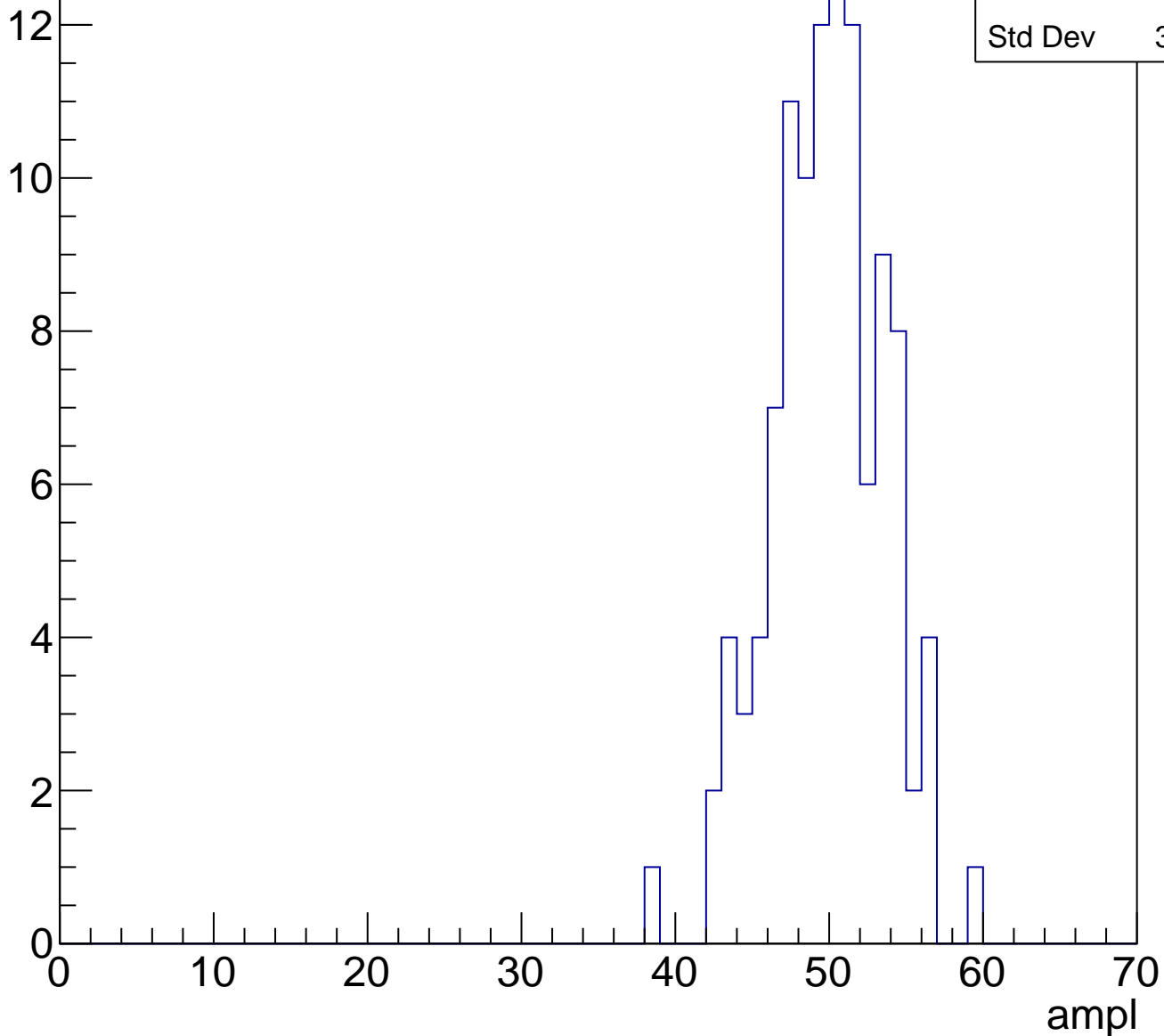


B0L101S, U22-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 109 |
| Mean | 49.44 |
| Std Dev | 3.616 |

Entry



B0L101S, U22-ch82, adc4

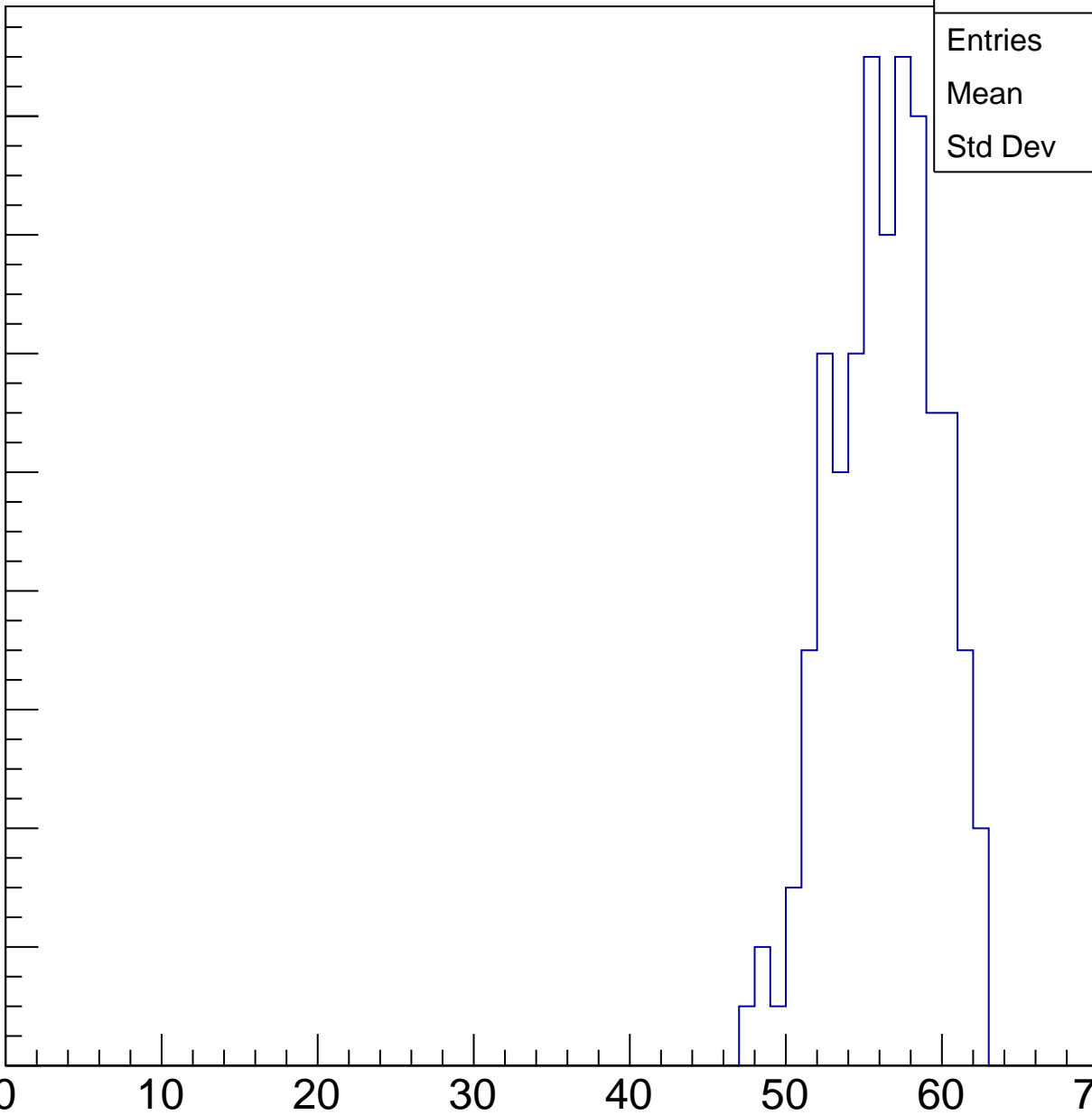
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 55.87 |
| Std Dev | 3.281 |

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch82, adc5

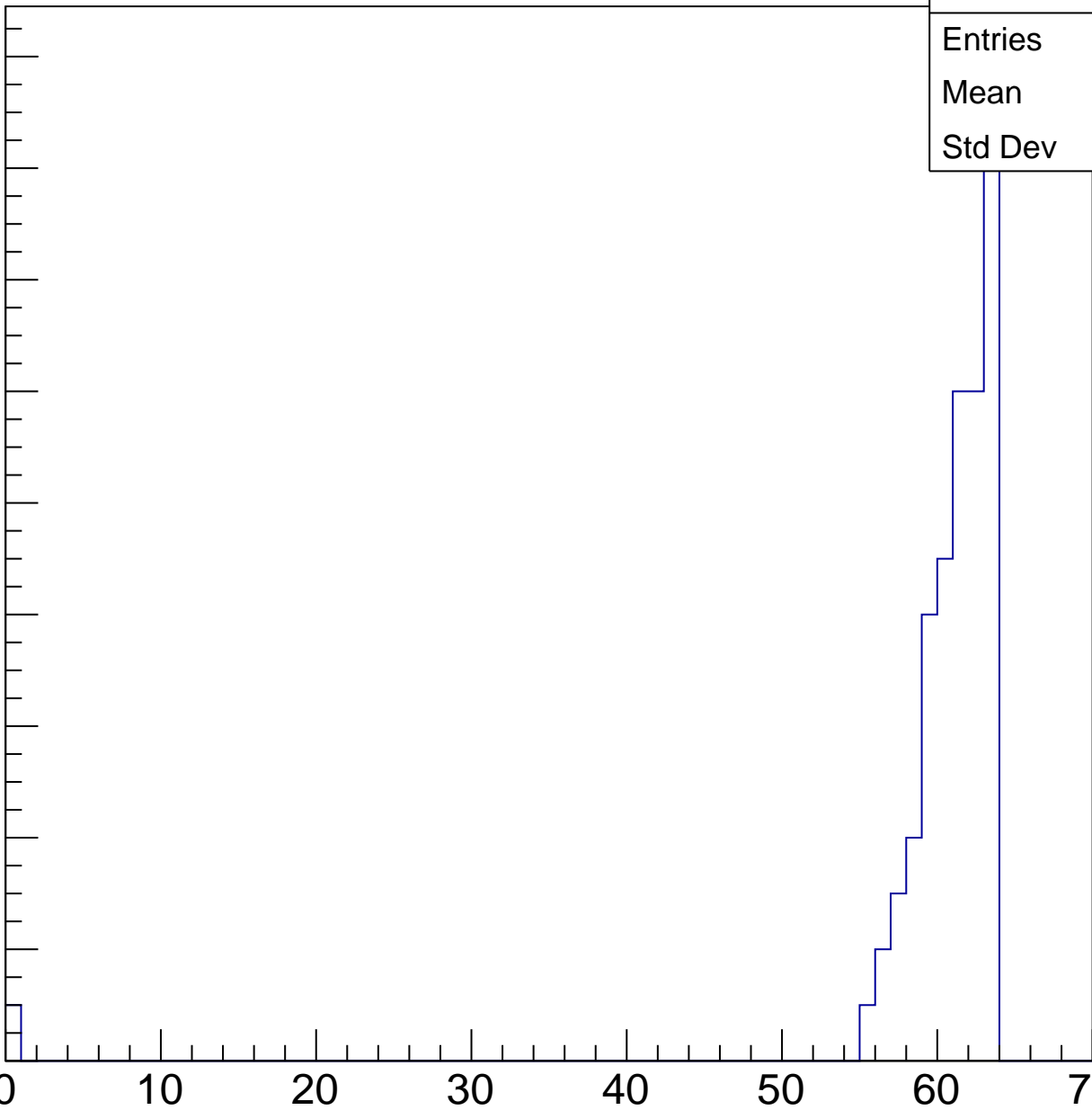
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 59.89 |
| Std Dev | 7.492 |

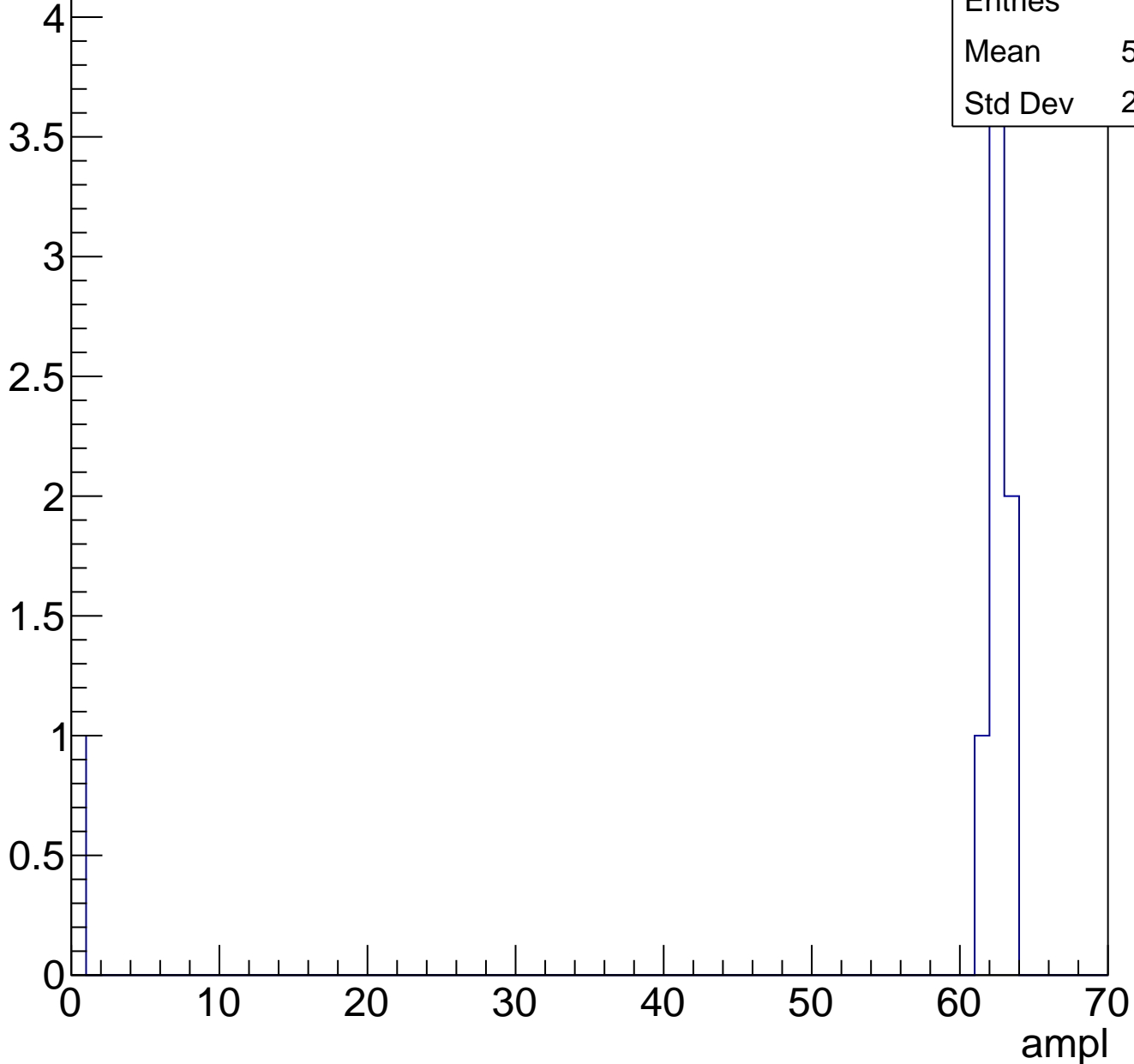
ampl



B0L101S, U22-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch83, adc0

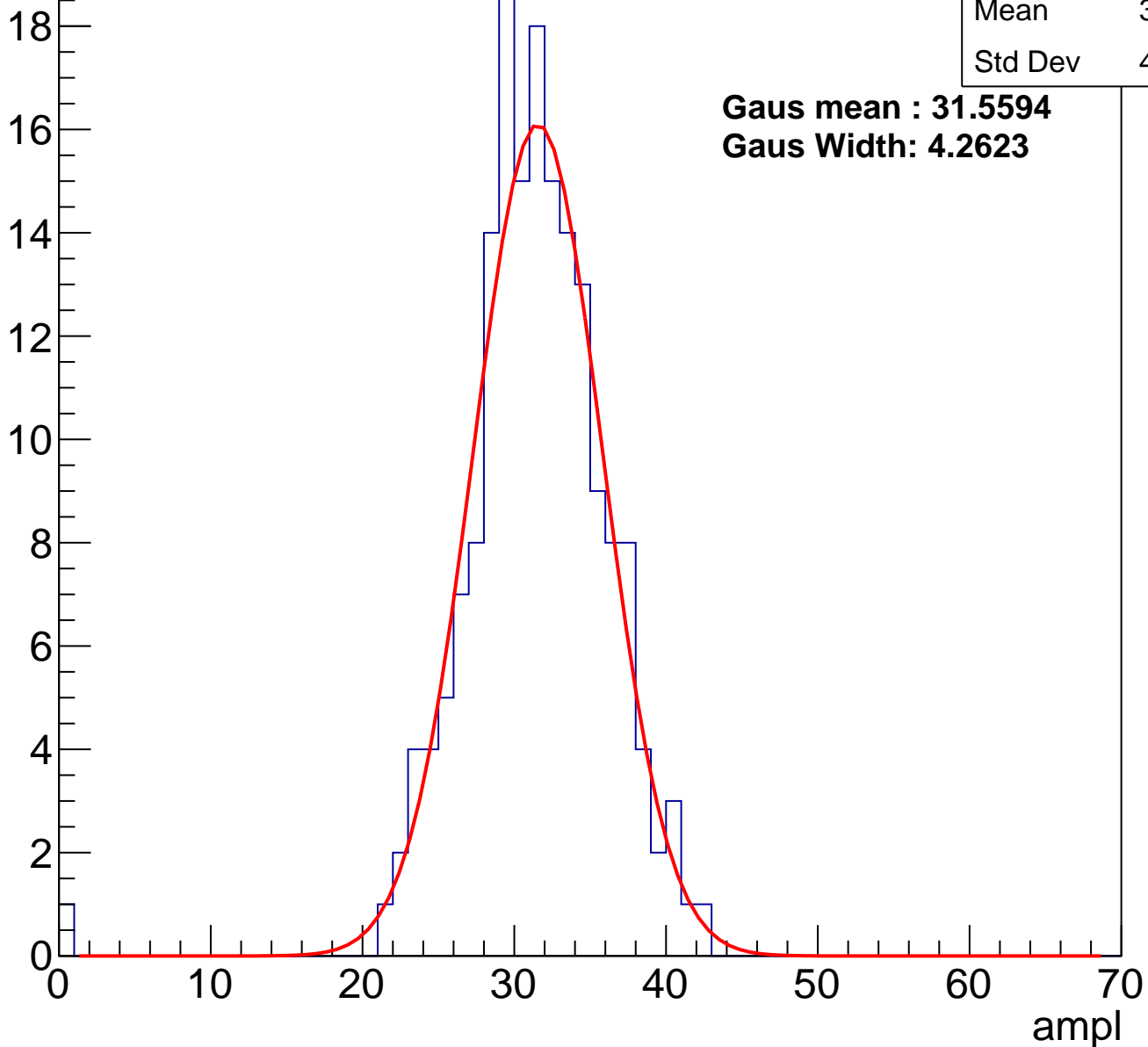
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 176 |
| Mean | 30.93 |
| Std Dev | 4.757 |

Gaus mean : 31.5594

Gaus Width: 4.2623

Entry



B0L101S, U22-ch83, adc1

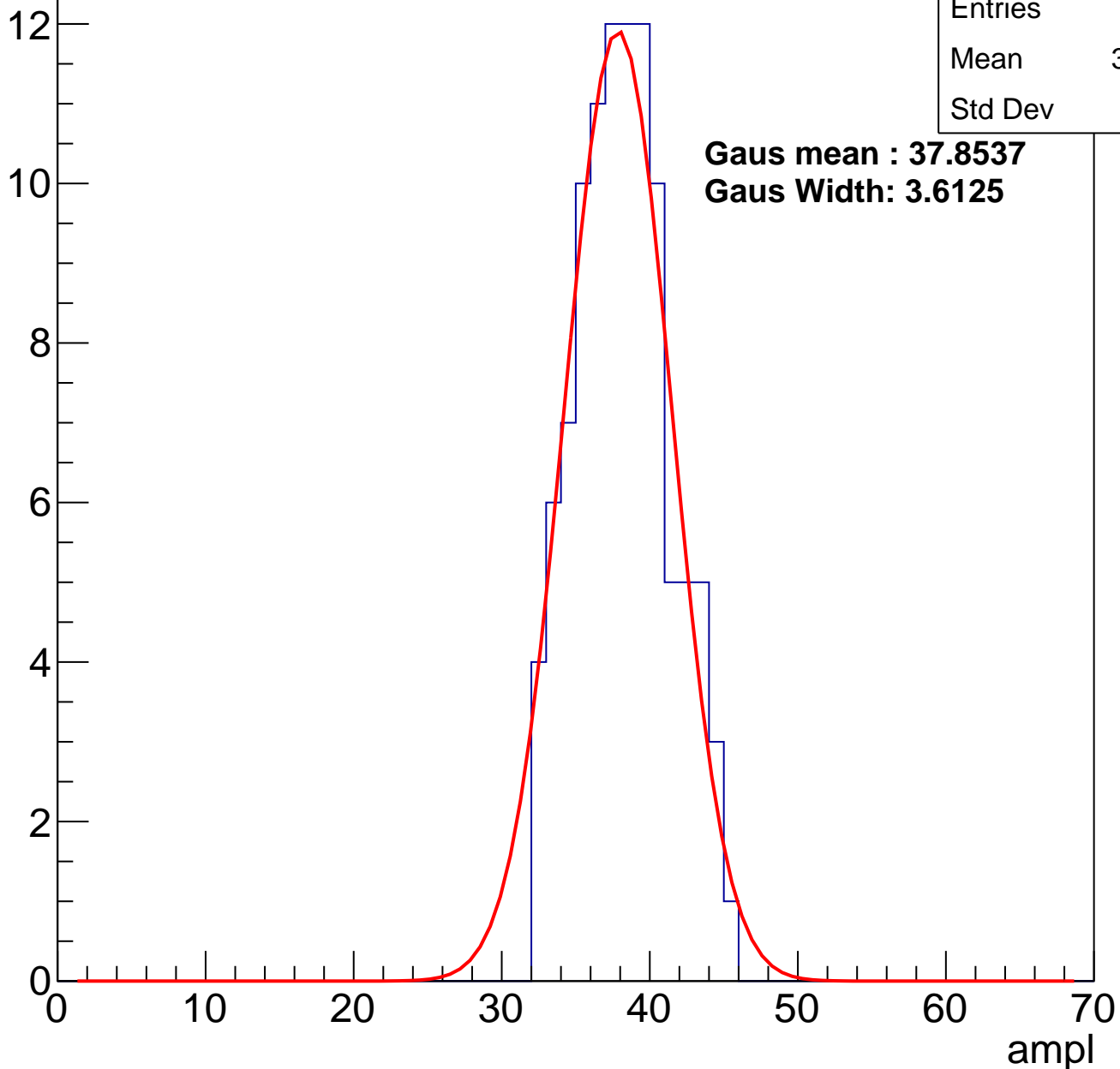
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 103 |
| Mean | 37.72 |
| Std Dev | 3.12 |

Gaus mean : 37.8537

Gaus Width: 3.6125

Entry



B0L101S, U22-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 43.75 |
| Std Dev | 3.566 |

Gaus mean : 44.1063

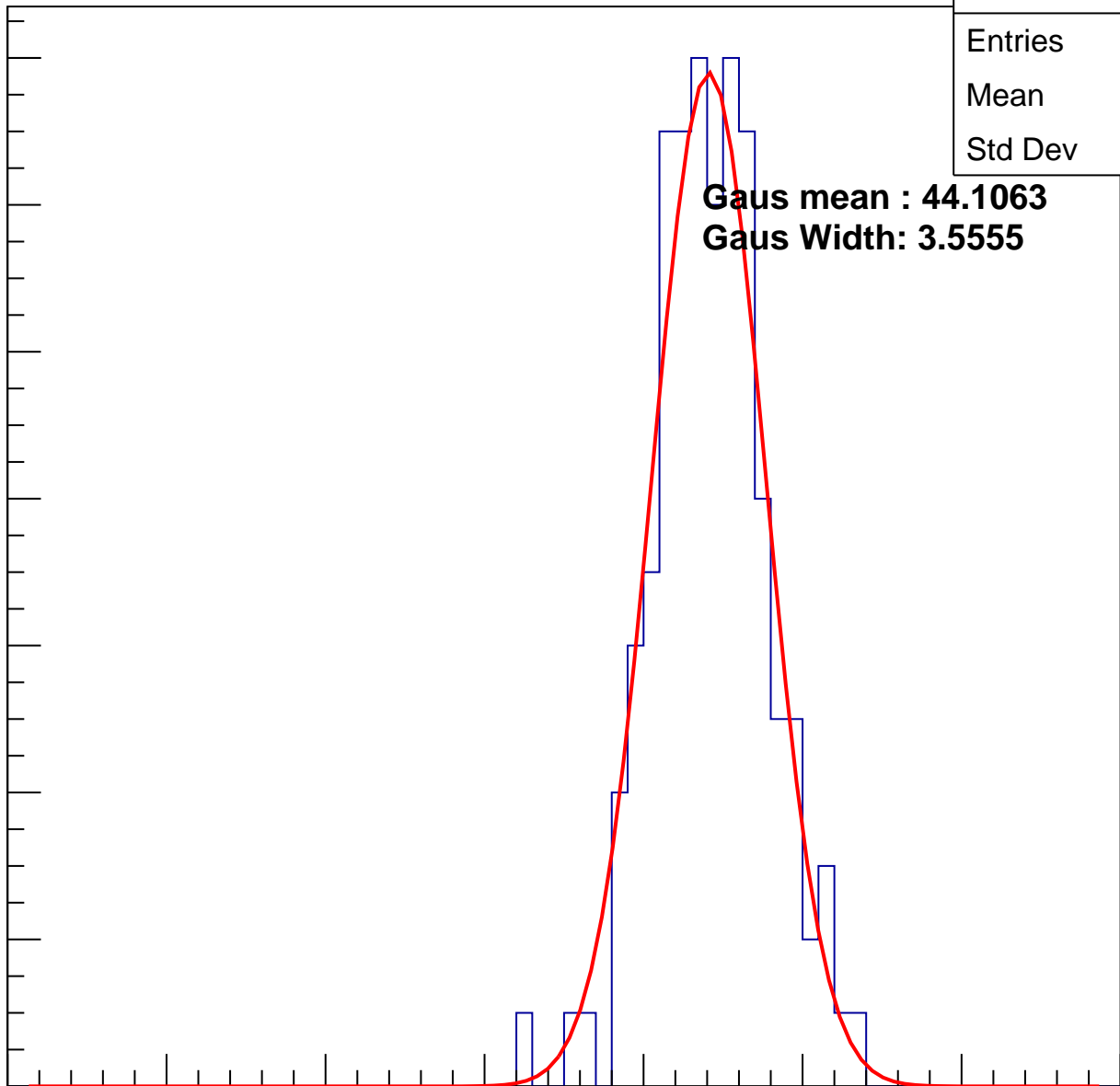
Gaus Width: 3.5555

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

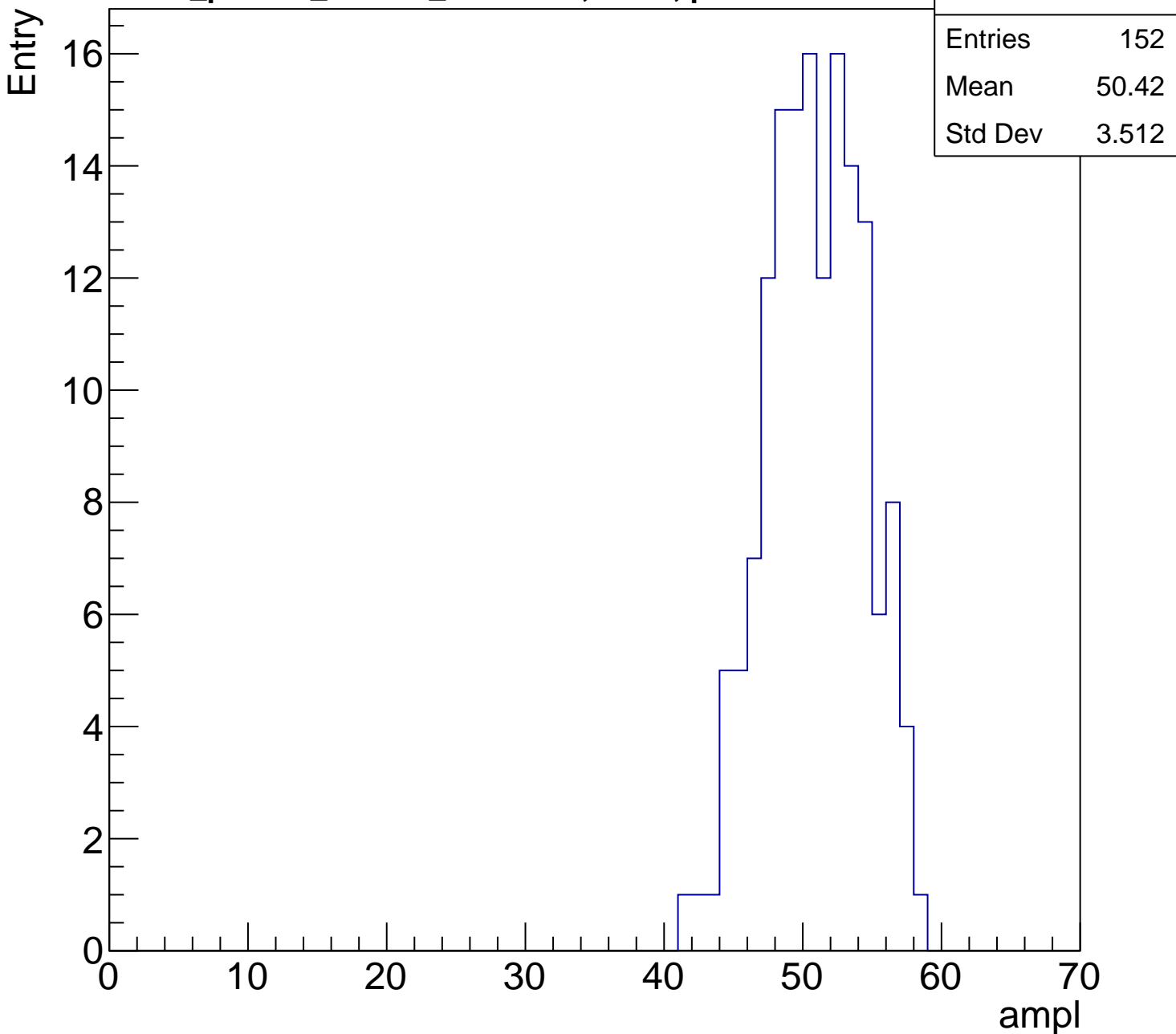
| | |
|---------|-------|
| Entries | 152 |
| Mean | 50.42 |
| Std Dev | 3.512 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch83, adc4

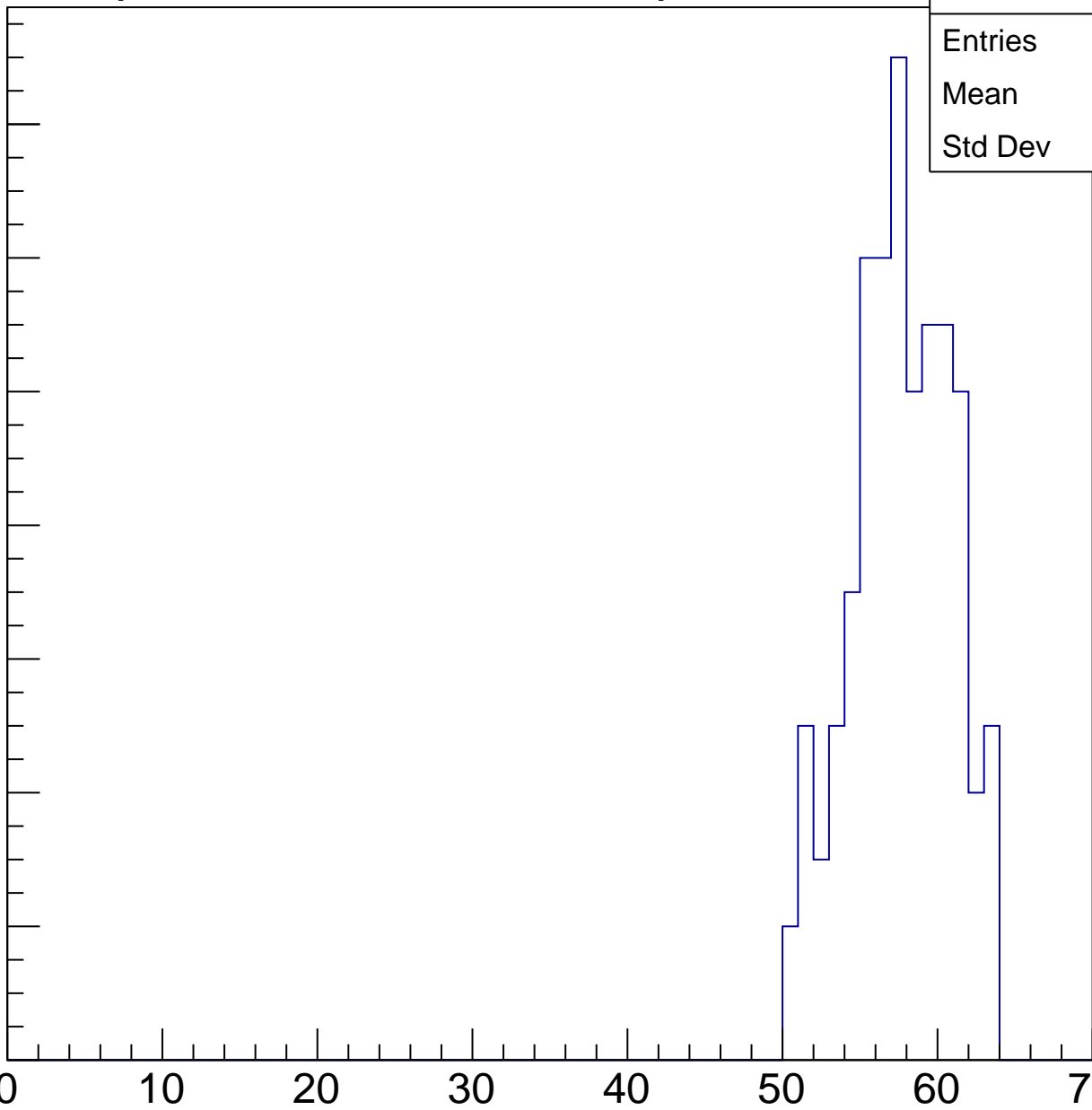
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 112 |
| Mean | 57.17 |
| Std Dev | 3.198 |

ampl



B0L101S, U22-ch83, adc5

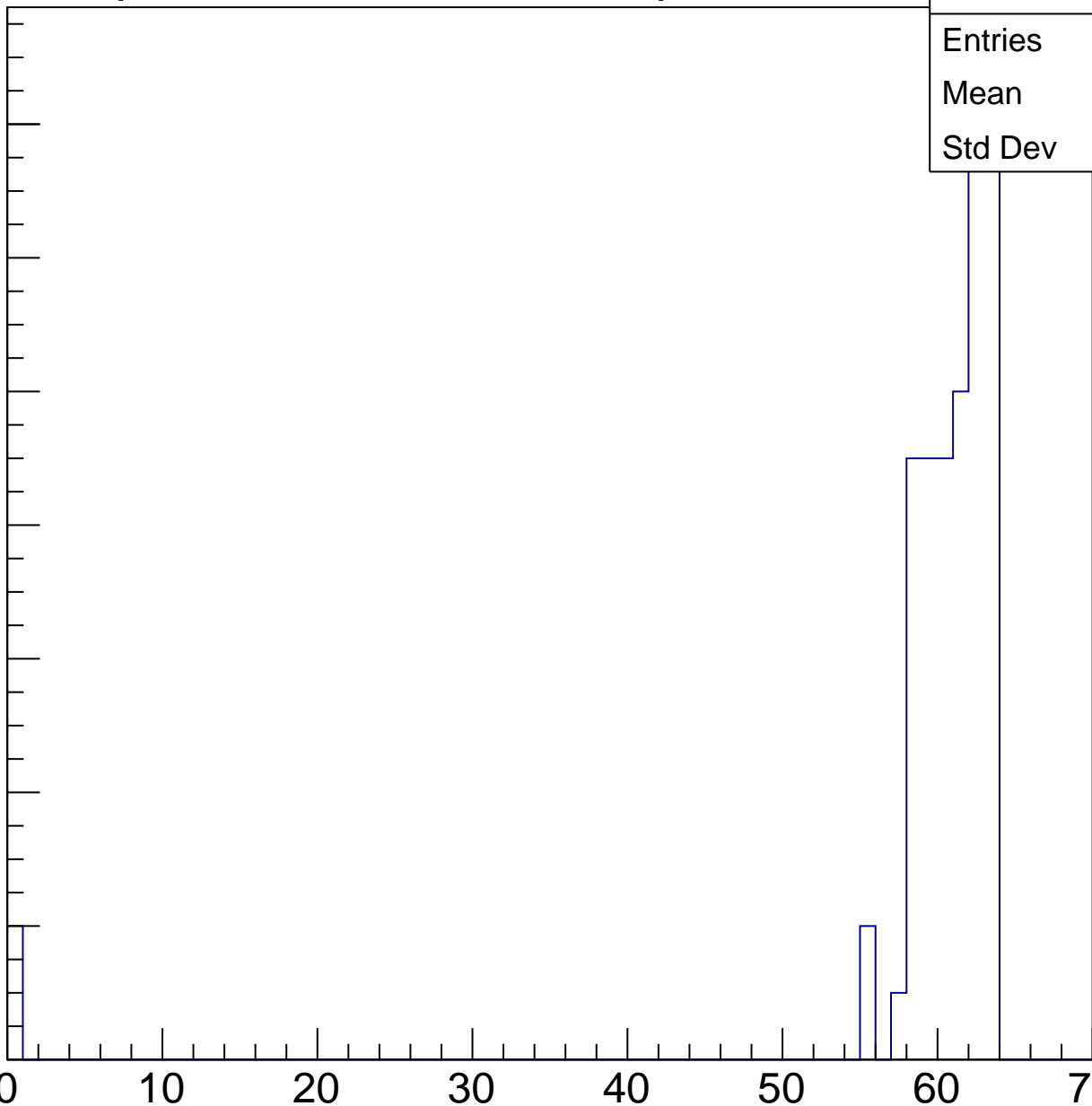
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 58.92 |
| Std Dev | 10.22 |

ampl



B0L101S, U22-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch84, adc0

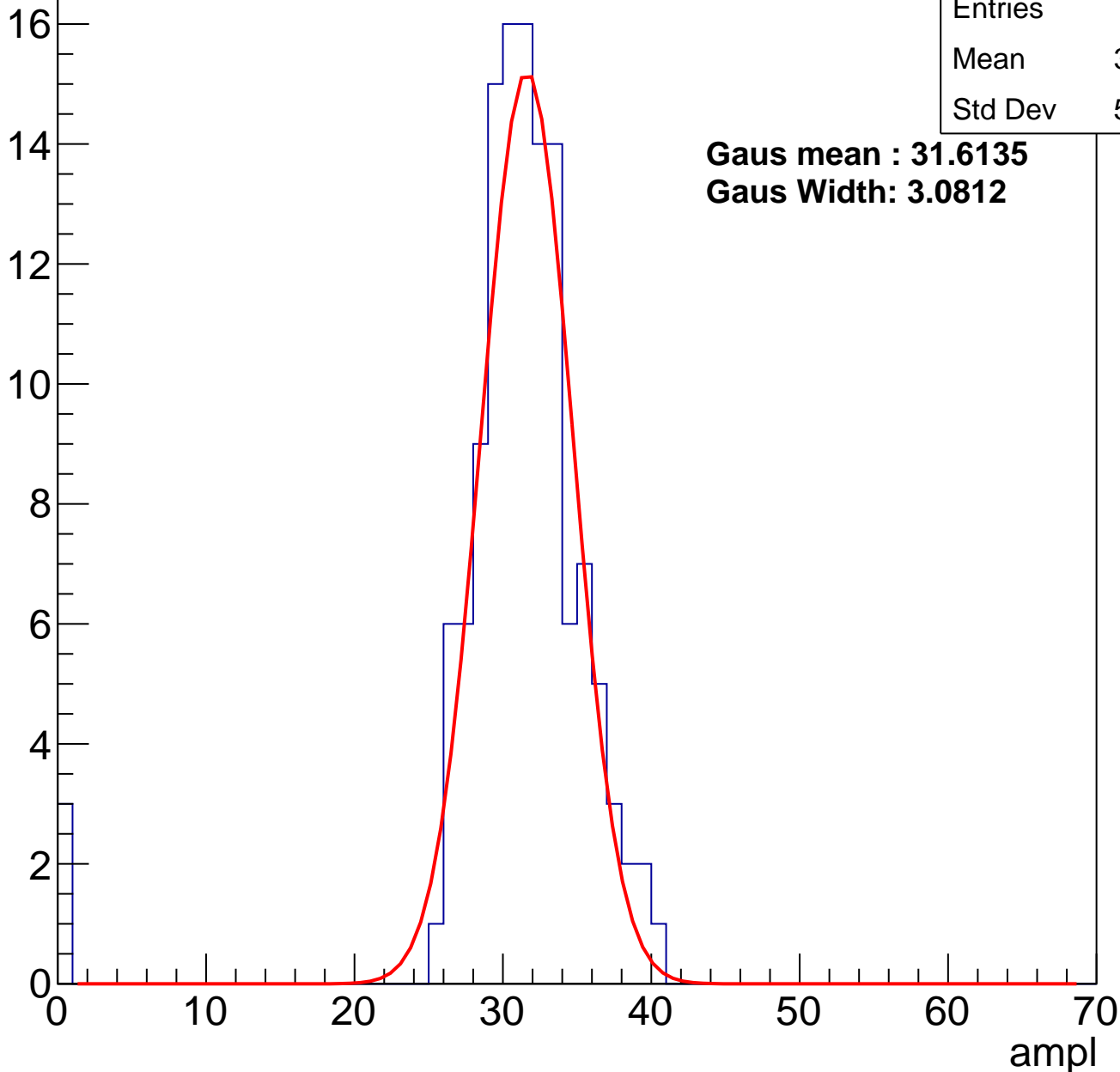
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 126 |
| Mean | 30.56 |
| Std Dev | 5.686 |

Gaus mean : 31.6135

Gaus Width: 3.0812

Entry

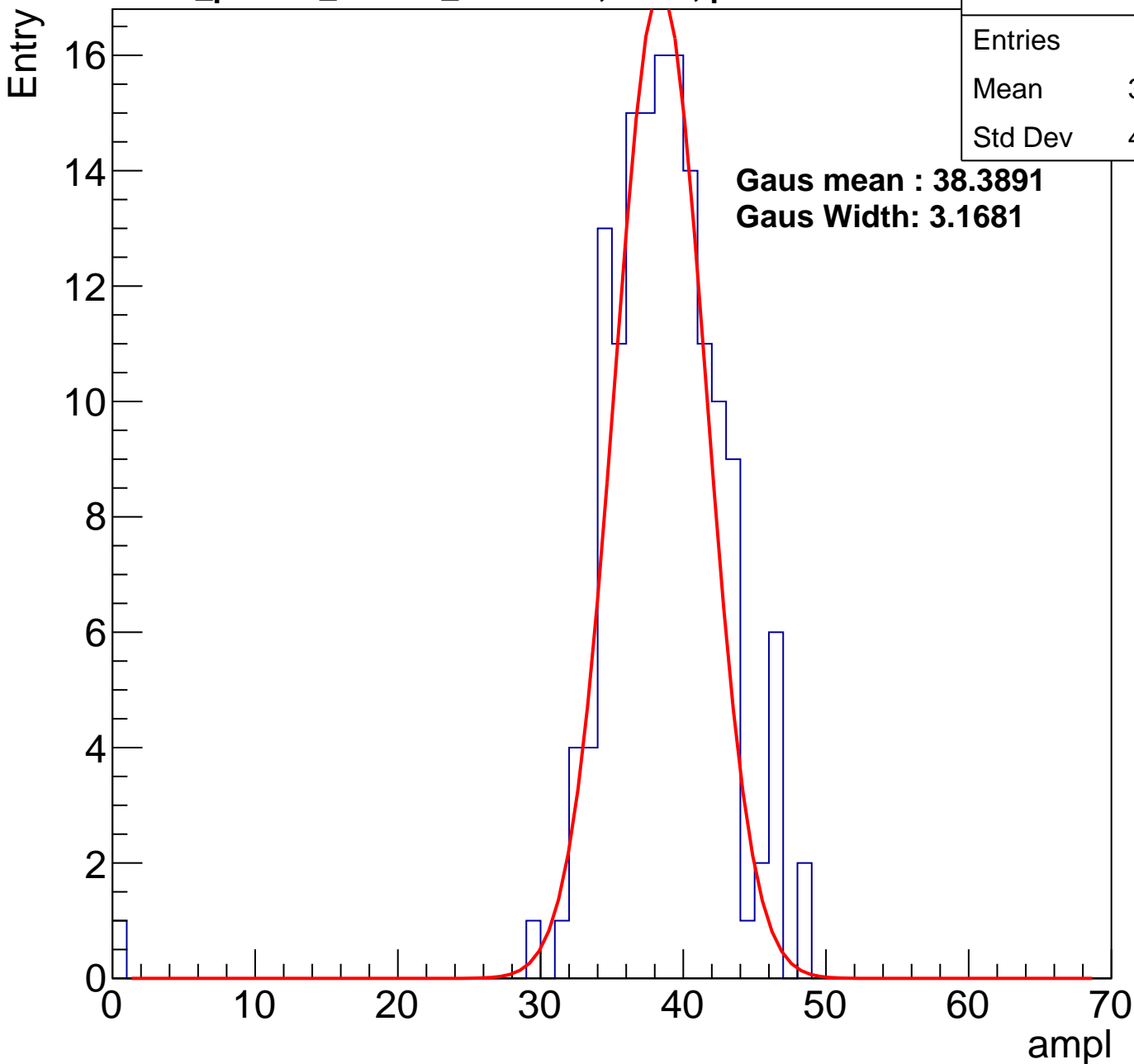


B0L101S, U22-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 152 |
| Mean | 38.14 |
| Std Dev | 4.779 |

Gaus mean : 38.3891
Gaus Width: 3.1681



B0L101S, U22-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

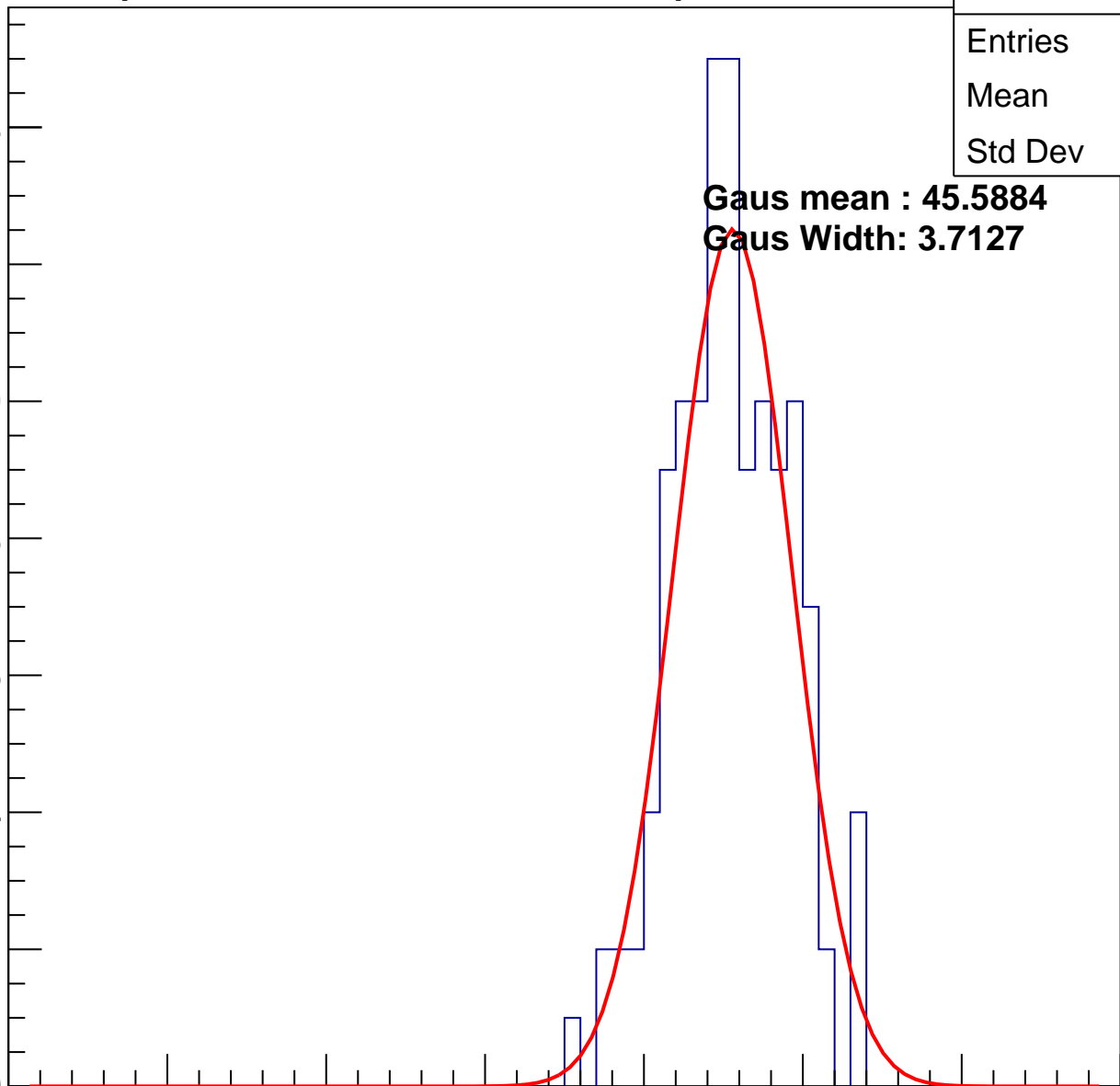
| | |
|---------|-------|
| Entries | 121 |
| Mean | 45.02 |
| Std Dev | 3.614 |

Gaus mean : 45.5884

Gaus Width: 3.7127

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch84, adc3

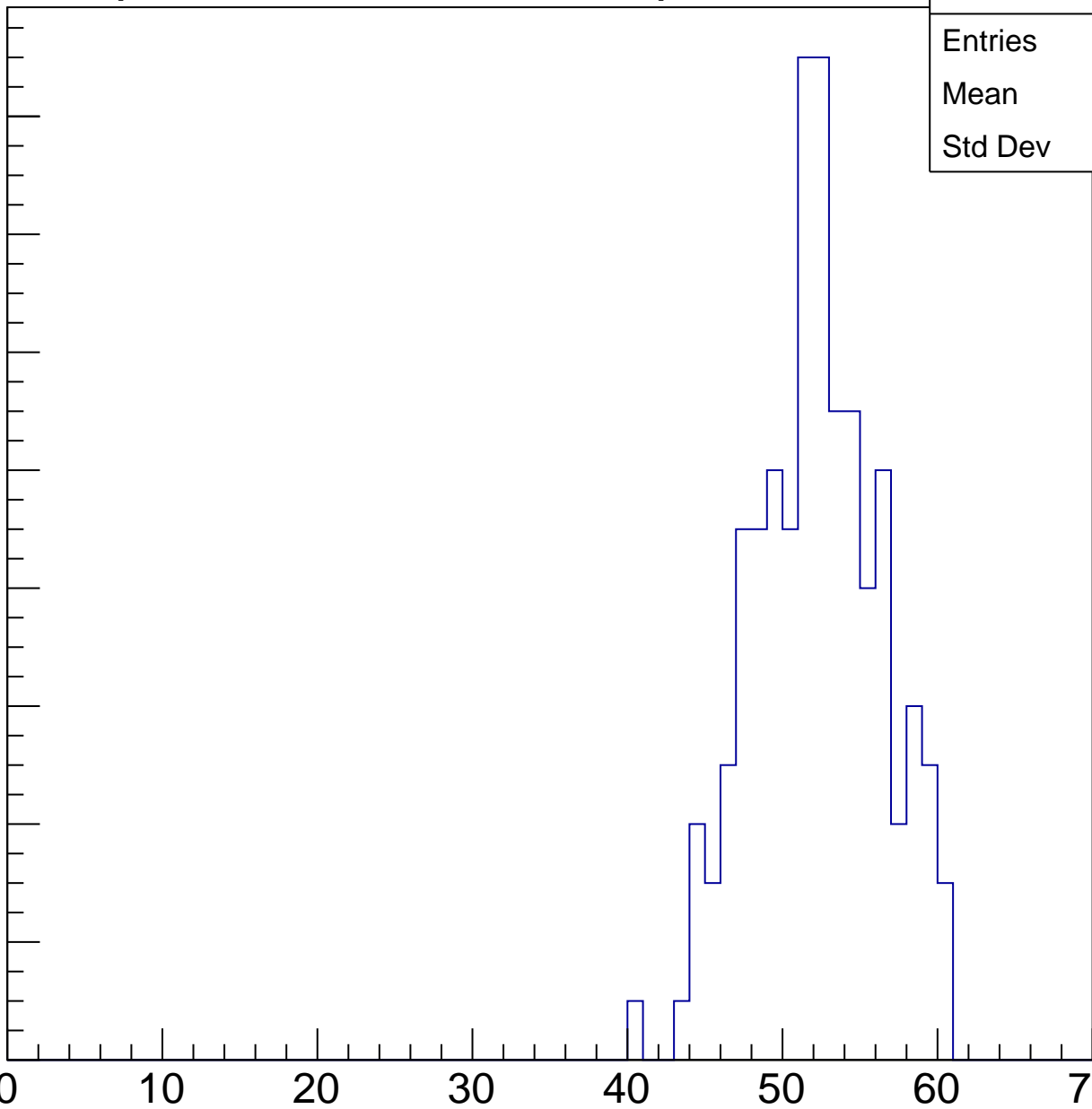
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 143 |
| Mean | 51.73 |
| Std Dev | 4.054 |

ampl



B0L101S, U22-ch84, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

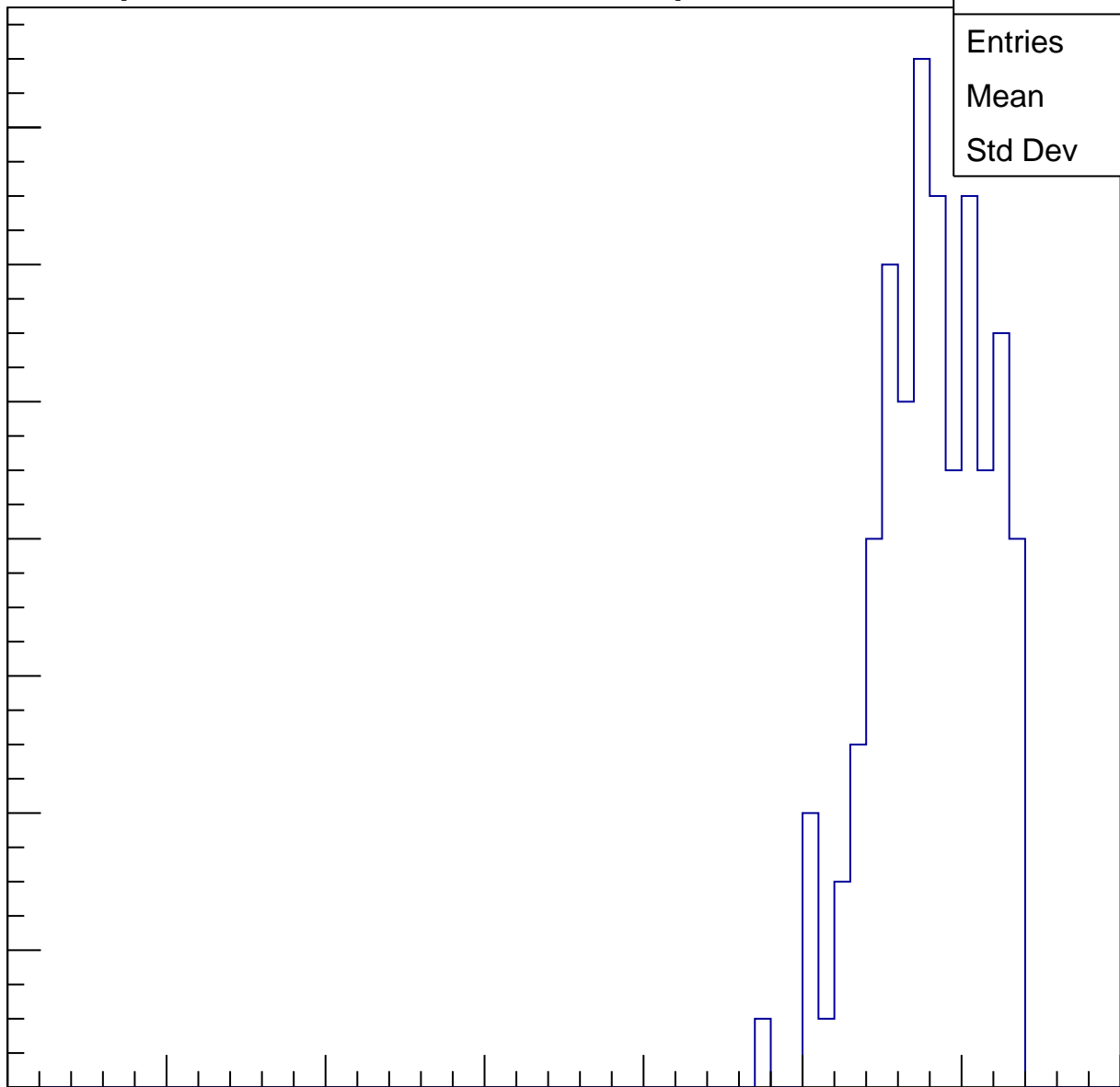
50

60

70

ampl

| | |
|---------|-------|
| Entries | 122 |
| Mean | 57.59 |
| Std Dev | 3.445 |



B0L101S, U22-ch84, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.37 |
| Std Dev | 12.58 |

B0L101S, U22-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

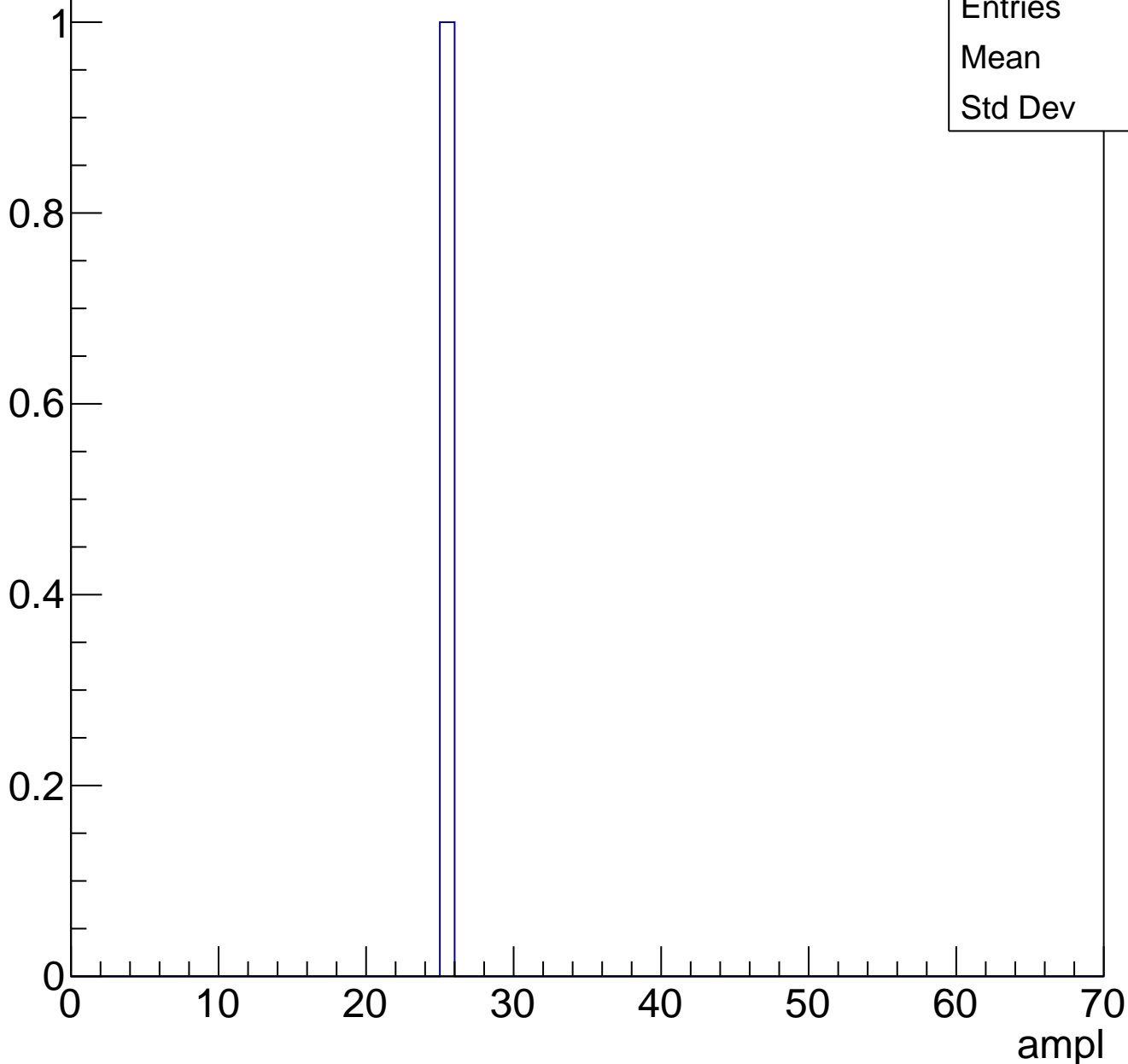
Entry



B0L101S, U22-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch85, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 127 |
| Mean | 29.35 |
| Std Dev | 5.058 |

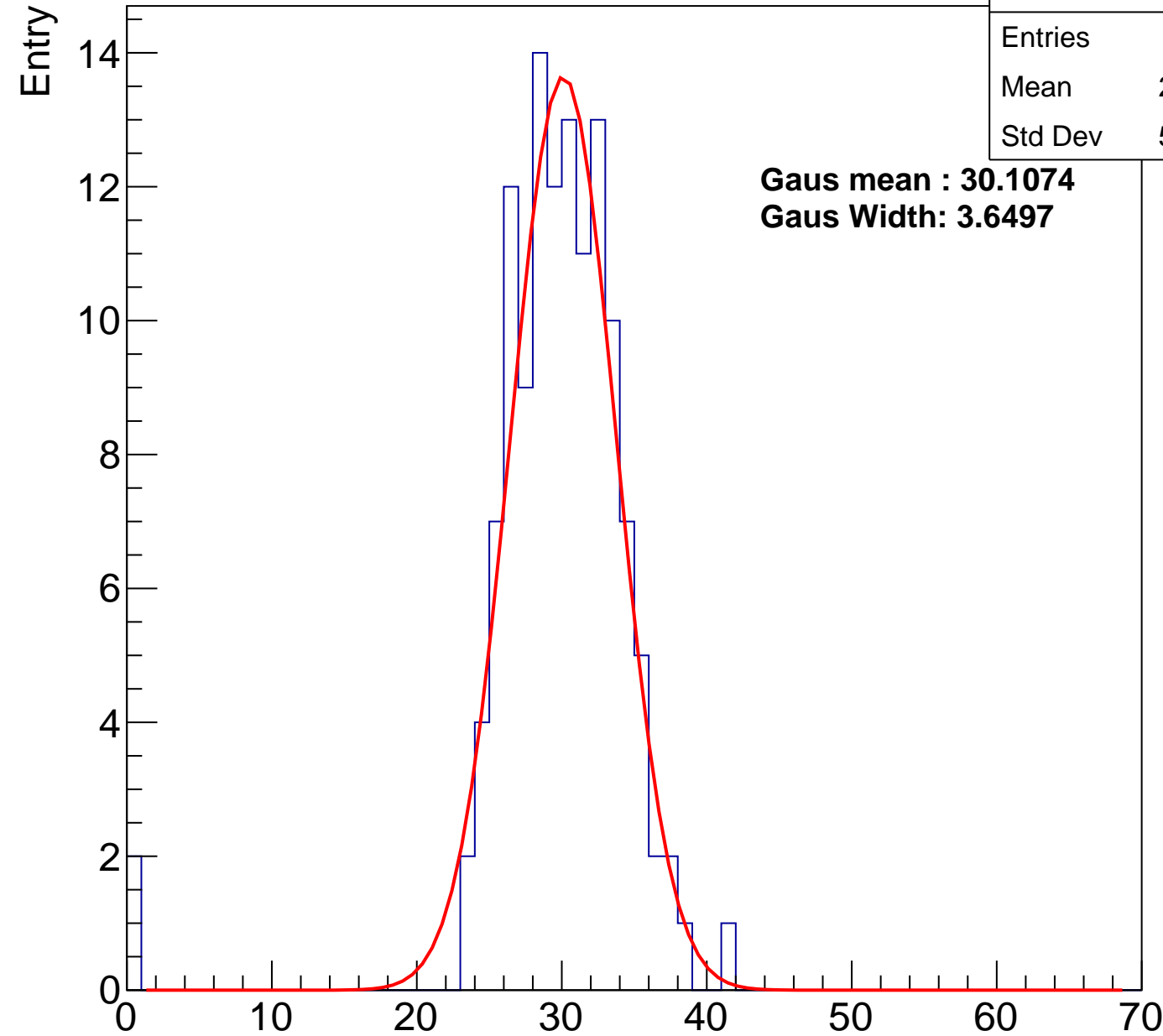
Gaus mean : 30.1074

Gaus Width: 3.6497

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch85, adc1

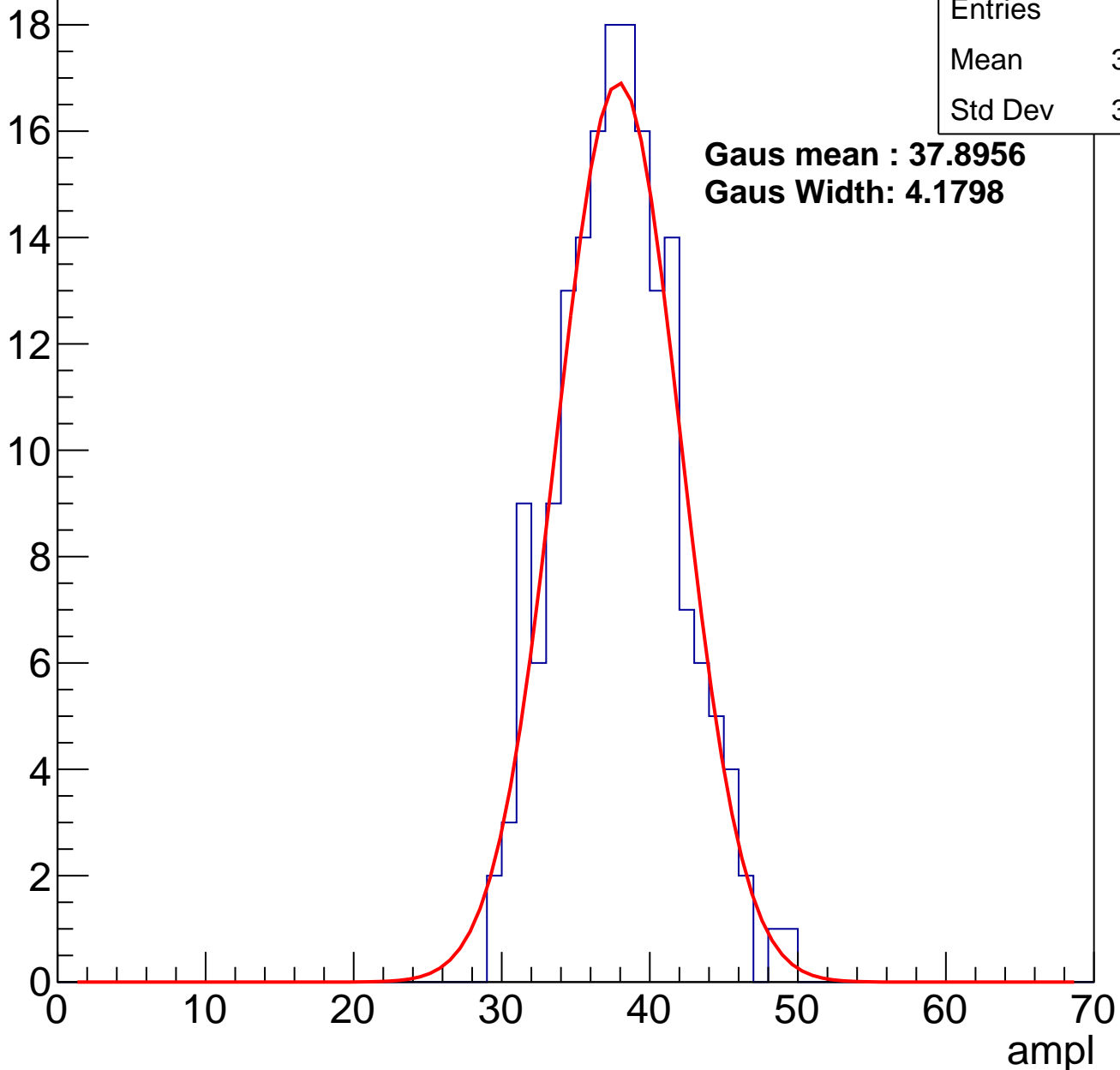
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 177 |
| Mean | 37.47 |
| Std Dev | 3.966 |

Gaus mean : 37.8956

Gaus Width: 4.1798

Entry



B0L101S, U22-ch85, adc2

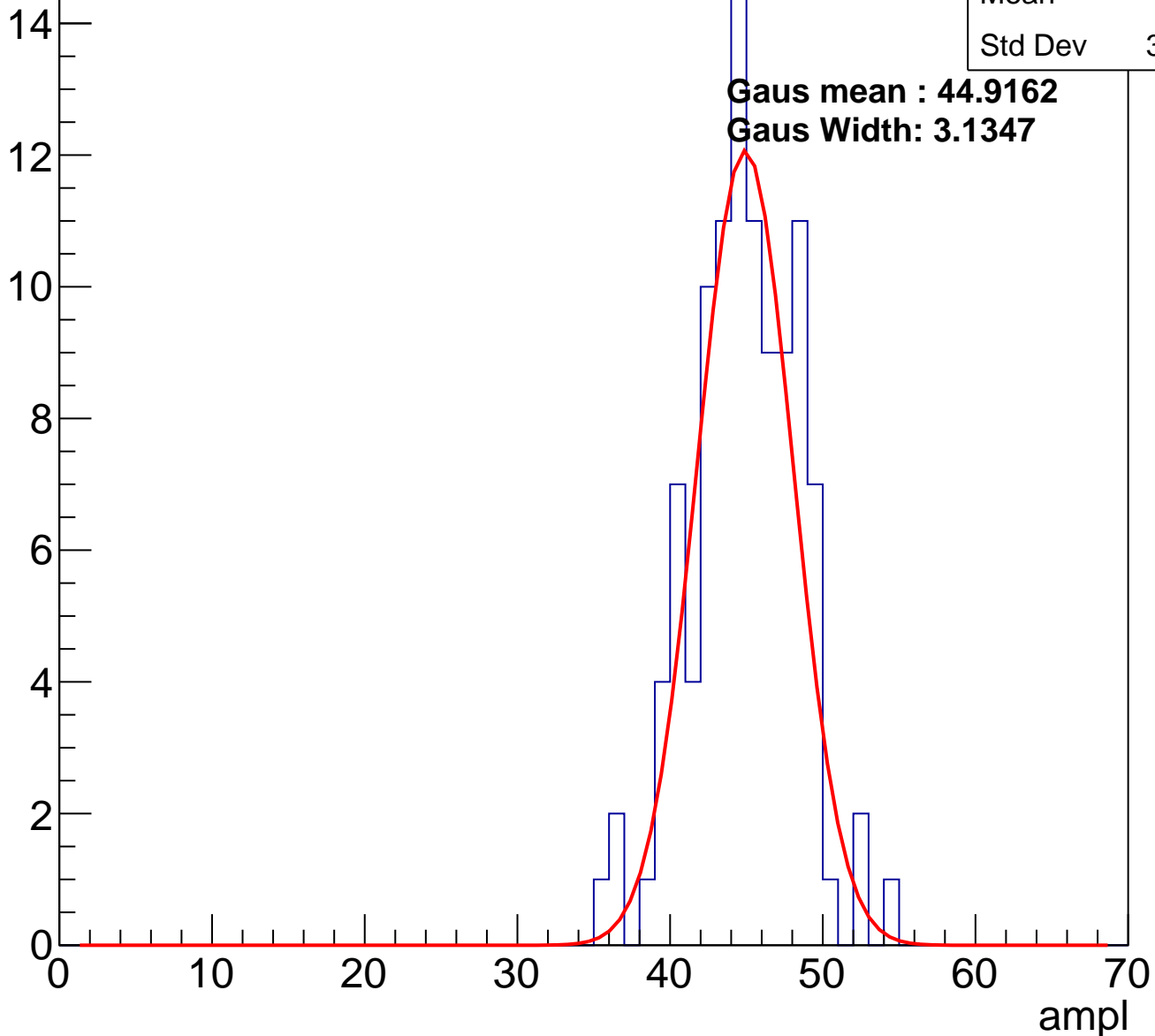
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 106 |
| Mean | 44.42 |
| Std Dev | 3.459 |

Gaus mean : 44.9162

Gaus Width: 3.1347

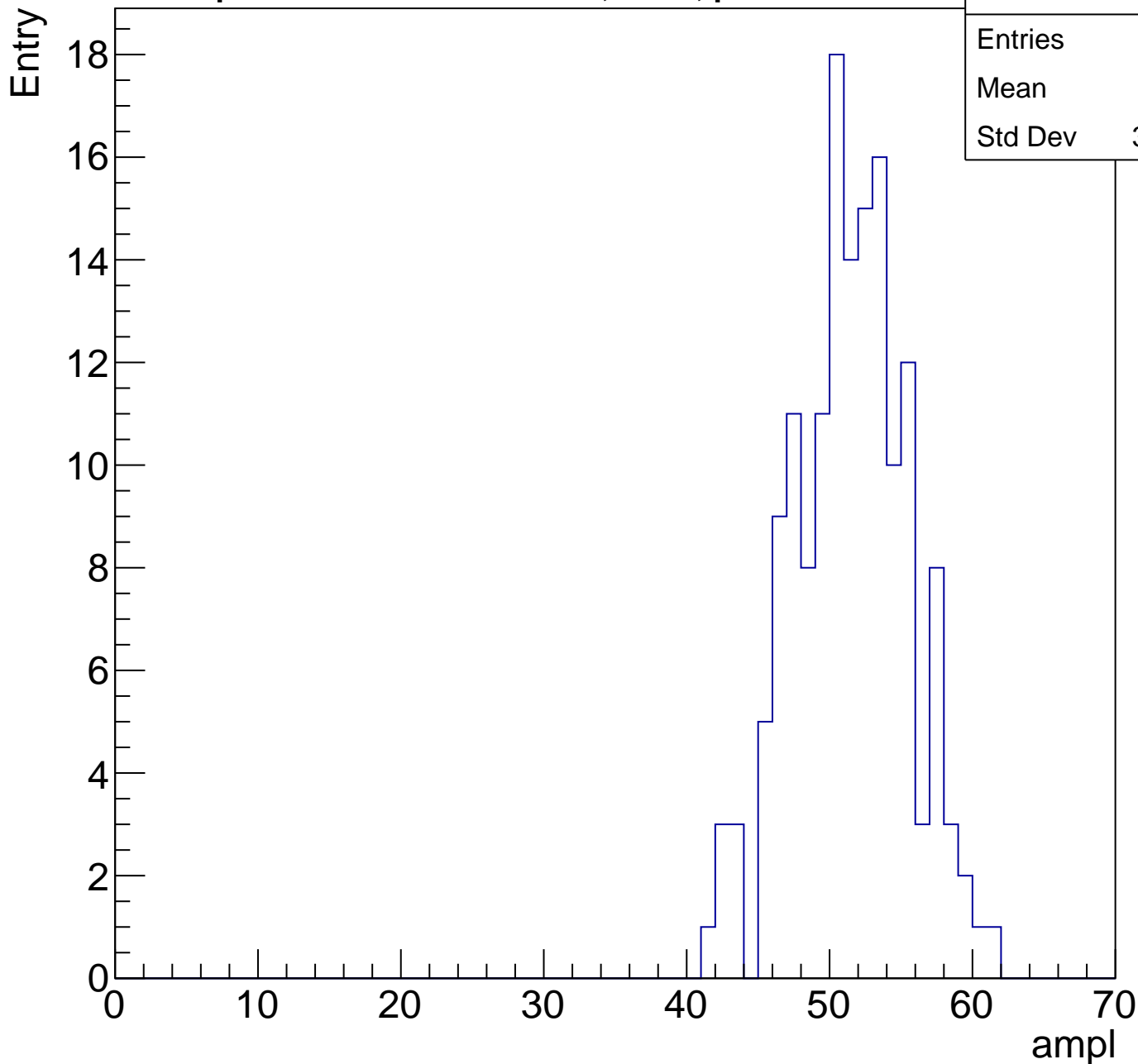
Entry



B0L101S, U22-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 154 |
| Mean | 51 |
| Std Dev | 3.972 |



B0L101S, U22-ch85, adc4

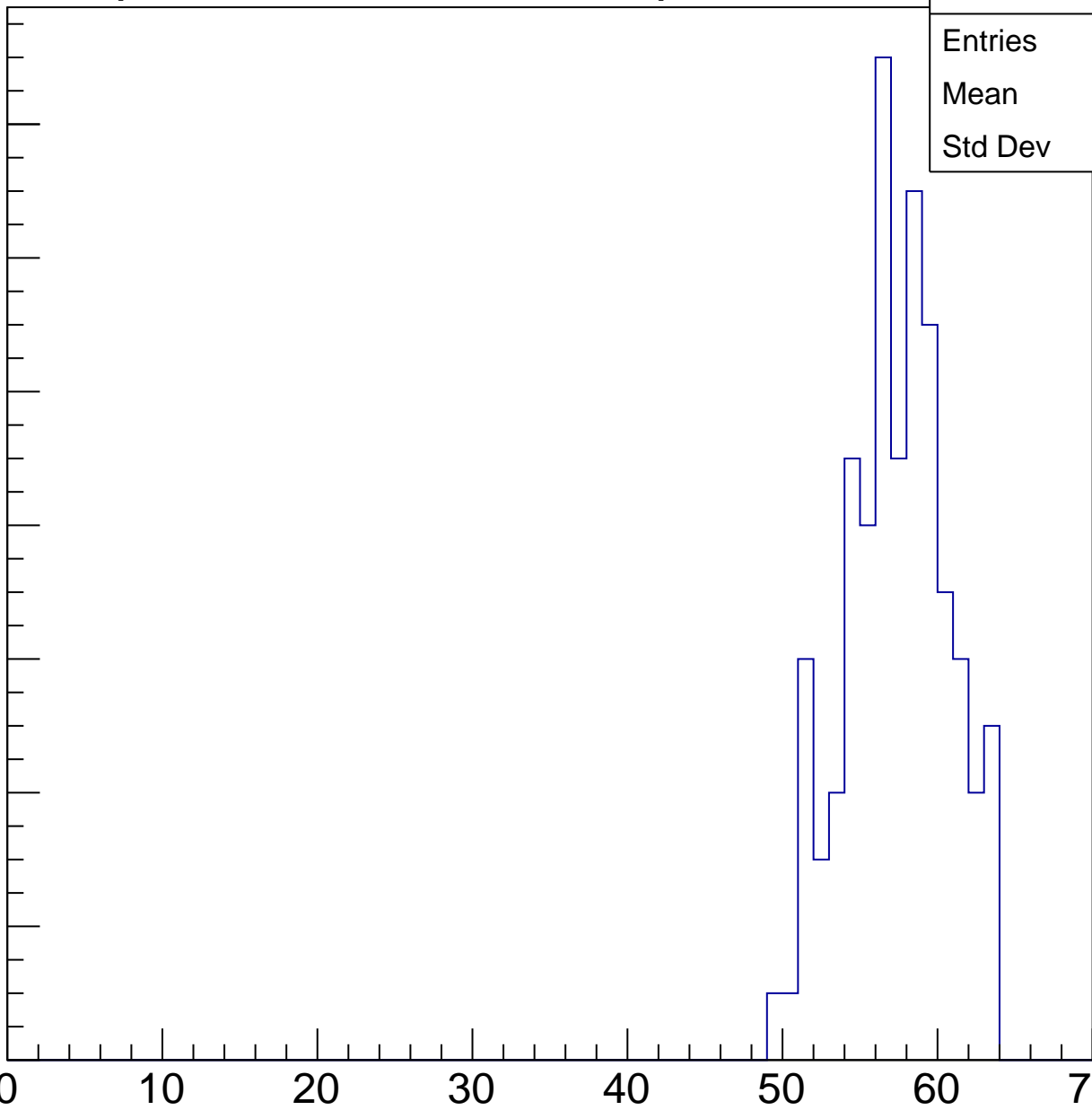
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 102 |
| Mean | 56.9 |
| Std Dev | 3.271 |

ampl



B0L101S, U22-ch85, adc5

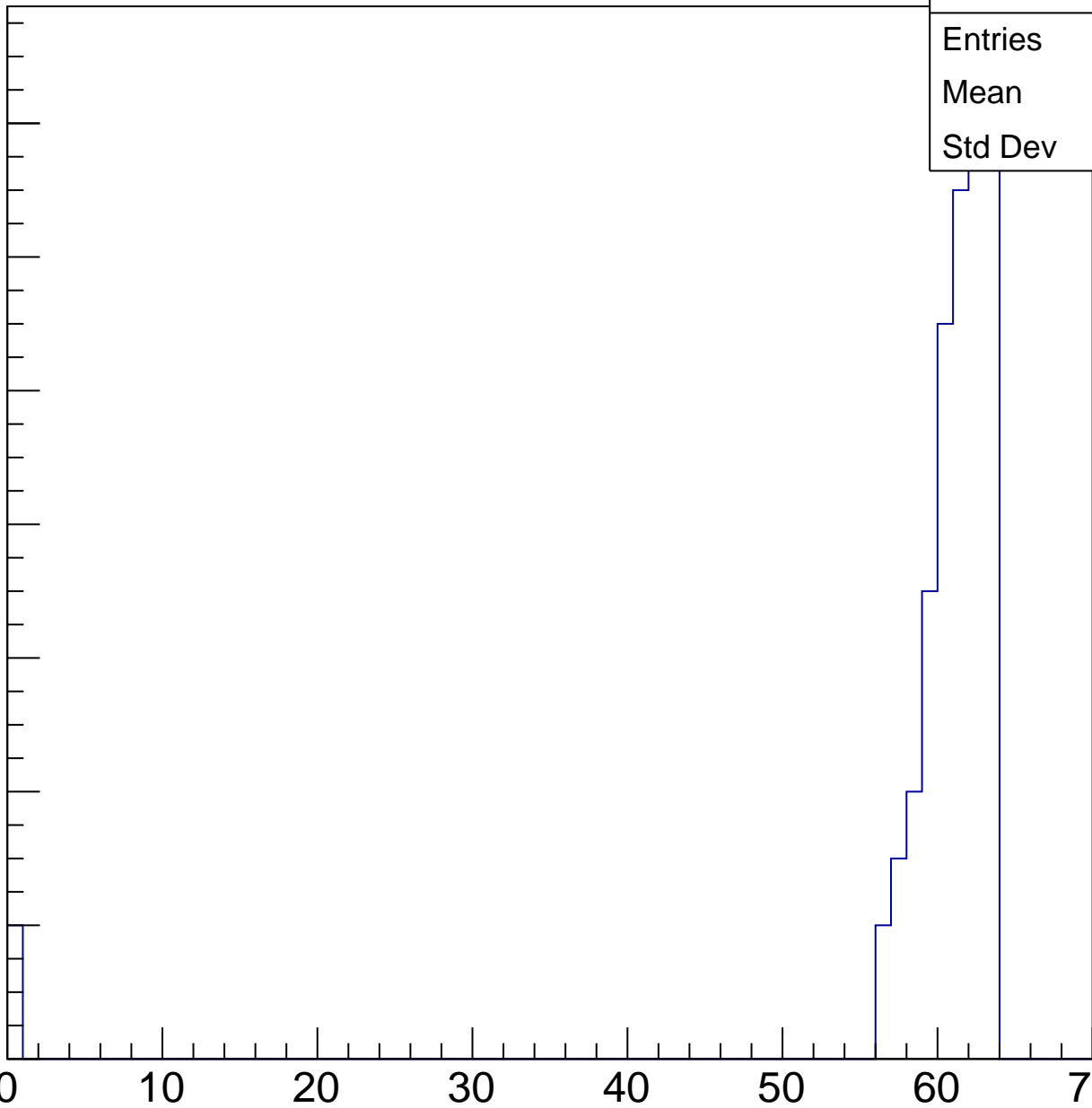
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 59.07 |
| Std Dev | 10.23 |

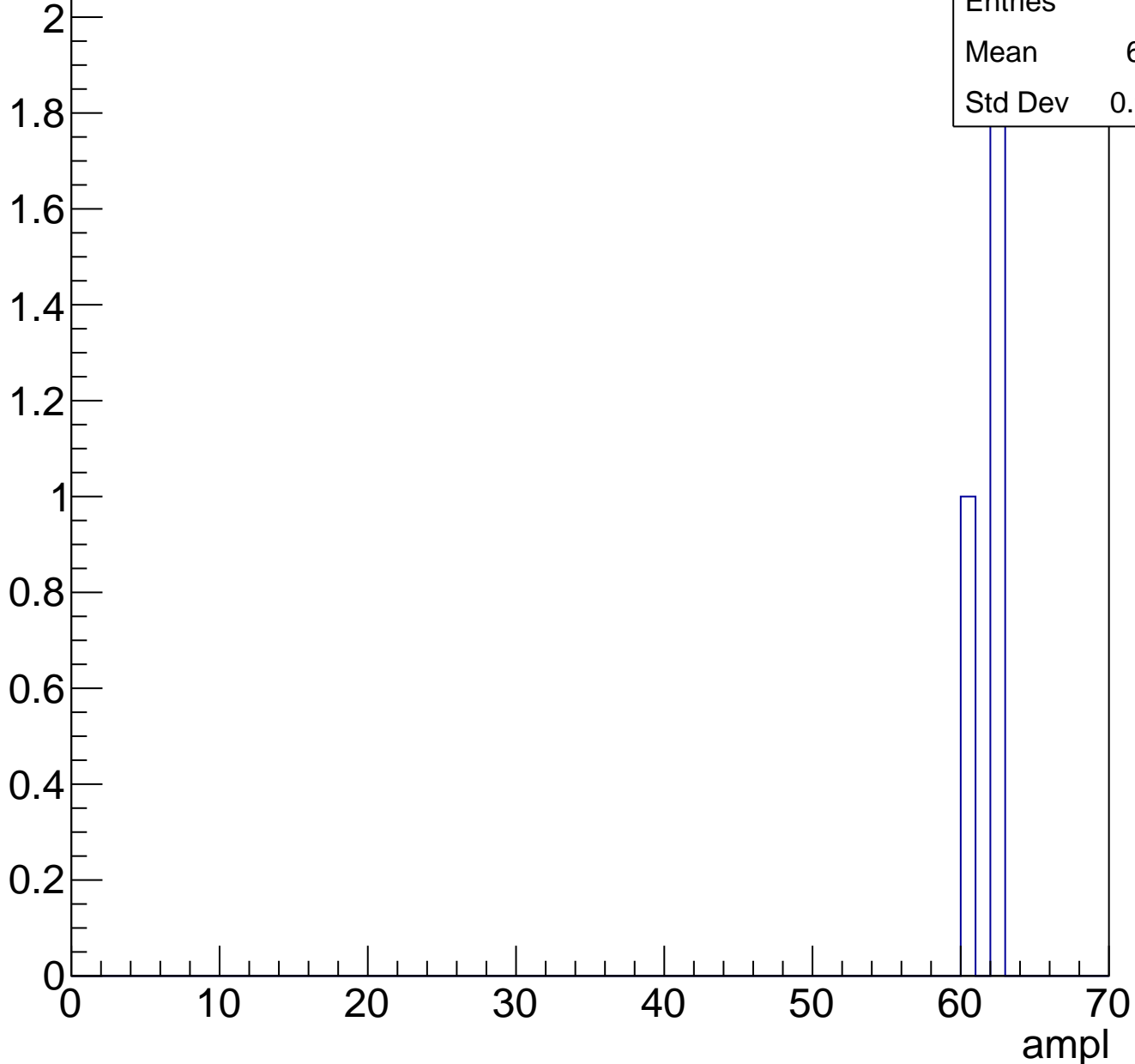
ampl



B0L101S, U22-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

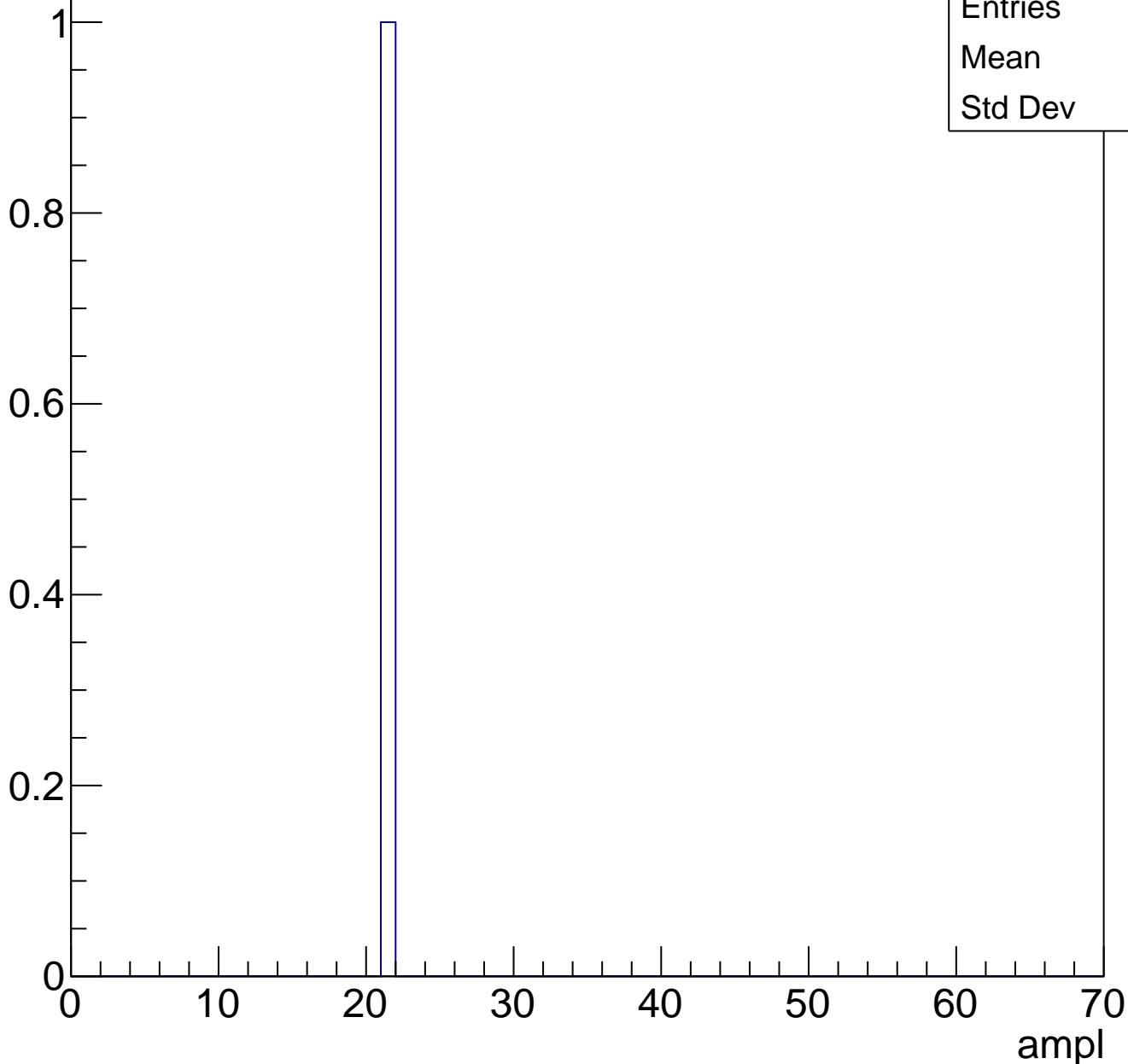
Entry



B0L101S, U22-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch86, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 108 |
| Mean | 27.92 |
| Std Dev | 4.007 |

Gaus mean : 27.8205

Gaus Width: 4.0297

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

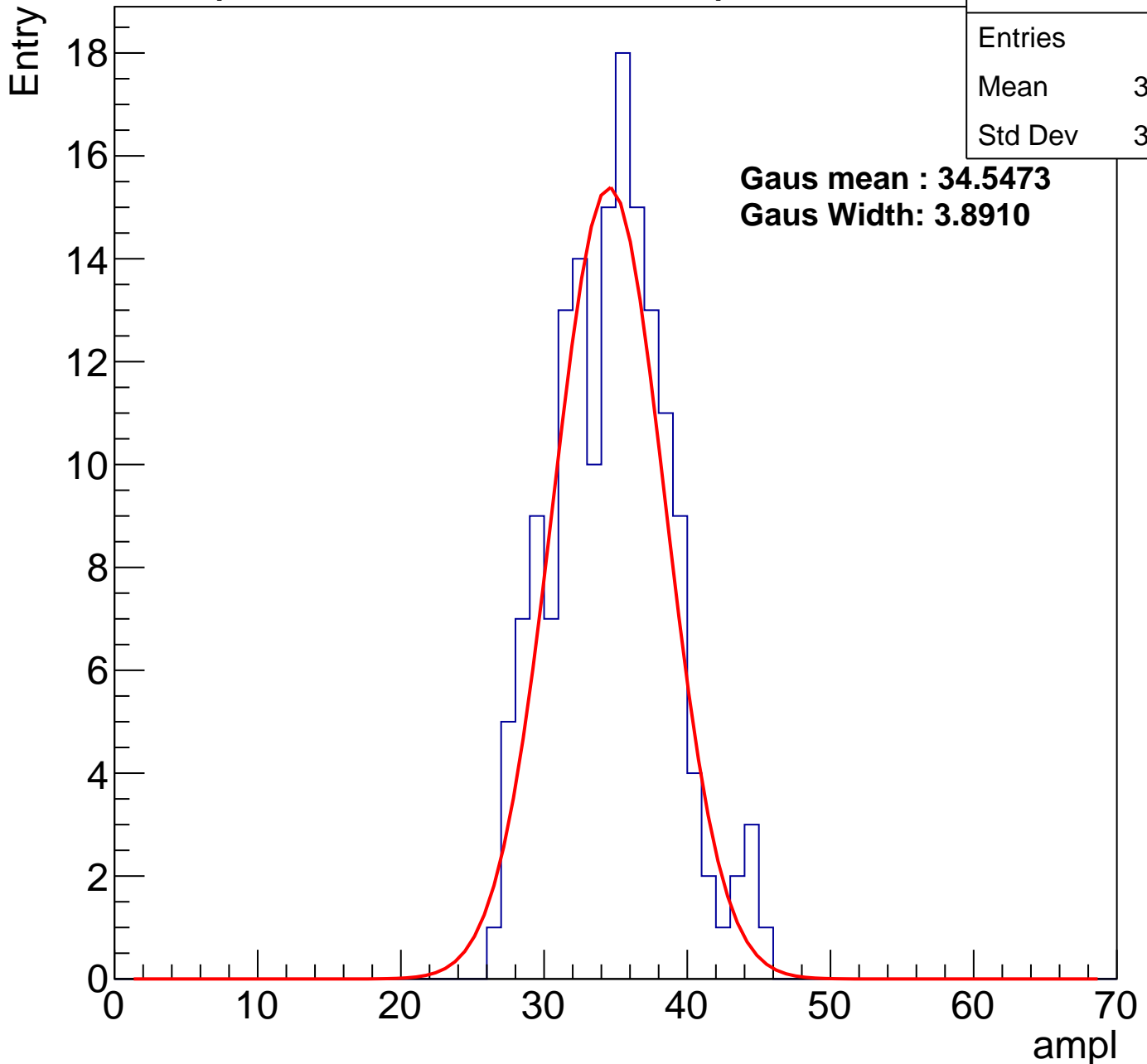
70

B0L101S, U22-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 160 |
| Mean | 34.29 |
| Std Dev | 3.994 |

Gaus mean : 34.5473
Gaus Width: 3.8910



B0L101S, U22-ch86, adc2

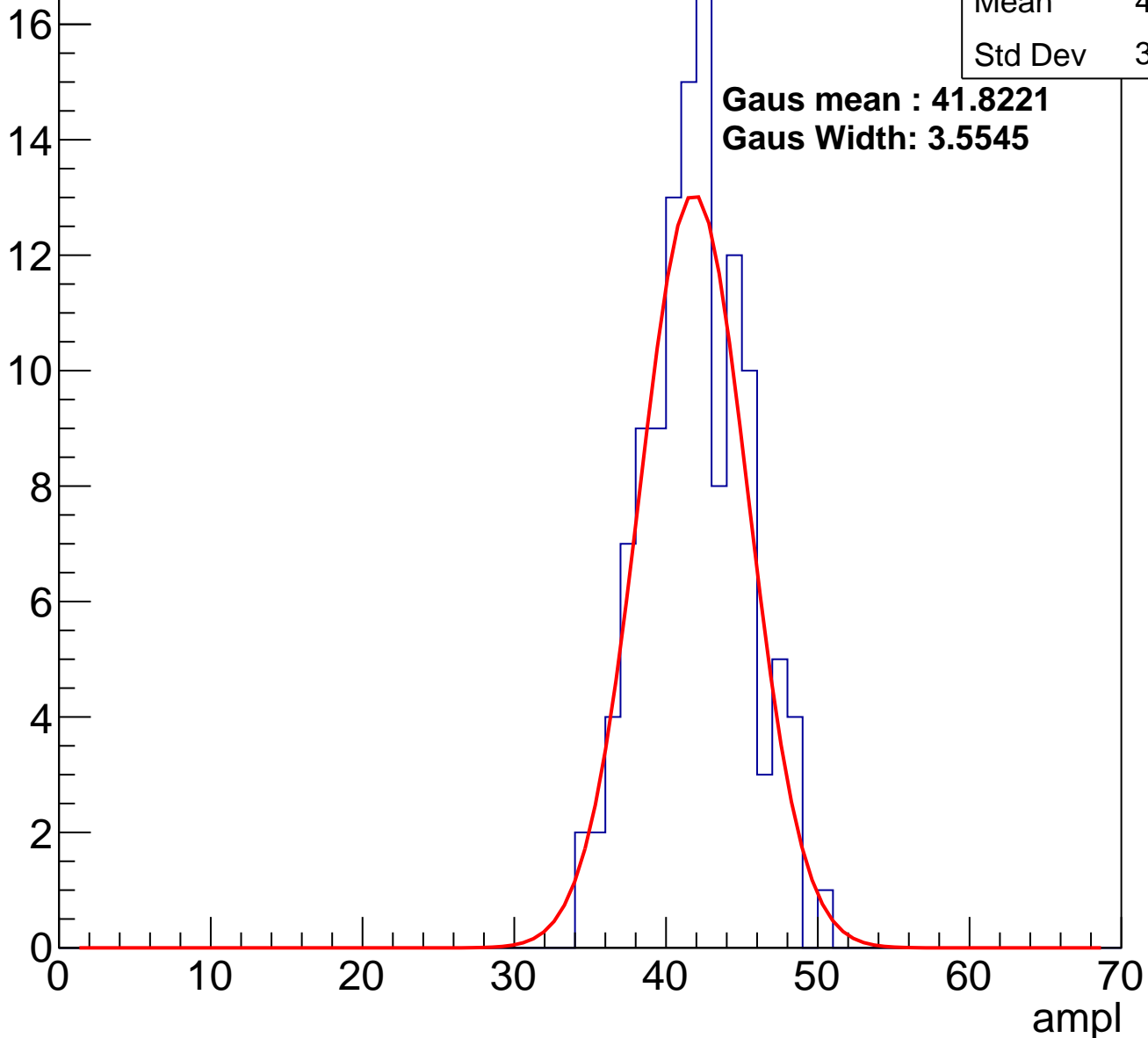
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 121 |
| Mean | 41.49 |
| Std Dev | 3.318 |

Gaus mean : 41.8221

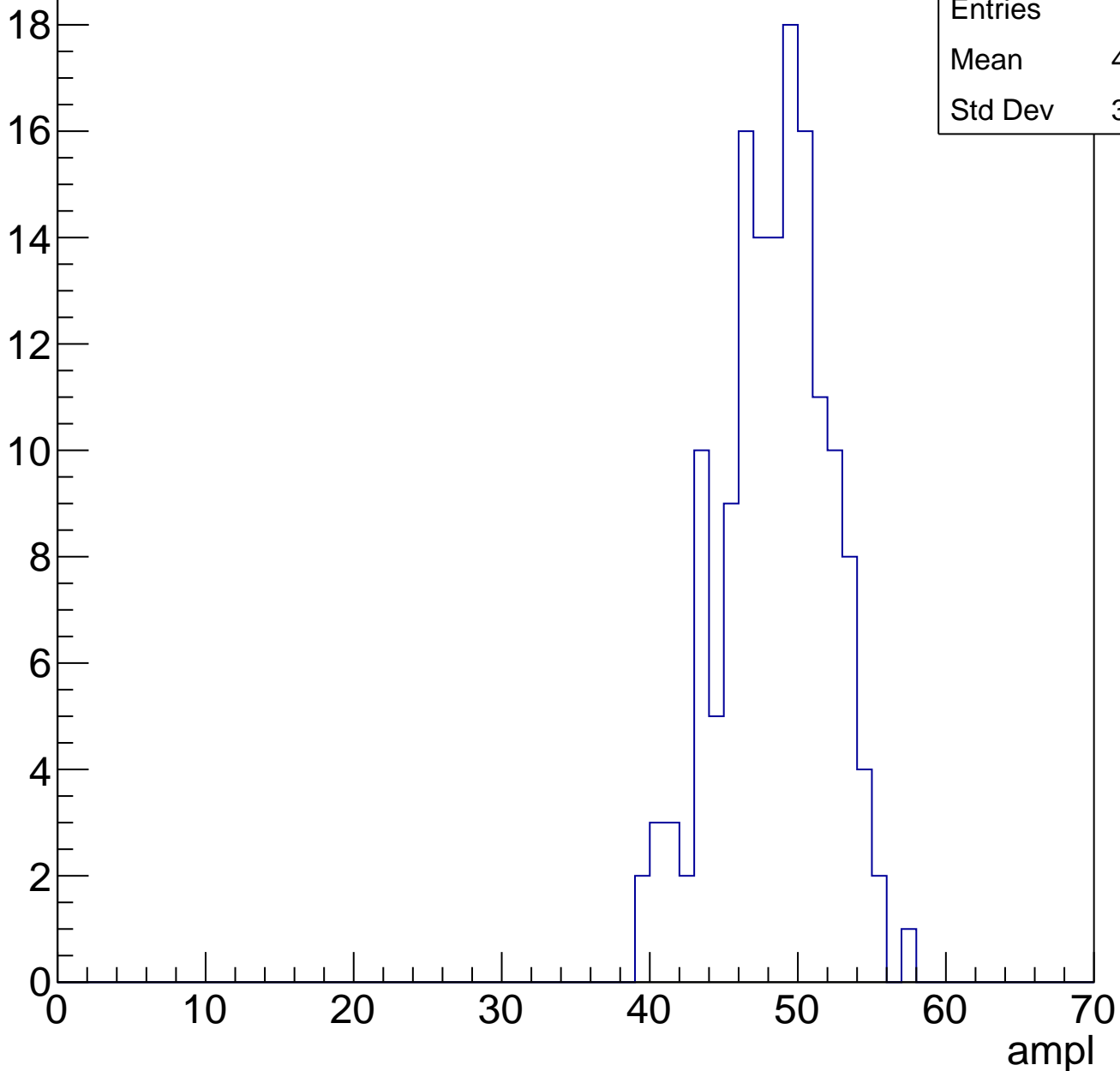
Gaus Width: 3.5545



B0L101S, U22-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

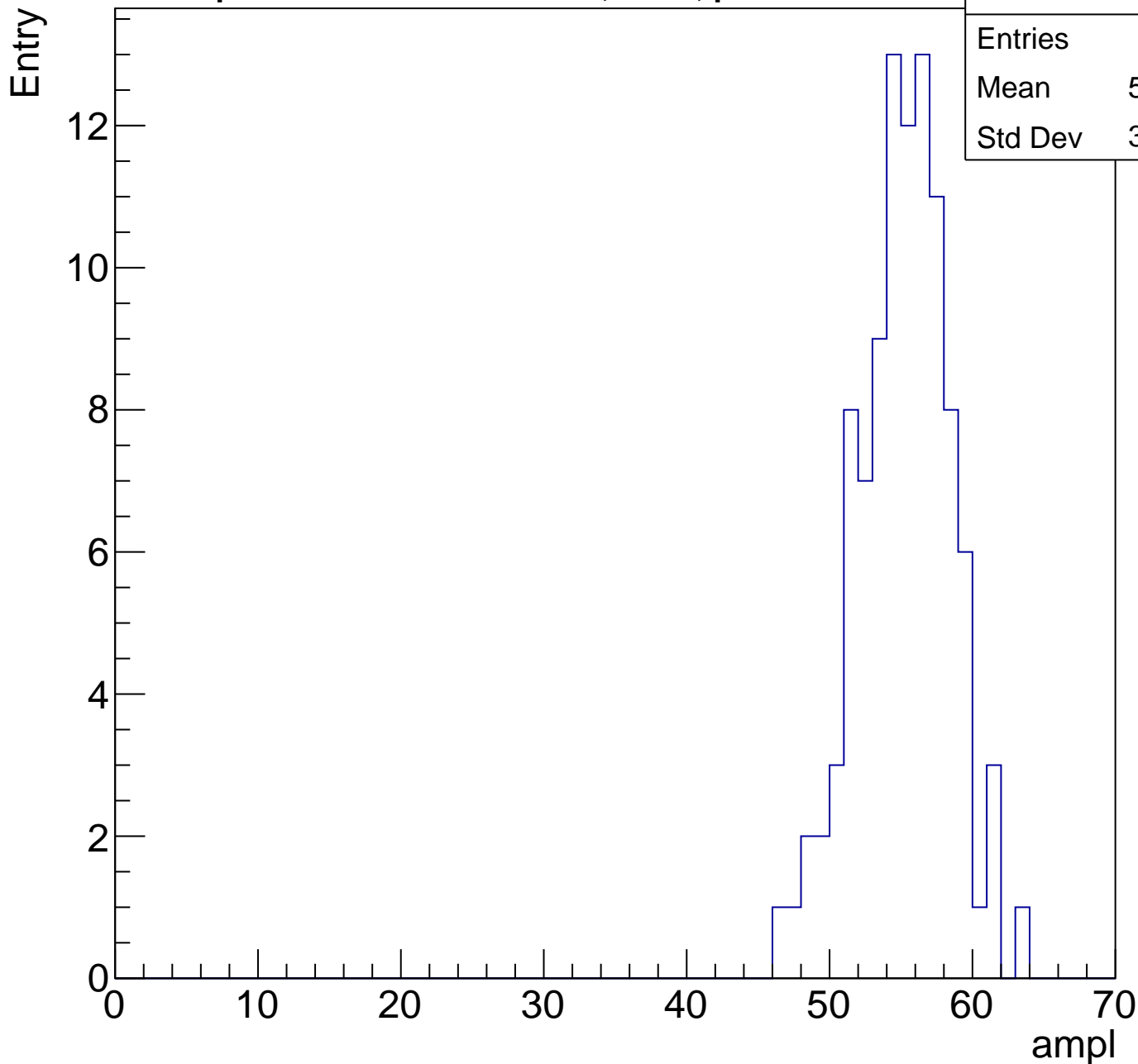
Entry



B0L101S, U22-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 101 |
| Mean | 54.72 |
| Std Dev | 3.215 |



B0L101S, U22-ch86, adc5

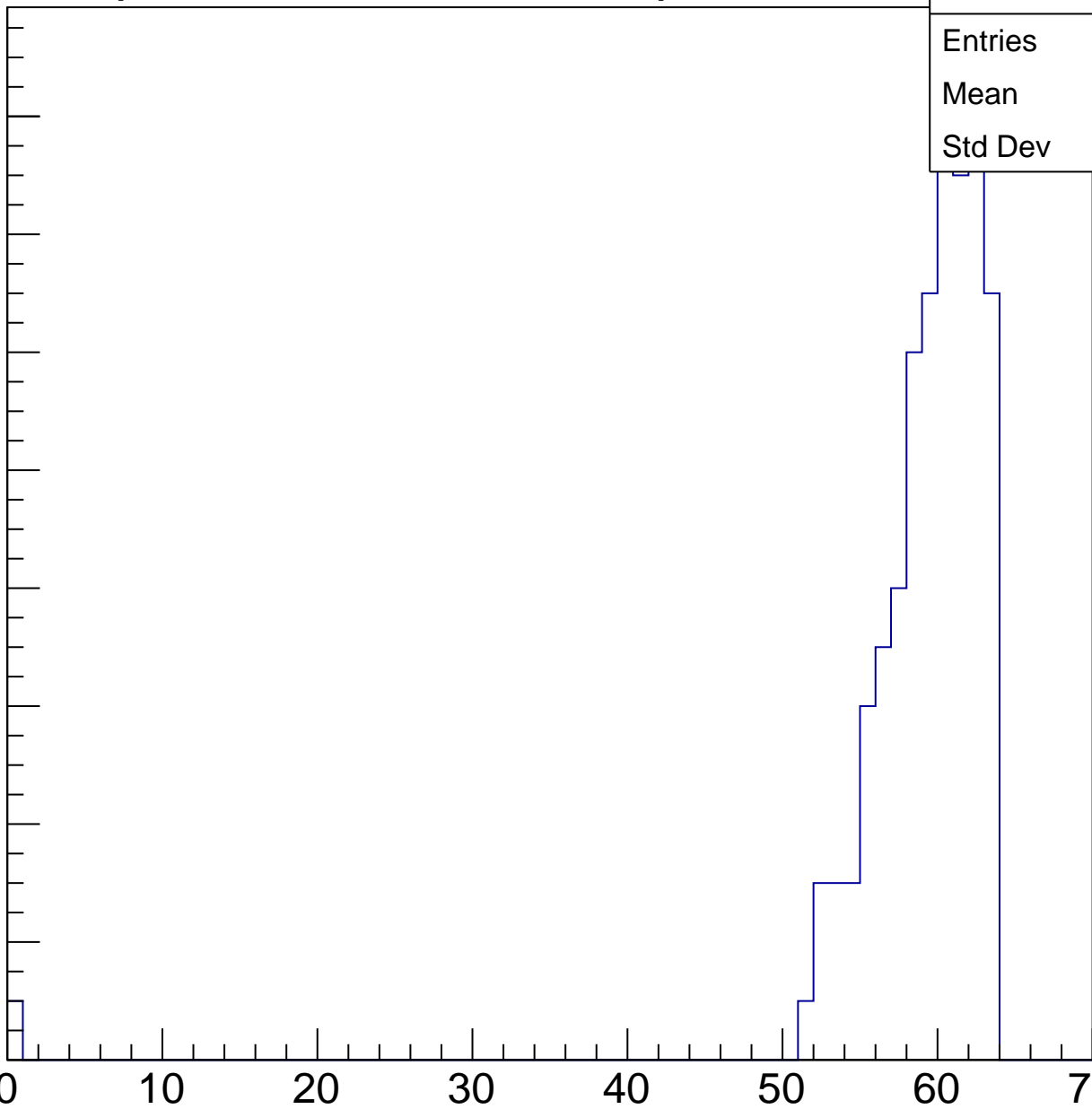
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 118 |
| Mean | 58.6 |
| Std Dev | 6.167 |

ampl

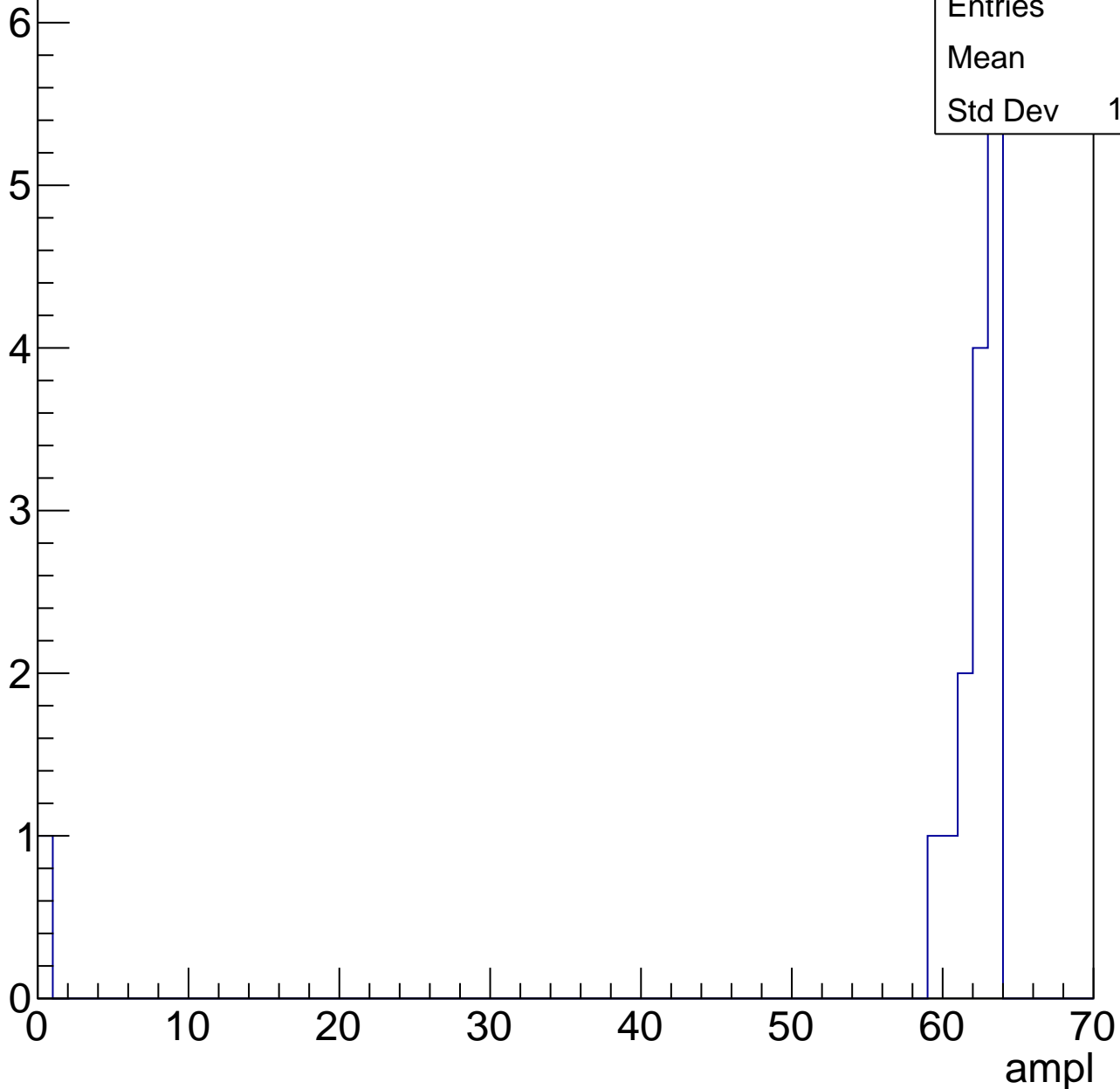


B0L101S, U22-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

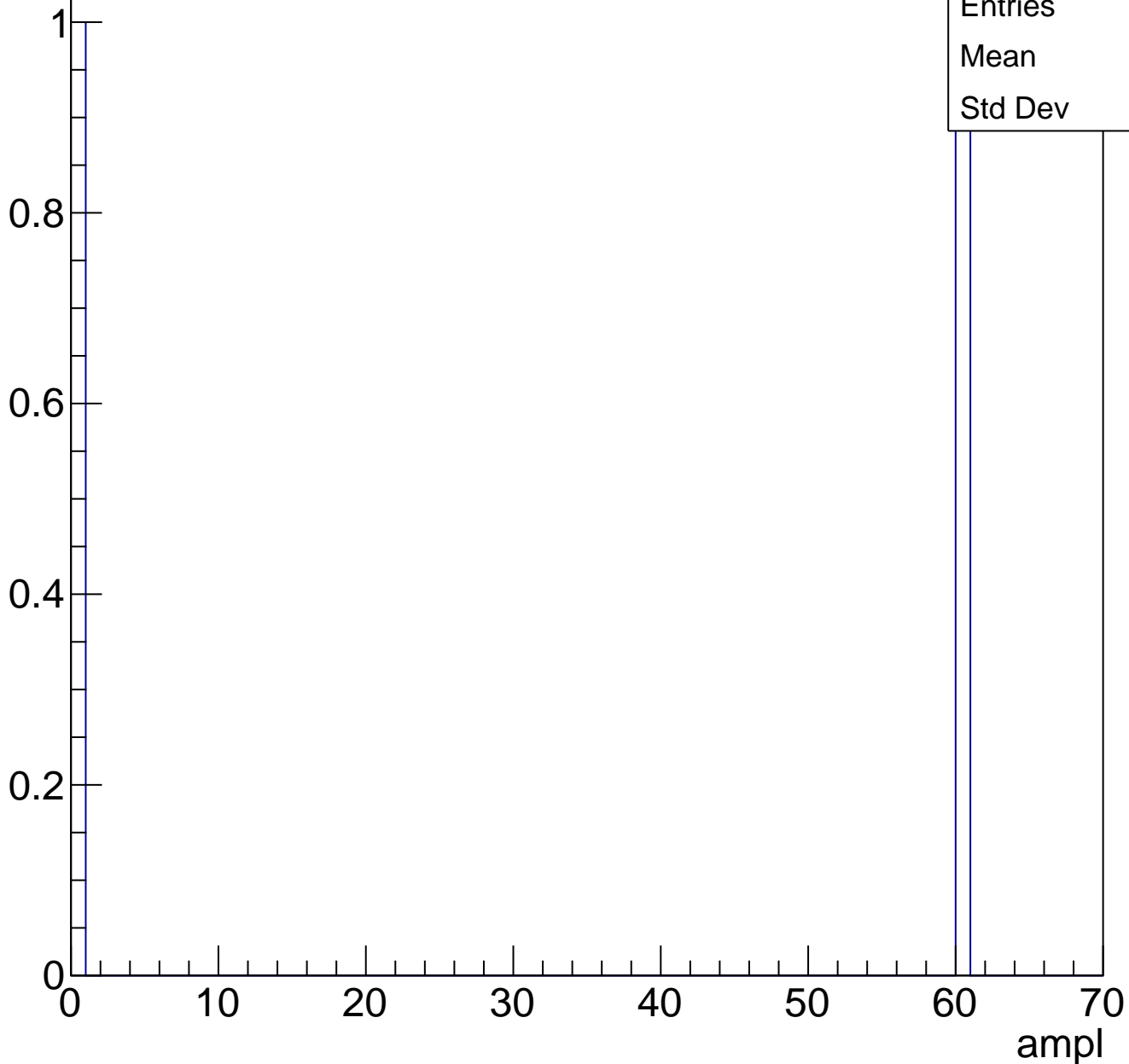
| | |
|---------|-------|
| Entries | 15 |
| Mean | 57.8 |
| Std Dev | 15.49 |



B0L101S, U22-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch87, adc0

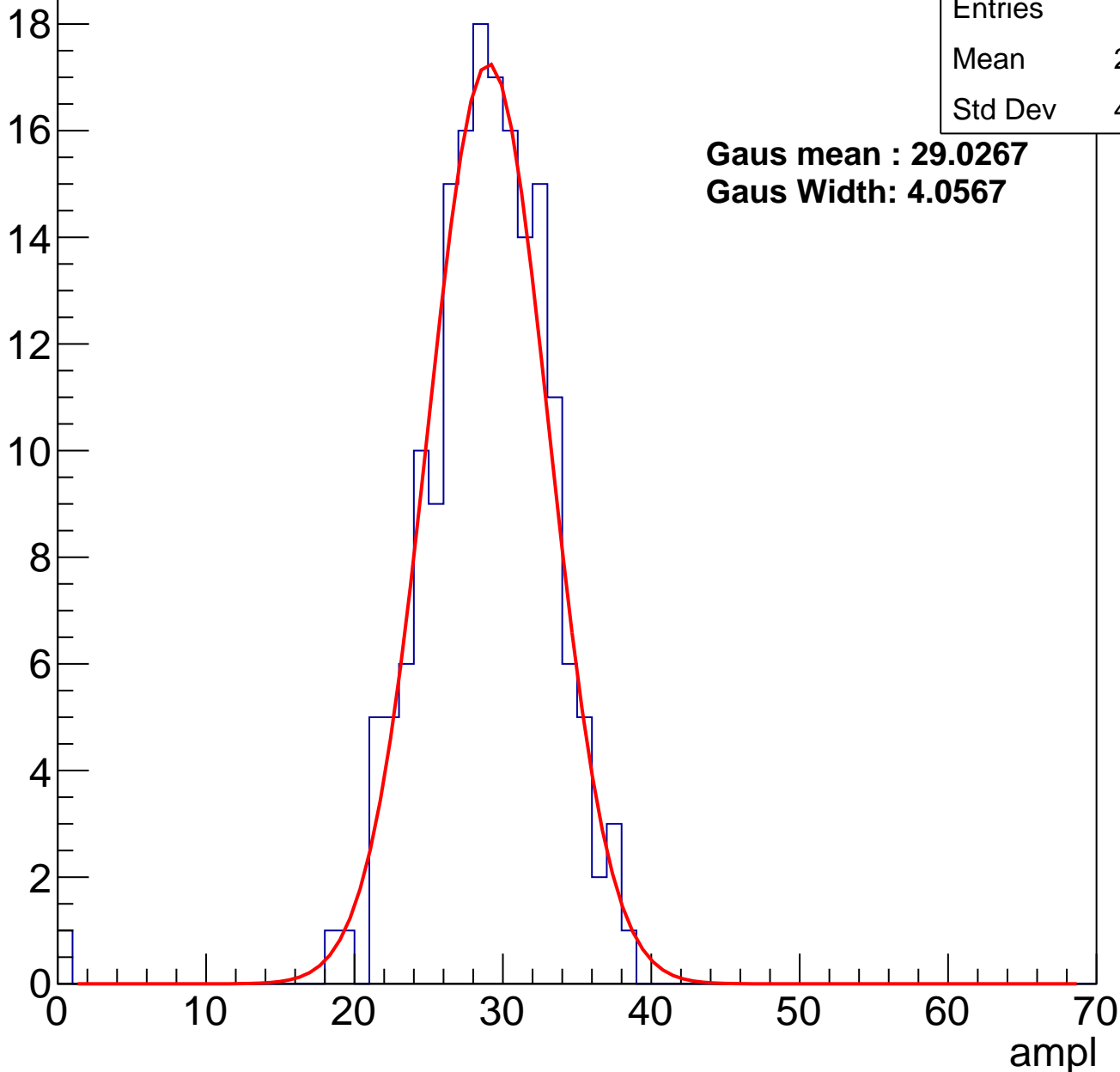
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 177 |
| Mean | 28.42 |
| Std Dev | 4.434 |

Gaus mean : 29.0267

Gaus Width: 4.0567

Entry



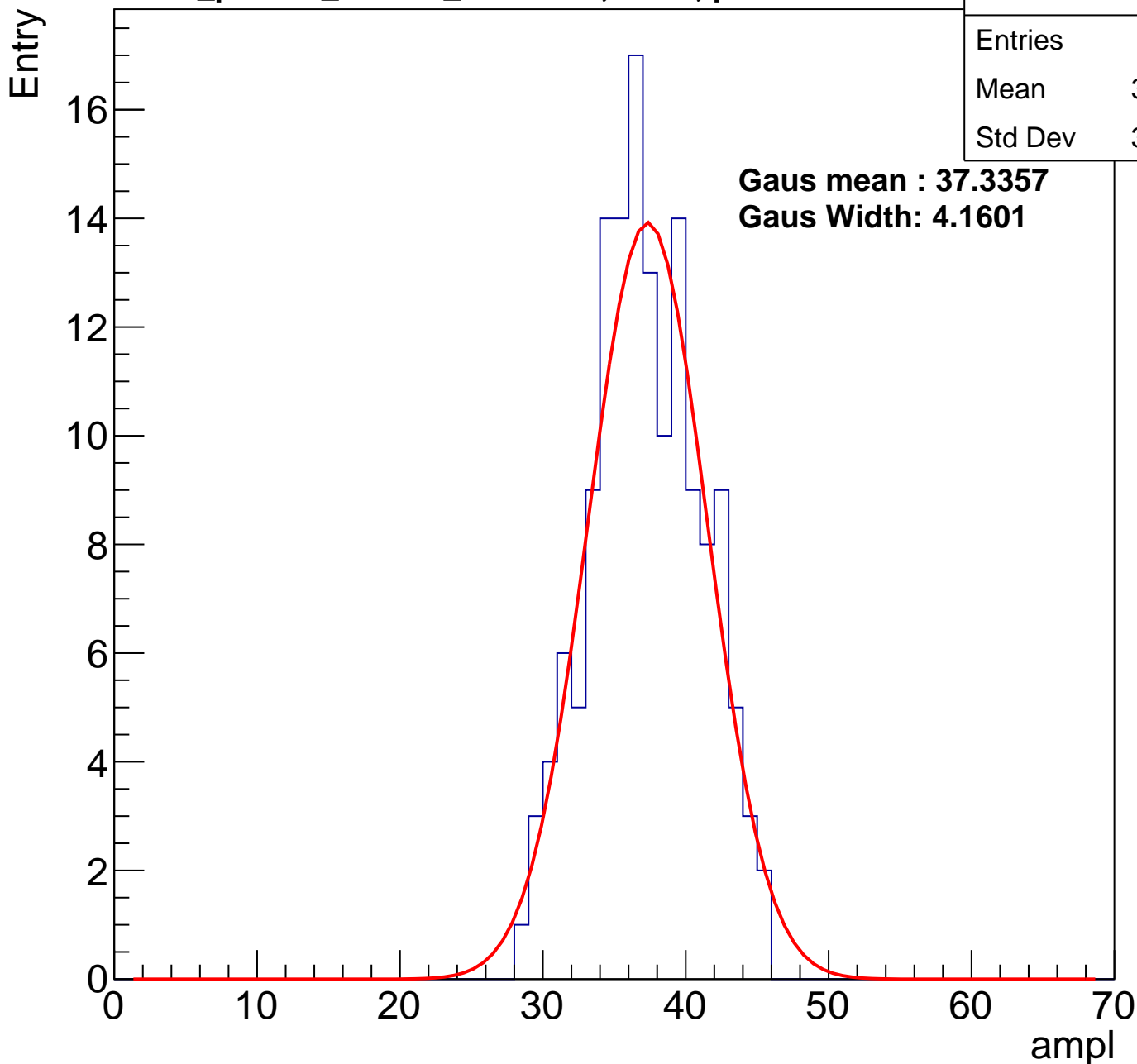
B0L101S, U22-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 146 |
| Mean | 36.75 |
| Std Dev | 3.792 |

Gaus mean : 37.3357

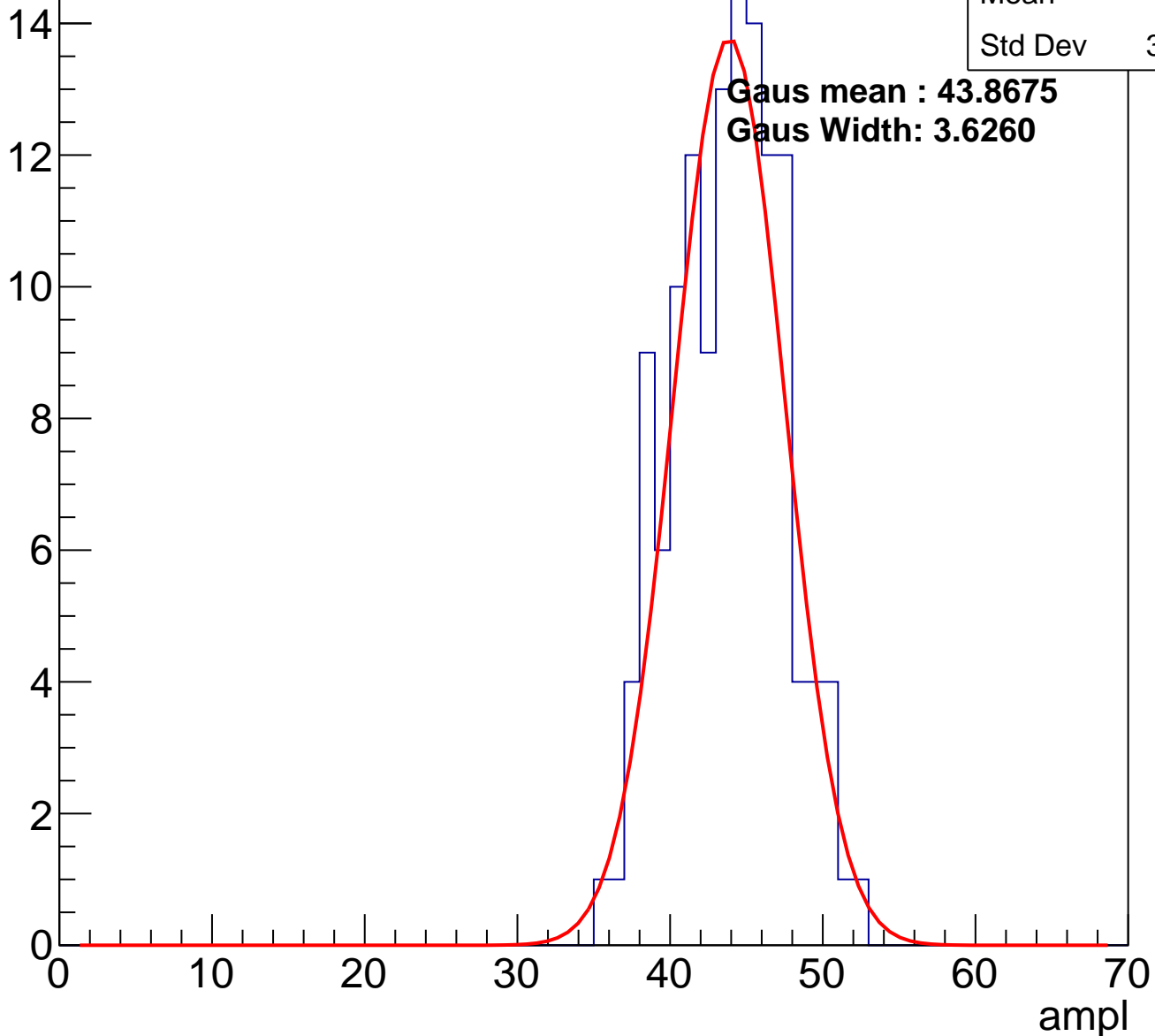
Gaus Width: 4.1601



B0L101S, U22-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 120 |
| Mean | 49.86 |
| Std Dev | 3.436 |

Entry

14

12

10

8

6

4

2

0

0

10

20

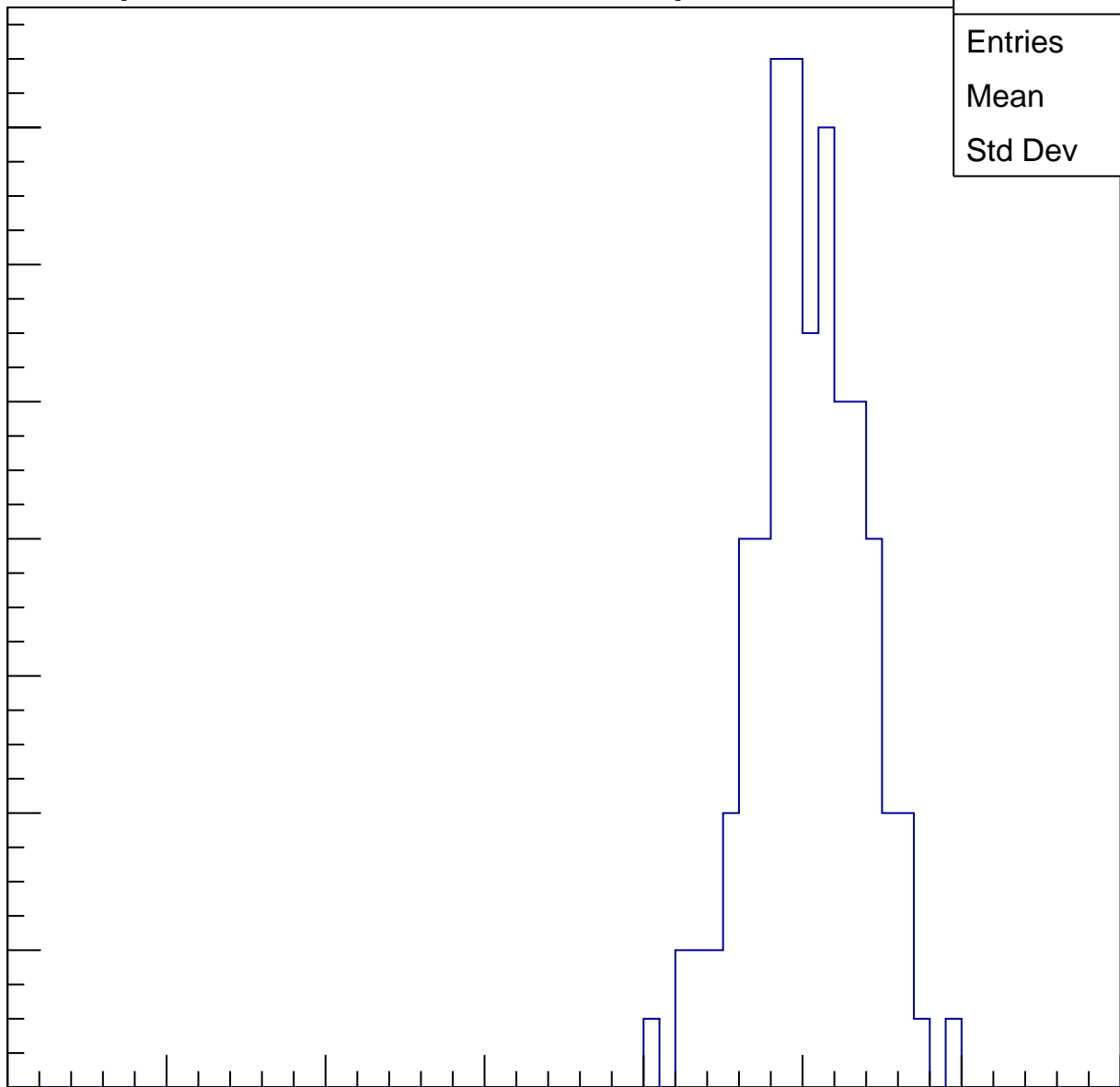
30

40

50

60

ampl



B0L101S, U22-ch87, adc4

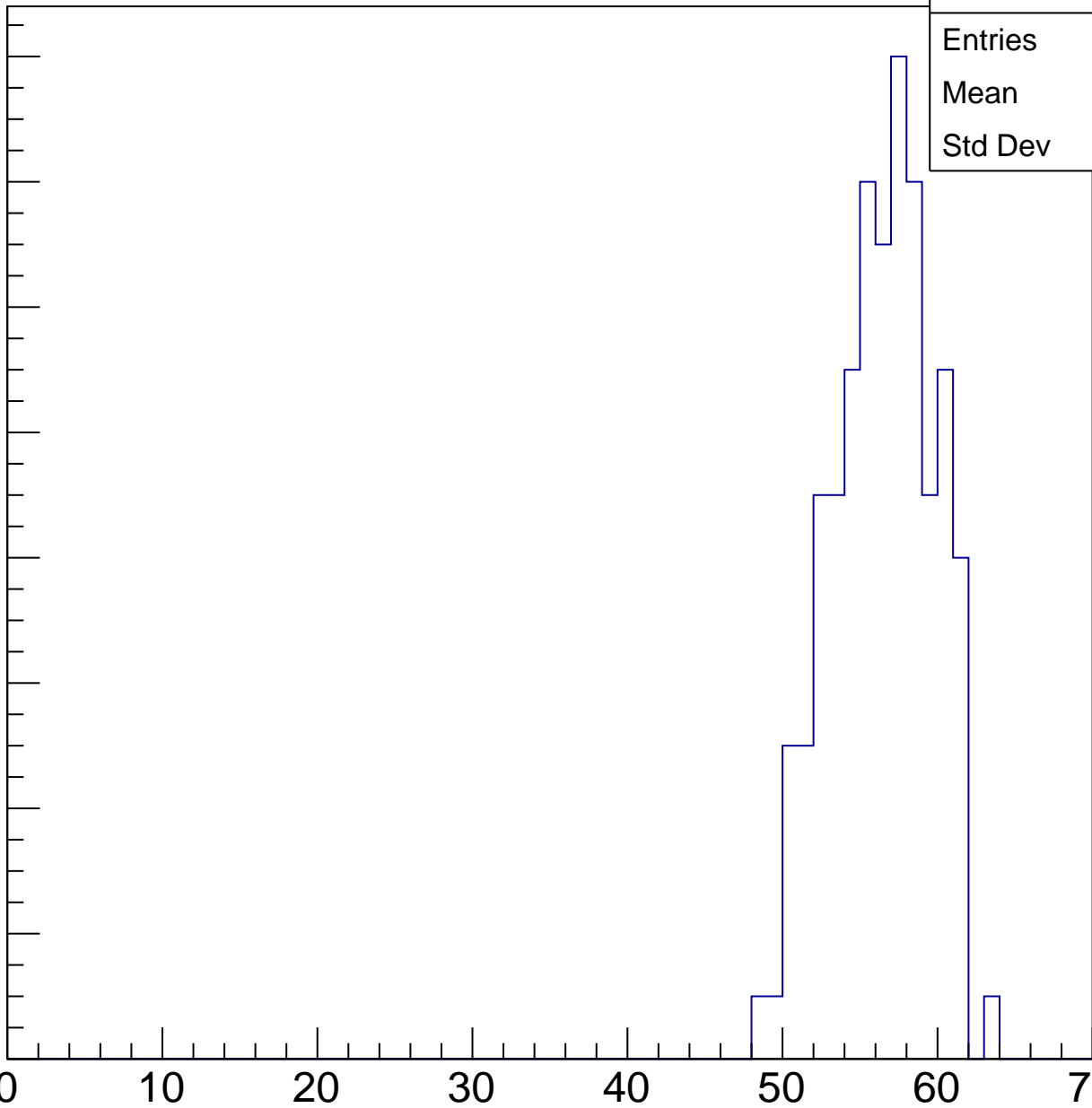
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 127 |
| Mean | 55.94 |
| Std Dev | 3.173 |

ampl



B0L101S, U22-ch87, adc5

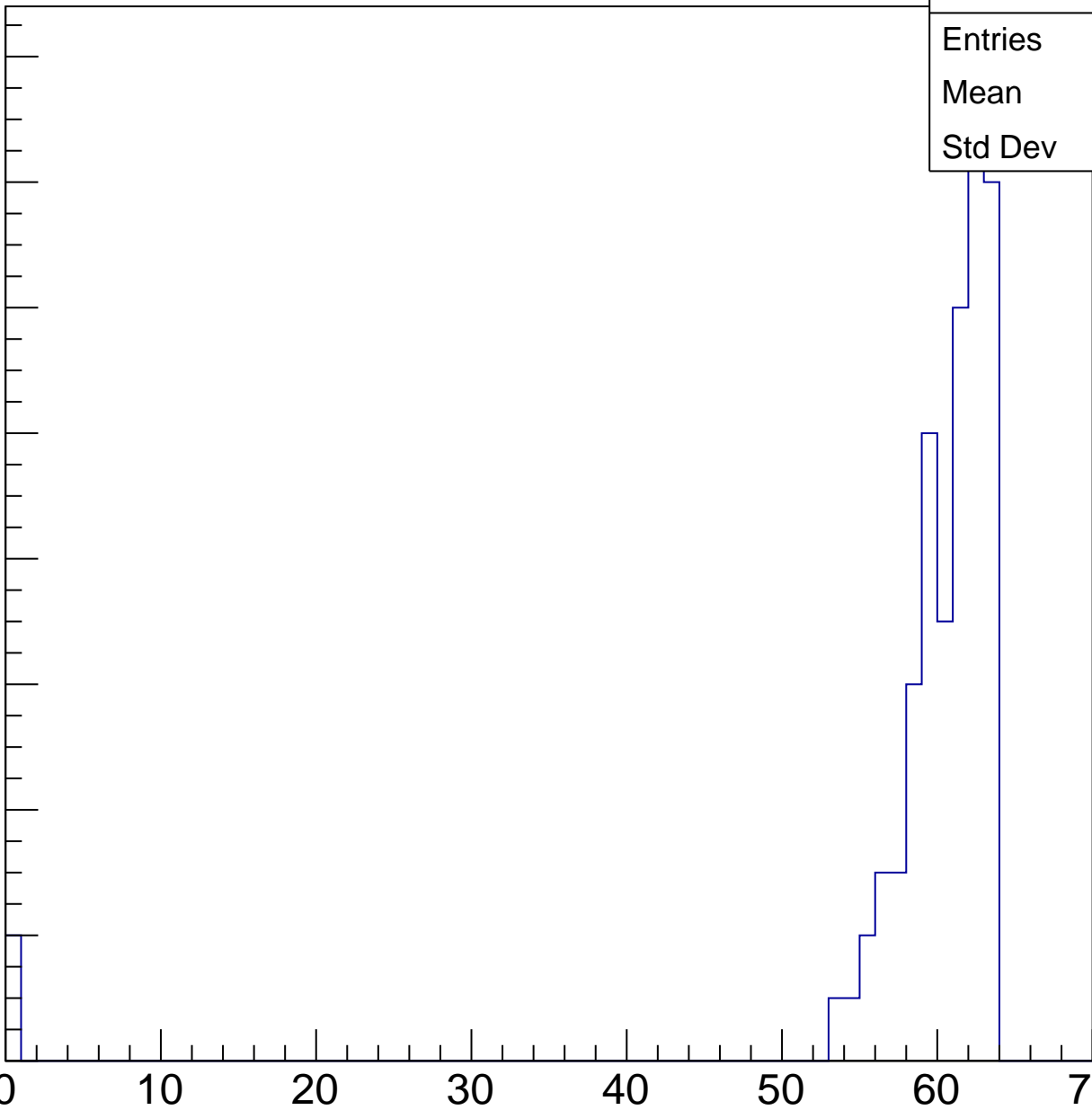
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 77 |
| Mean | 58.7 |
| Std Dev | 9.88 |

ampl

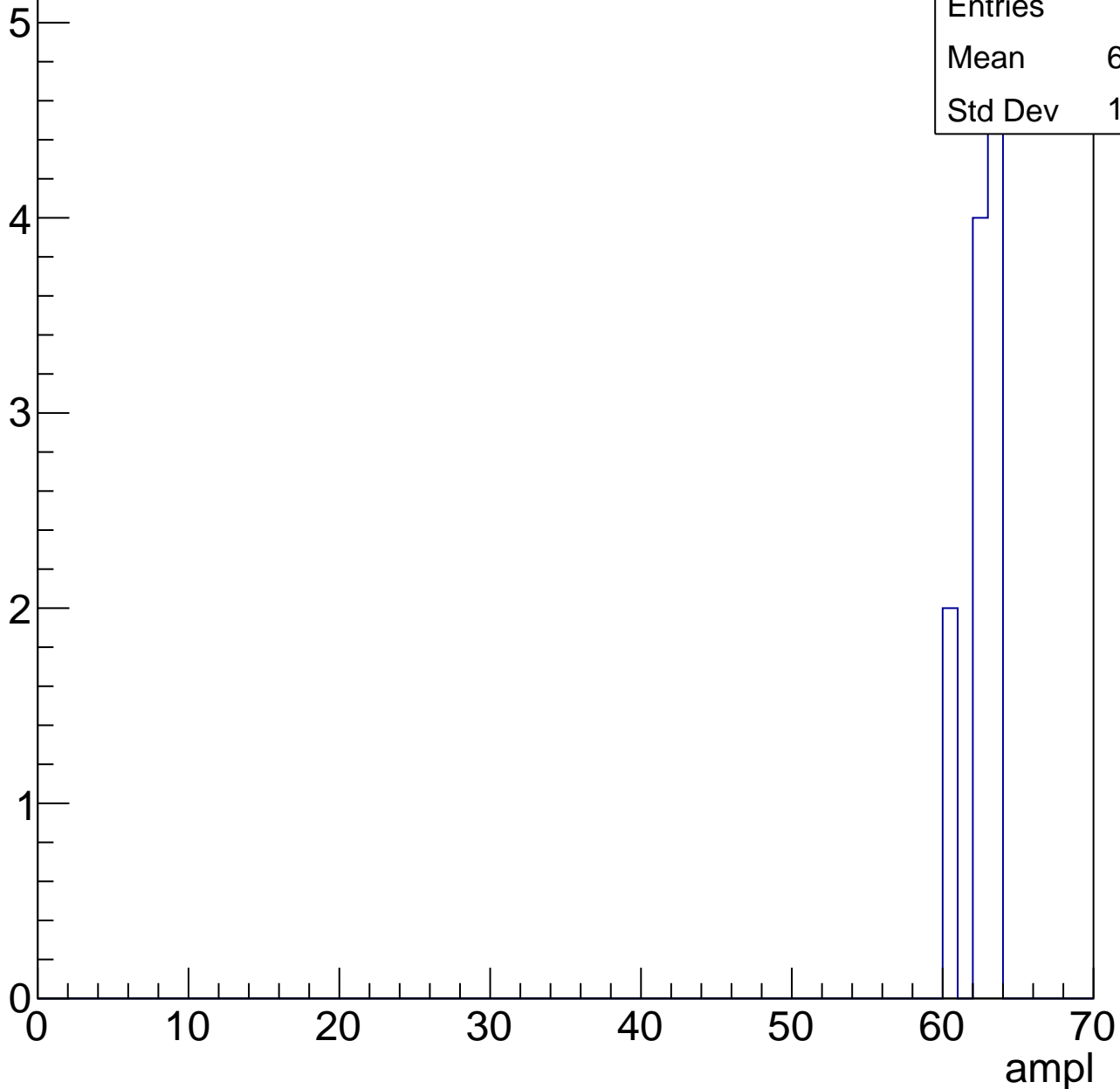


B0L101S, U22-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

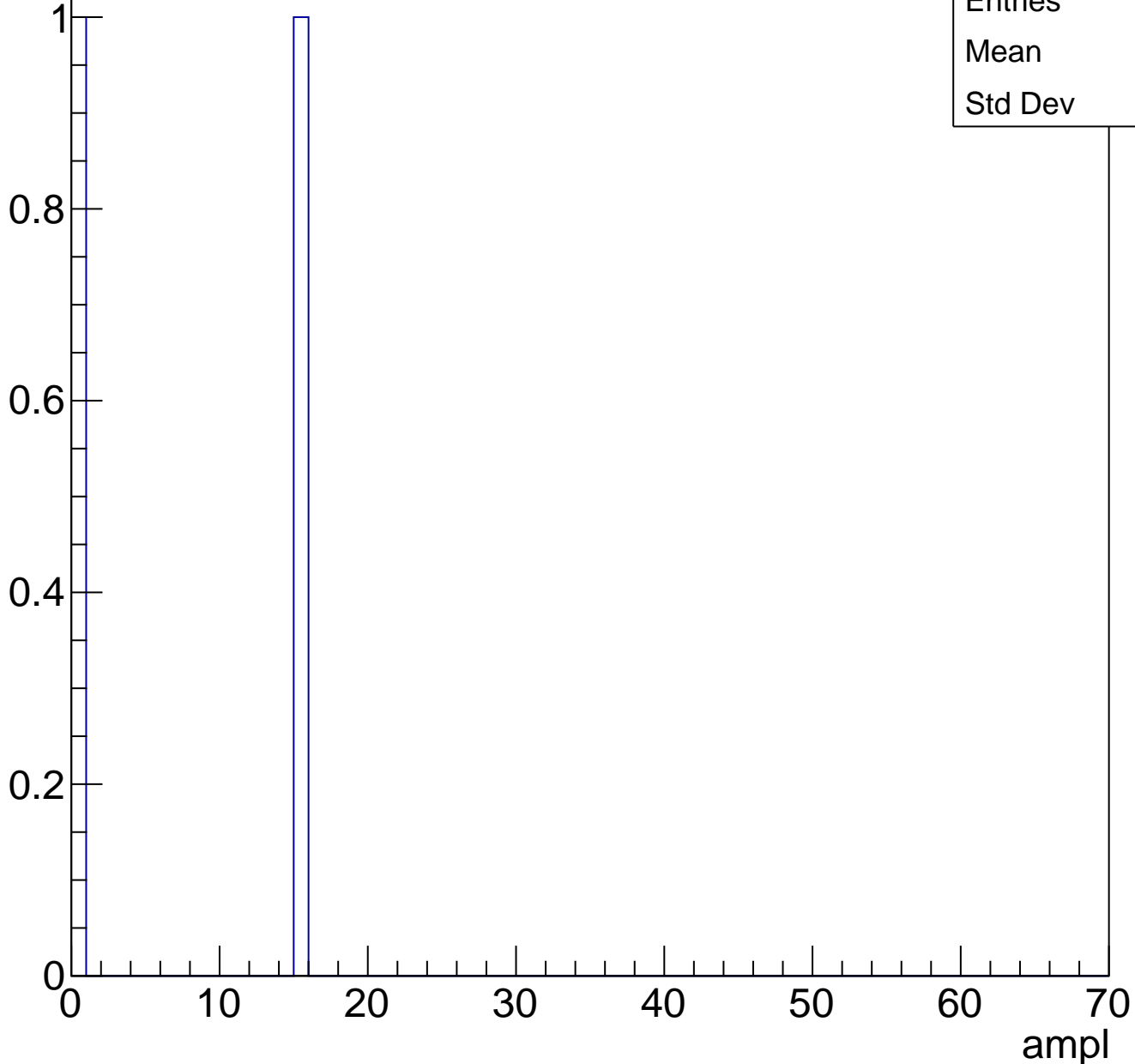
| | |
|---------|-------|
| Entries | 11 |
| Mean | 62.09 |
| Std Dev | 1.083 |



B0L101S, U22-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-----|
| Entries | 2 |
| Mean | 7.5 |
| Std Dev | 7.5 |

B0L101S, U22-ch88, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

162

Mean

28.8

Std Dev

3.792

Gaus mean : 29.0639

Gaus Width: 4.0670

0

10

20

30

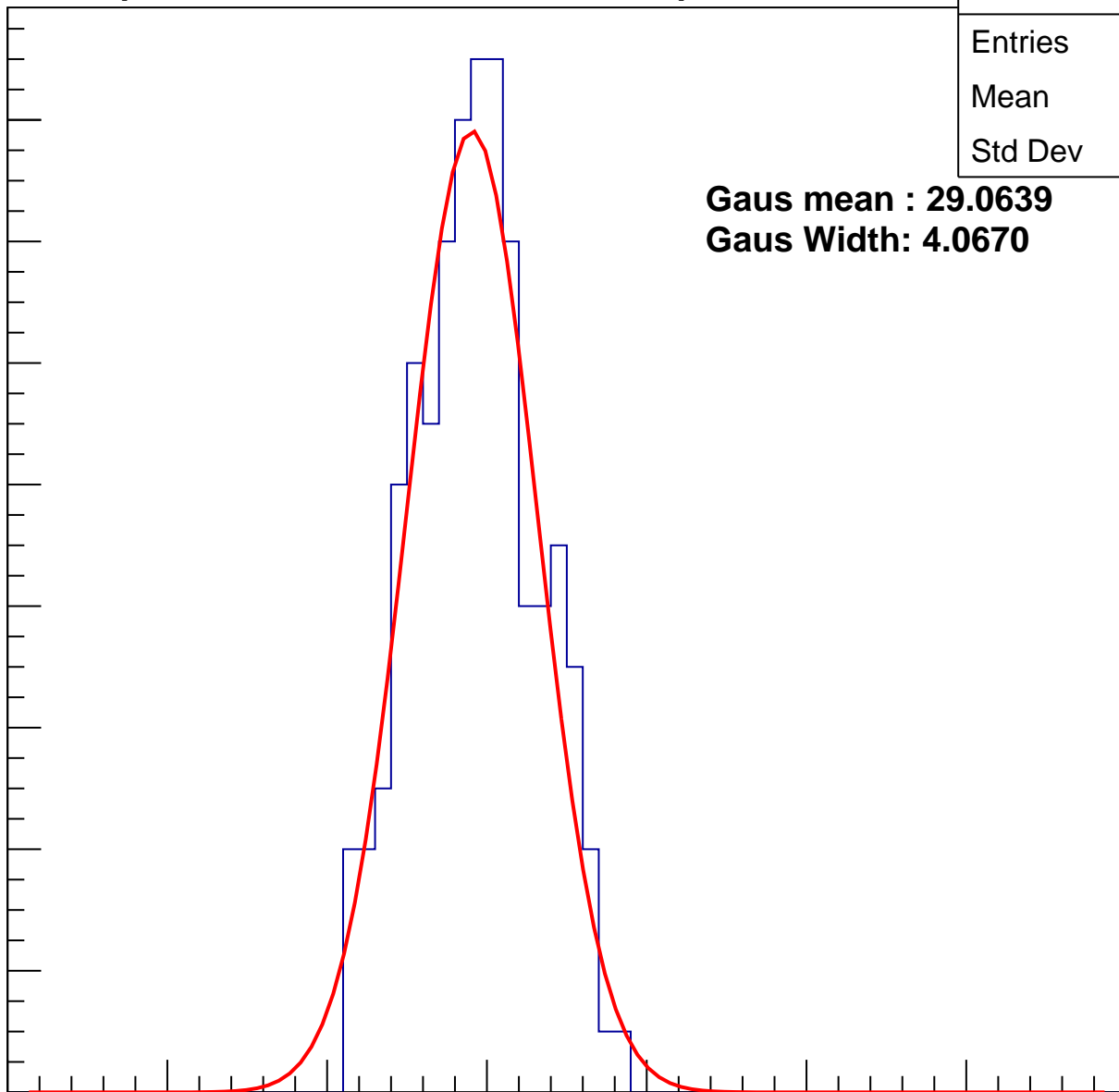
40

50

60

70

ampl



B0L101S, U22-ch88, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

106

Mean

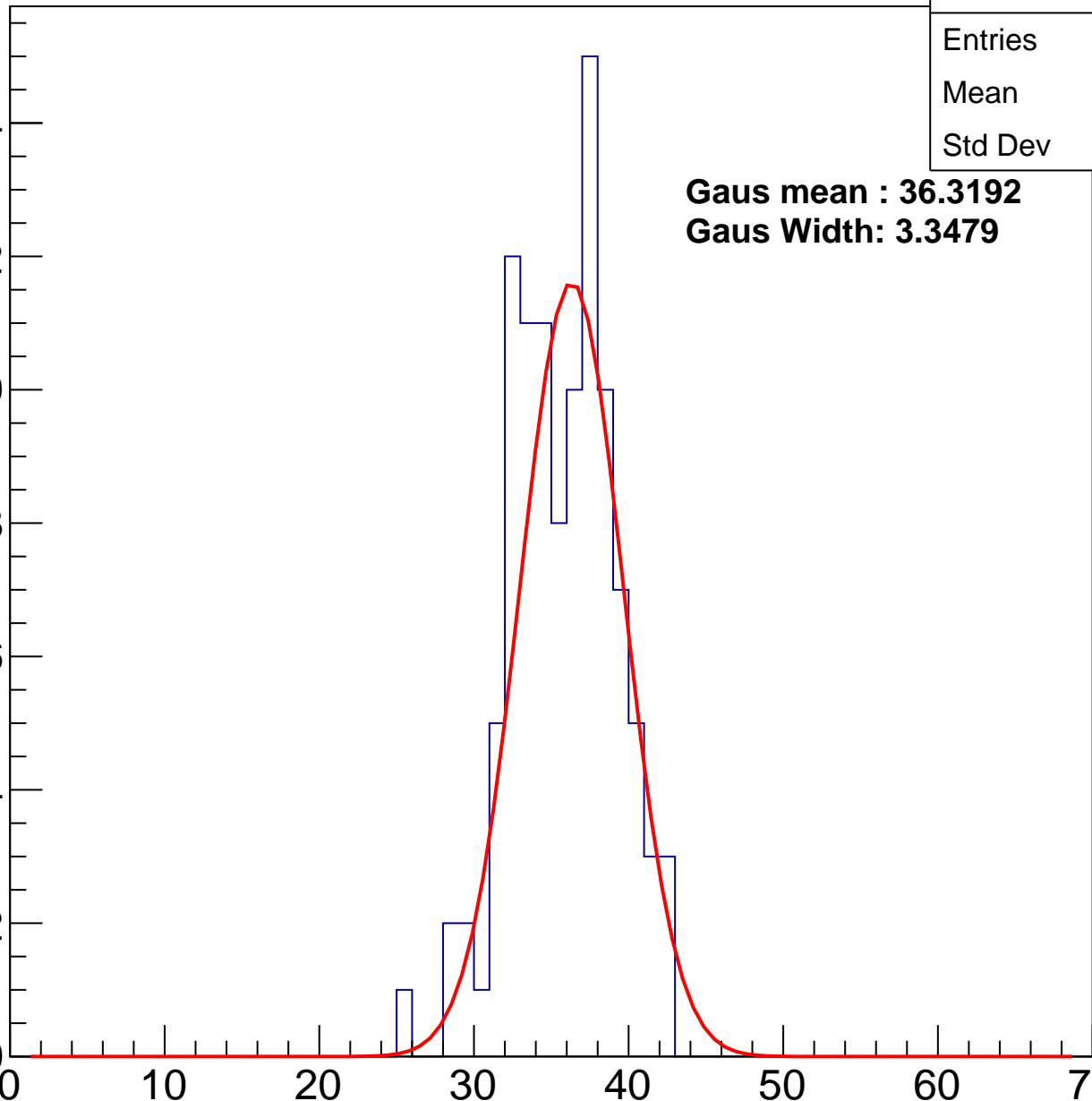
35.3

Std Dev

3.337

Gaus mean : 36.3192

Gaus Width: 3.3479



B0L101S, U22-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

118

Mean

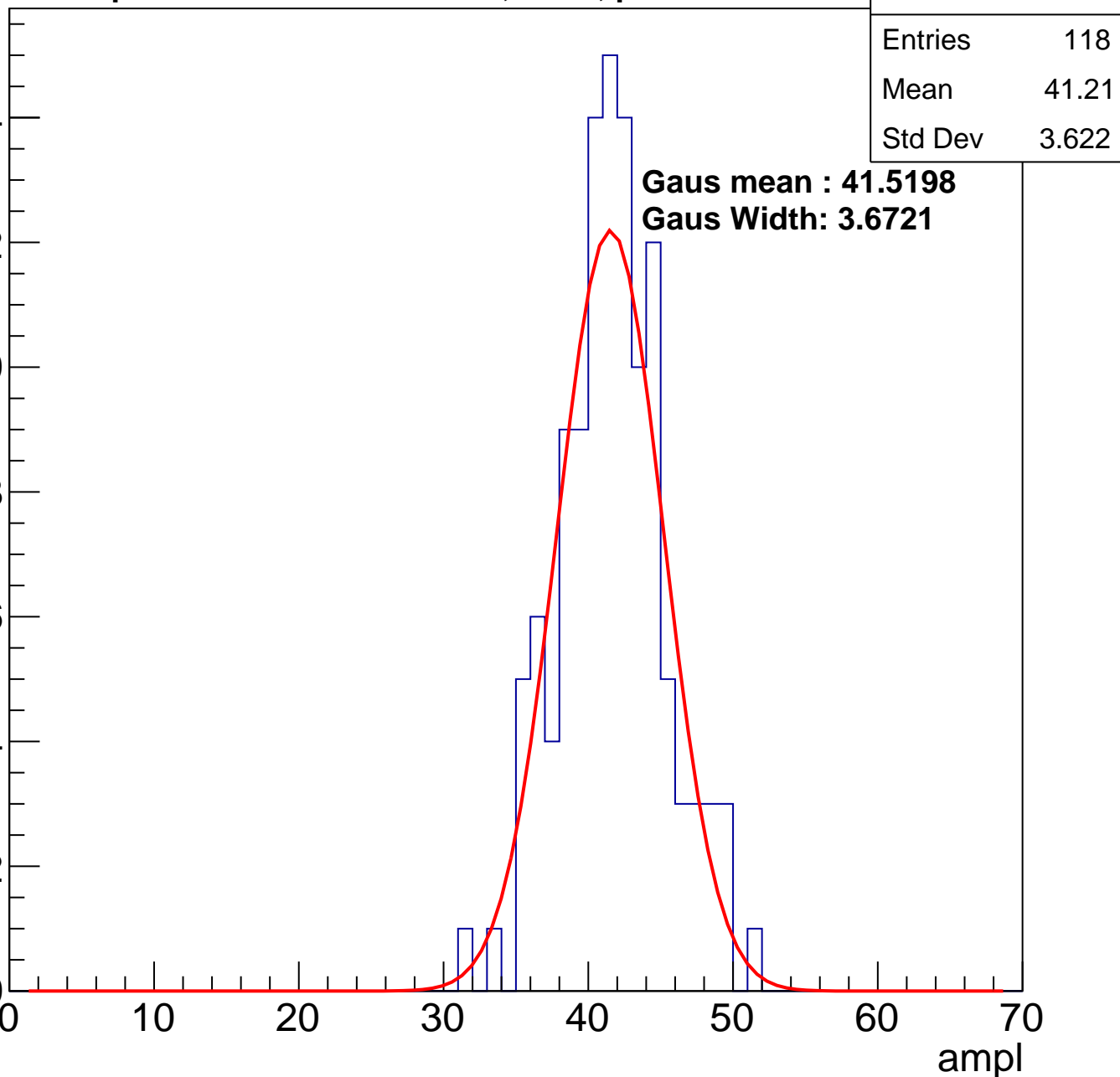
41.21

Std Dev

3.622

Gaus mean : 41.5198

Gaus Width: 3.6721



B0L101S, U22-ch88, adc3

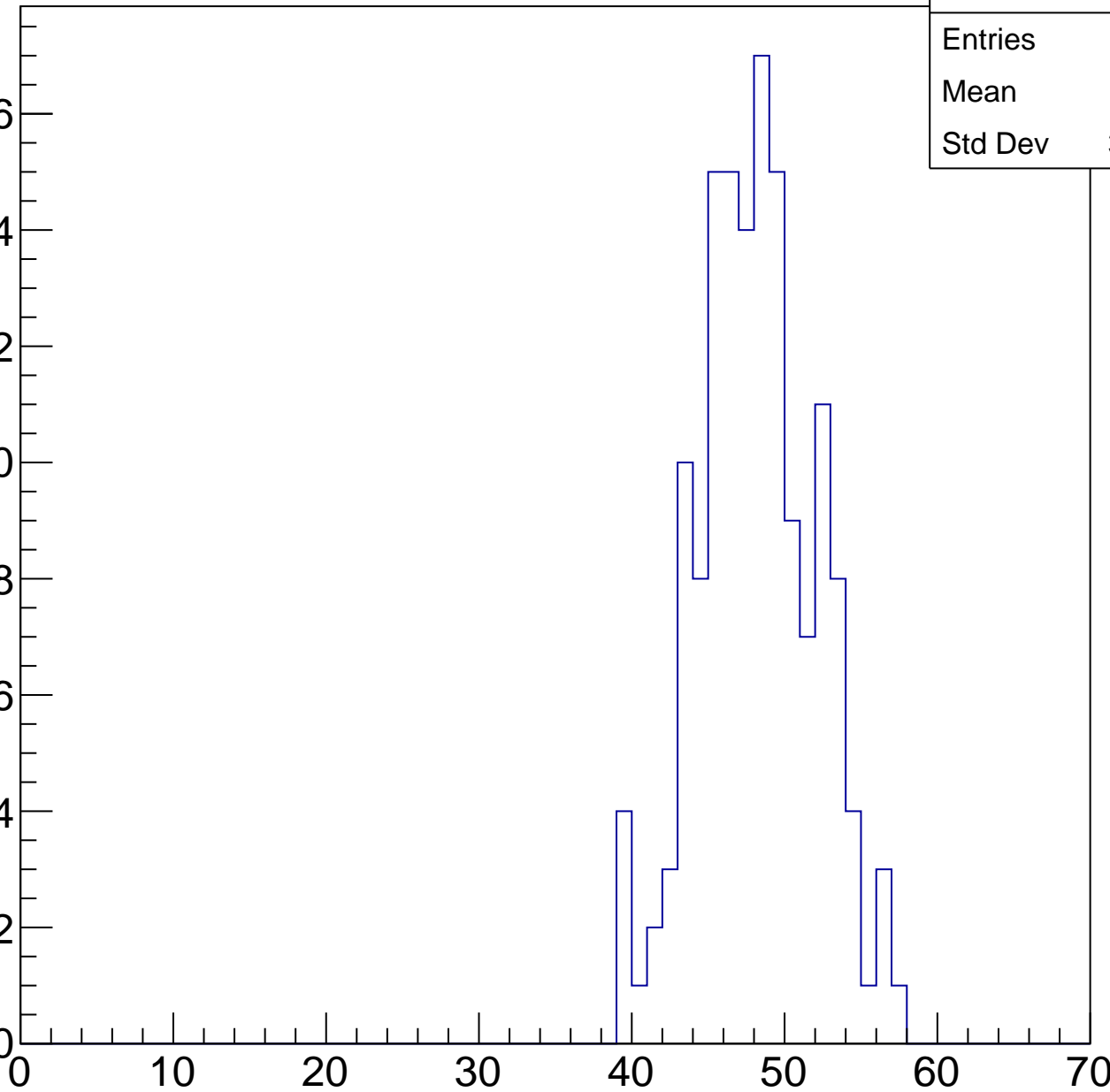
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 47.7 |
| Std Dev | 3.788 |

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch88, adc4

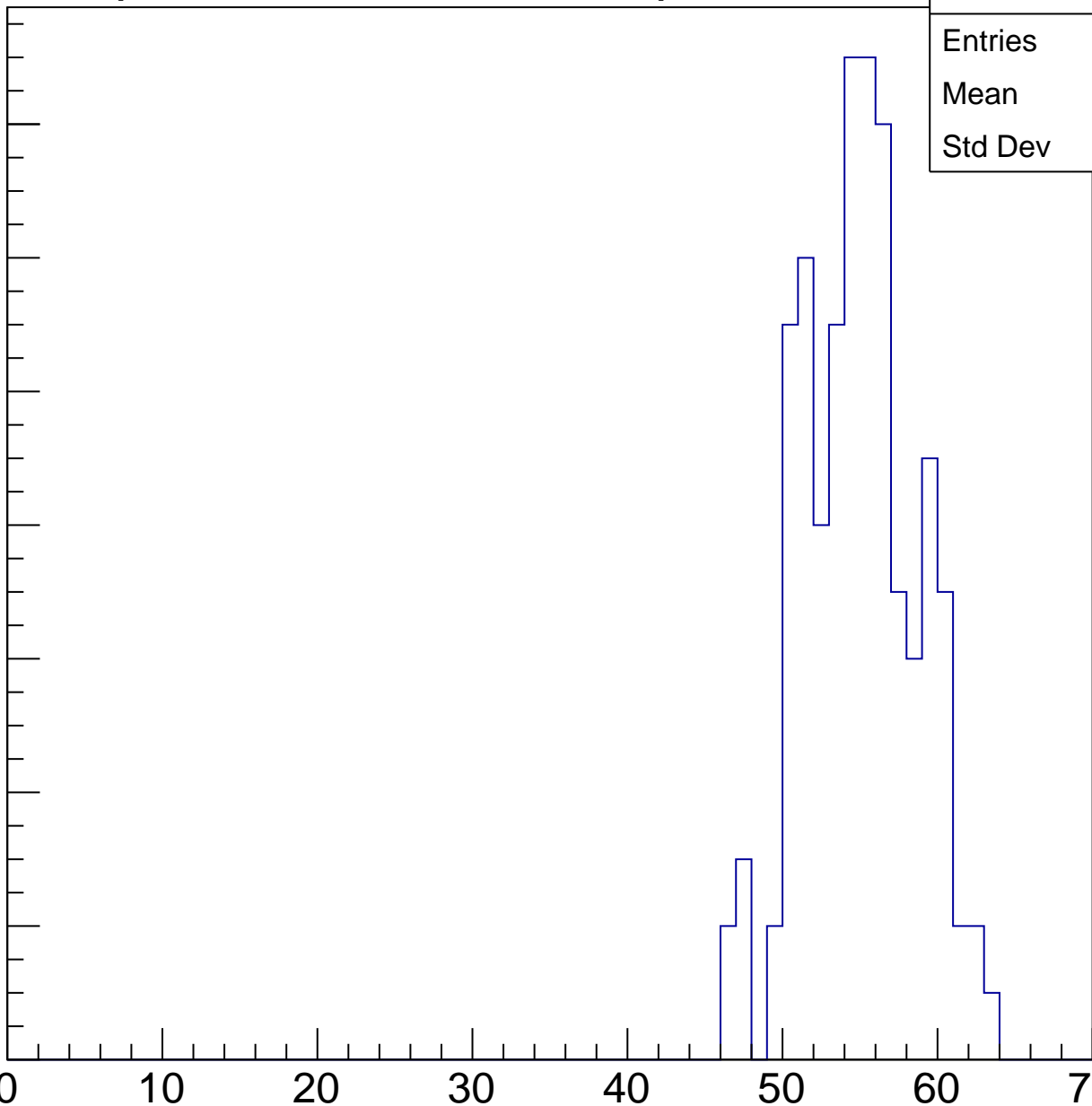
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 127 |
| Mean | 54.47 |
| Std Dev | 3.591 |

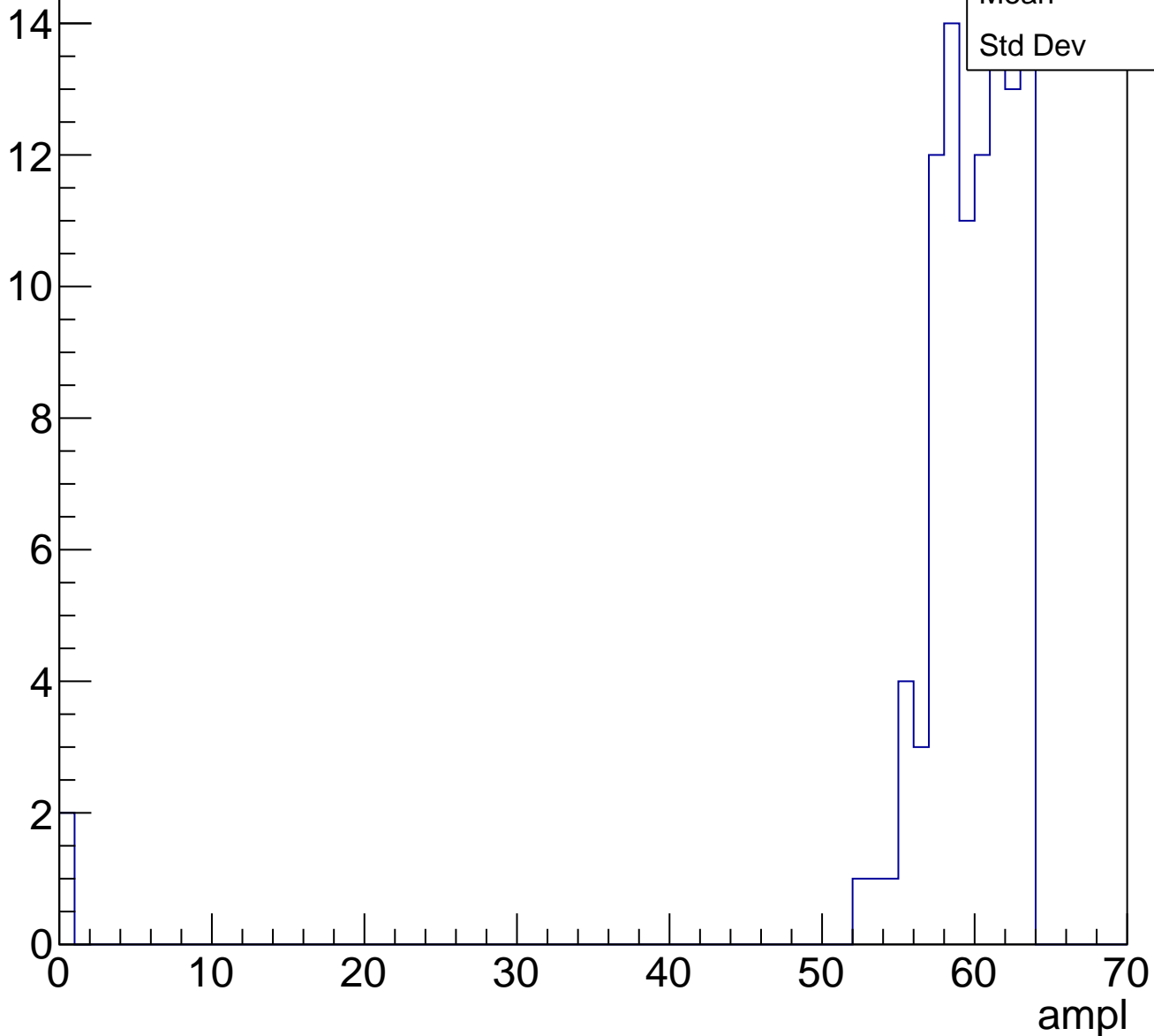
ampl



B0L101S, U22-ch88, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

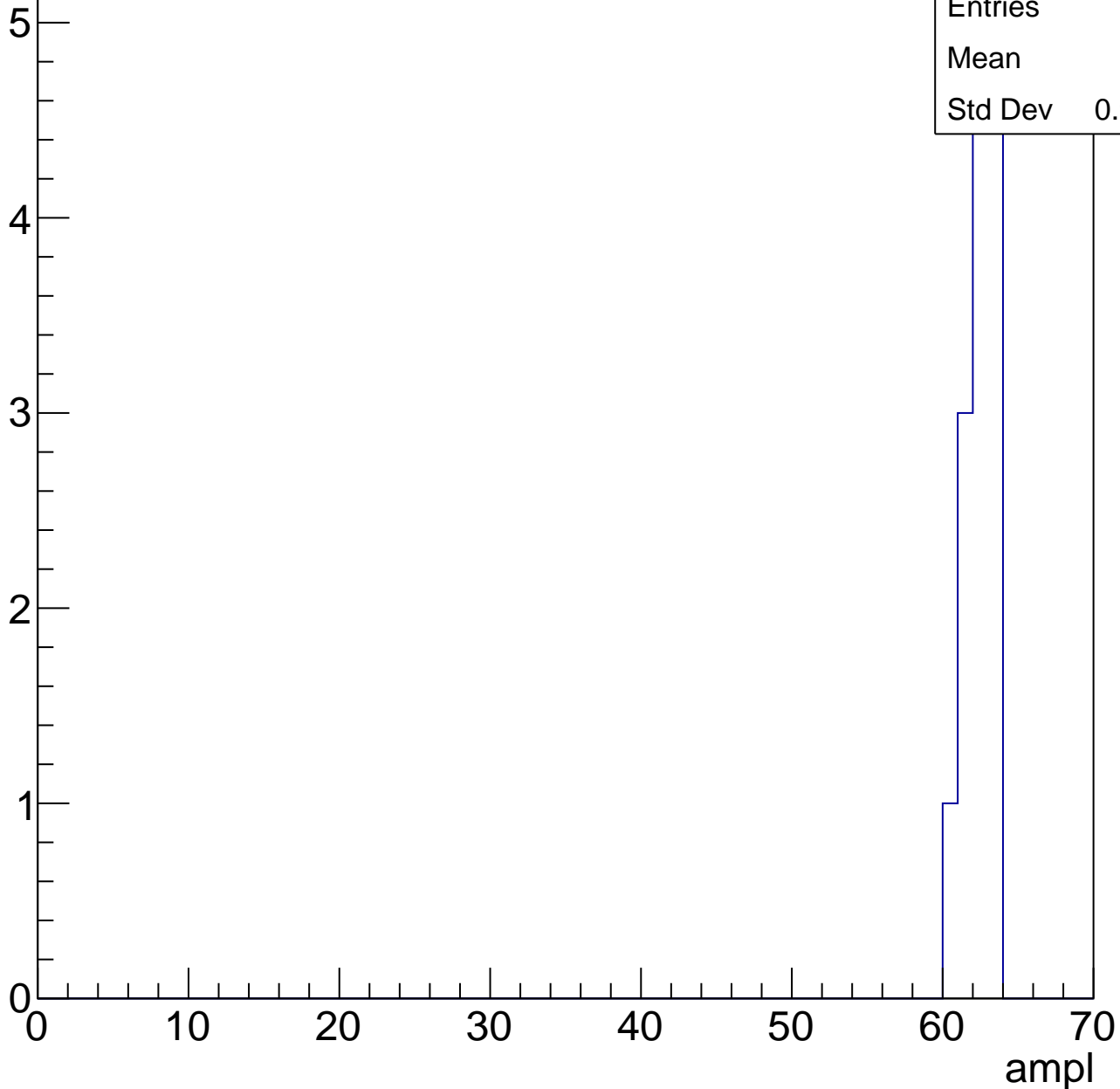


B0L101S, U22-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|--------|
| Entries | 14 |
| Mean | 62 |
| Std Dev | 0.9258 |



B0L101S, U22-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch89, adc0

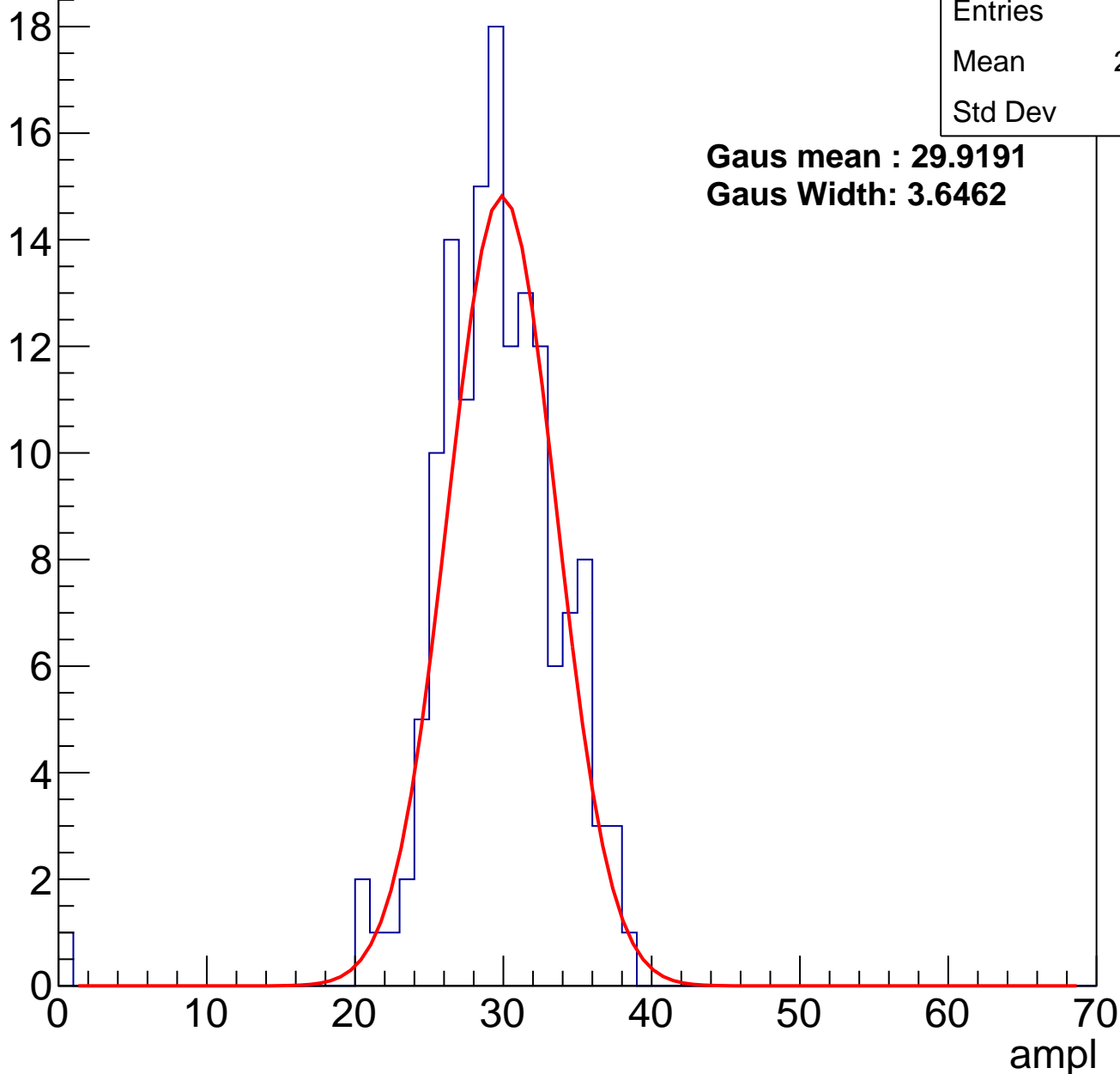
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 29.12 |
| Std Dev | 4.4 |

Gaus mean : 29.9191

Gaus Width: 3.6462

Entry



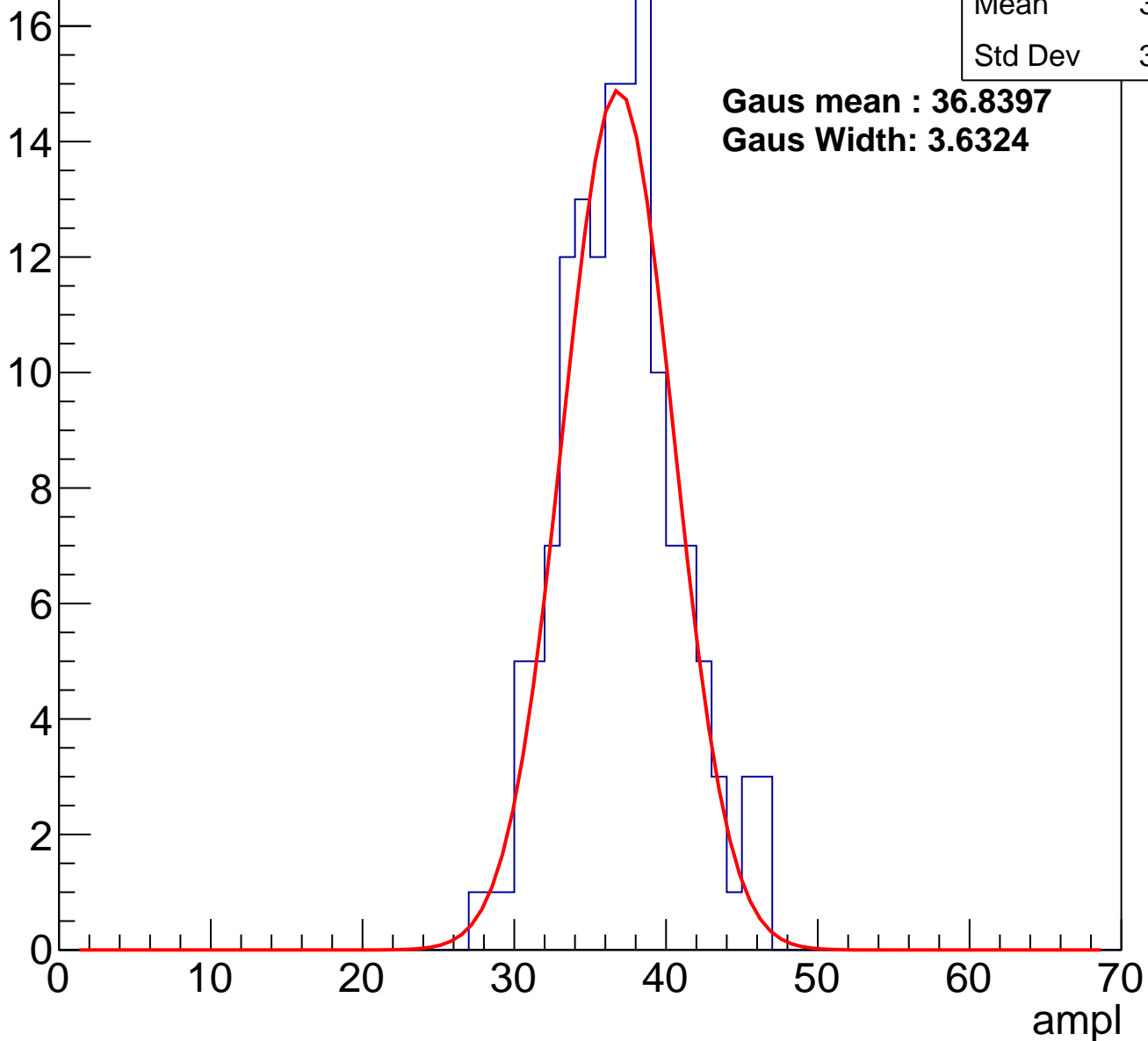
B0L101S, U22-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 36.54 |
| Std Dev | 3.864 |

Gaus mean : 36.8397
Gaus Width: 3.6324

Entry



B0L101S, U22-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

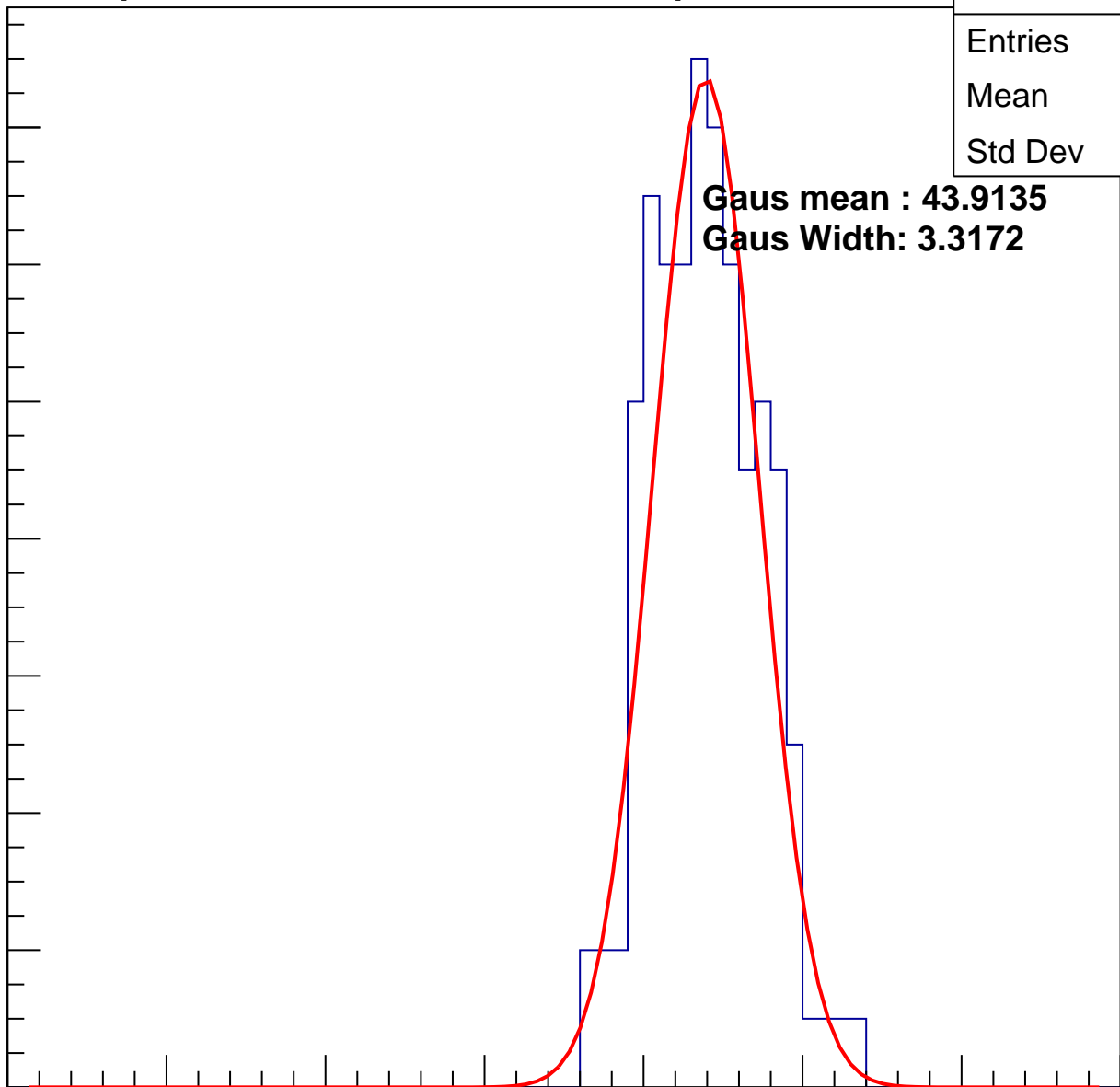
70

ampl

| | |
|---------|-------|
| Entries | 131 |
| Mean | 43.48 |
| Std Dev | 3.416 |

Gaus mean : 43.9135

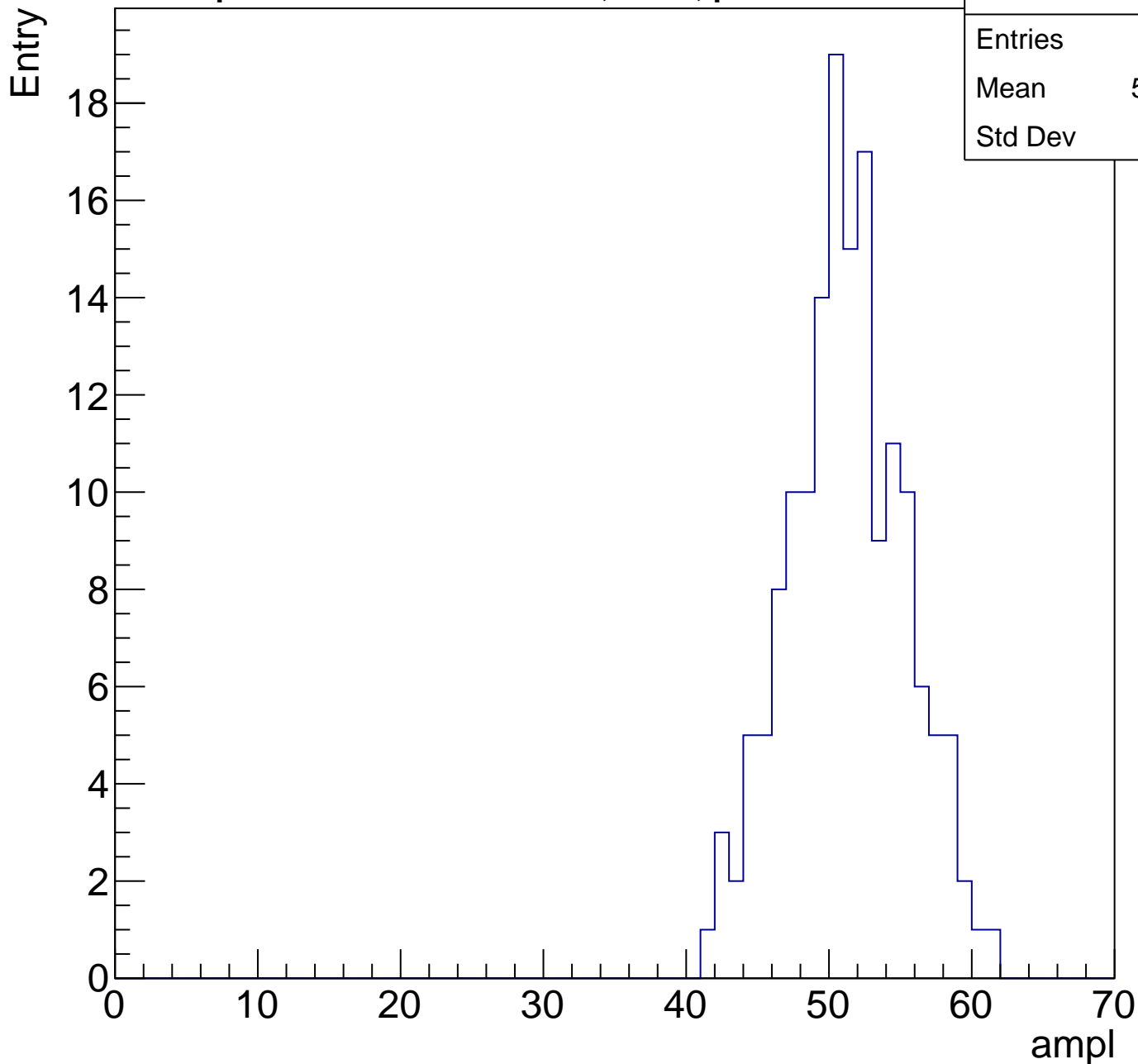
Gaus Width: 3.3172



B0L101S, U22-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 50.77 |
| Std Dev | 4.05 |

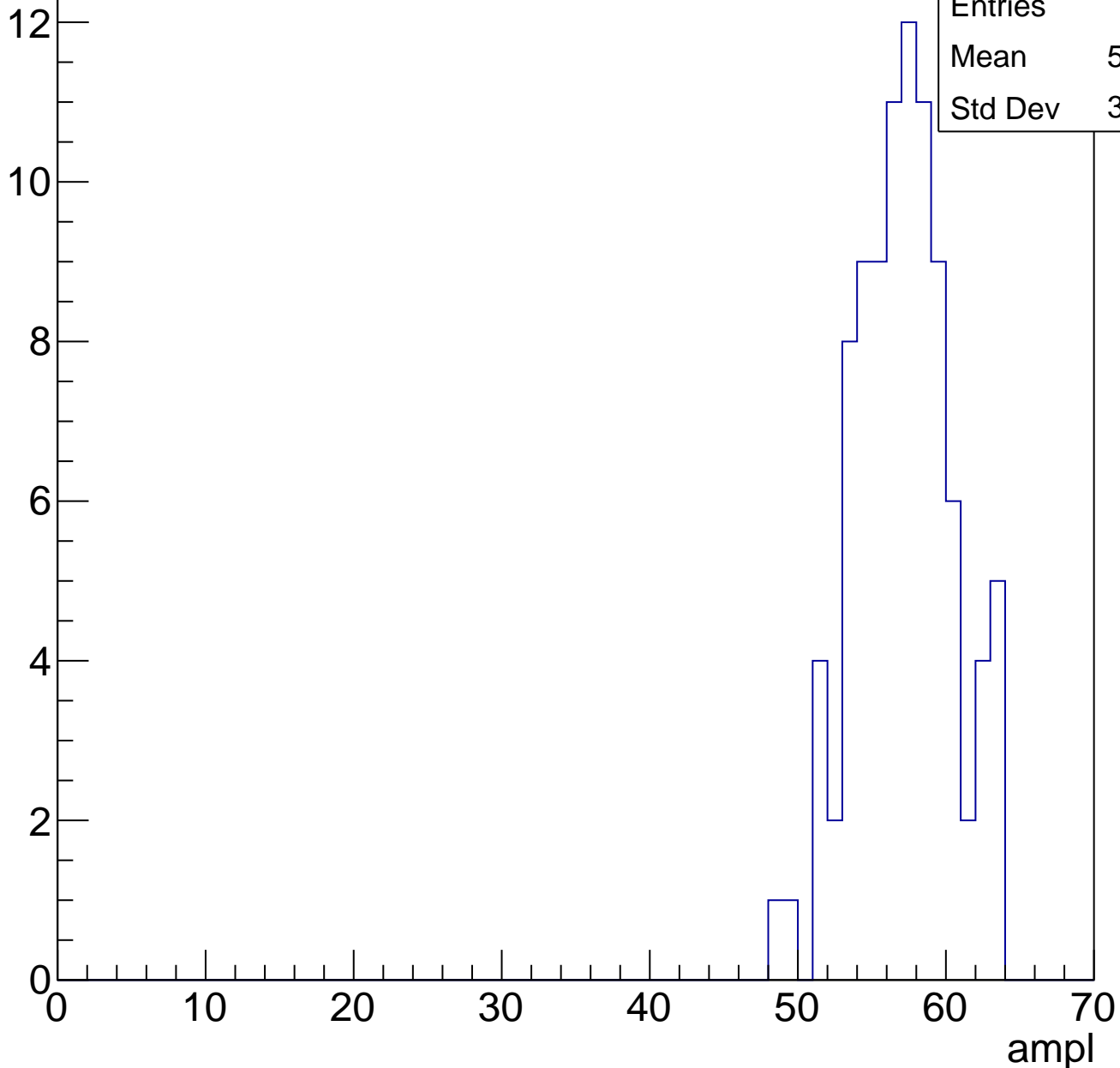


B0L101S, U22-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 94 |
| Mean | 56.64 |
| Std Dev | 3.255 |

Entry



B0L101S, U22-ch89, adc5

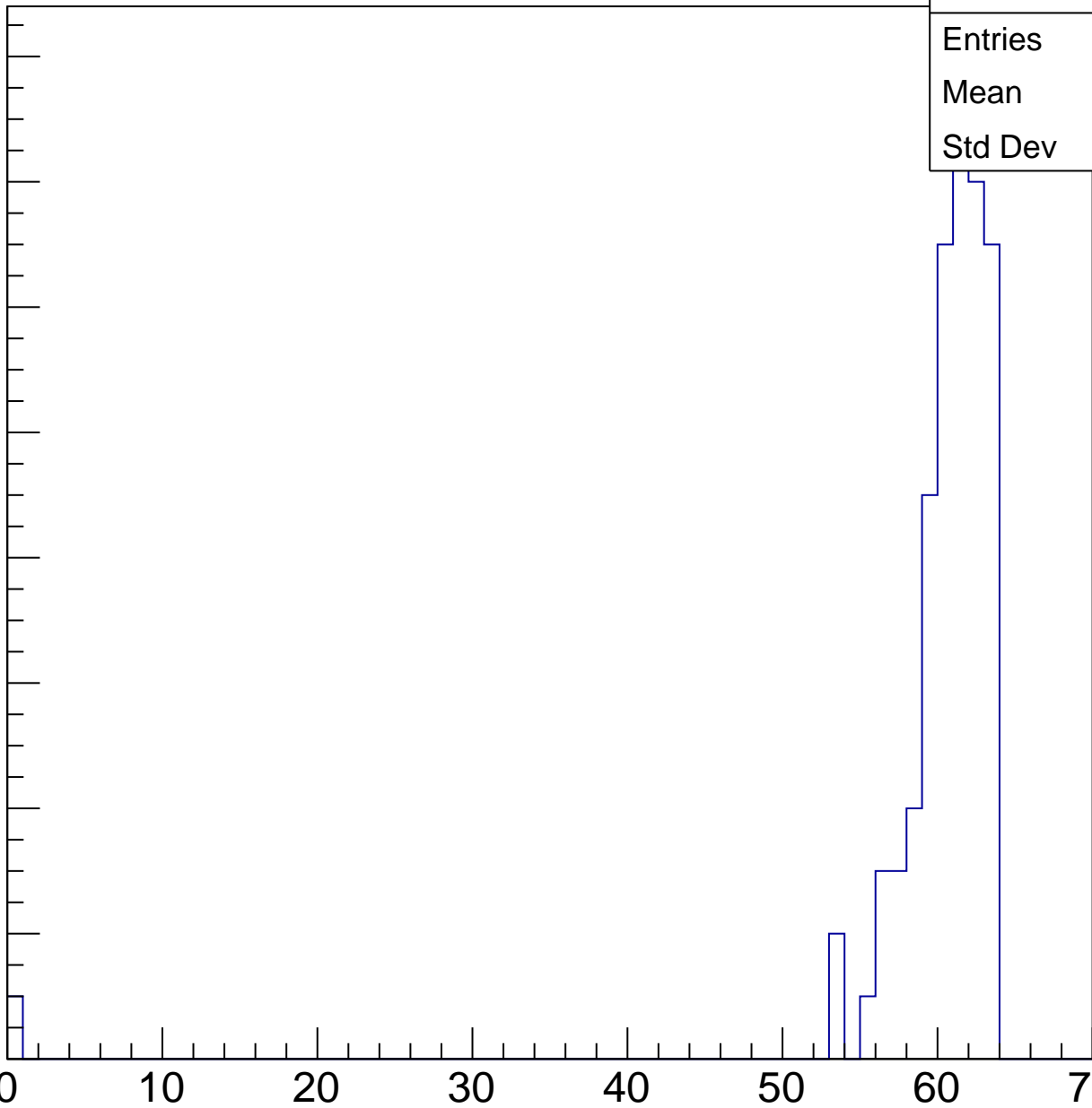
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 59.57 |
| Std Dev | 7.113 |

ampl



B0L101S, U22-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 132 |
| Mean | 30.34 |
| Std Dev | 4.552 |

Gaus mean : 30.8649

Gaus Width: 3.7609

0

10

20

30

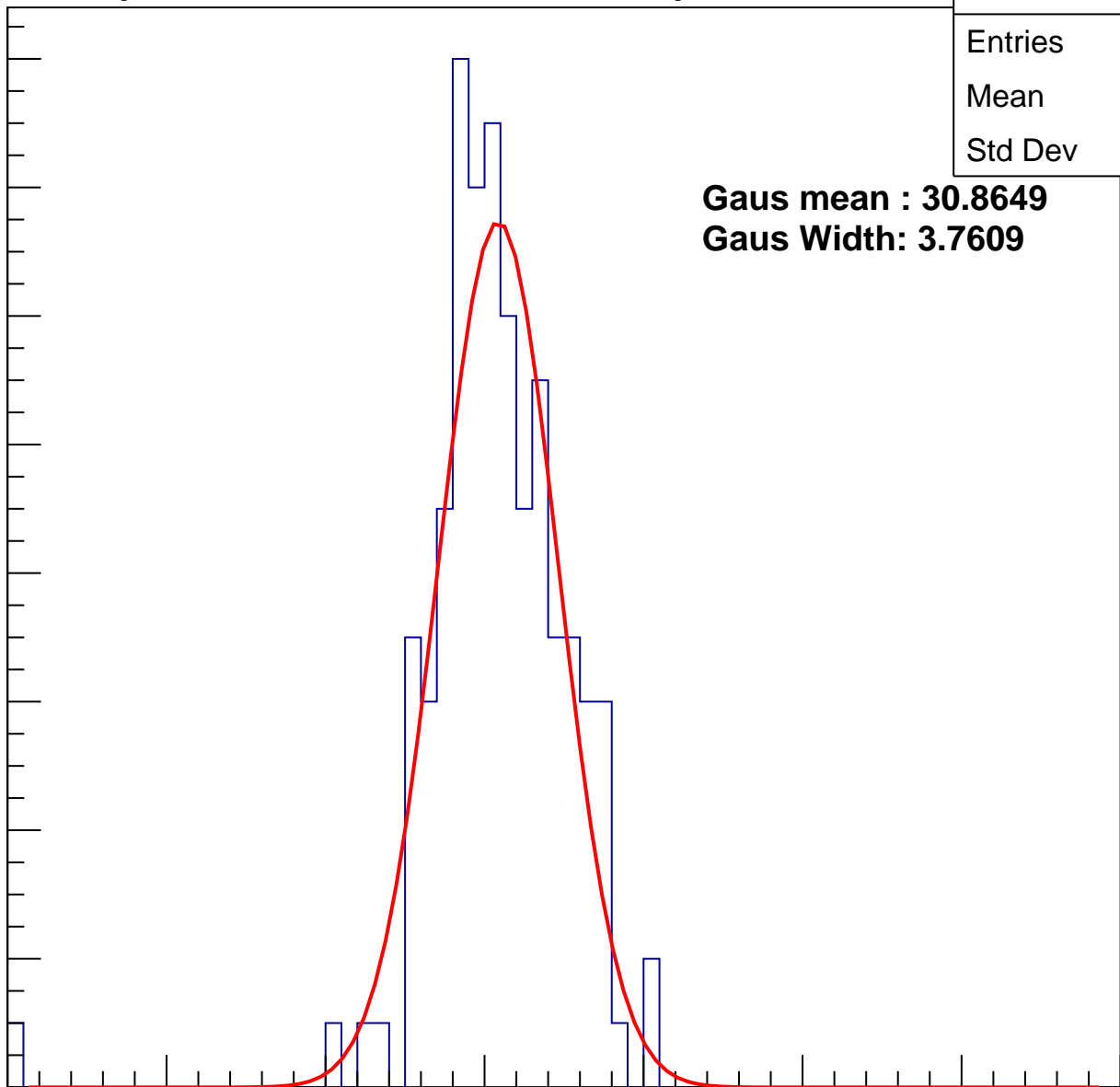
40

50

60

70

ampl



B0L101S, U22-ch90, adc1

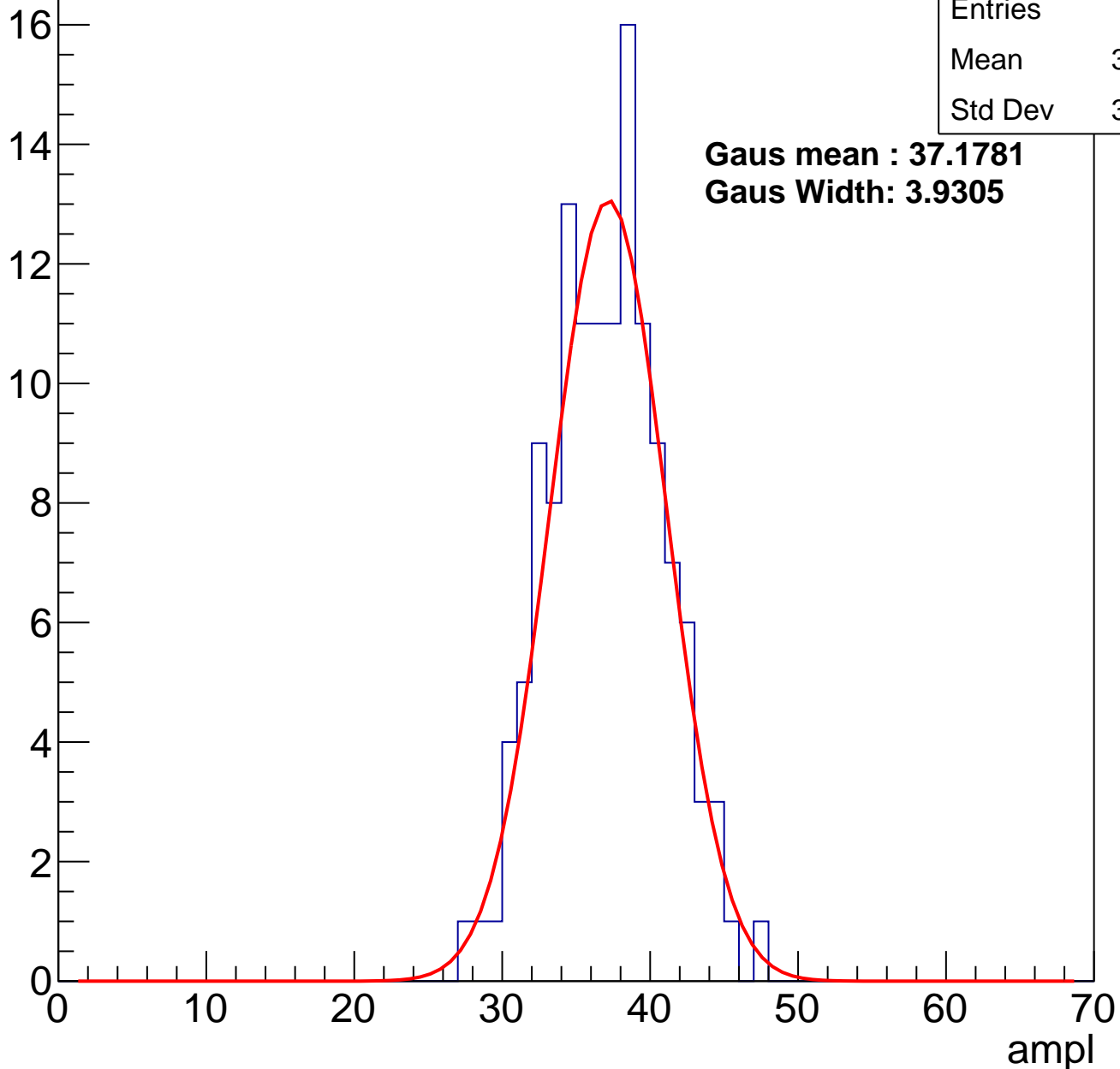
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 36.59 |
| Std Dev | 3.839 |

Gaus mean : 37.1781

Gaus Width: 3.9305

Entry



B0L101S, U22-ch90, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 43.97 |
| Std Dev | 3.733 |

Gaus mean : 44.2861

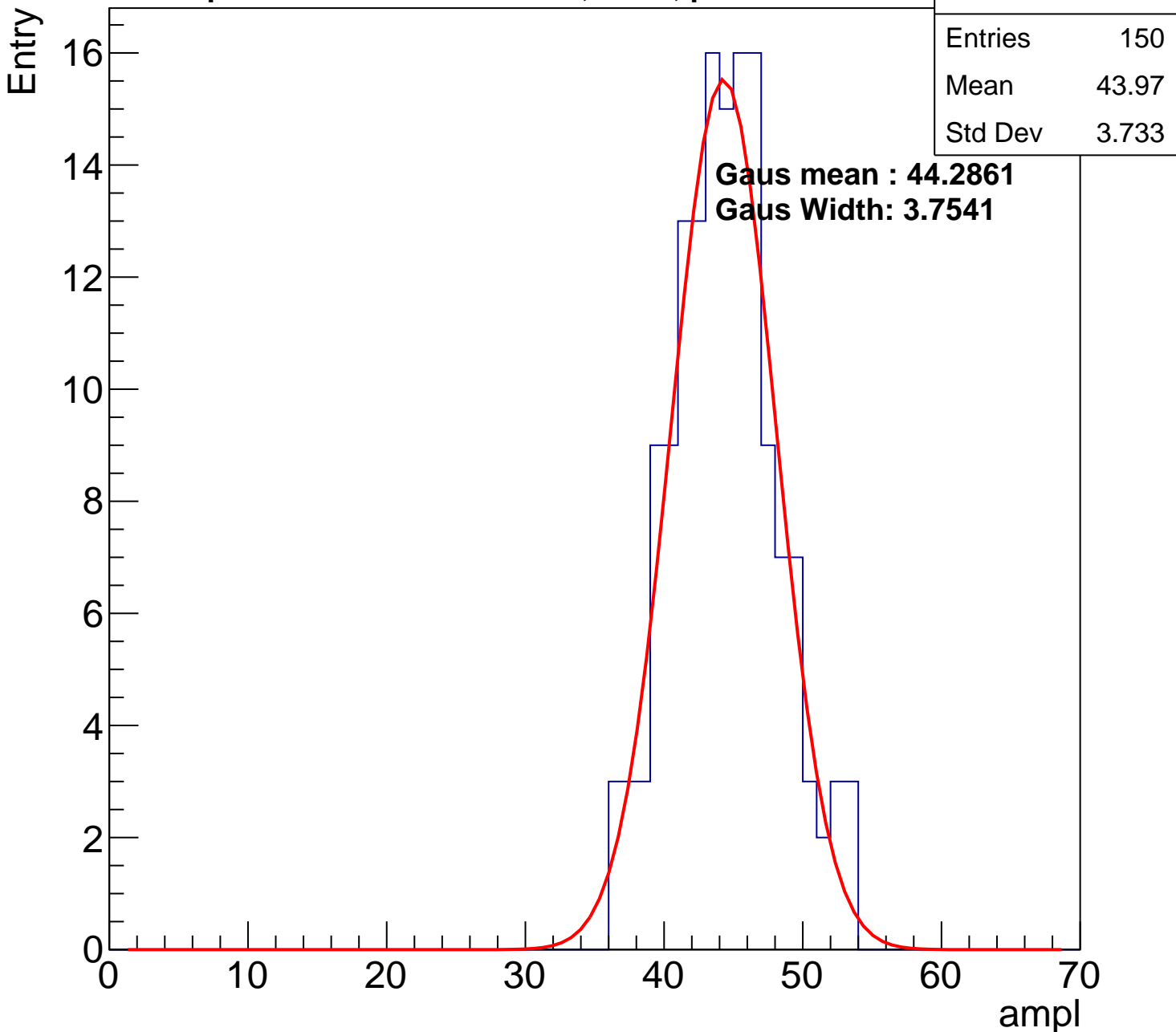
Gaus Width: 3.7541

Entry

16
14
12
10
8
6
4
2
0

ampl

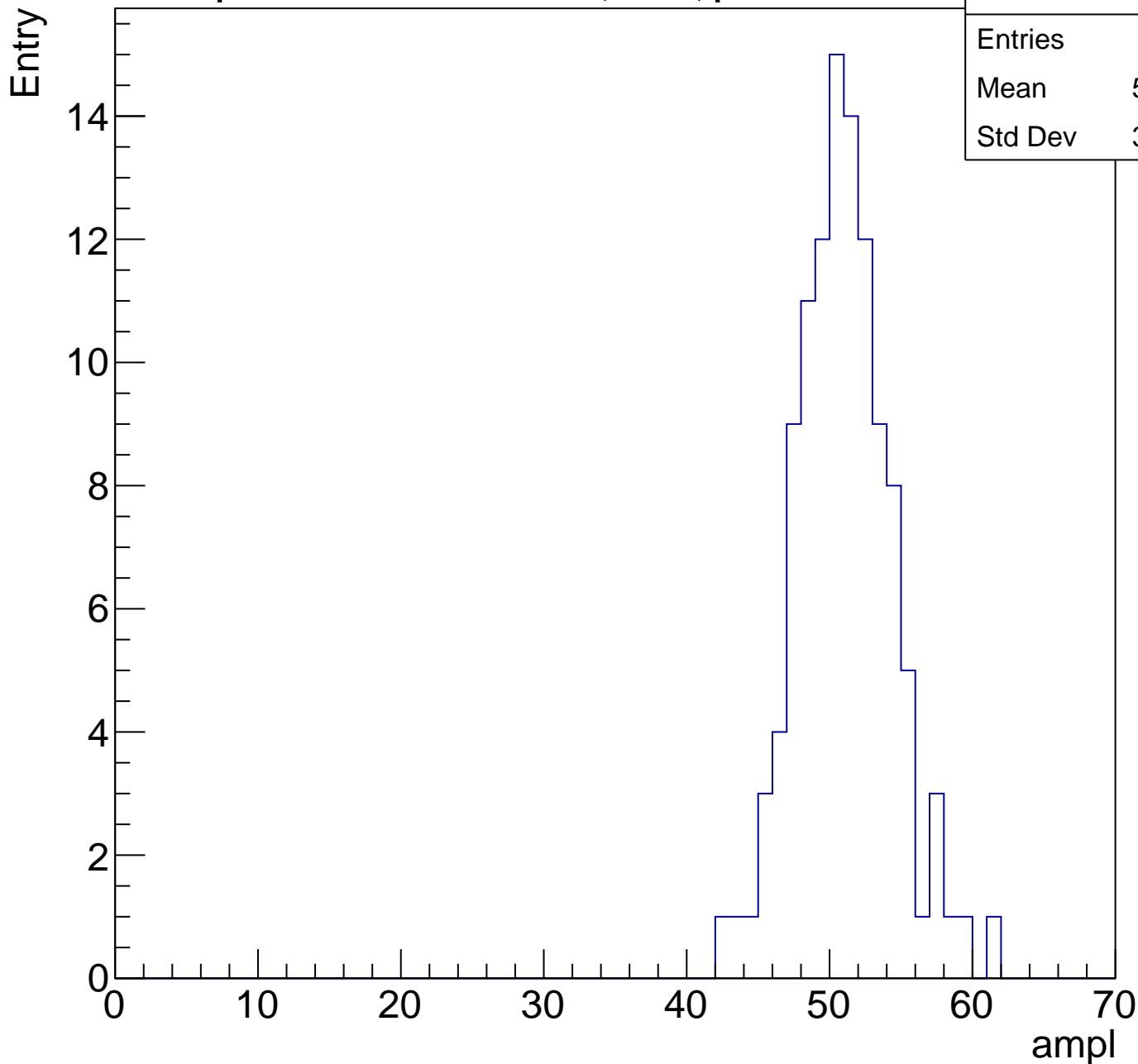
0 10 20 30 40 50 60 70



B0L101S, U22-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 112 |
| Mean | 50.57 |
| Std Dev | 3.319 |



B0L101S, U22-ch90, adc4

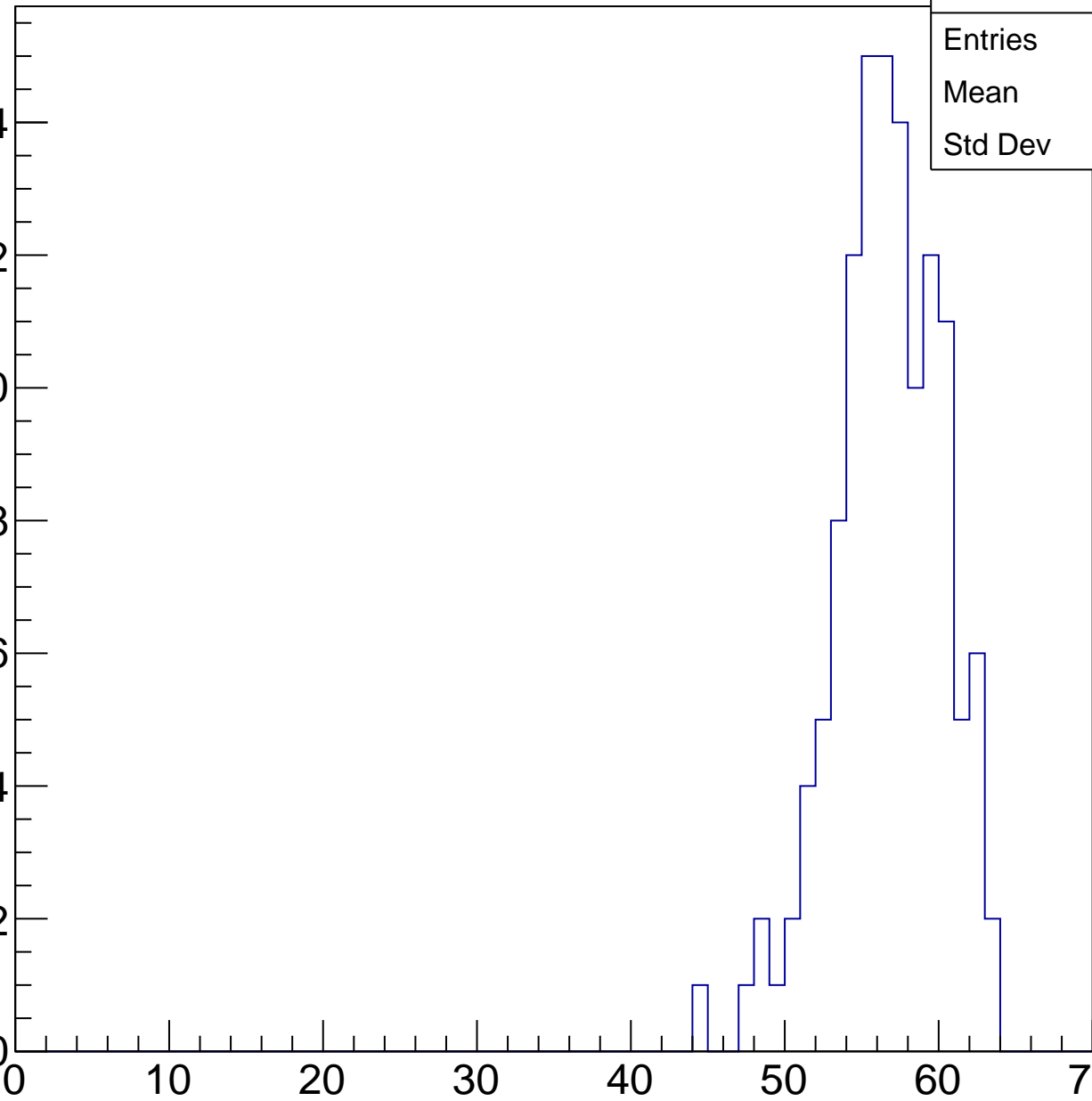
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 126 |
| Mean | 56.24 |
| Std Dev | 3.517 |

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch90, adc5

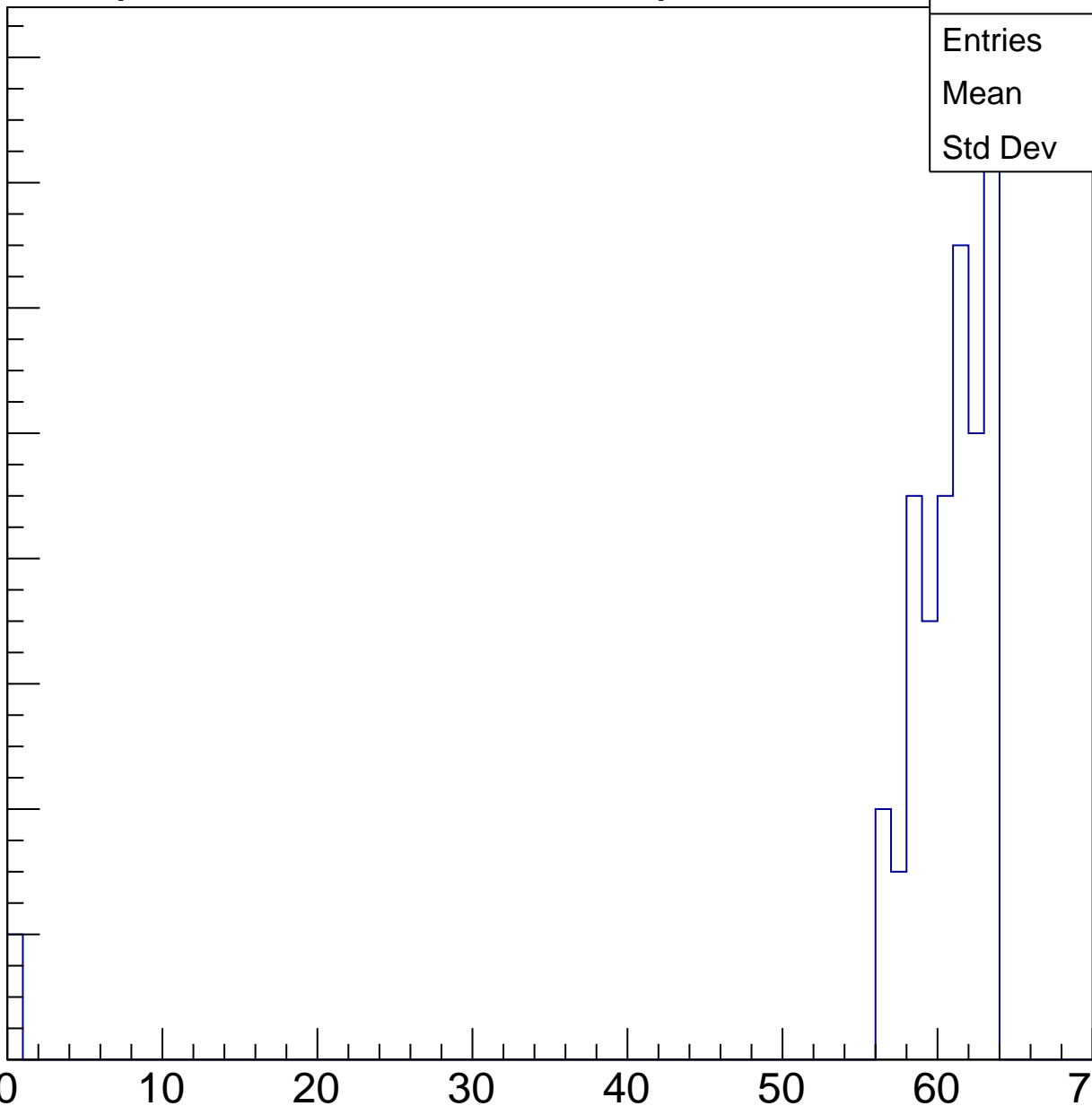
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 58.78 |
| Std Dev | 10.08 |

ampl



B0L101S, U22-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch91, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 29.49 |
| Std Dev | 5.076 |

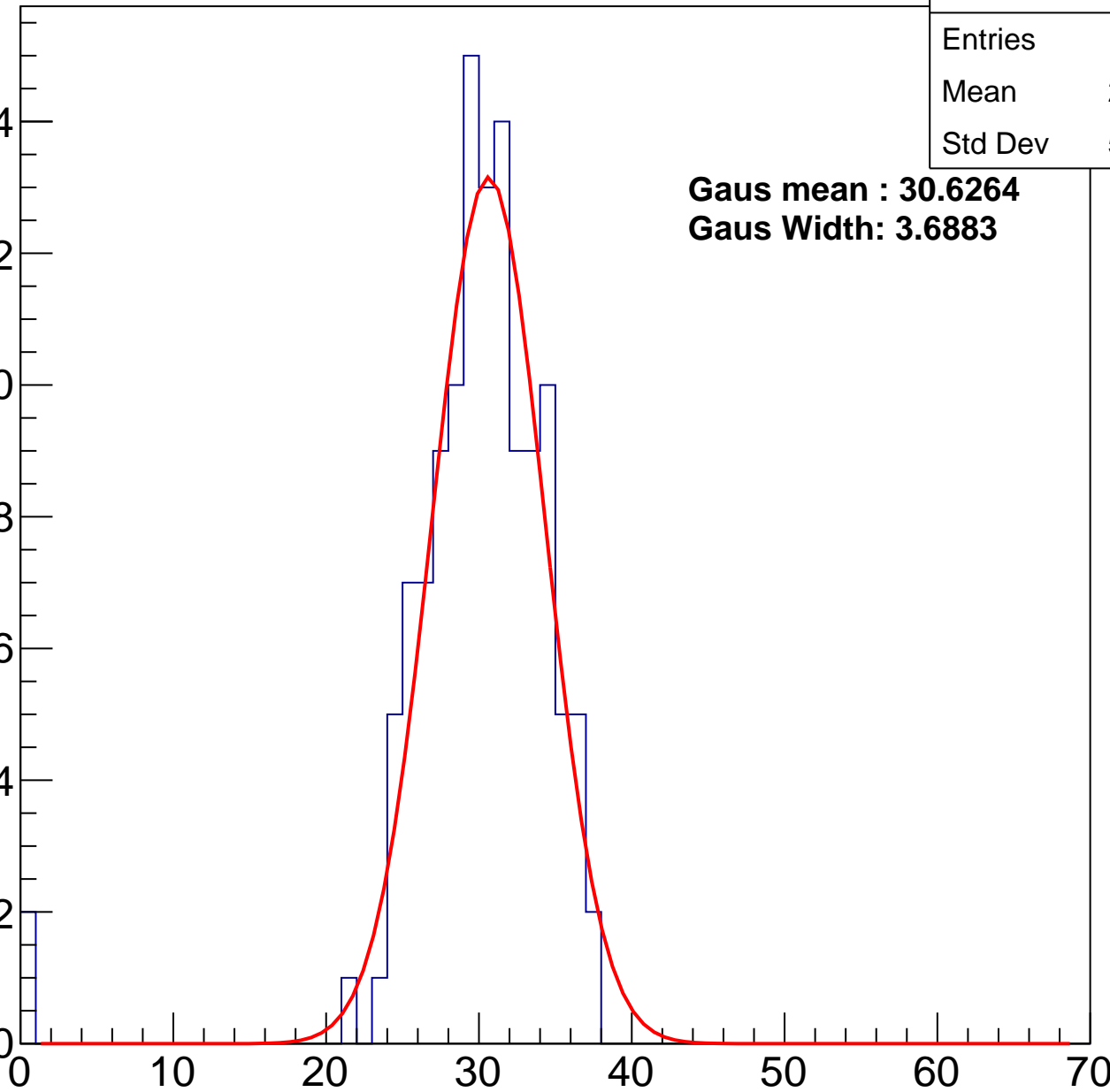
Gaus mean : 30.6264

Gaus Width: 3.6883

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch91, adc1

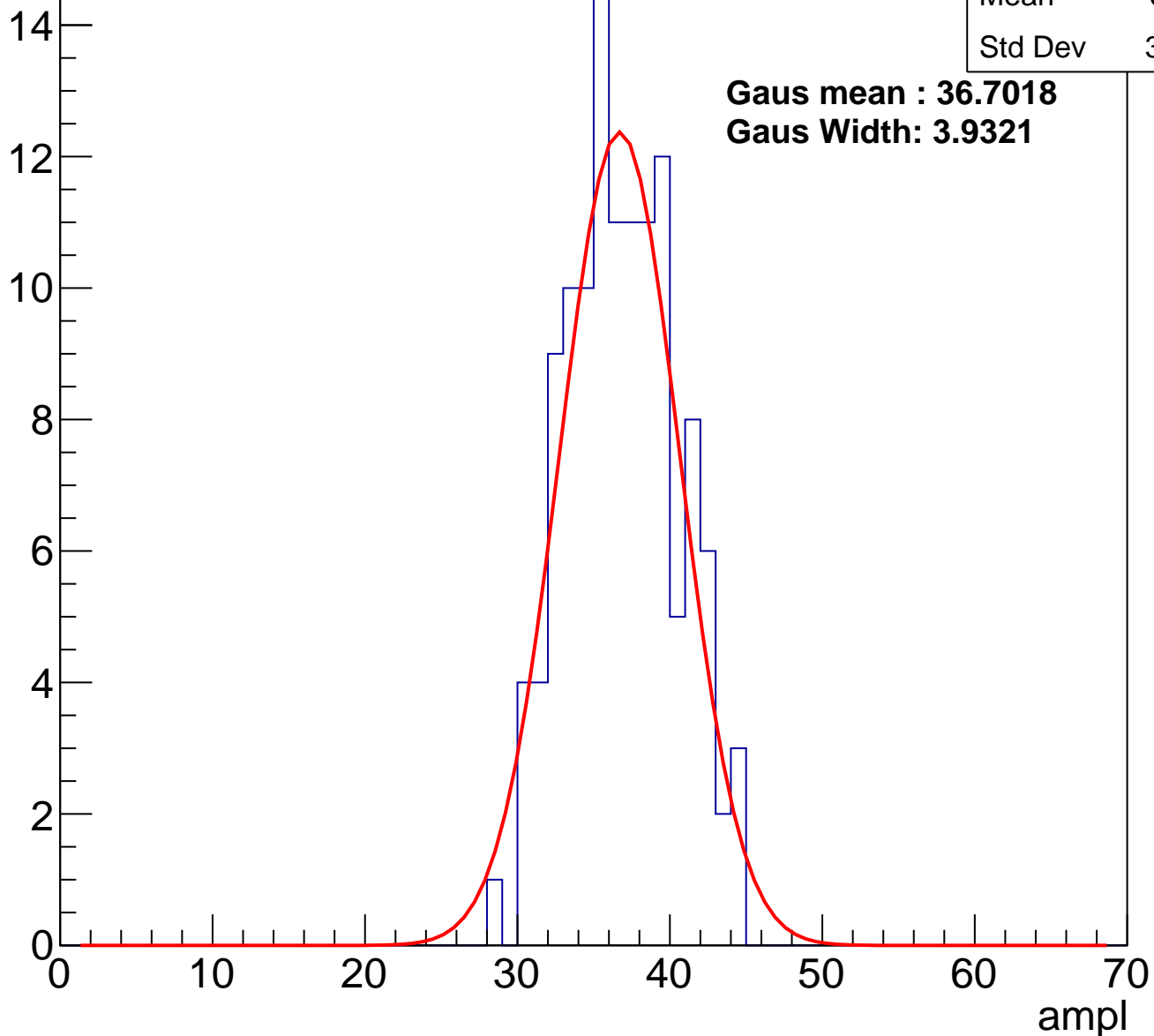
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.41 |
| Std Dev | 3.536 |

Gaus mean : 36.7018

Gaus Width: 3.9321

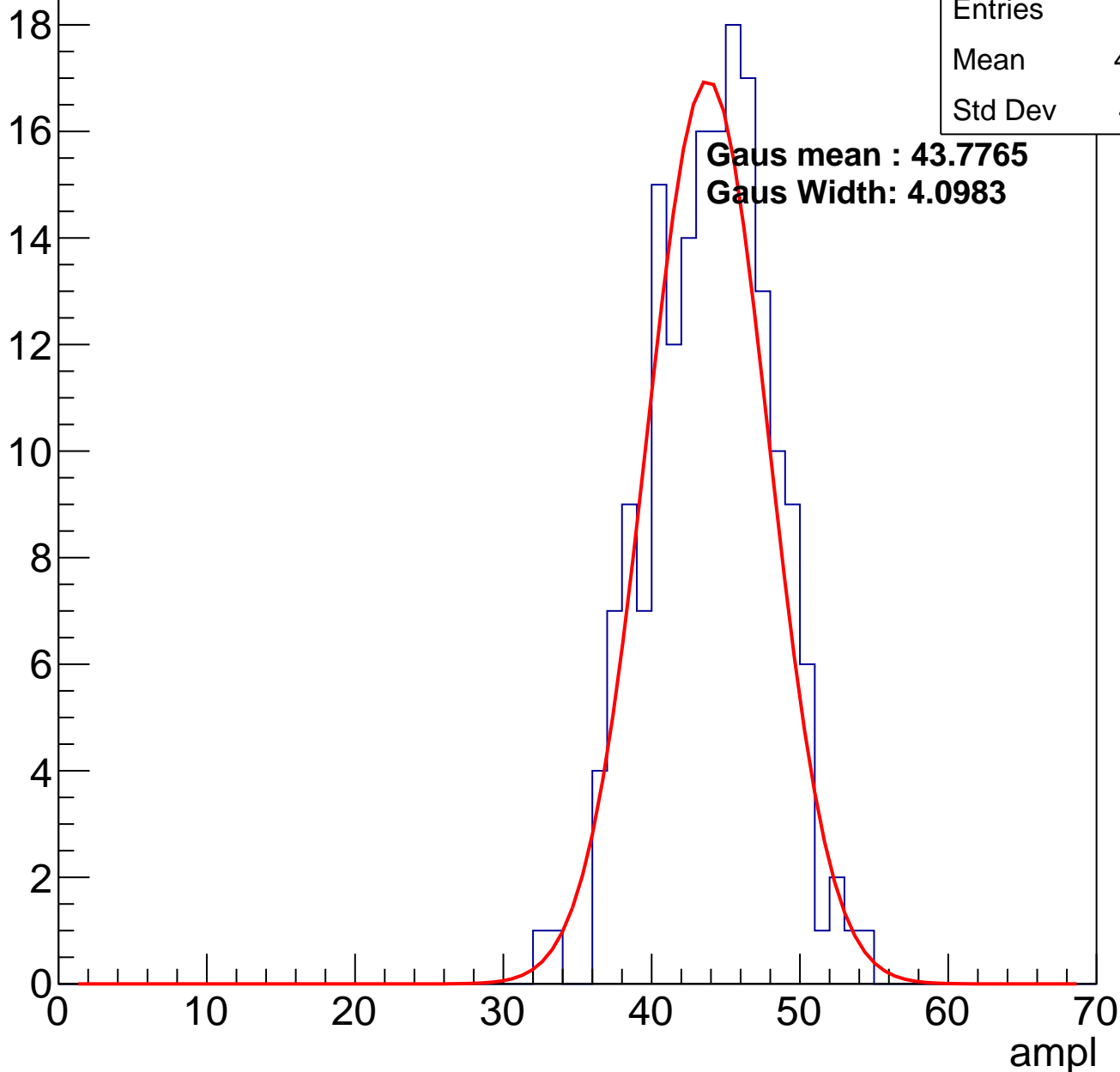
Entry



B0L101S, U22-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

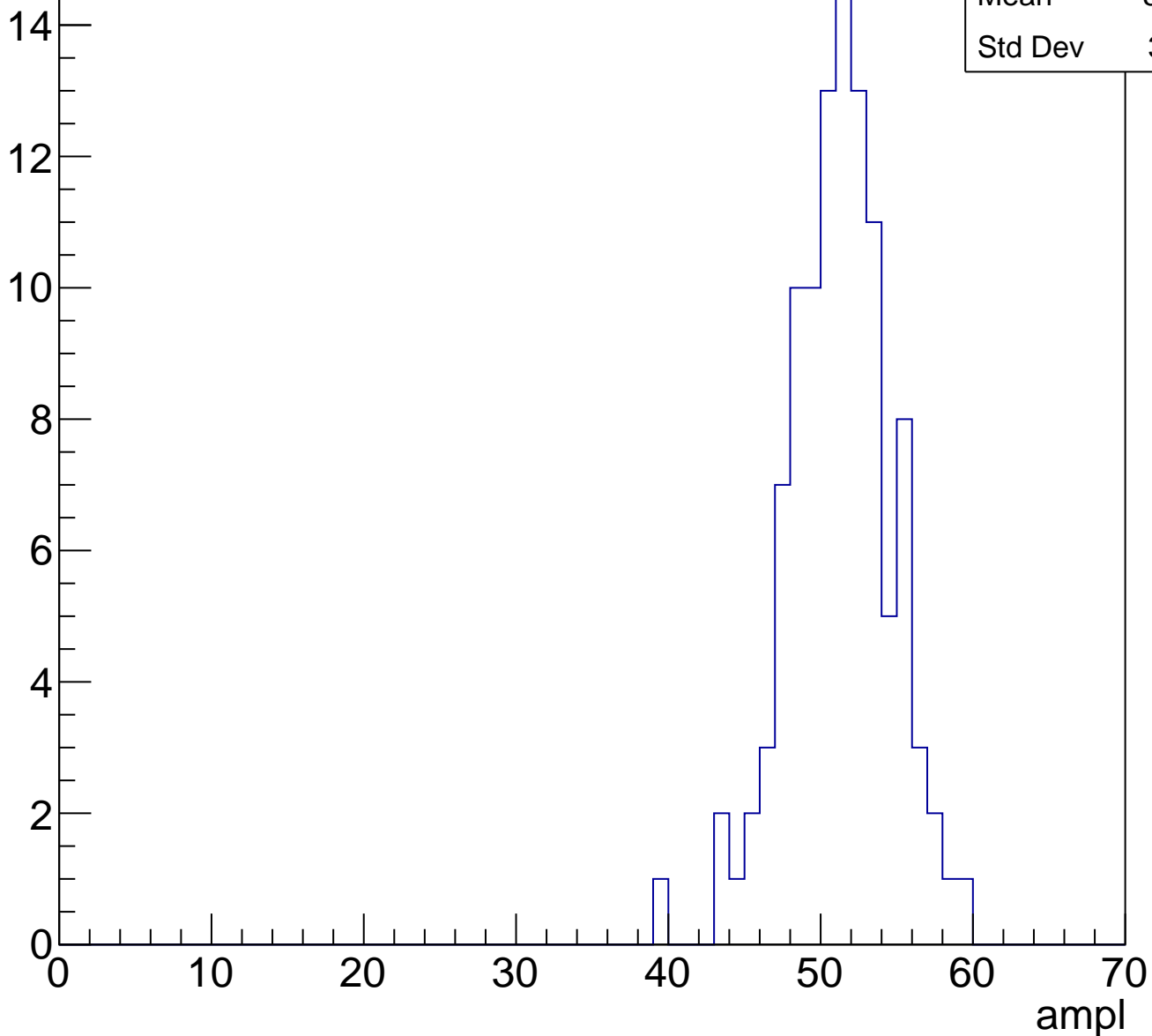


B0L101S, U22-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 108 |
| Mean | 50.73 |
| Std Dev | 3.341 |

Entry



B0L101S, U22-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

127

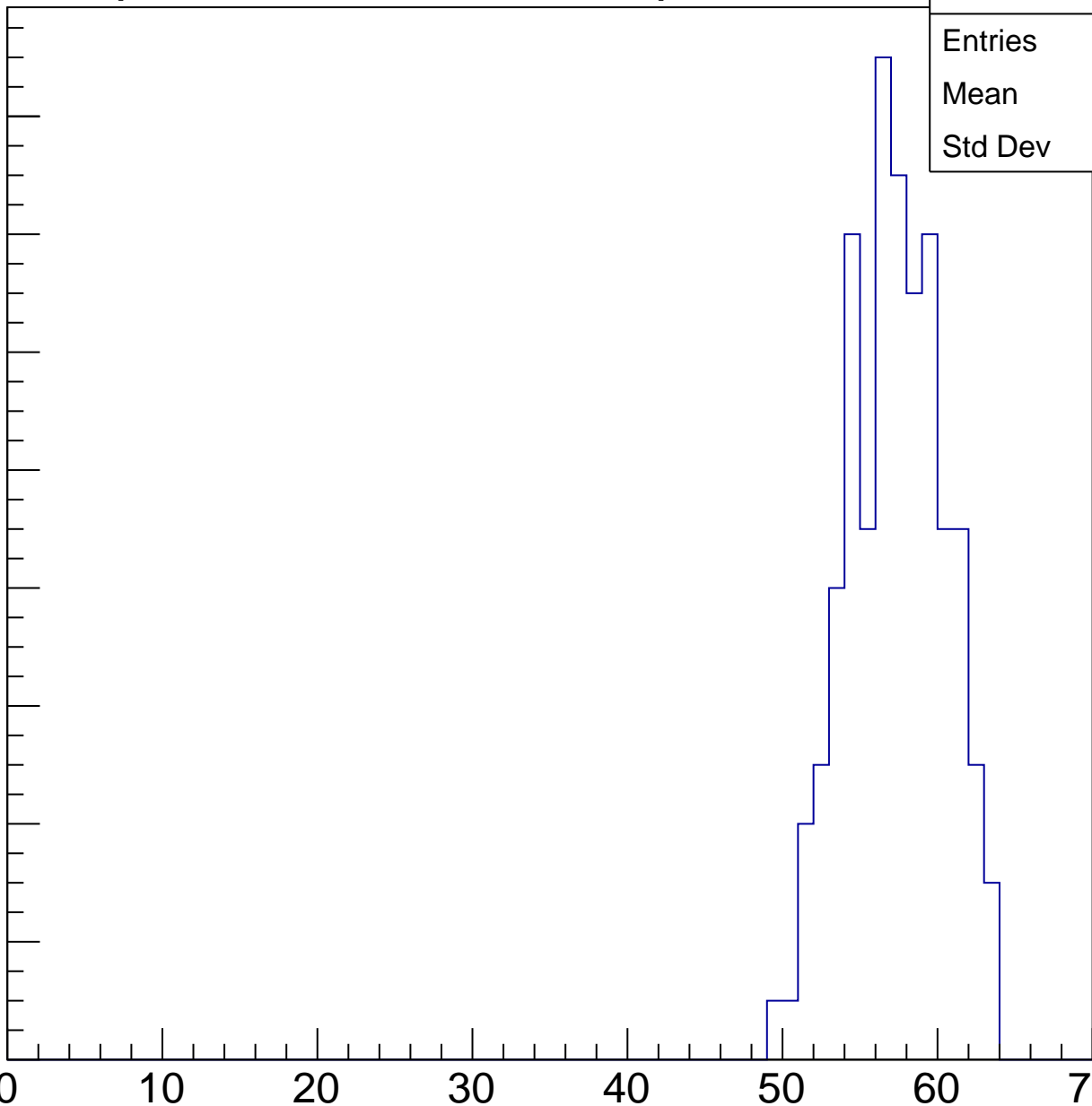
Mean

56.8

Std Dev

3.088

ampl



B0L101S, U22-ch91, adc5

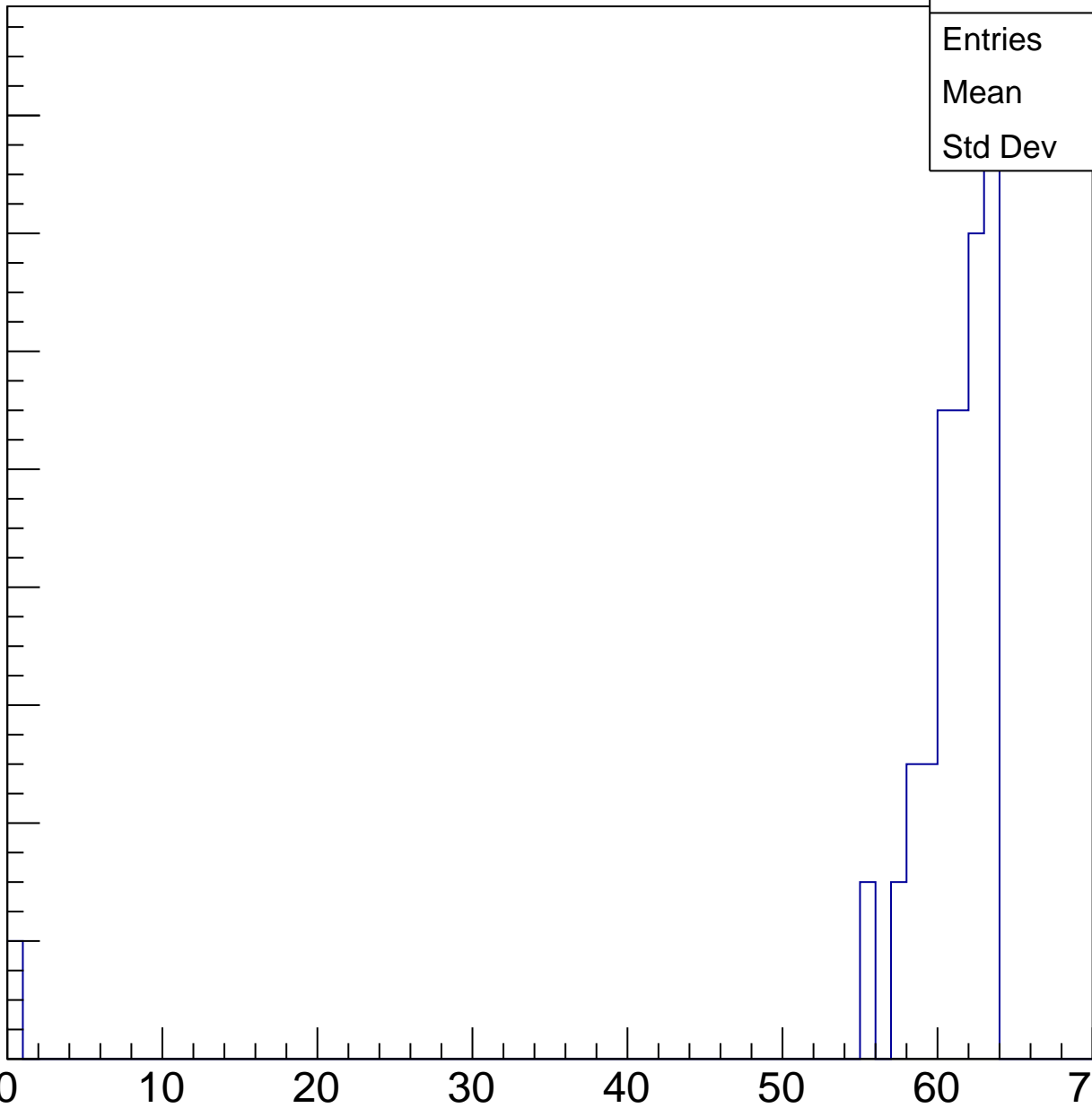
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 59.03 |
| Std Dev | 10.26 |

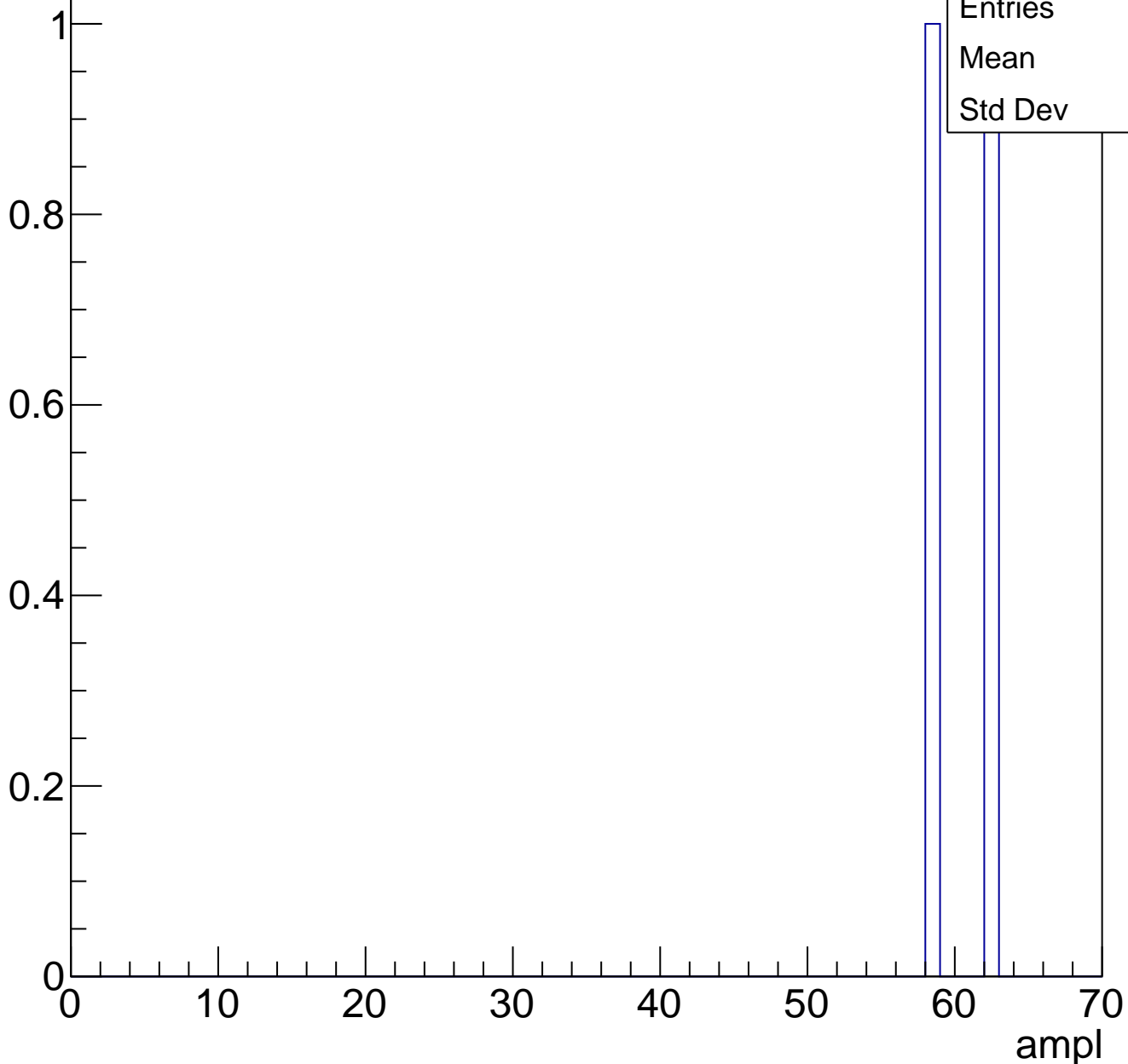
ampl



B0L101S, U22-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch92, adc0

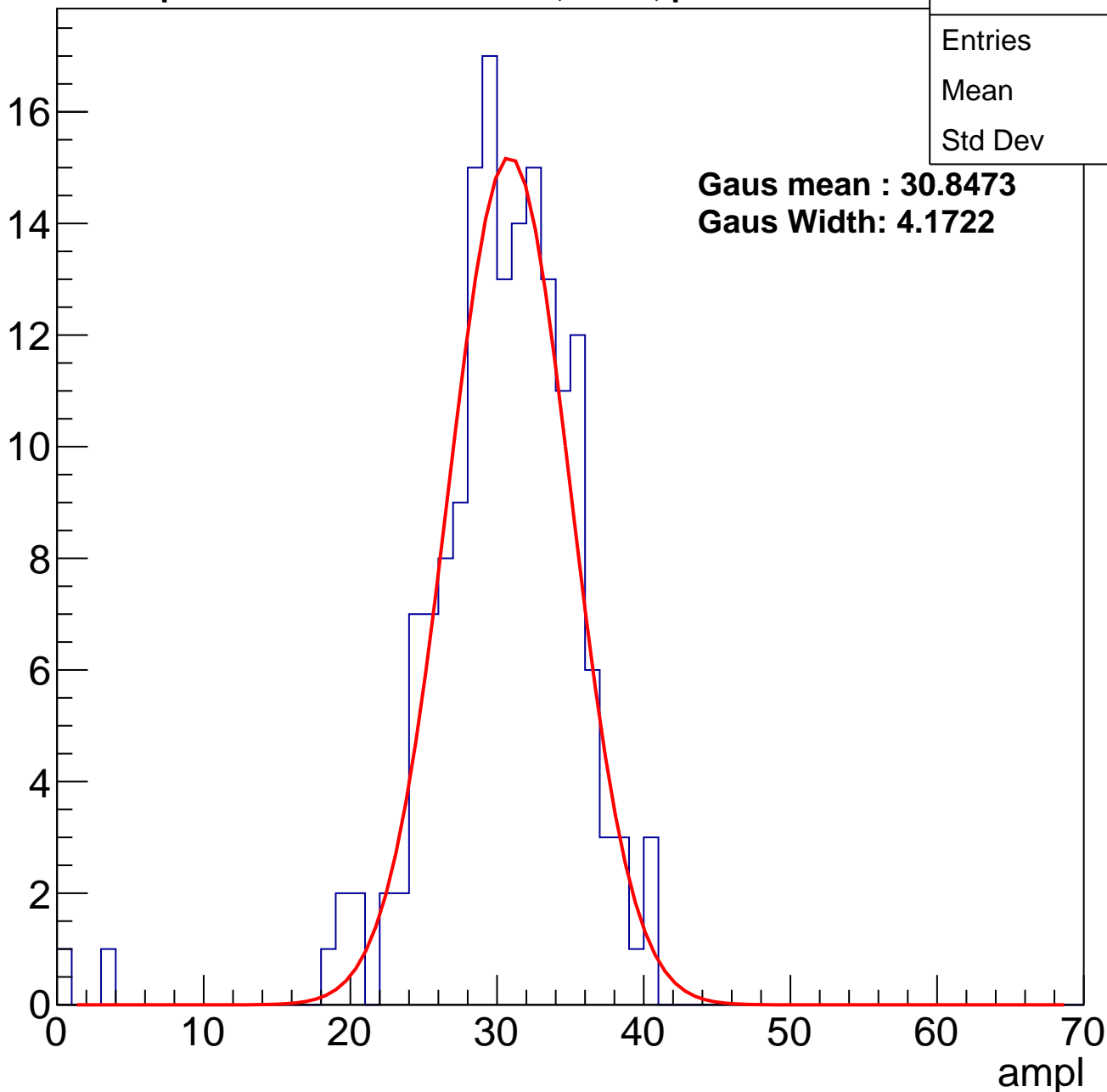
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 168 |
| Mean | 29.9 |
| Std Dev | 5.32 |

Gaus mean : 30.8473

Gaus Width: 4.1722

Entry



B0L101S, U22-ch92, adc1

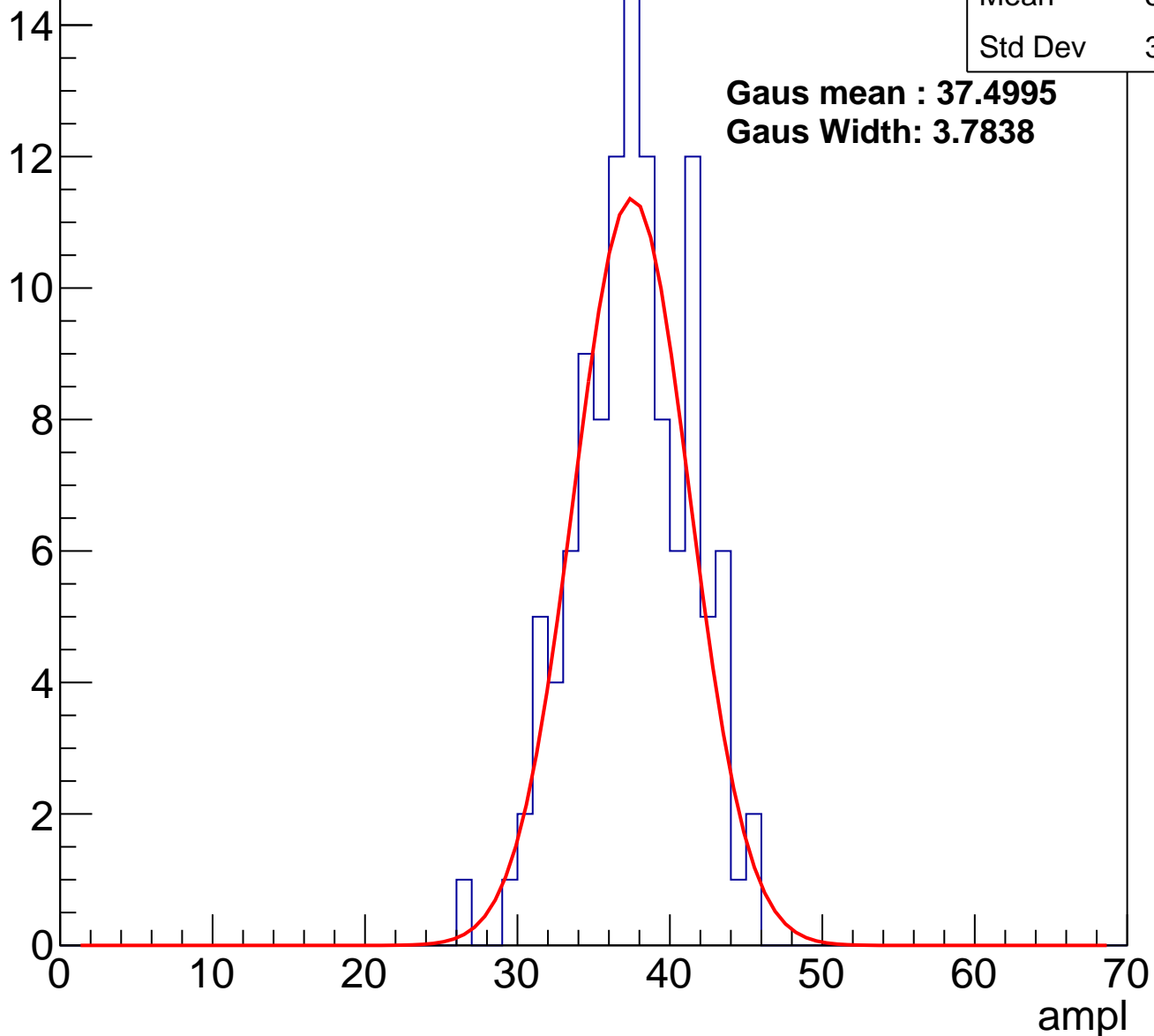
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 115 |
| Mean | 37.14 |
| Std Dev | 3.718 |

Gaus mean : 37.4995

Gaus Width: 3.7838

Entry



B0L101S, U22-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

160

Mean

44.03

Std Dev

5.115

Gaus mean : 44.8039

Gaus Width: 3.9105

0

10

20

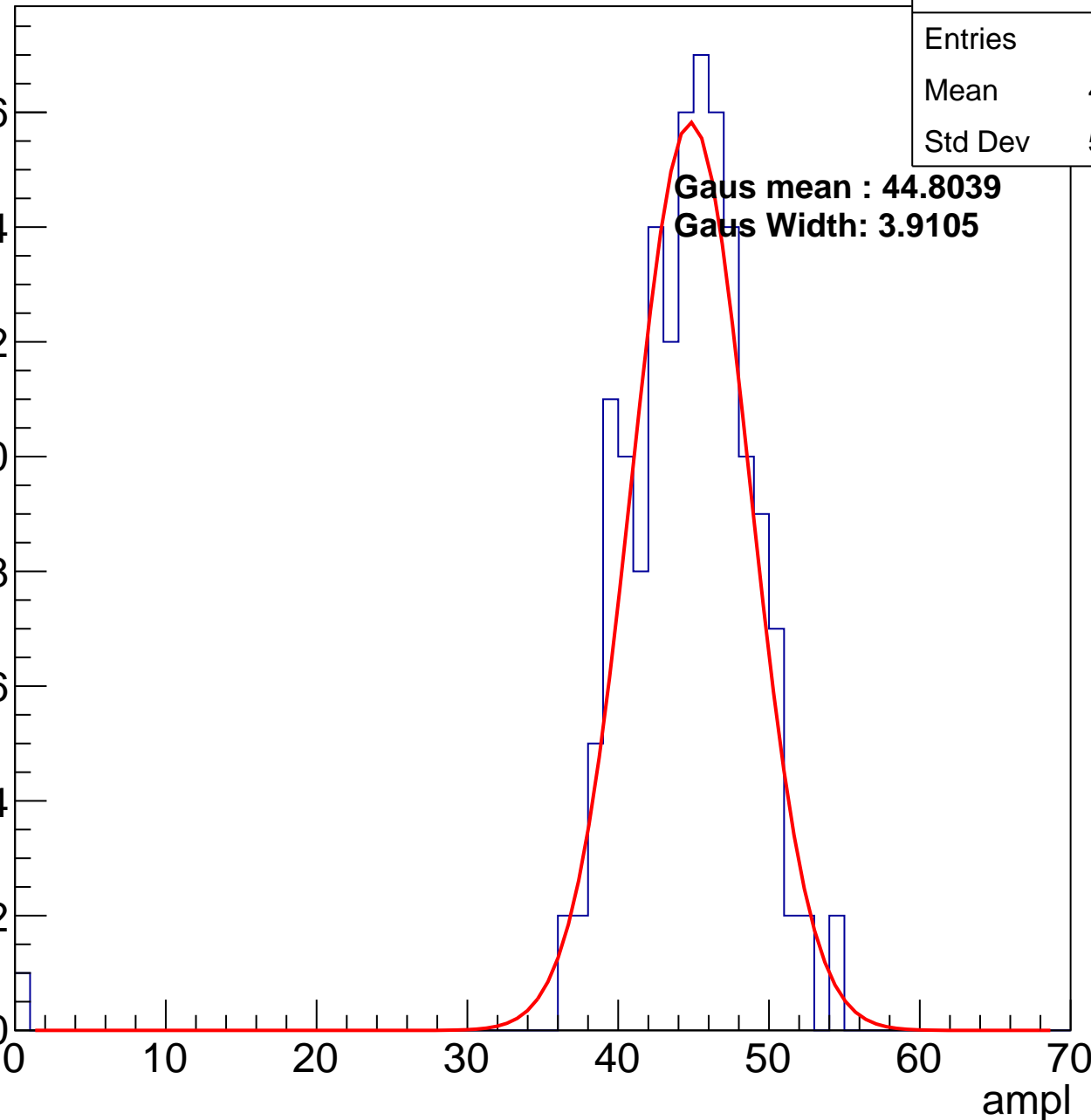
30

40

50

60

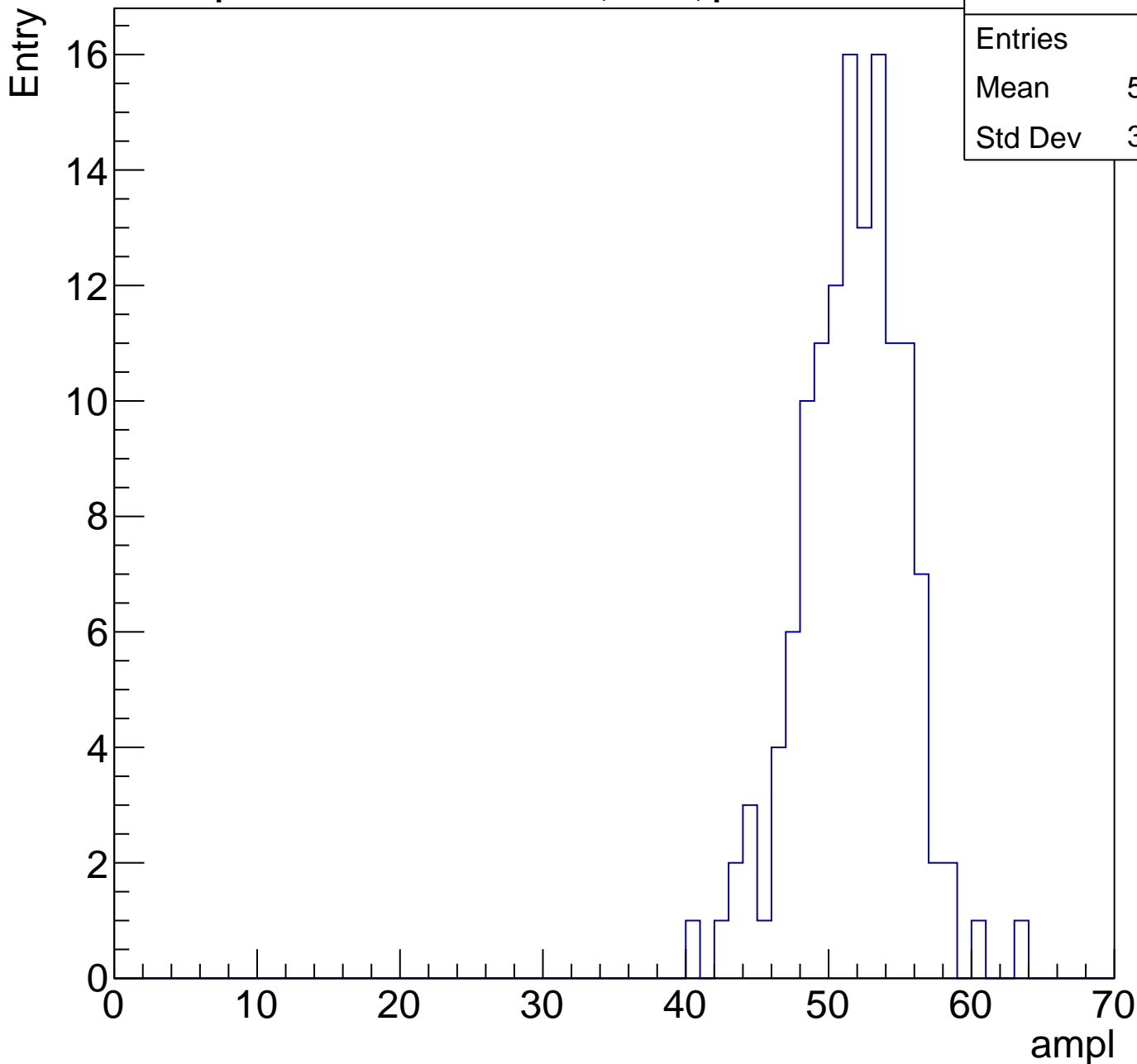
ampl



B0L101S, U22-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 131 |
| Mean | 51.25 |
| Std Dev | 3.674 |



B0L101S, U22-ch92, adc4

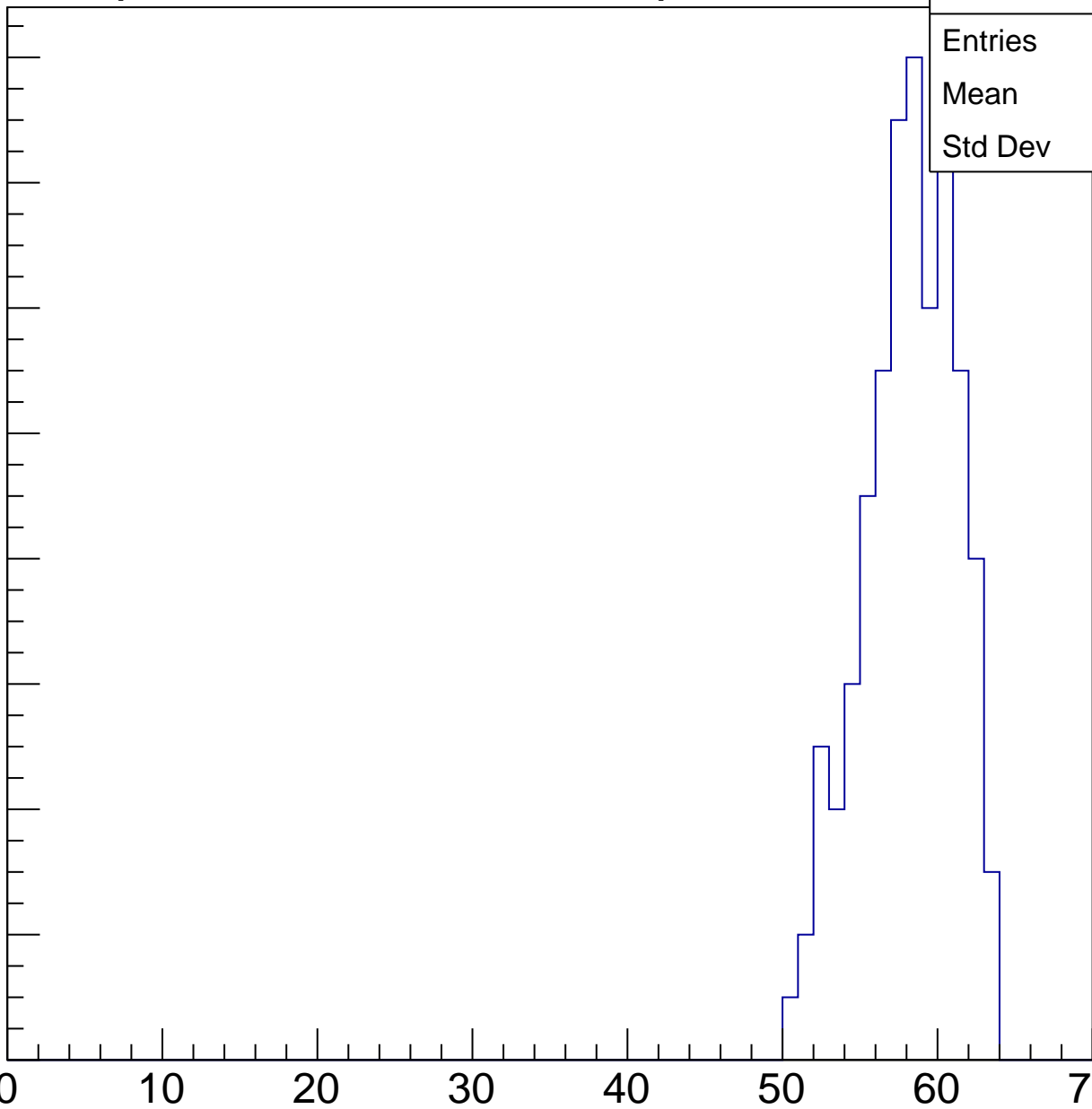
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 118 |
| Mean | 57.68 |
| Std Dev | 2.968 |

ampl



B0L101S, U22-ch92, adc5

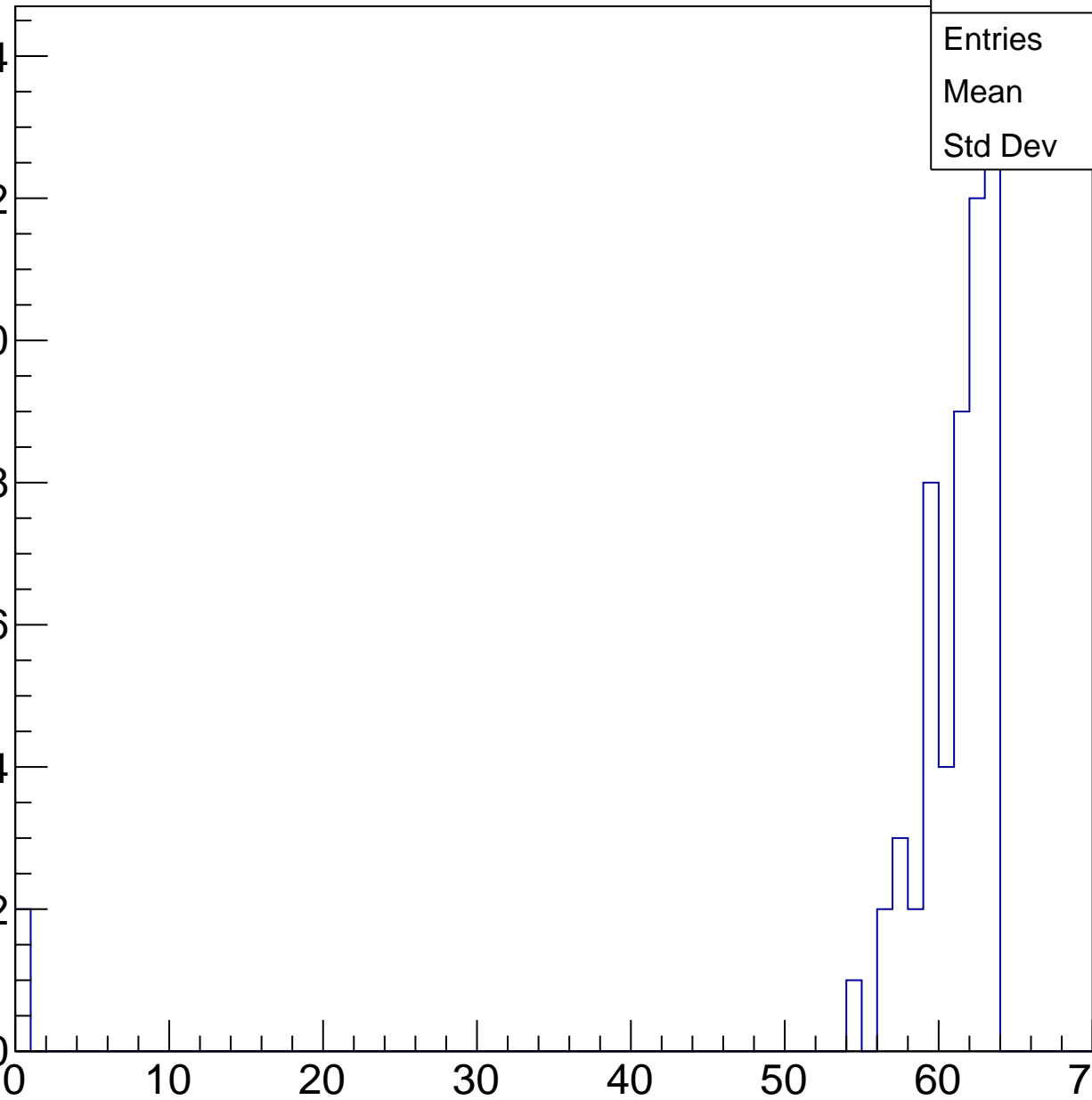
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 57 |
| Mean | 58.6 |
| Std Dev | 11.38 |

ampl



B0L101S, U22-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

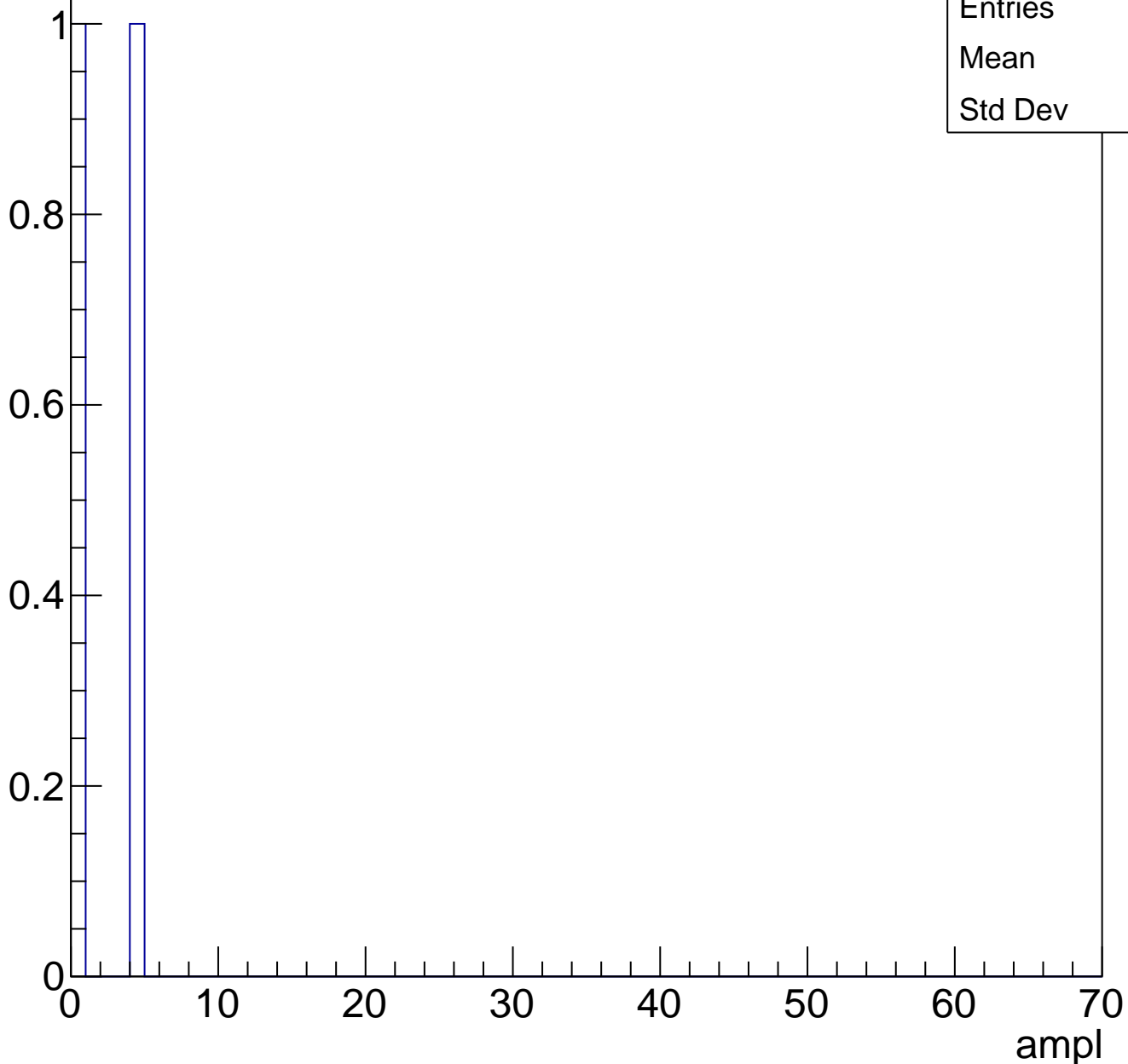
Entry



B0L101S, U22-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 2 |
| Std Dev | 2 |

B0L101S, U22-ch93, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 30.28 |
| Std Dev | 3.772 |

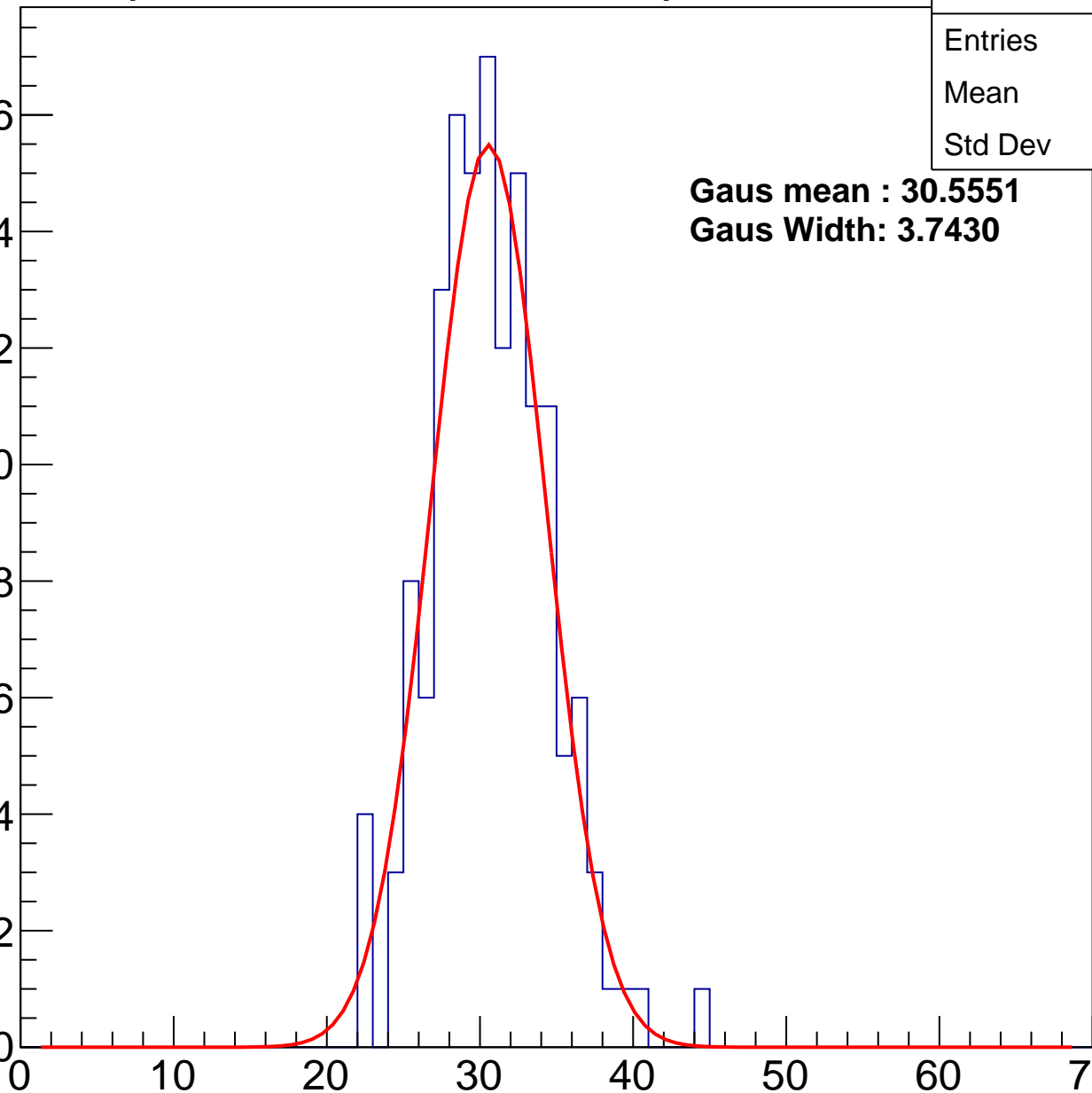
Gaus mean : 30.5551

Gaus Width: 3.7430

Entry

16
14
12
10
8
6
4
2
0

ampl

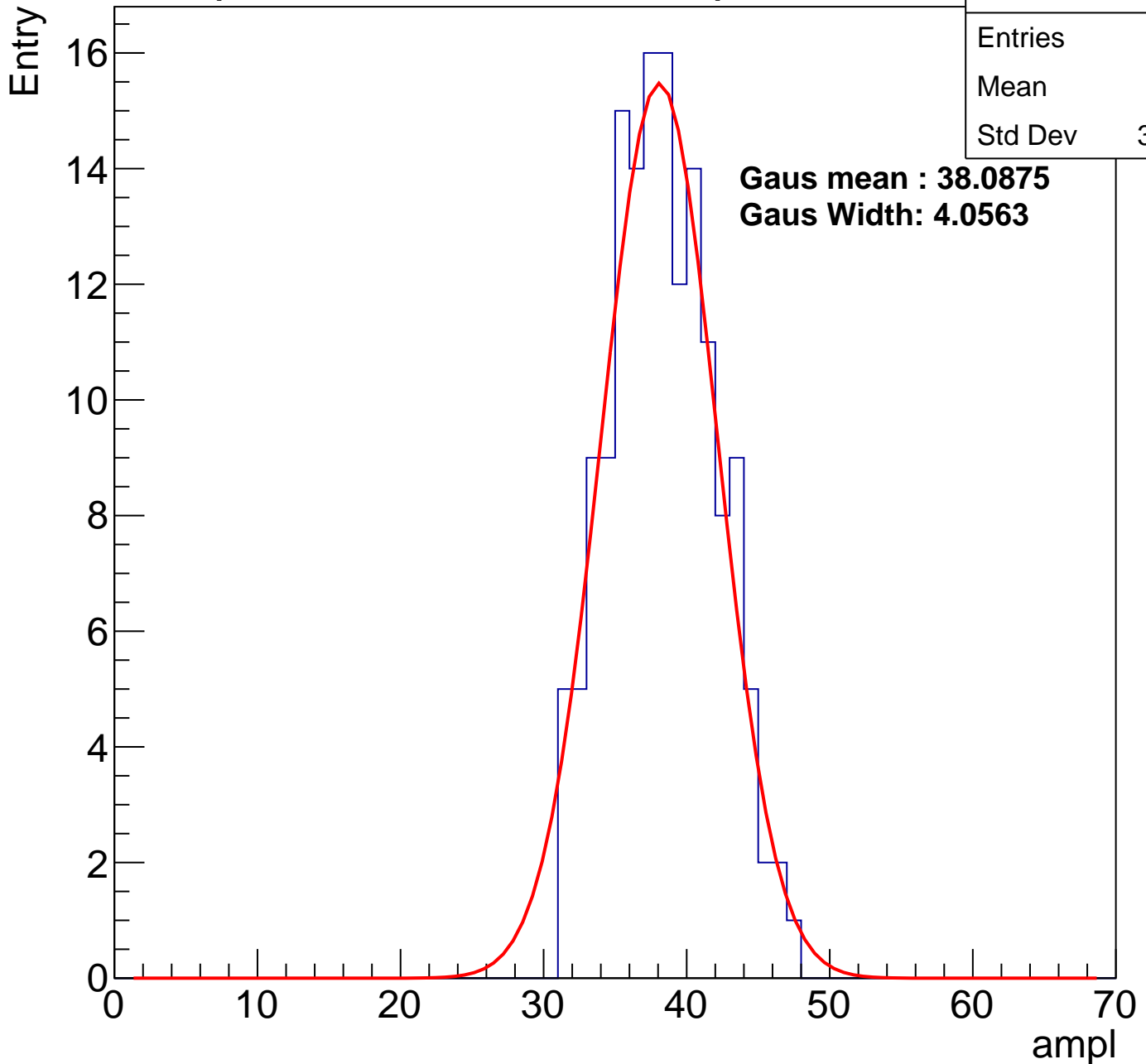


B0L101S, U22-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 153 |
| Mean | 37.9 |
| Std Dev | 3.621 |

Gaus mean : 38.0875
Gaus Width: 4.0563



B0L101S, U22-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries 121

Mean 44.57

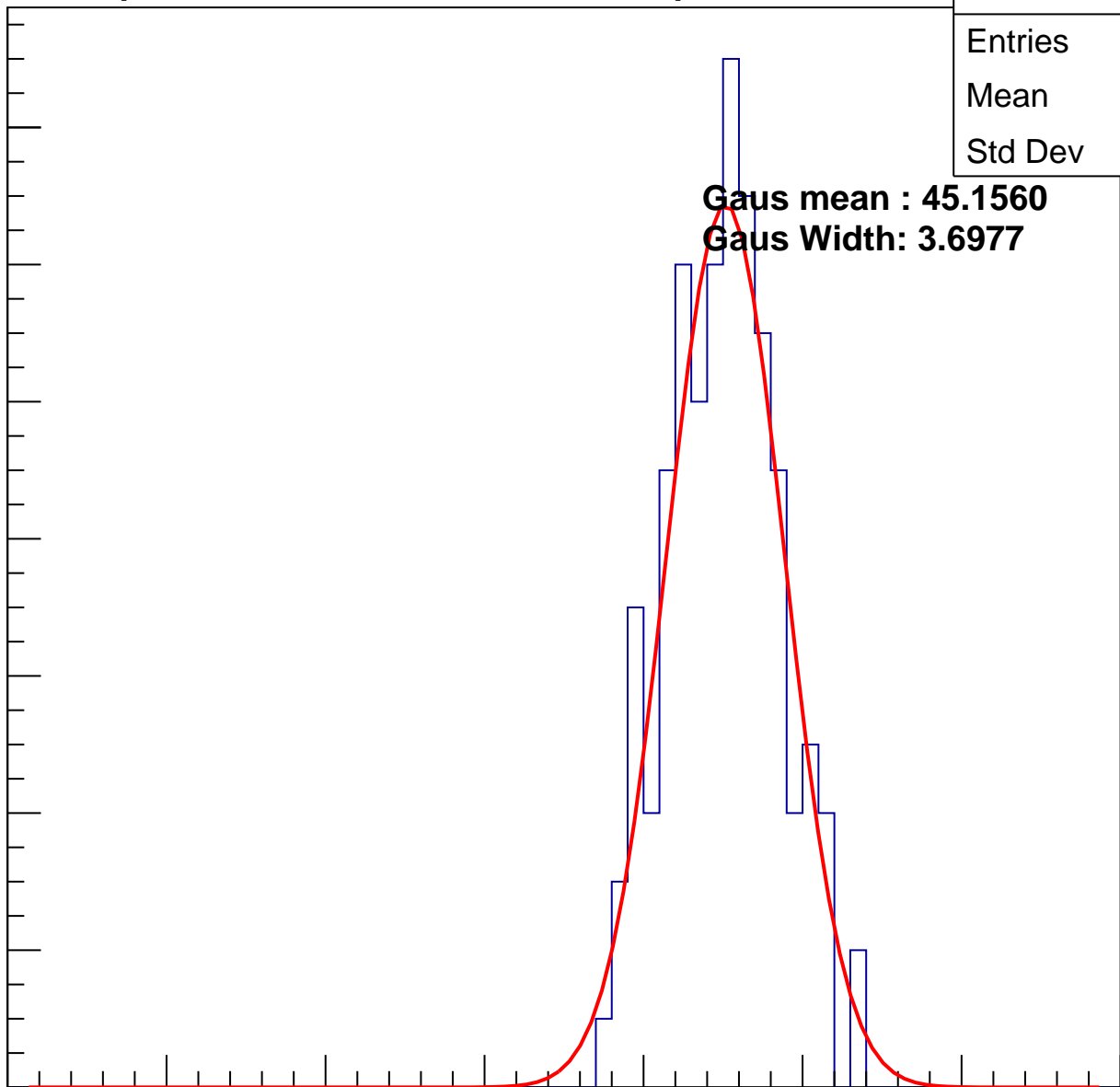
Std Dev 3.454

Gaus mean : 45.1560

Gaus Width: 3.6977

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch93, adc3

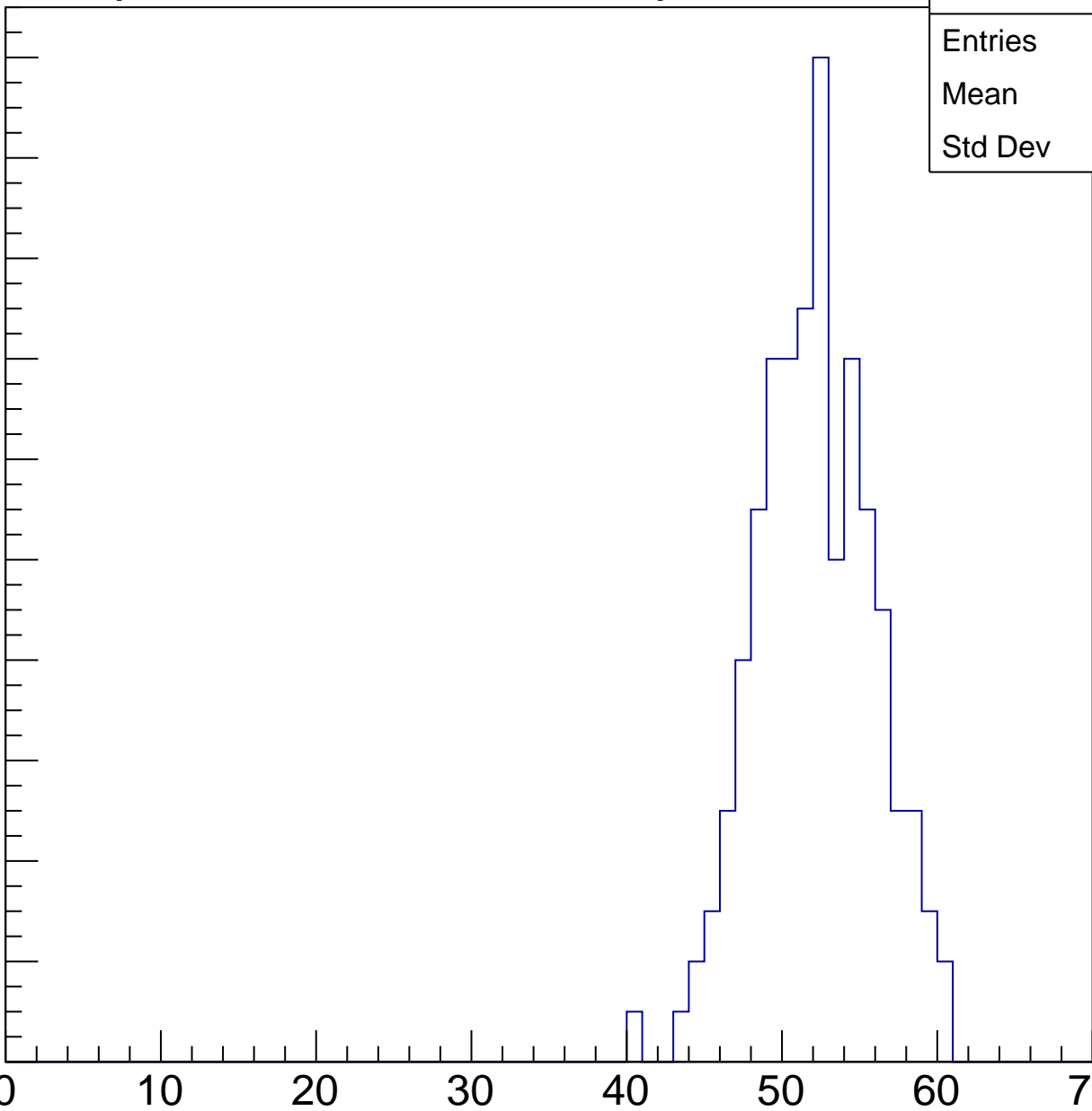
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 153 |
| Mean | 51.62 |
| Std Dev | 3.722 |

ampl

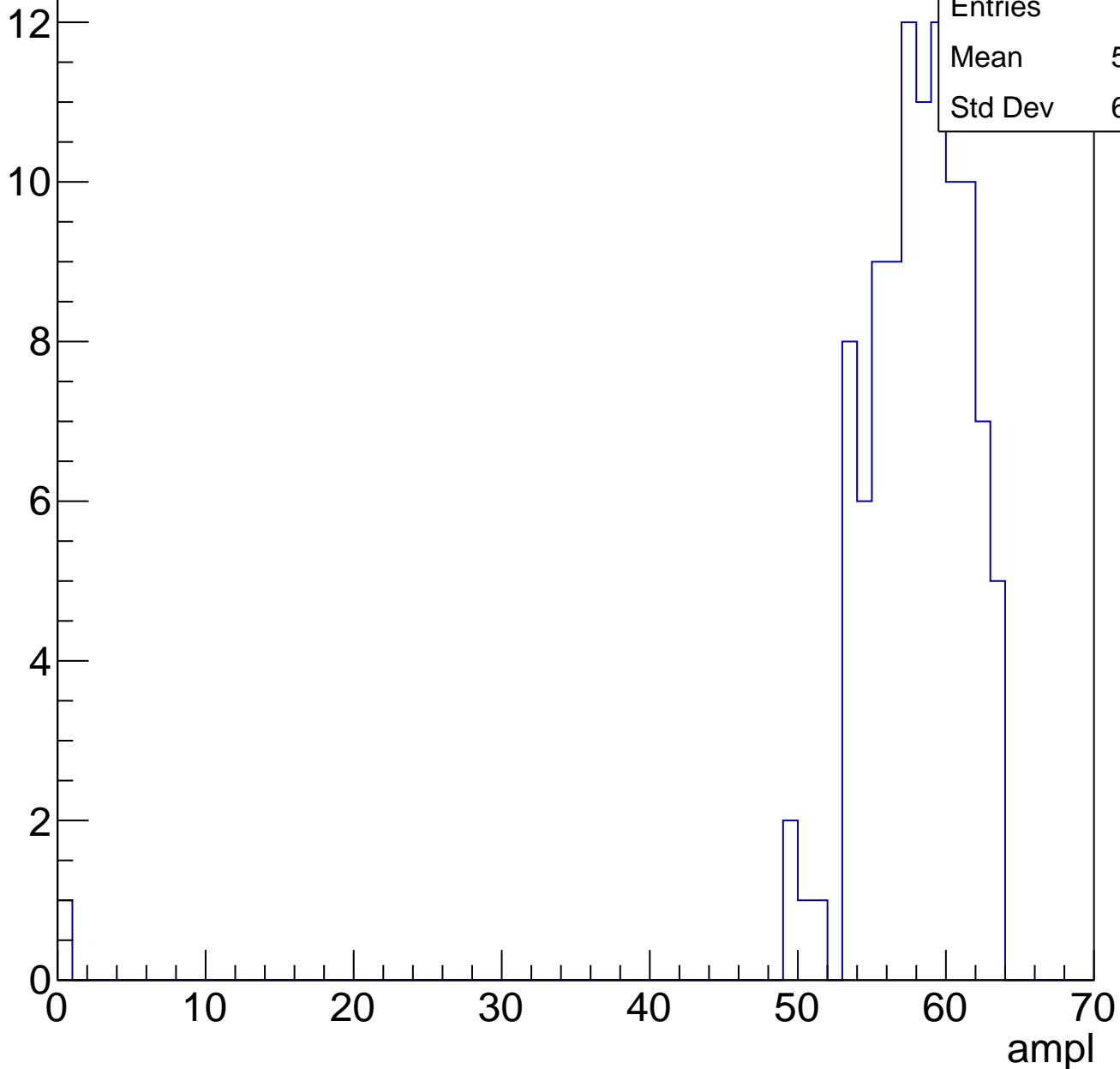


B0L101S, U22-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 104 |
| Mean | 57.07 |
| Std Dev | 6.469 |

Entry



B0L101S, U22-ch93, adc5

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 57 |
| Mean | 59.77 |
| Std Dev | 8.225 |

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

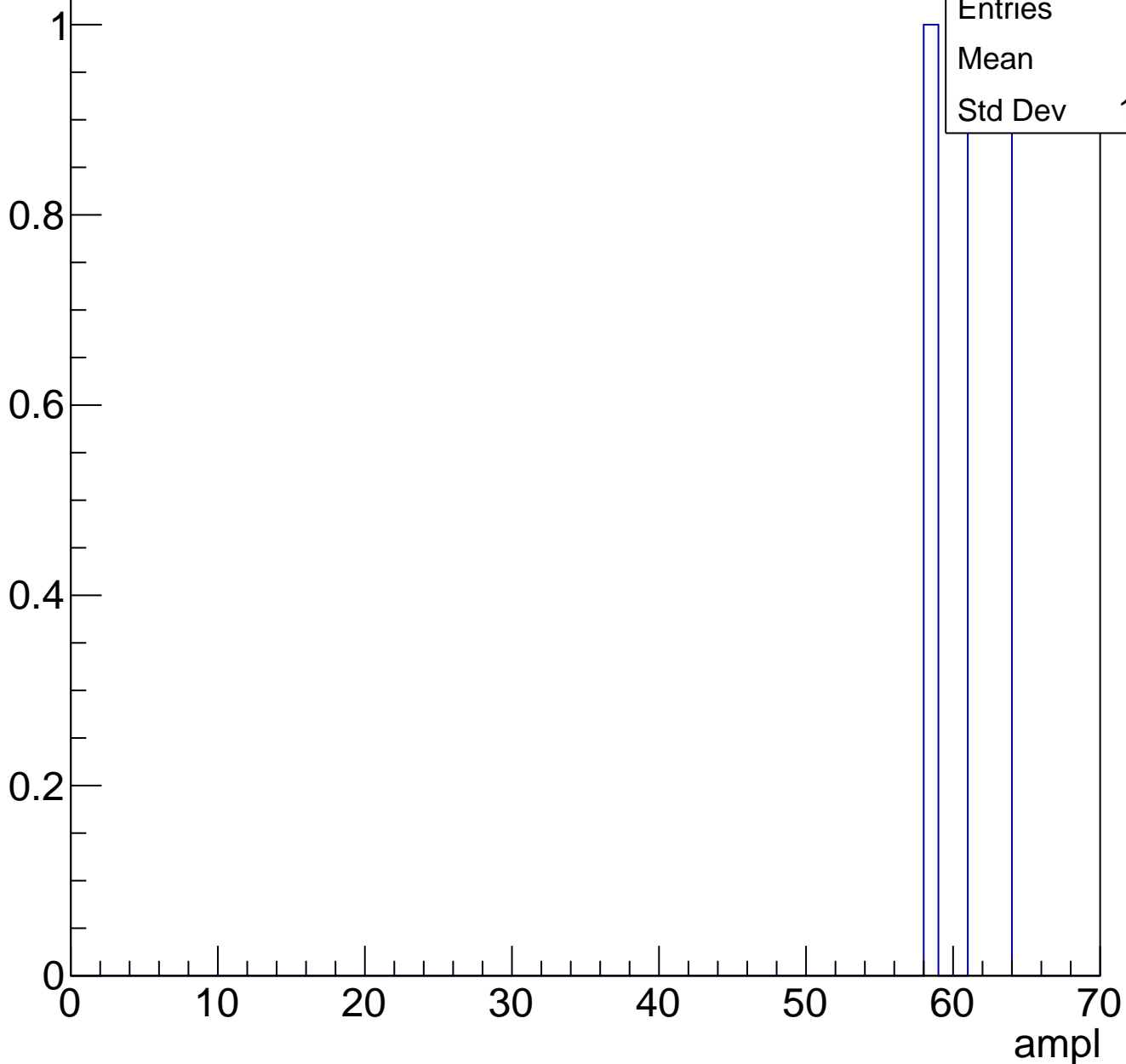
60

70

B0L101S, U22-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 26.42 |
| Std Dev | 4.763 |

Gaus mean : 27.3732

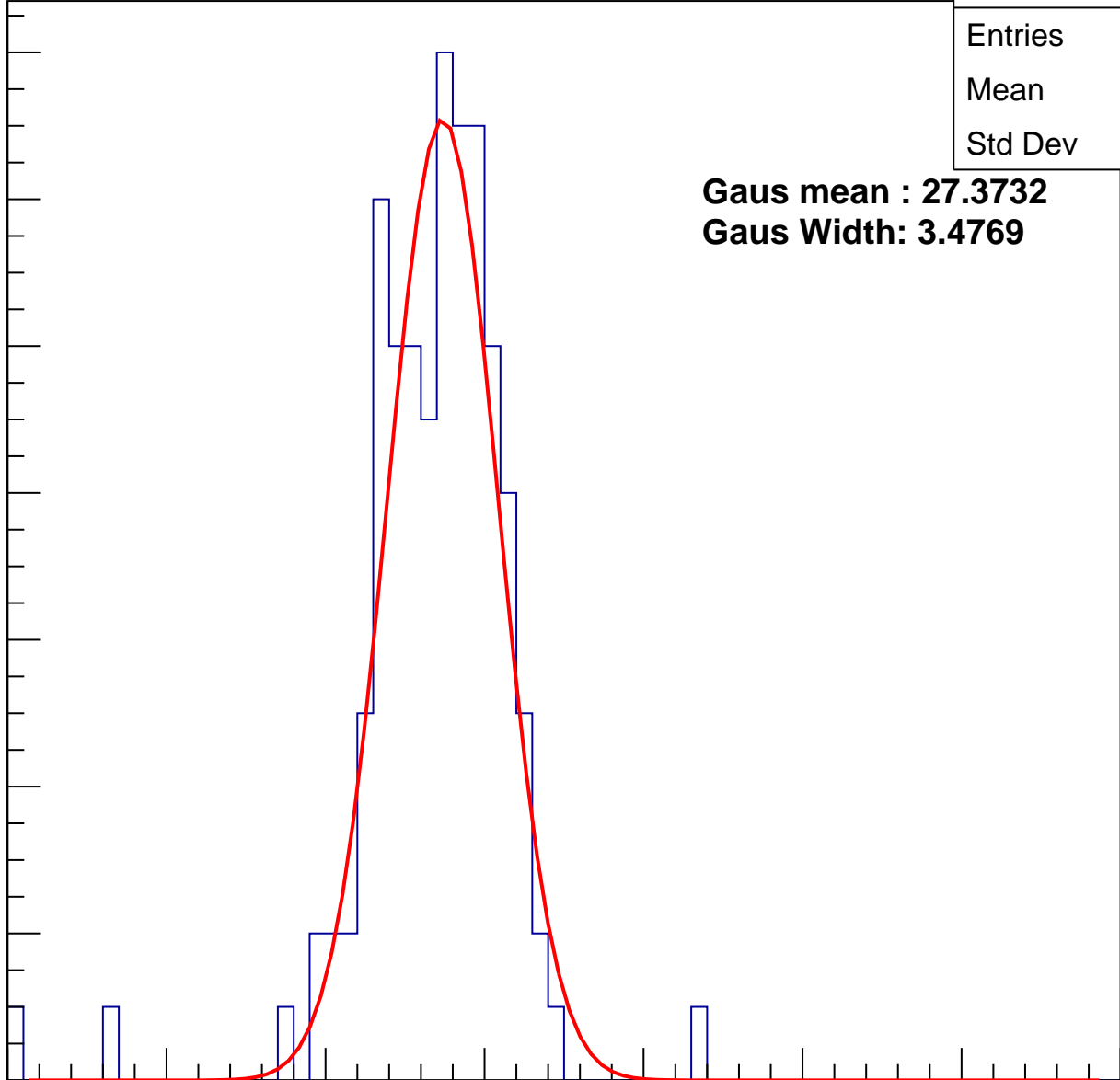
Gaus Width: 3.4769

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch94, adc1

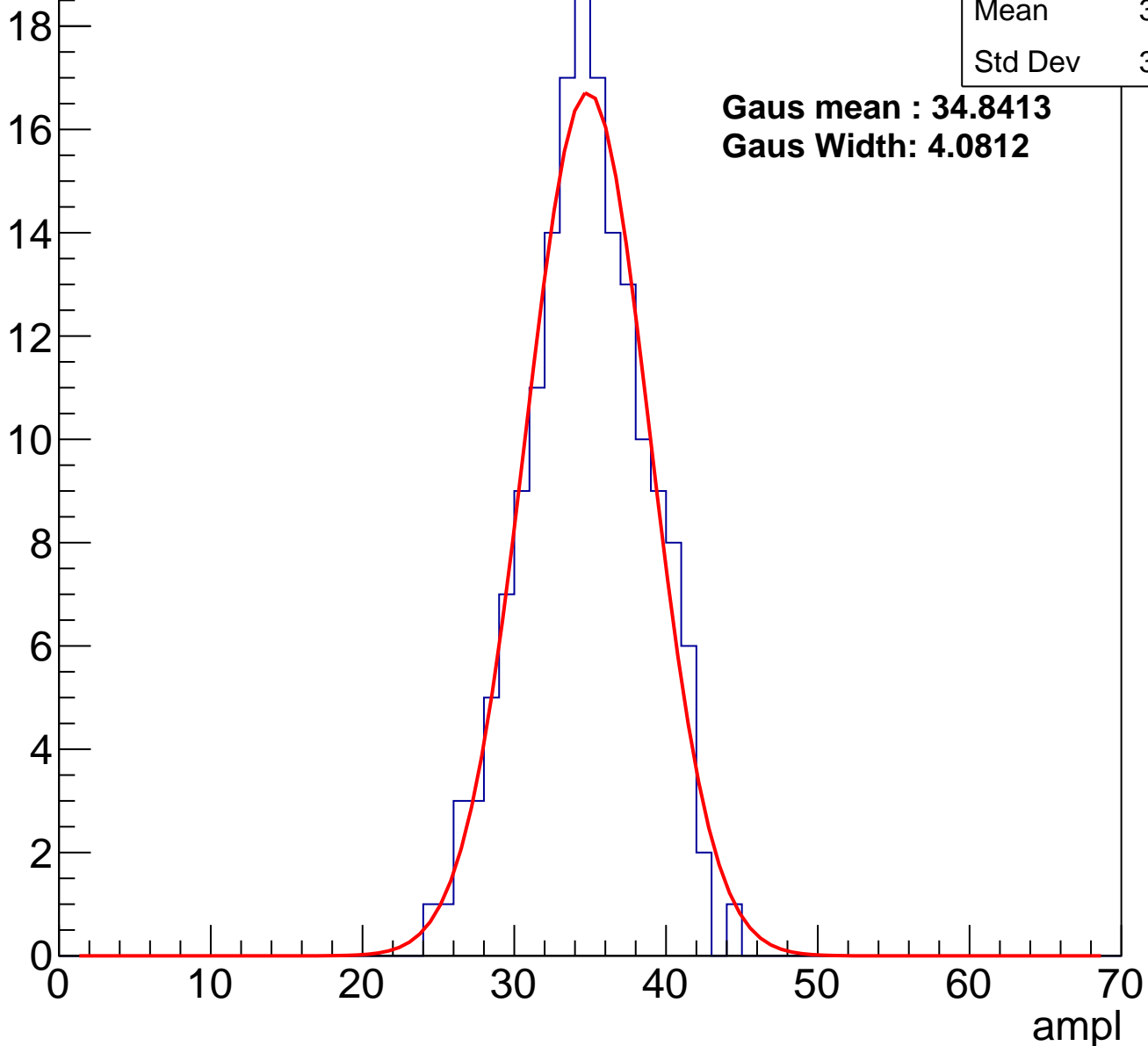
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 170 |
| Mean | 34.25 |
| Std Dev | 3.885 |

Gaus mean : 34.8413

Gaus Width: 4.0812

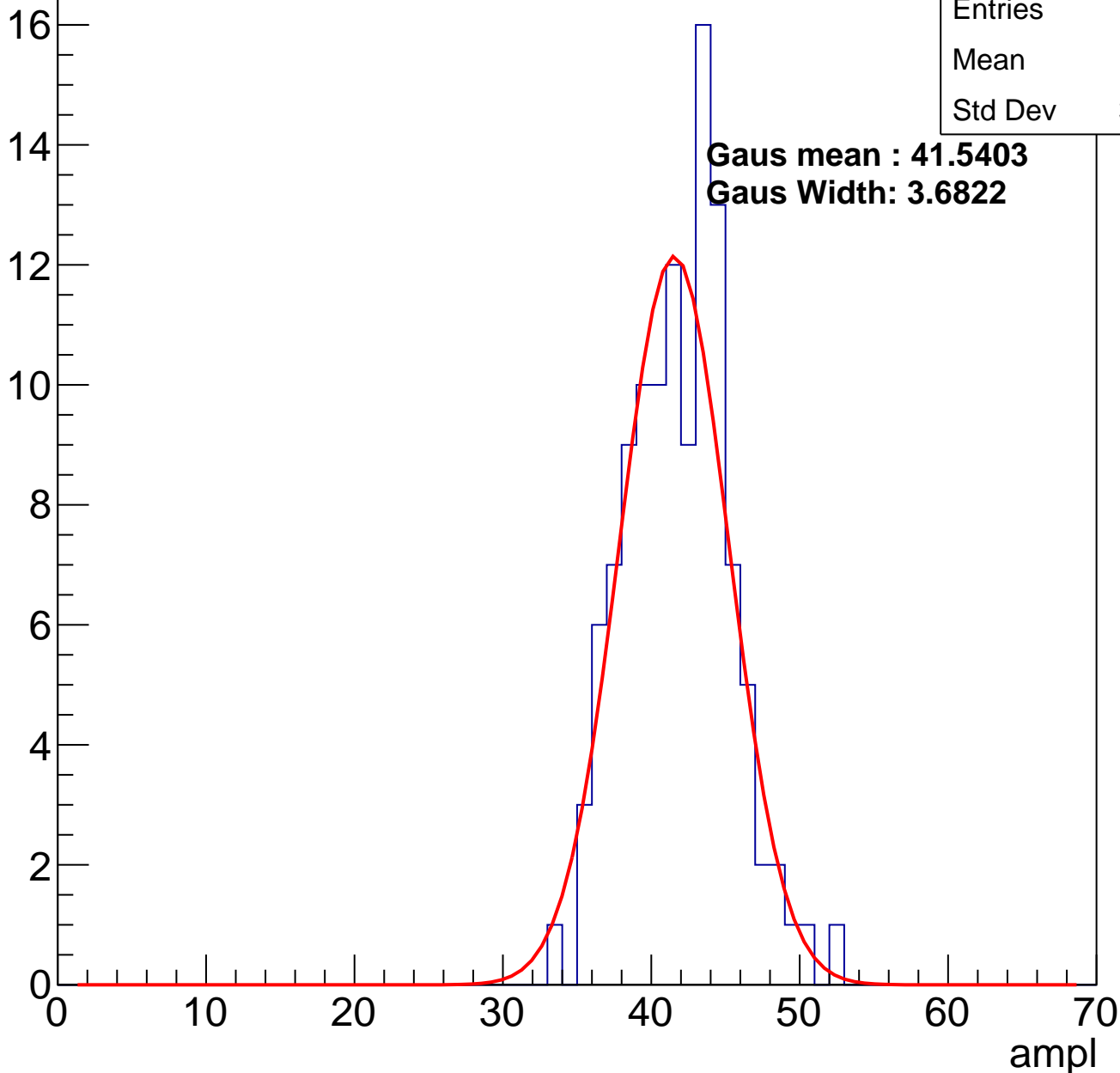
Entry



B0L101S, U22-ch94, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

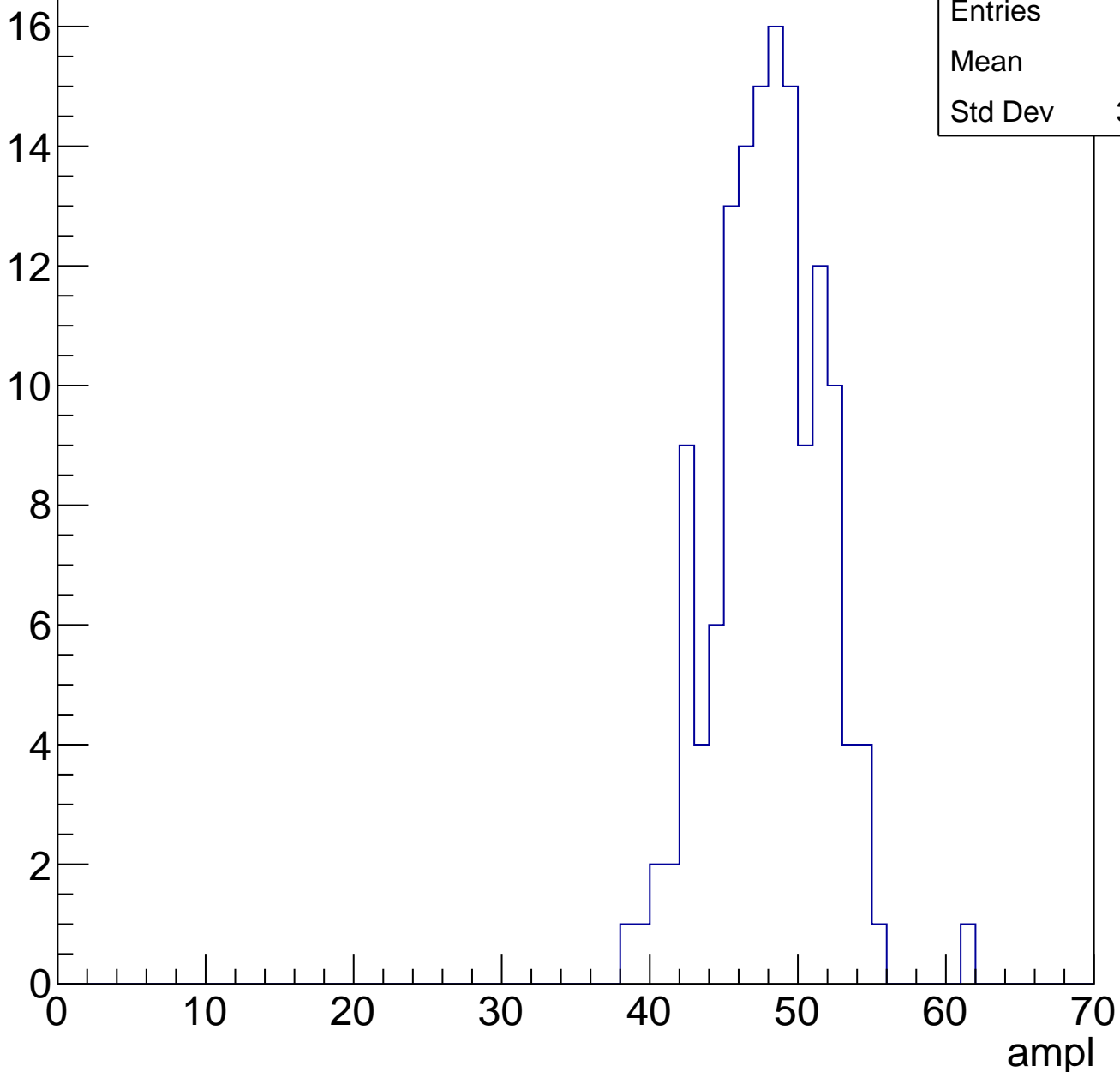


B0L101S, U22-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 139 |
| Mean | 47.6 |
| Std Dev | 3.671 |

Entry



B0L101S, U22-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

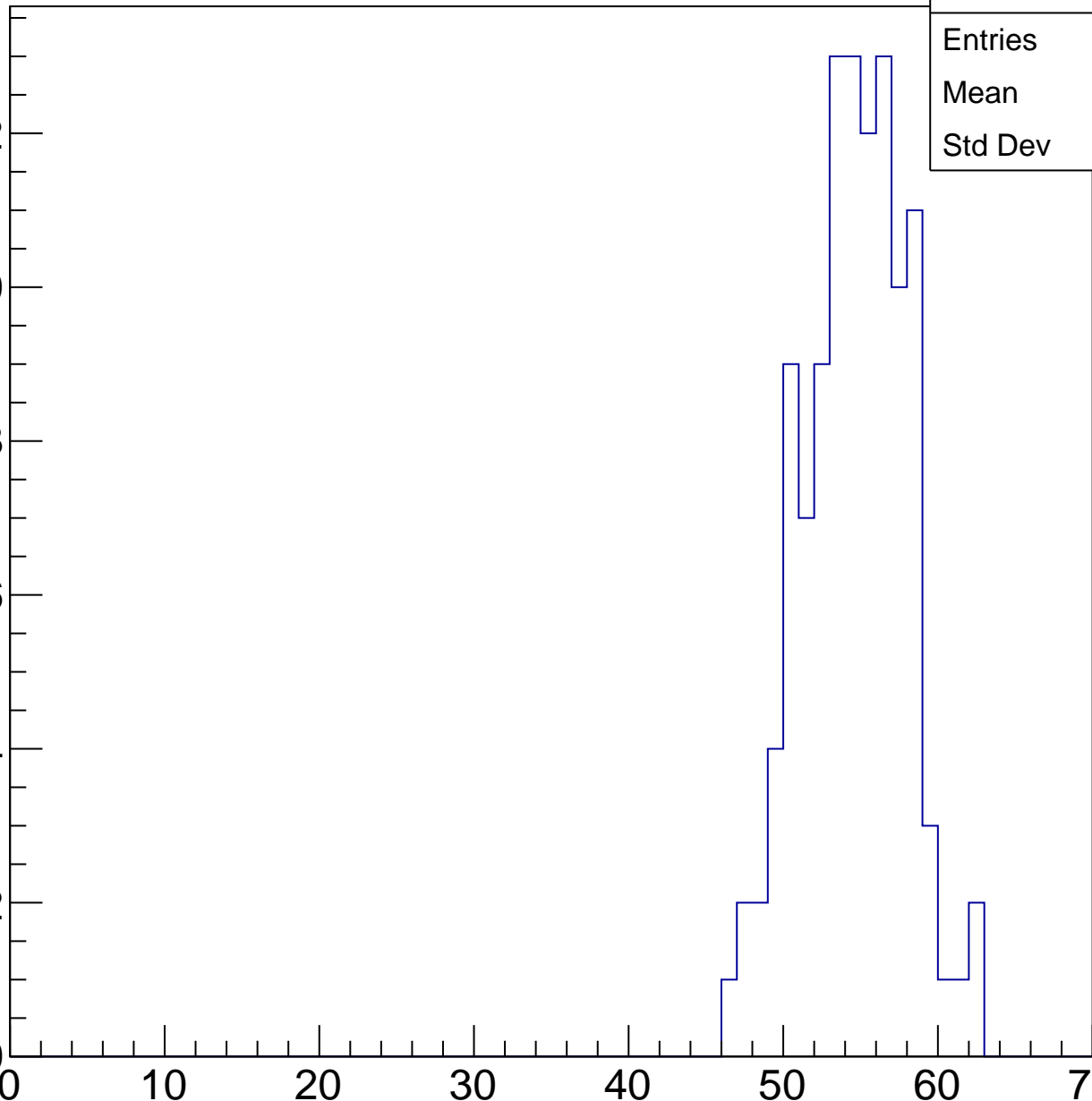
40

50

60

ampl

| | |
|---------|-------|
| Entries | 113 |
| Mean | 54.12 |
| Std Dev | 3.267 |



B0L101S, U22-ch94, adc5

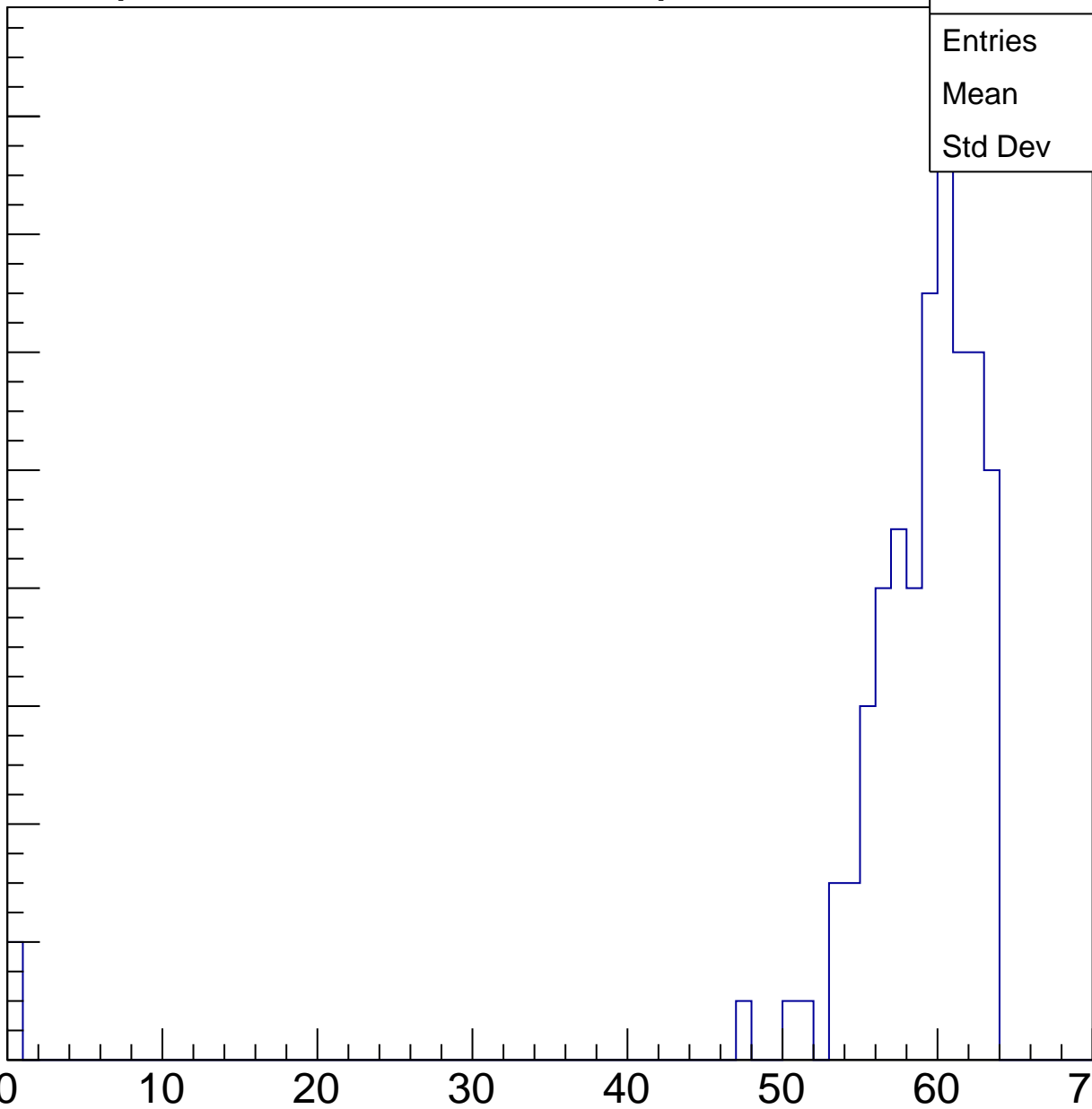
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 106 |
| Mean | 57.71 |
| Std Dev | 8.579 |

ampl

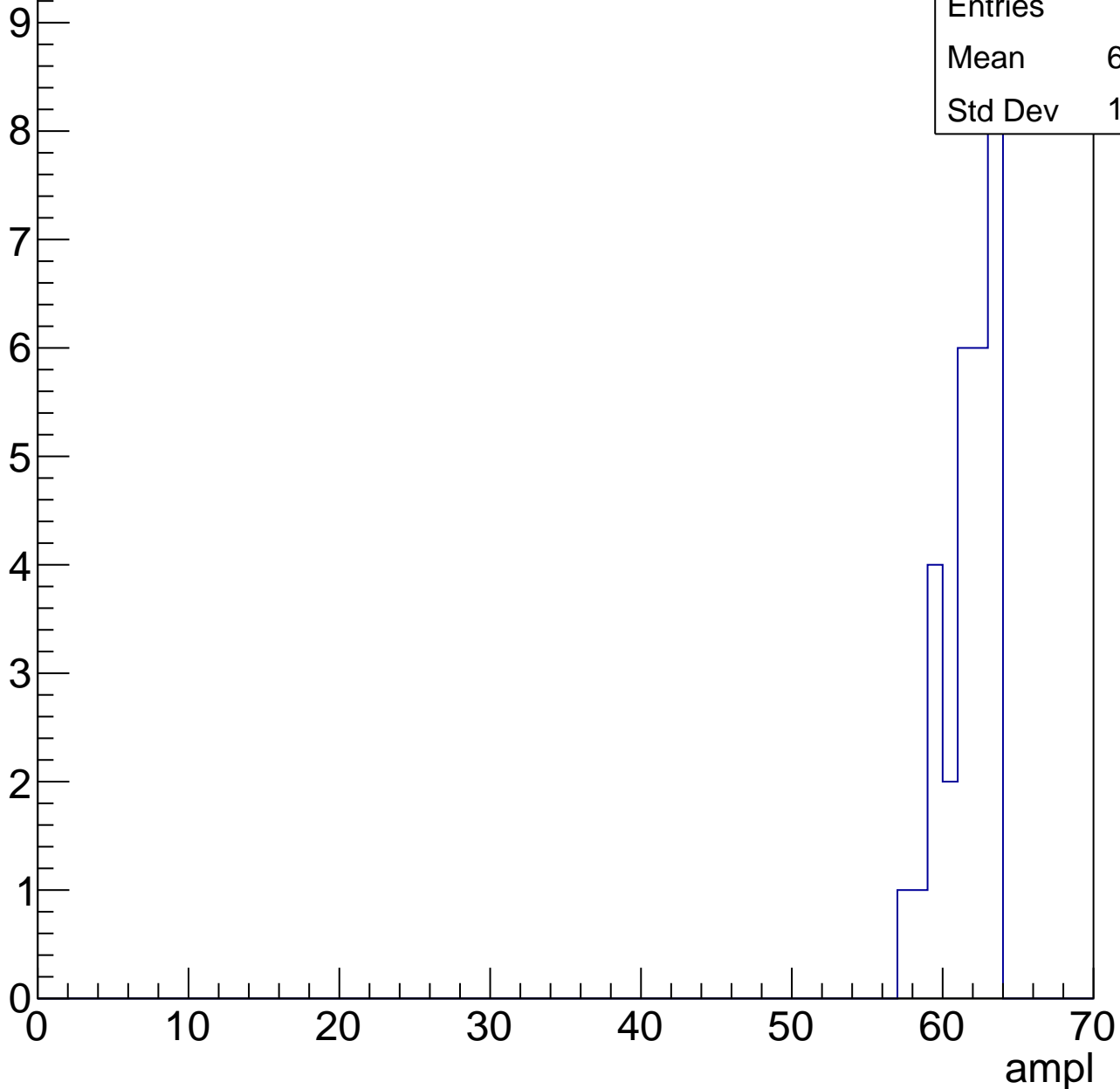


B0L101S, U22-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 61.24 |
| Std Dev | 1.695 |



B0L101S, U22-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch95, adc0

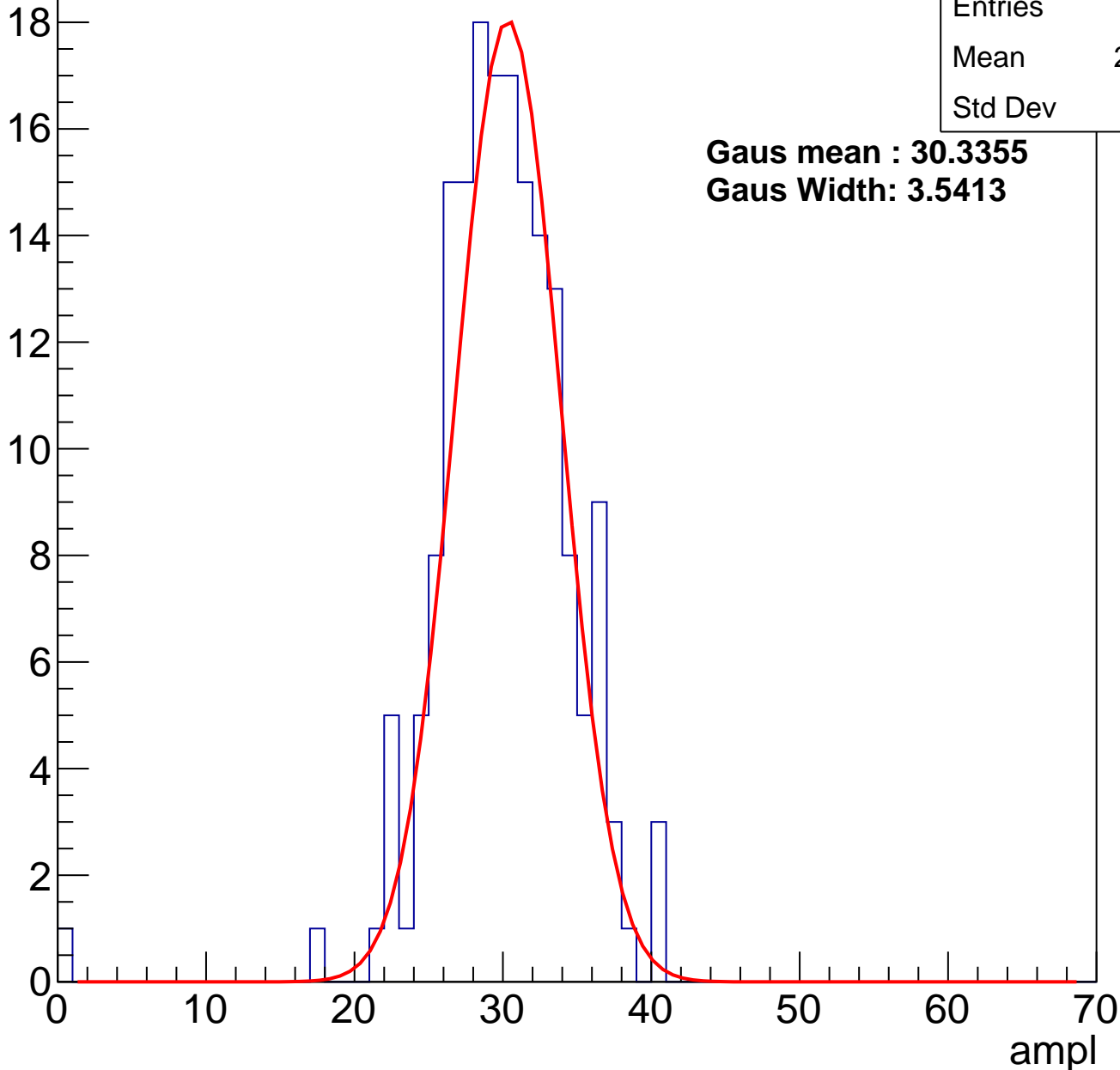
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 175 |
| Mean | 29.57 |
| Std Dev | 4.54 |

Gaus mean : 30.3355

Gaus Width: 3.5413

Entry



B0L101S, U22-ch95, adc1

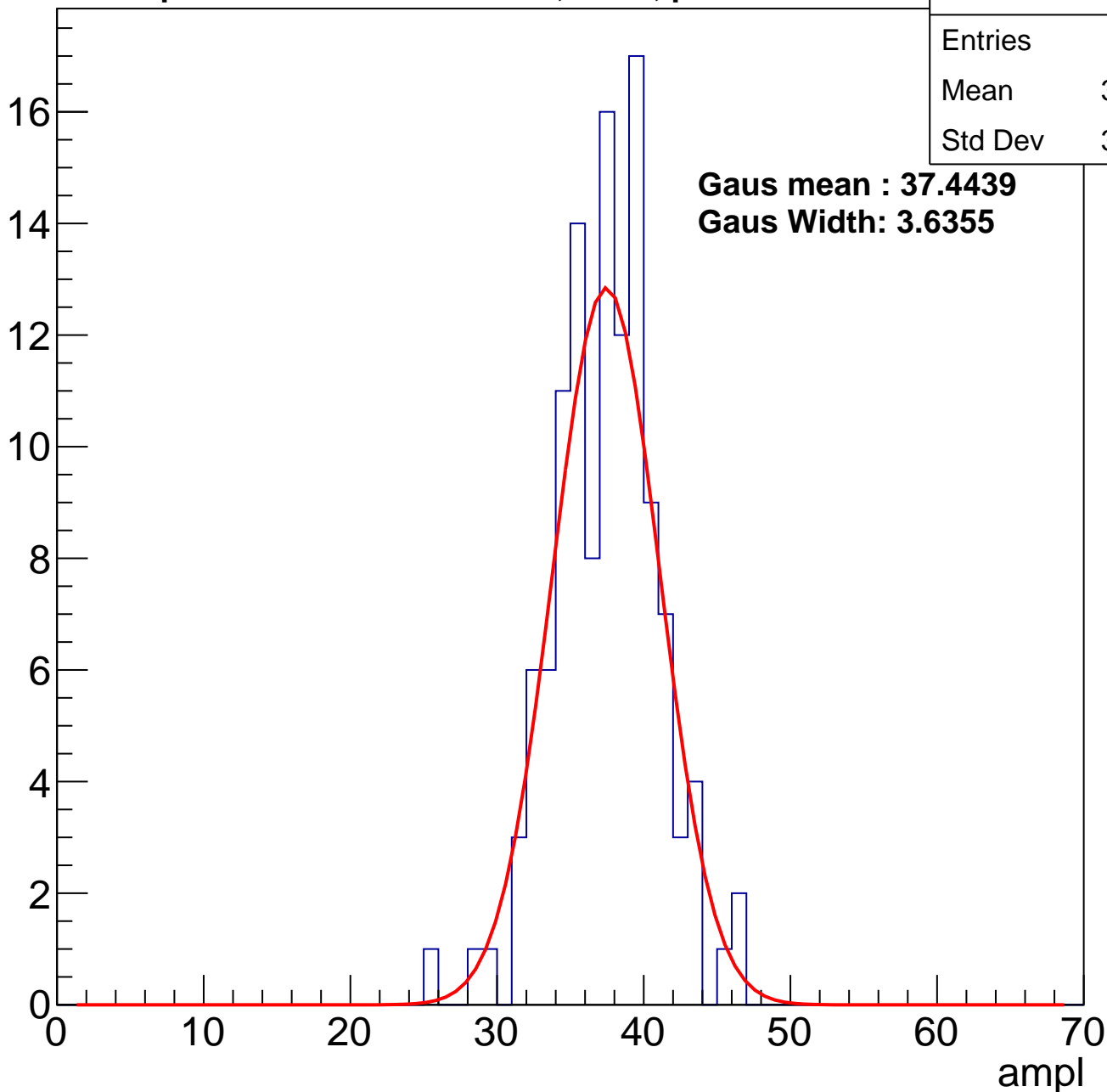
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.97 |
| Std Dev | 3.534 |

Gaus mean : 37.4439

Gaus Width: 3.6355

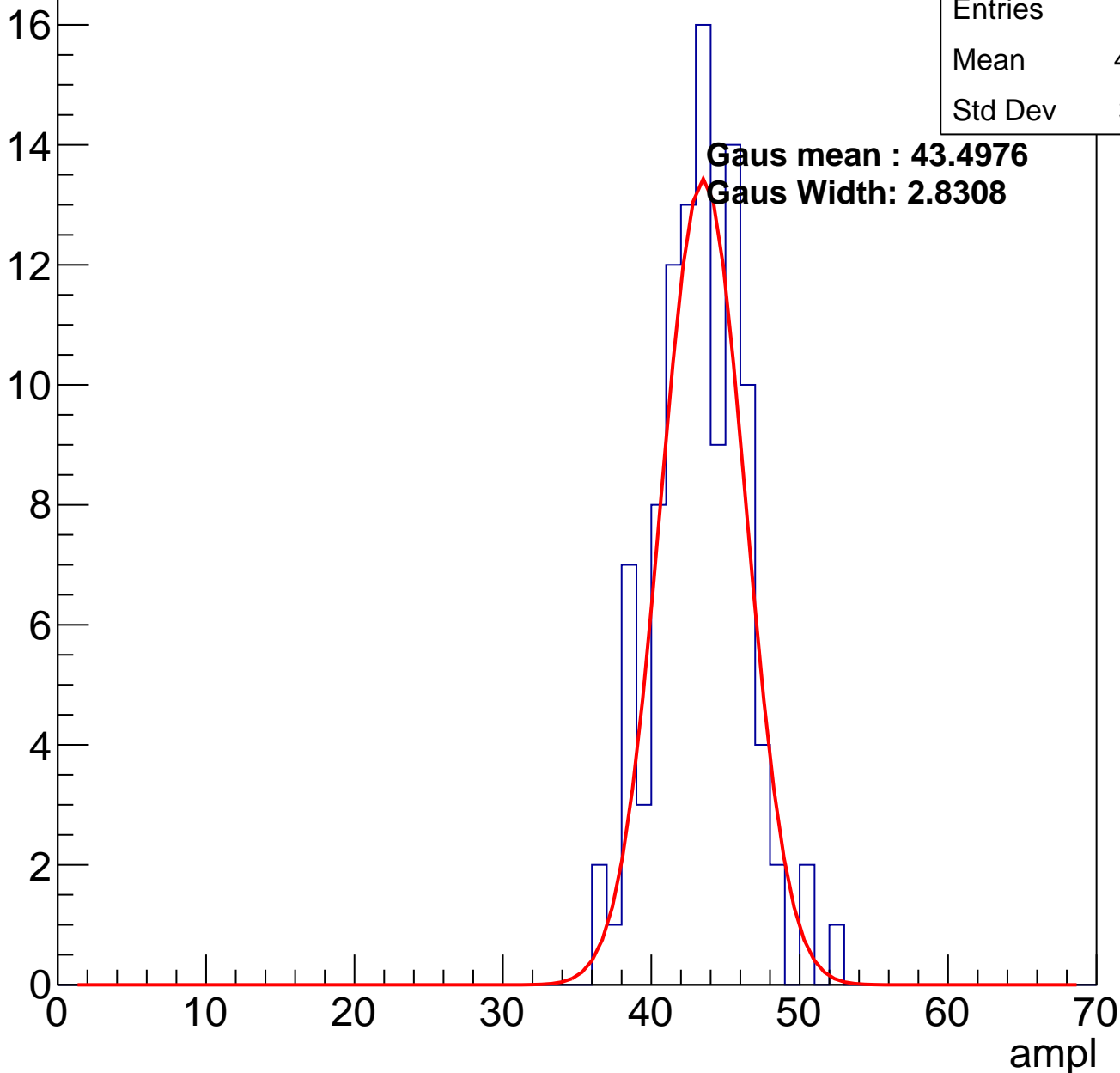
Entry



B0L101S, U22-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

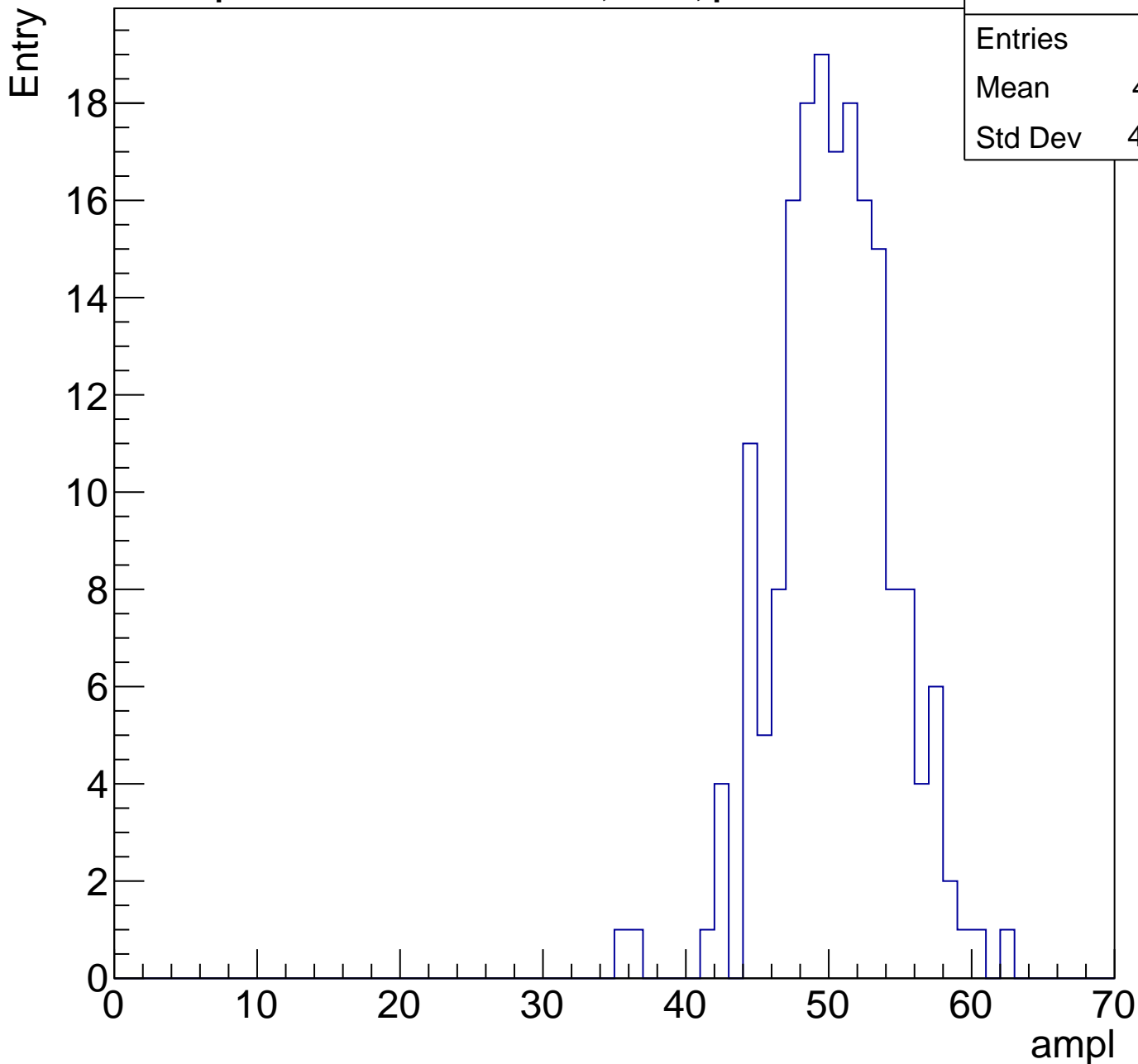
Entry



B0L101S, U22-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

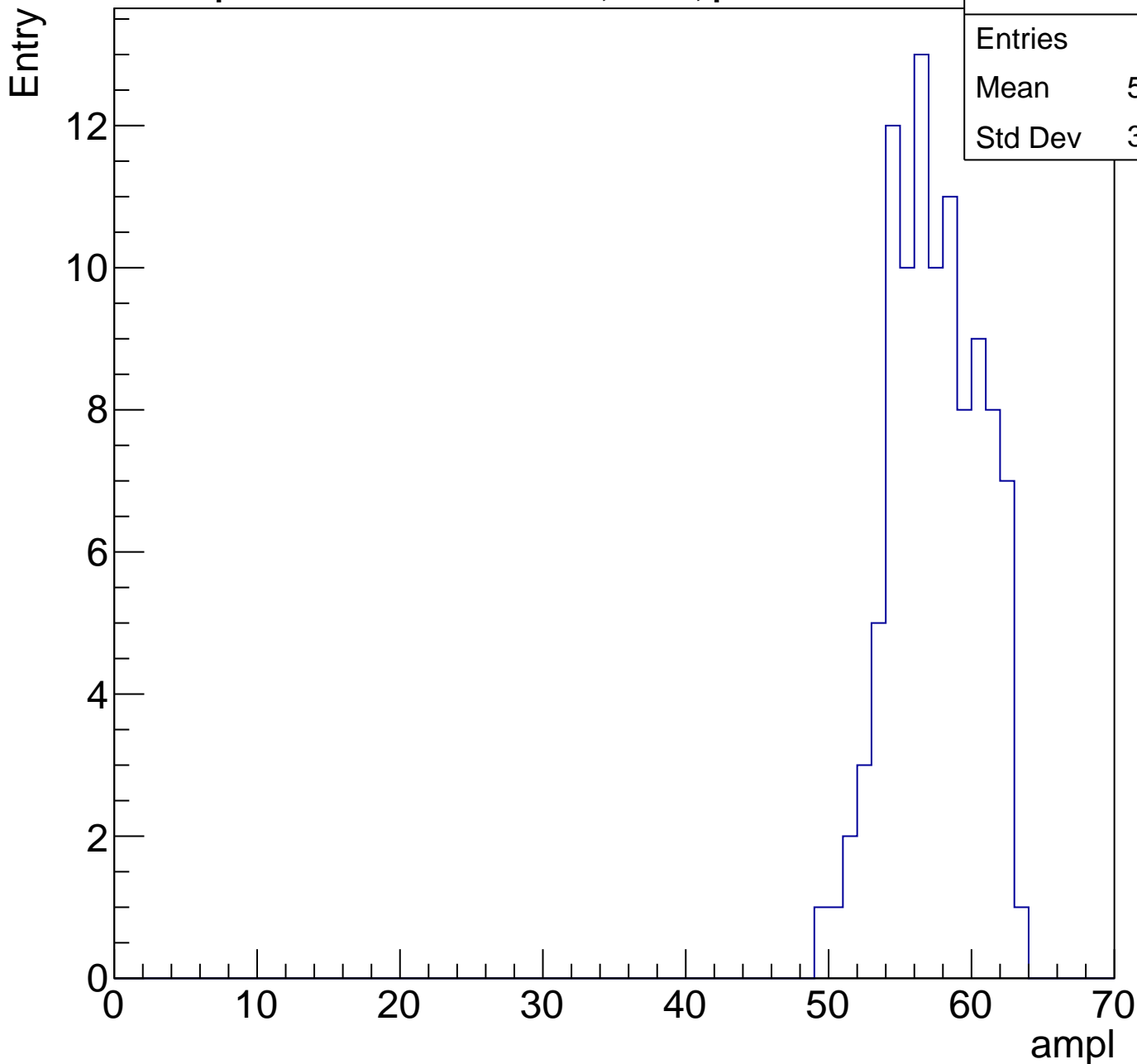
| | |
|---------|-------|
| Entries | 181 |
| Mean | 49.91 |
| Std Dev | 4.123 |



B0L101S, U22-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 101 |
| Mean | 56.96 |
| Std Dev | 3.089 |



B0L101S, U22-ch95, adc5

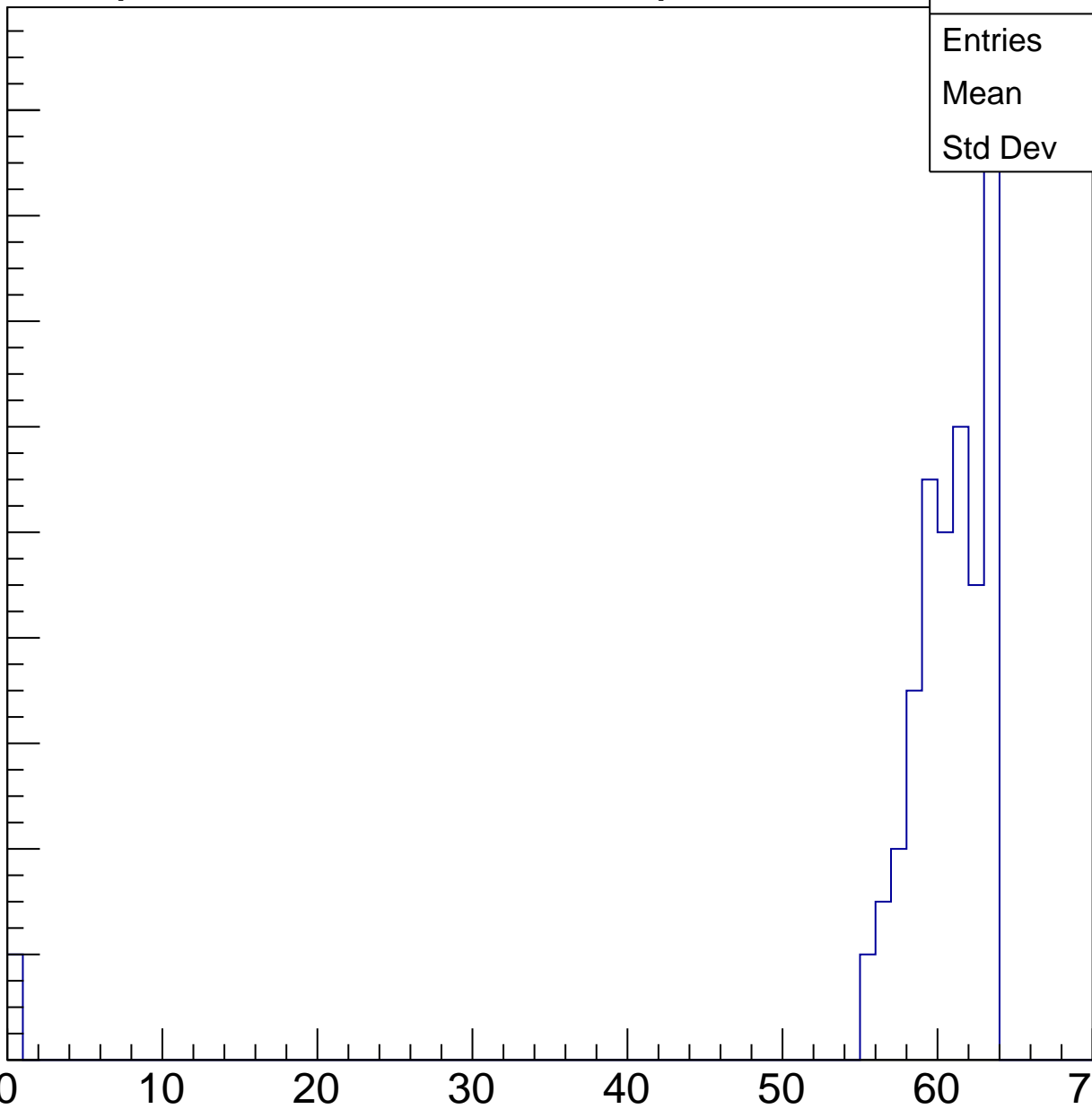
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 58.84 |
| Std Dev | 9.734 |

ampl



B0L101S, U22-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

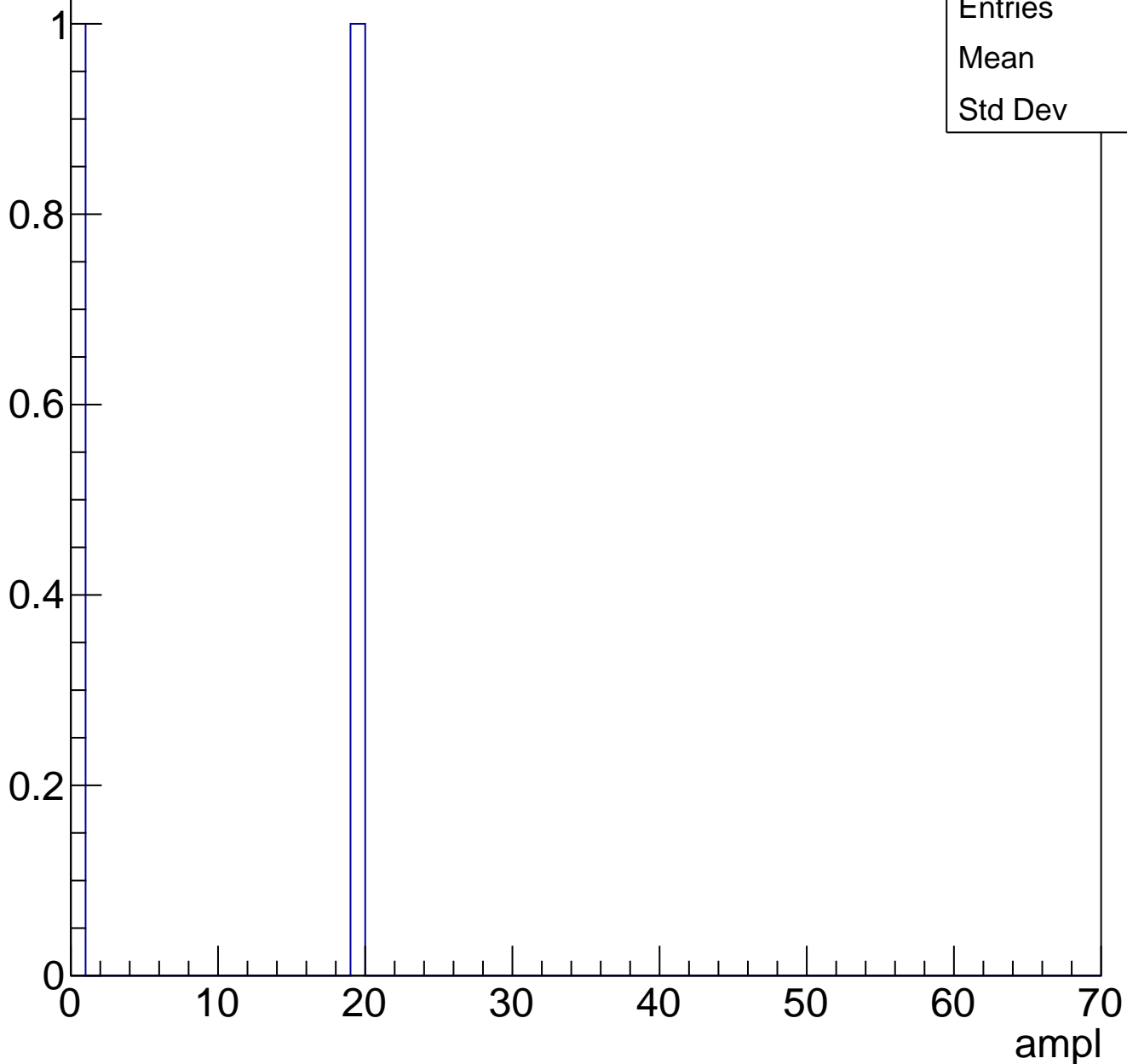
Entry



B0L101S, U22-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch96, adc0

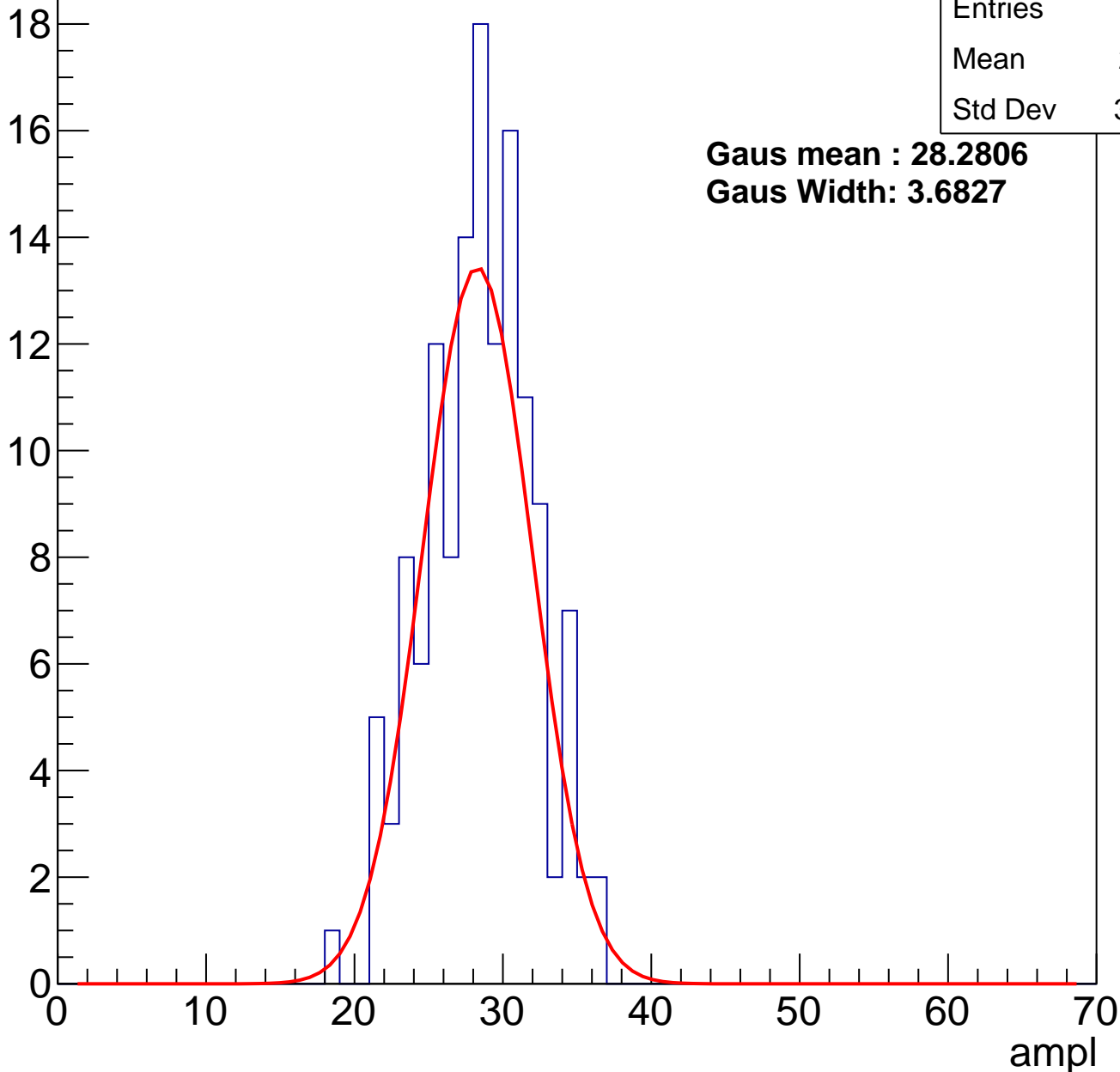
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 28.01 |
| Std Dev | 3.587 |

Gaus mean : 28.2806

Gaus Width: 3.6827

Entry



B0L101S, U22-ch96, adc1

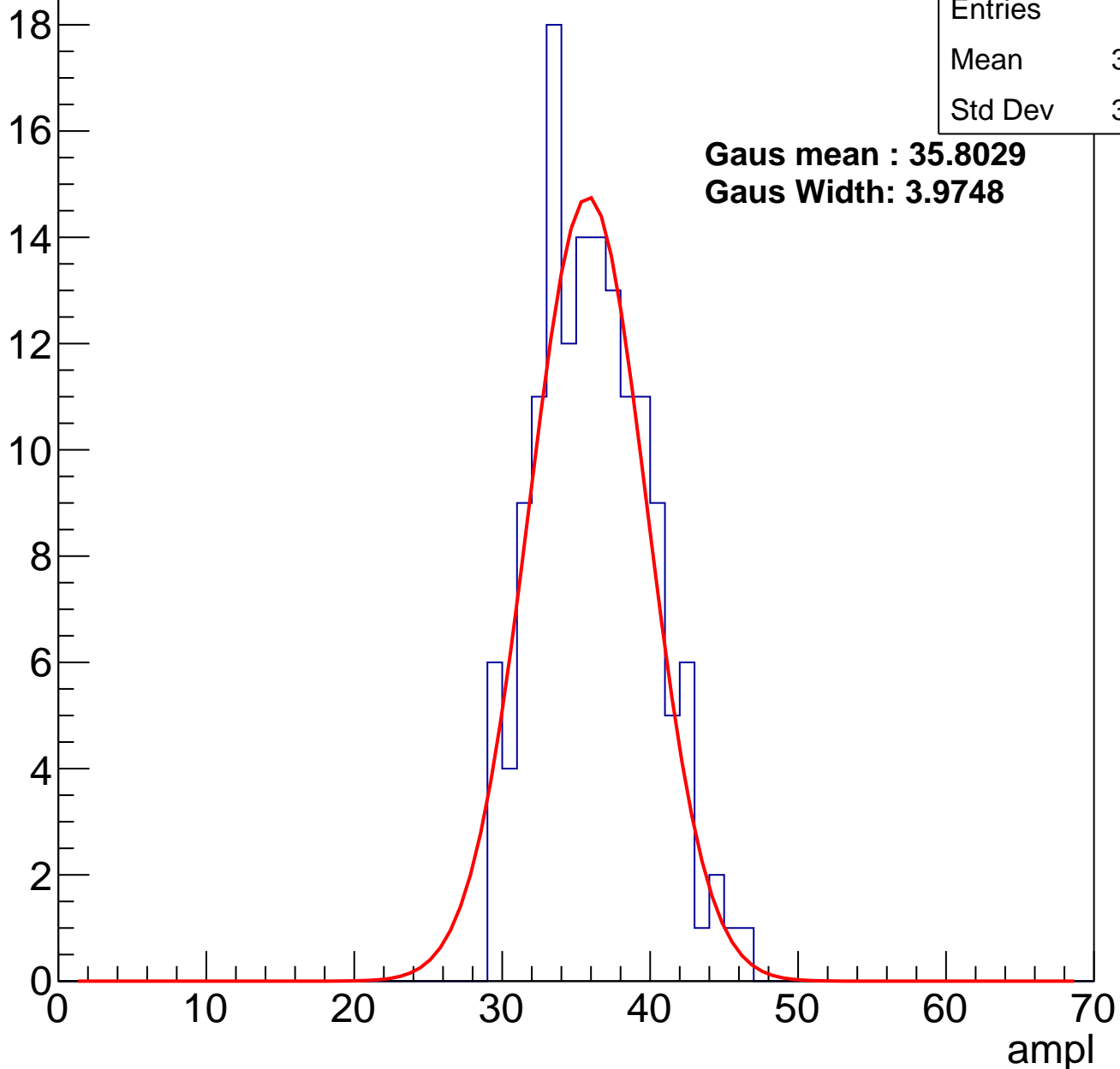
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 35.73 |
| Std Dev | 3.723 |

Gaus mean : 35.8029

Gaus Width: 3.9748

Entry



B0L101S, U22-ch96, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

124

Mean

41.98

Std Dev

3.735

Gaus mean : 42.2791

Gaus Width: 3.7945

0

10

20

30

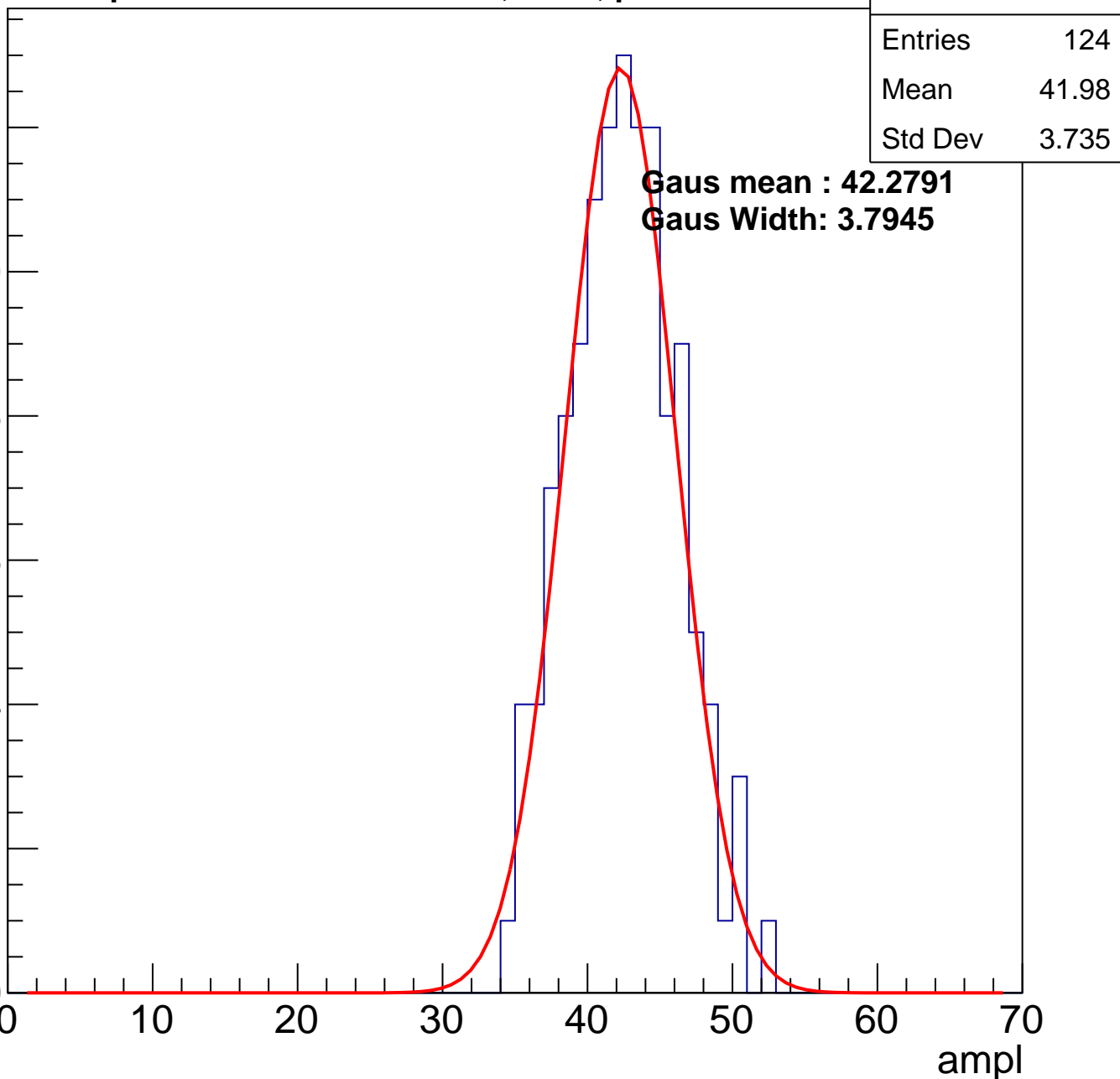
40

50

60

70

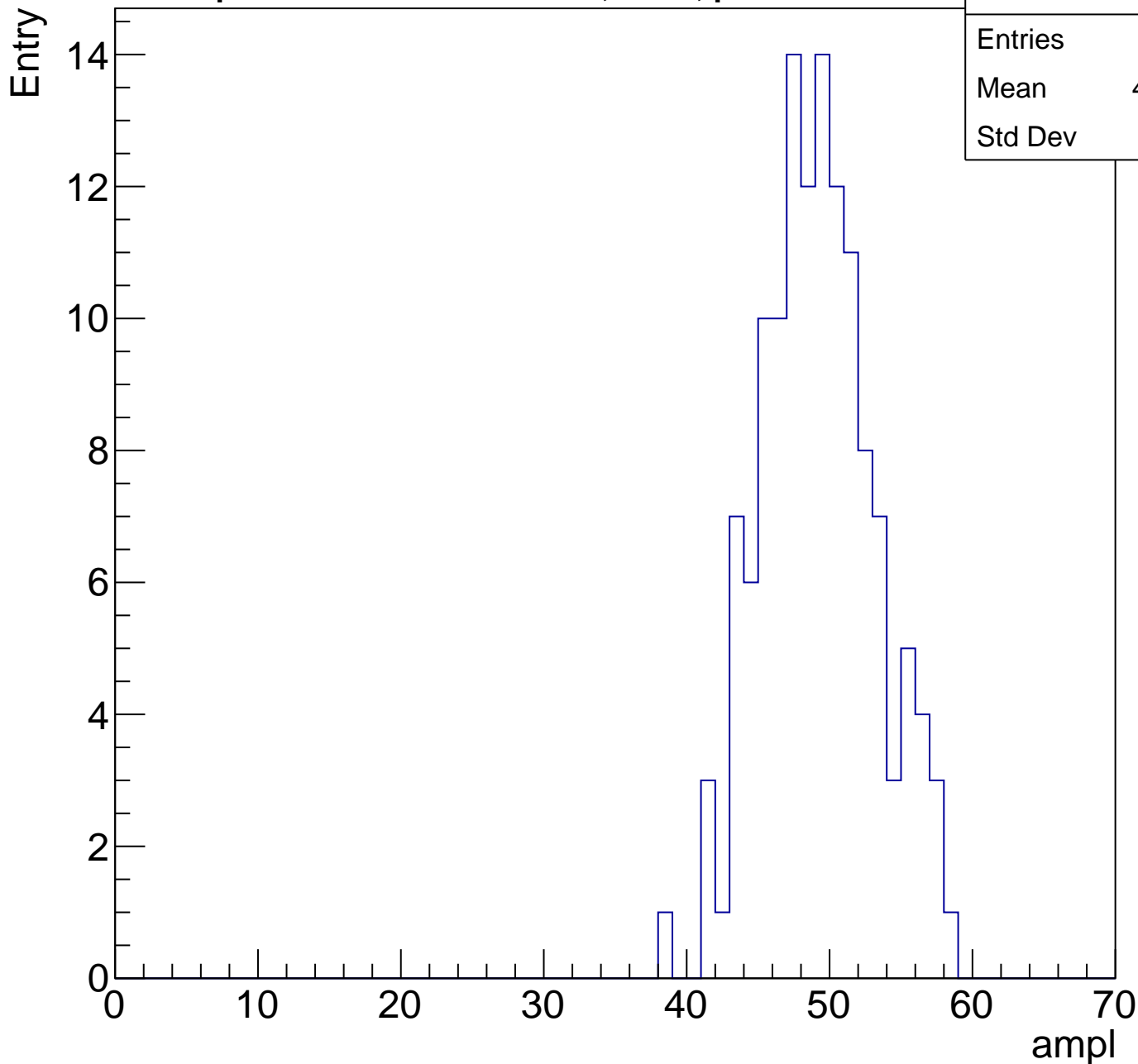
ampl



B0L101S, U22-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

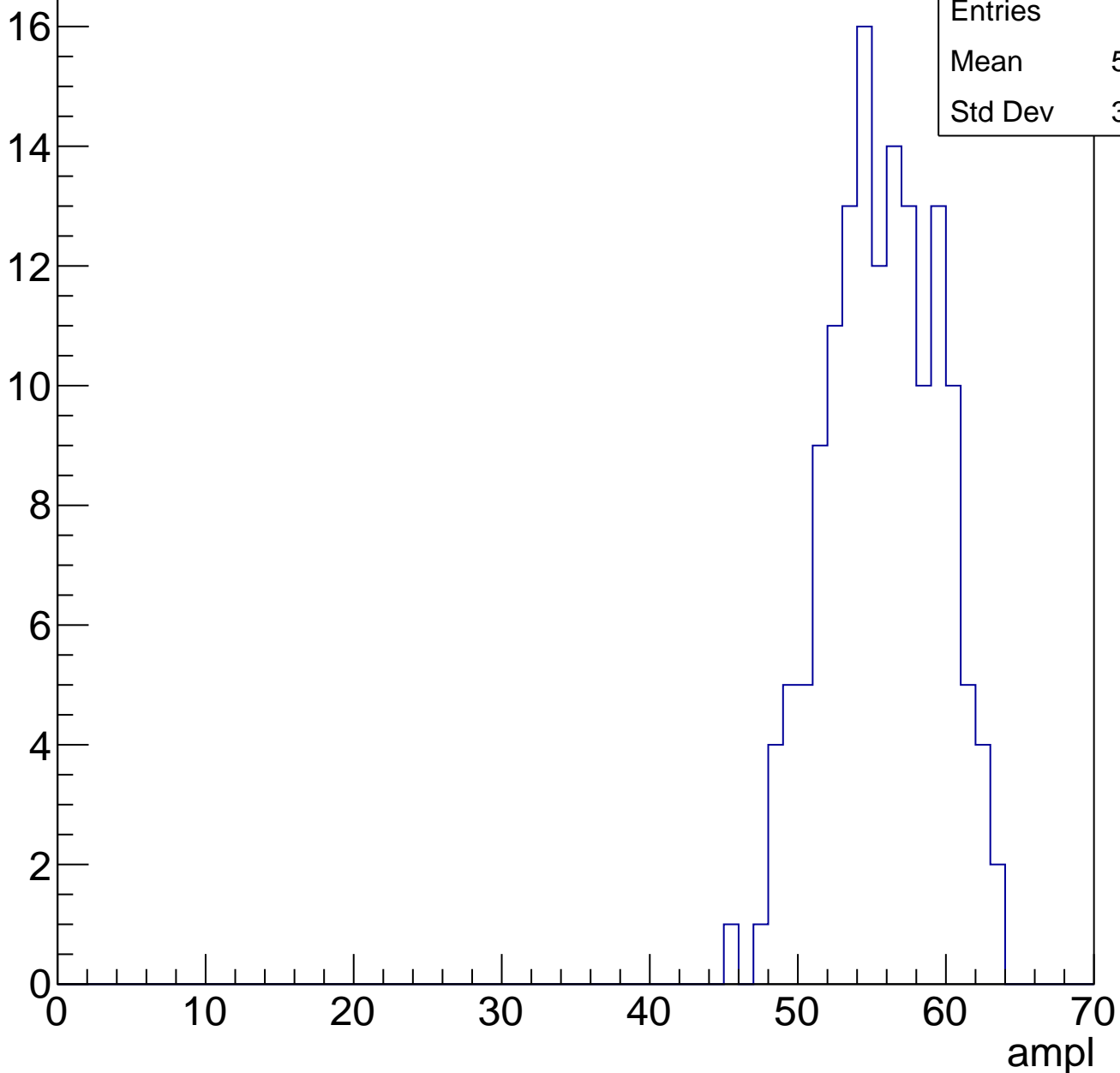
| | |
|---------|-------|
| Entries | 132 |
| Mean | 48.76 |
| Std Dev | 3.93 |



B0L101S, U22-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 148 |
| Mean | 55.23 |
| Std Dev | 3.754 |

B0L101S, U22-ch96, adc5

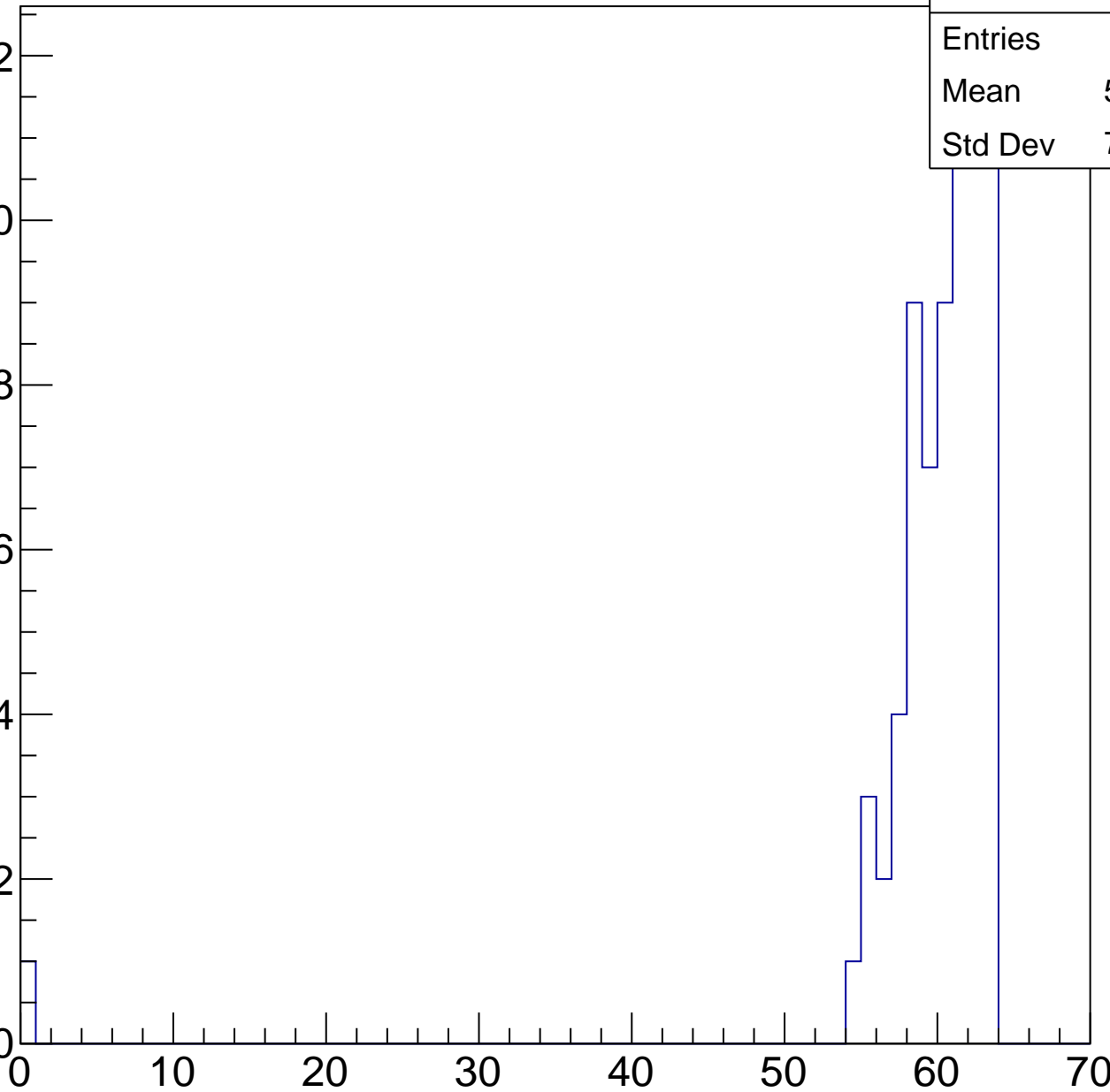
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 59.25 |
| Std Dev | 7.406 |

ampl

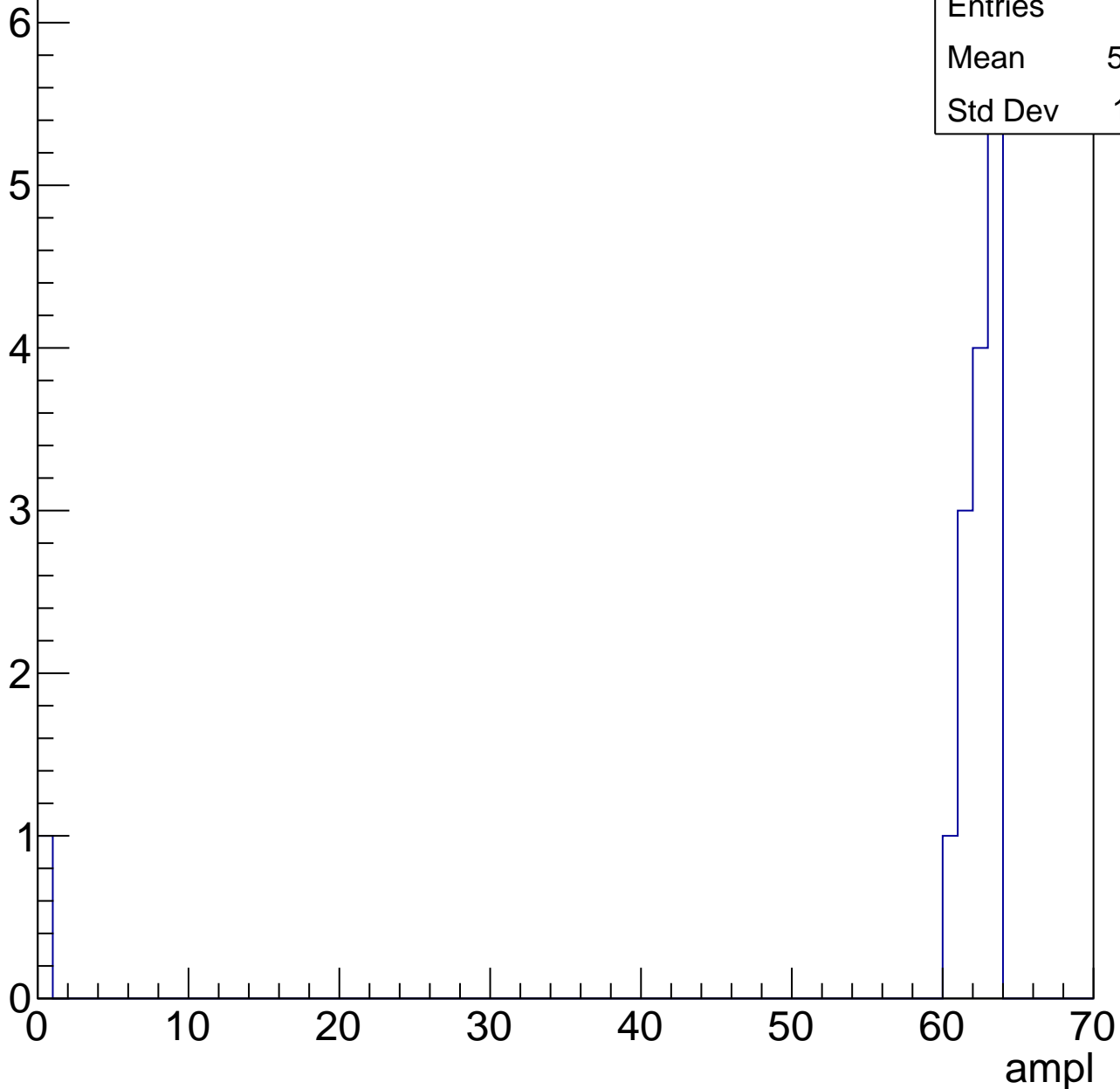


B0L101S, U22-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 57.93 |
| Std Dev | 15.51 |



B0L101S, U22-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch97, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 29.86 |
| Std Dev | 5.151 |

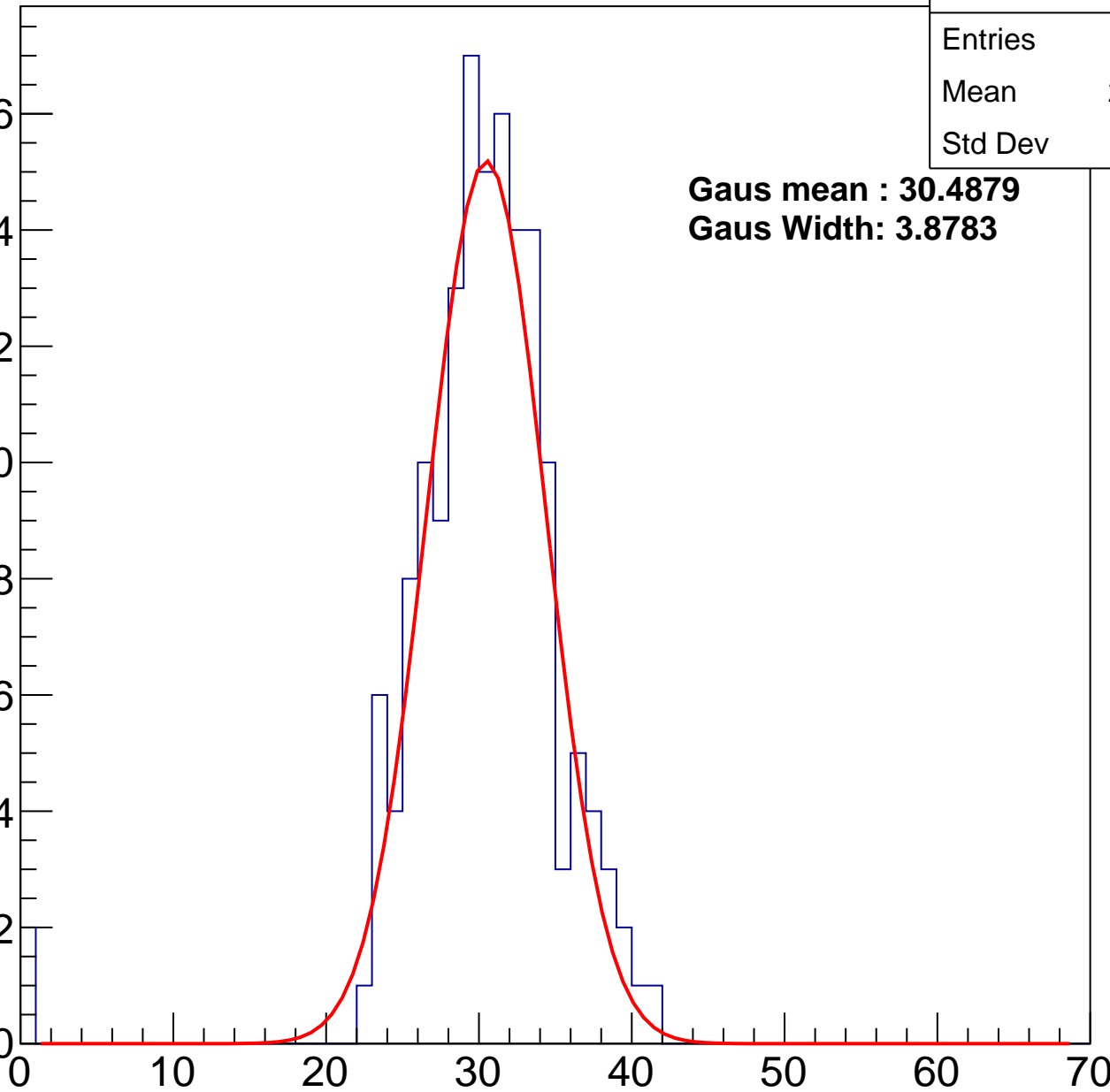
Gaus mean : 30.4879

Gaus Width: 3.8783

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch97, adc1

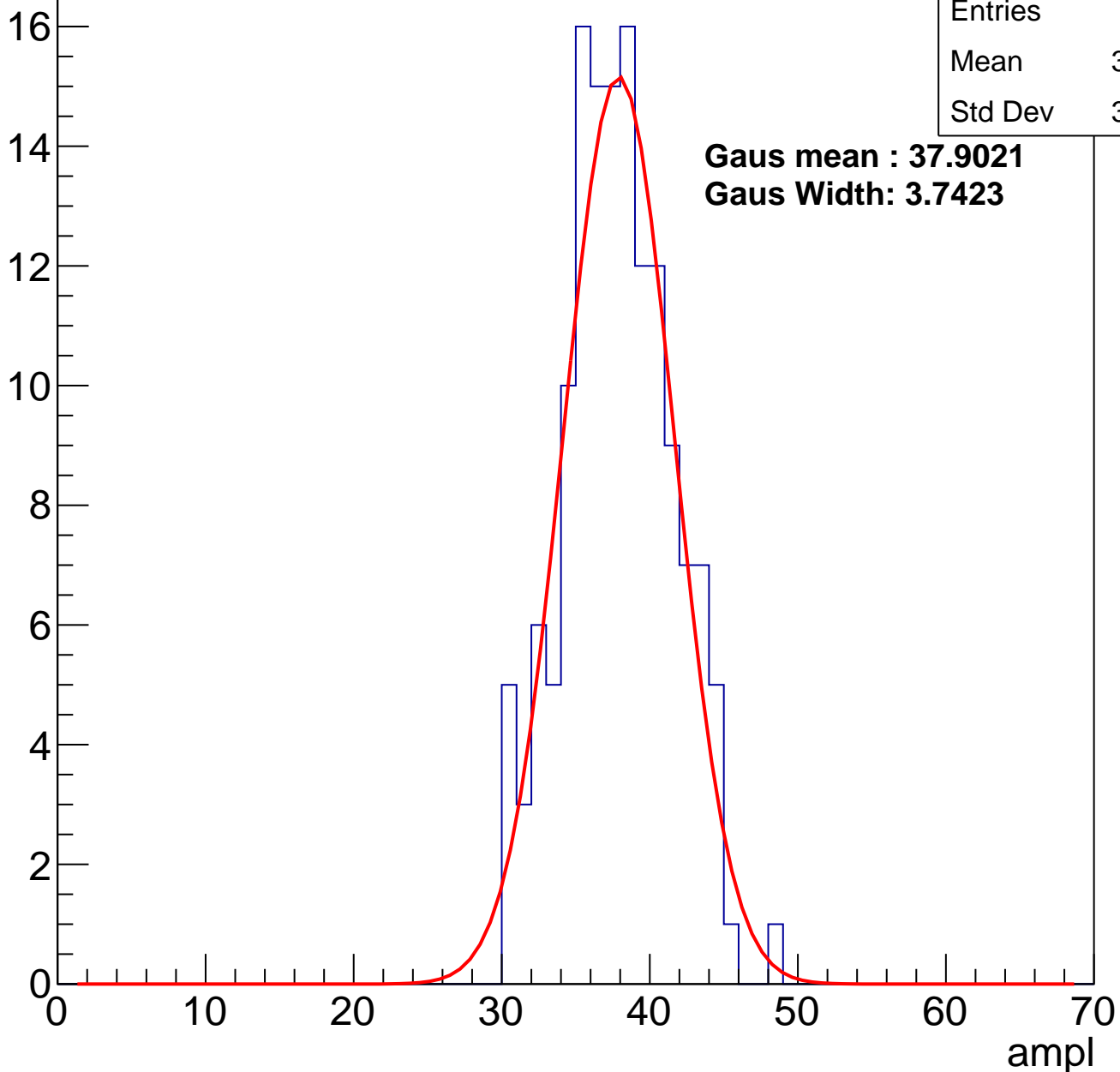
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 37.43 |
| Std Dev | 3.618 |

Gaus mean : 37.9021

Gaus Width: 3.7423

Entry



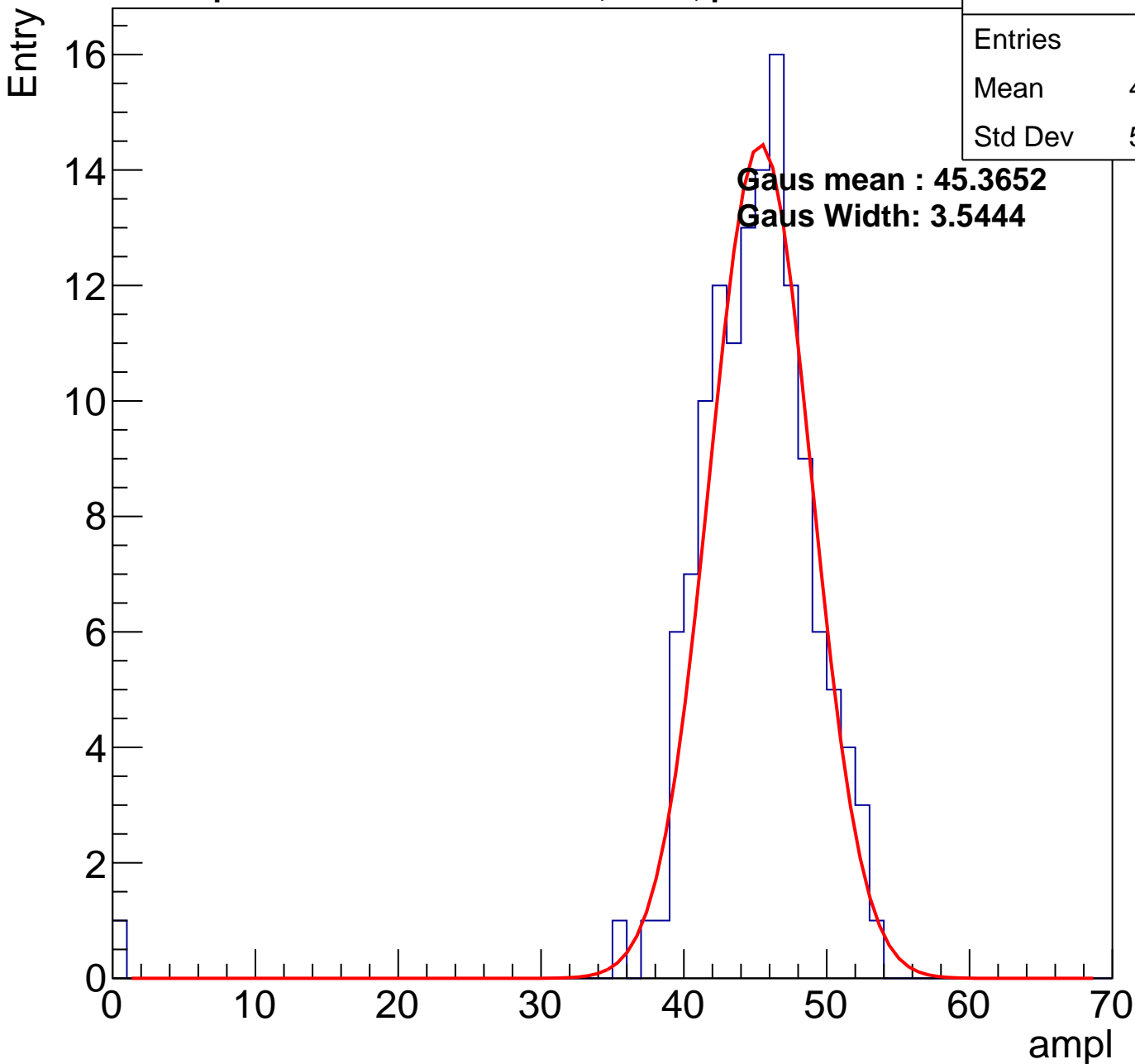
B0L101S, U22-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 44.38 |
| Std Dev | 5.219 |

Gaus mean : 45.3652

Gaus Width: 3.5444

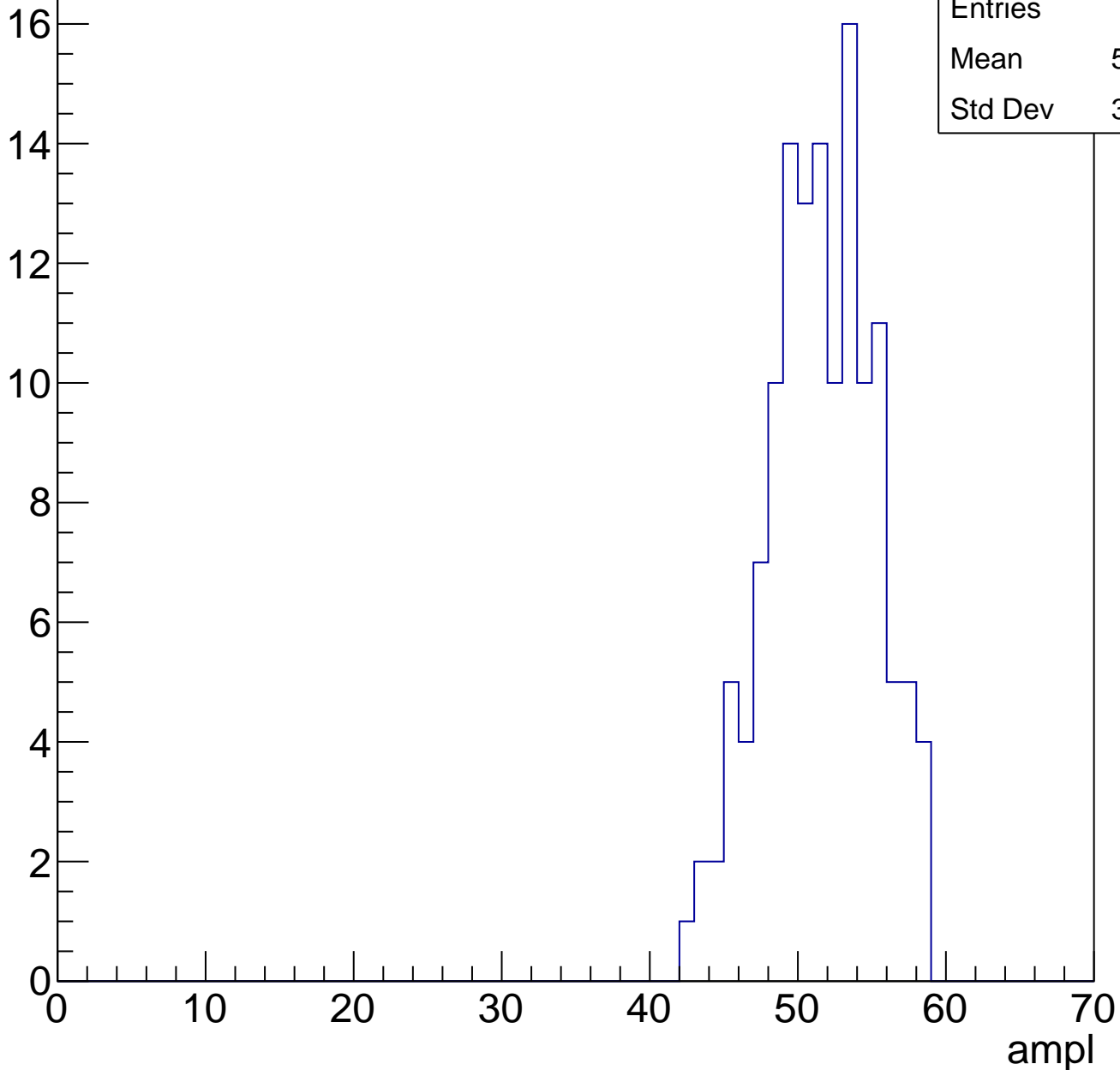


B0L101S, U22-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 51.08 |
| Std Dev | 3.573 |

Entry



B0L101S, U22-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

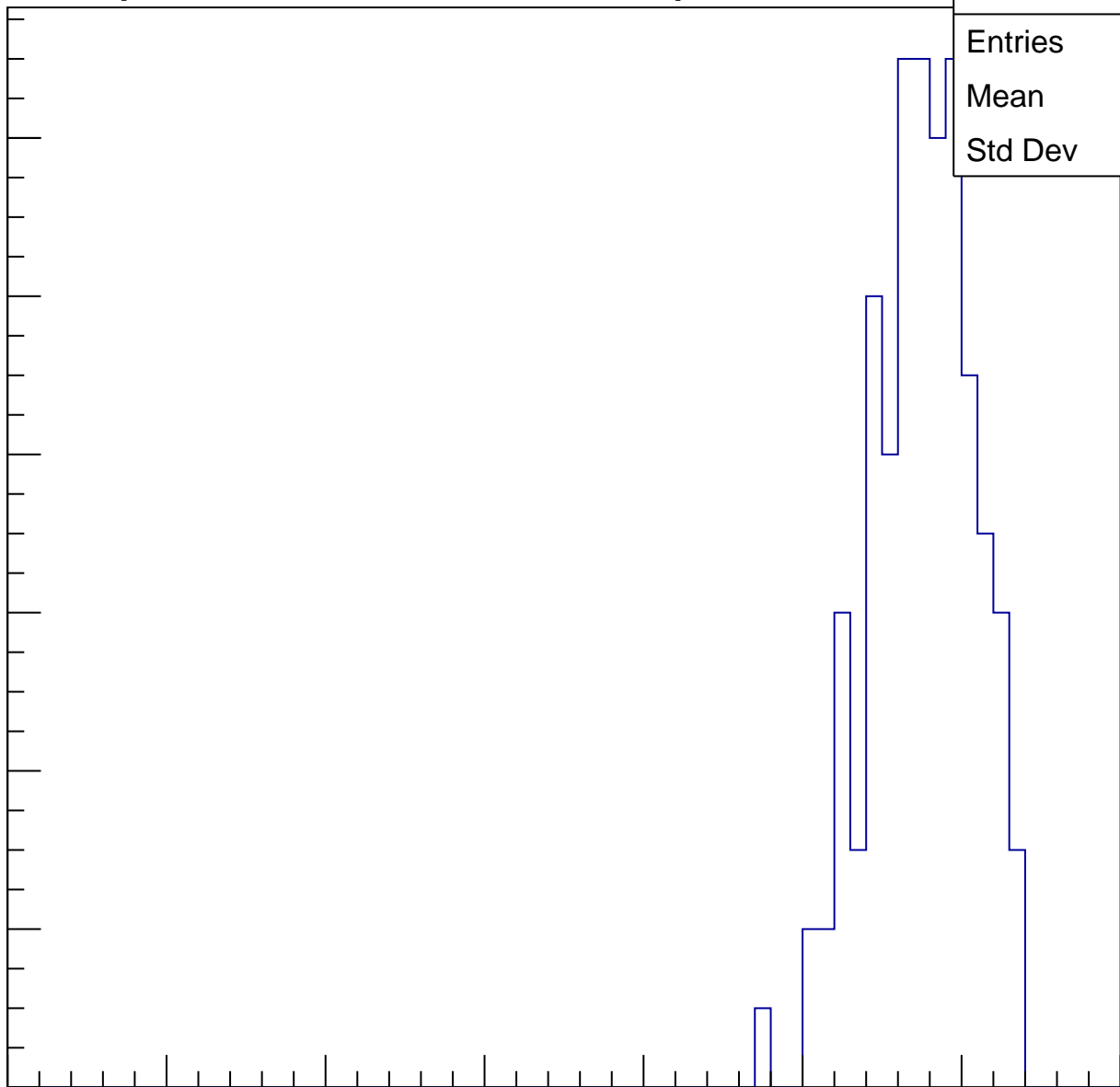
50

60

70

ampl

| | |
|---------|-------|
| Entries | 108 |
| Mean | 57.04 |
| Std Dev | 3.217 |



B0L101S, U22-ch97, adc5

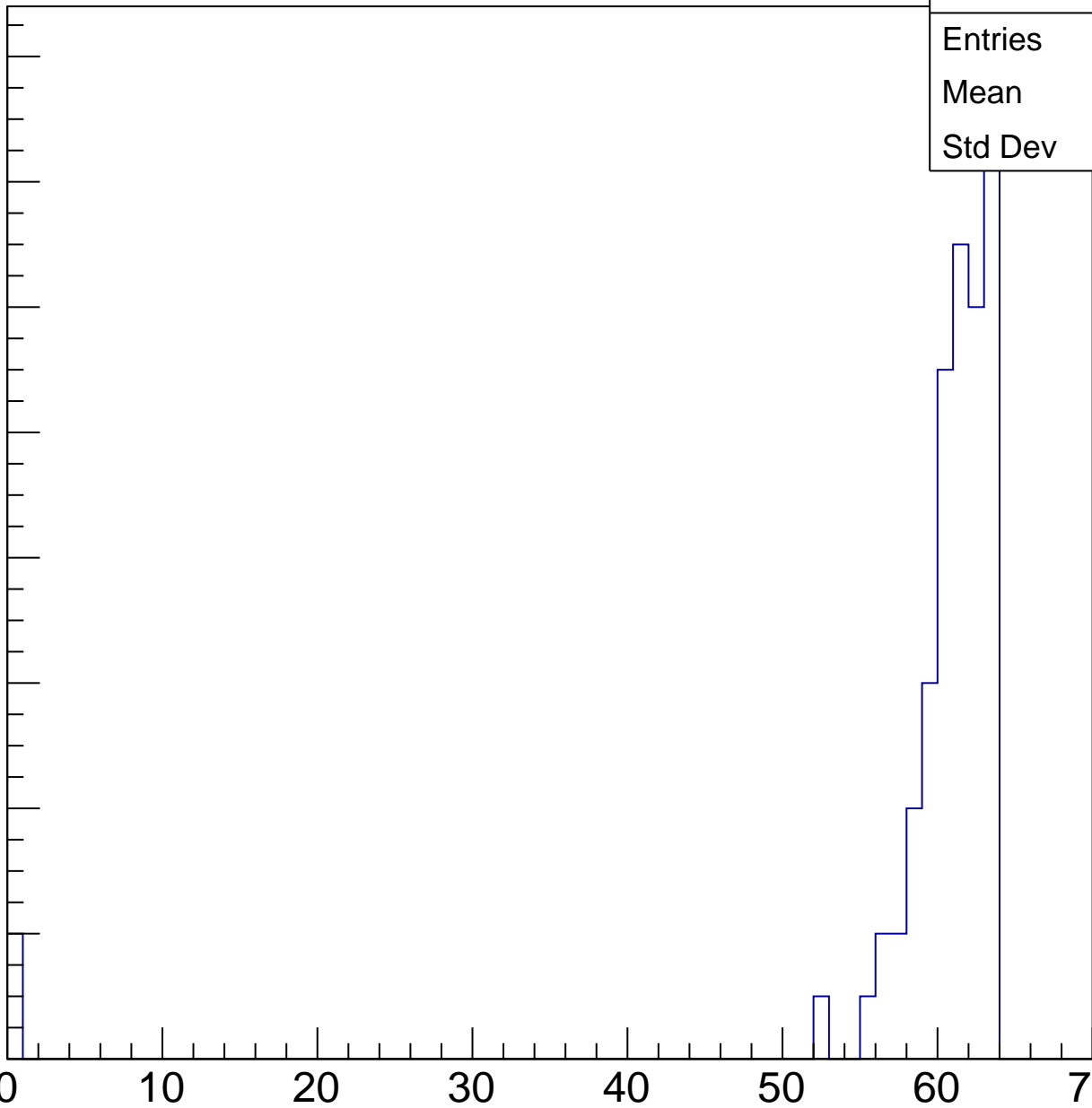
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.91 |
| Std Dev | 10.34 |

ampl



B0L101S, U22-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

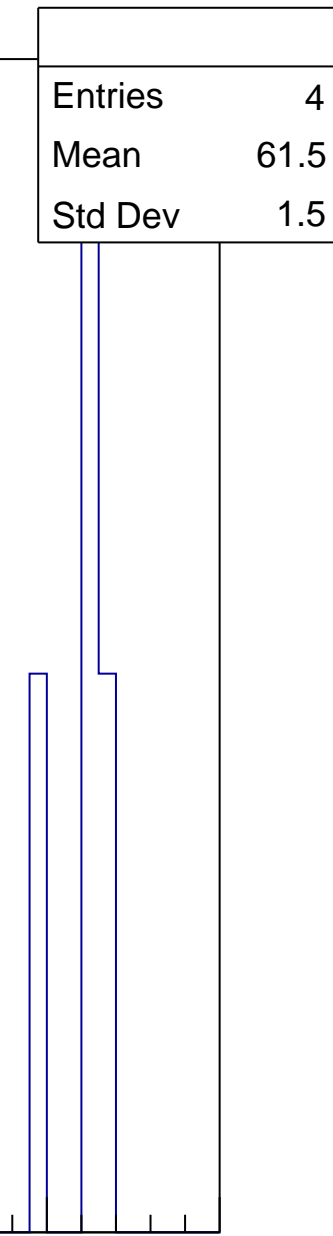
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 4 |
| Mean | 61.5 |
| Std Dev | 1.5 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 29.26 |
| Std Dev | 3.386 |

Gaus mean : 29.7138

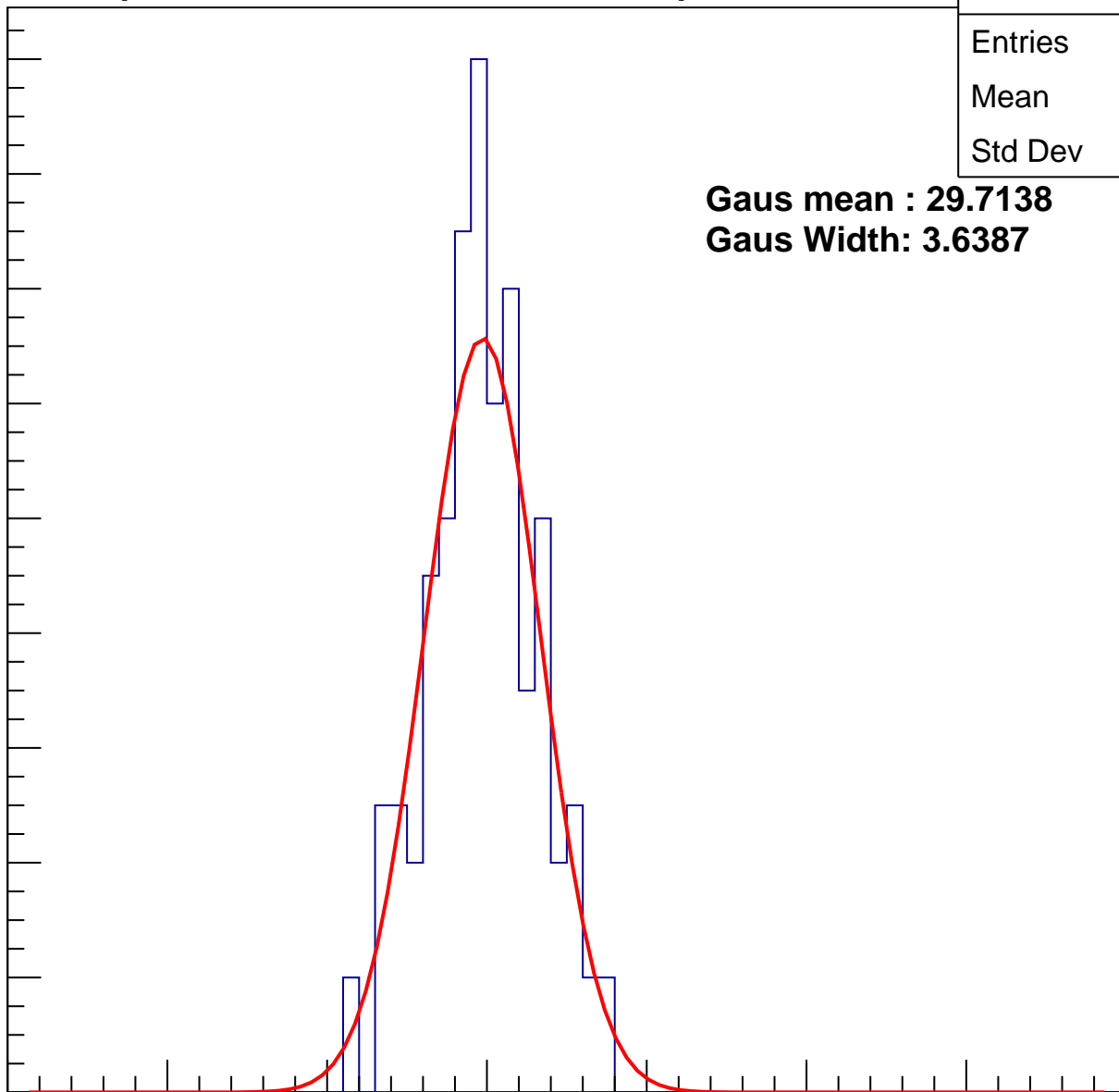
Gaus Width: 3.6387

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 153 |
| Mean | 36.4 |
| Std Dev | 3.976 |

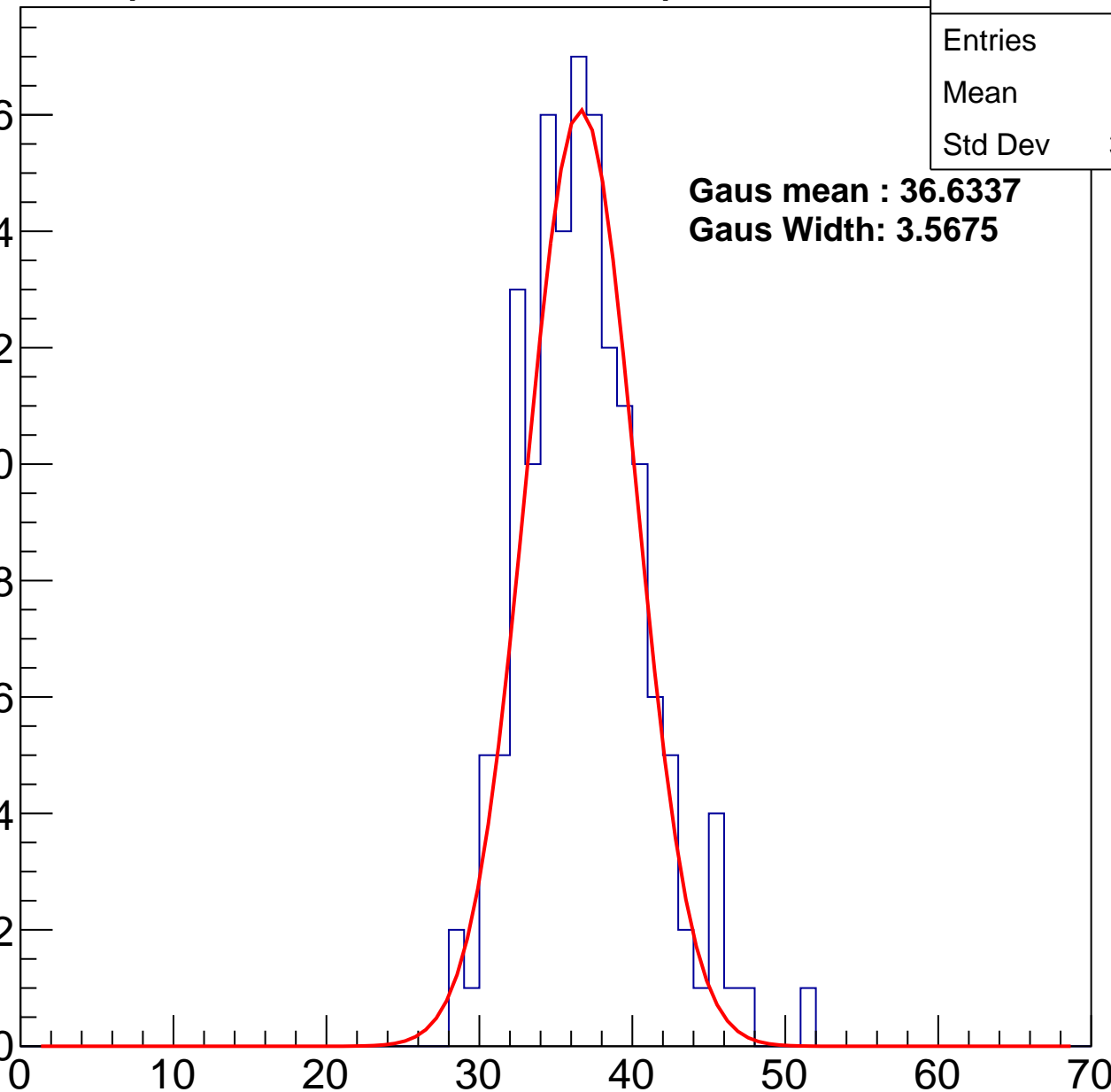
Gaus mean : 36.6337

Gaus Width: 3.5675

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch98, adc2

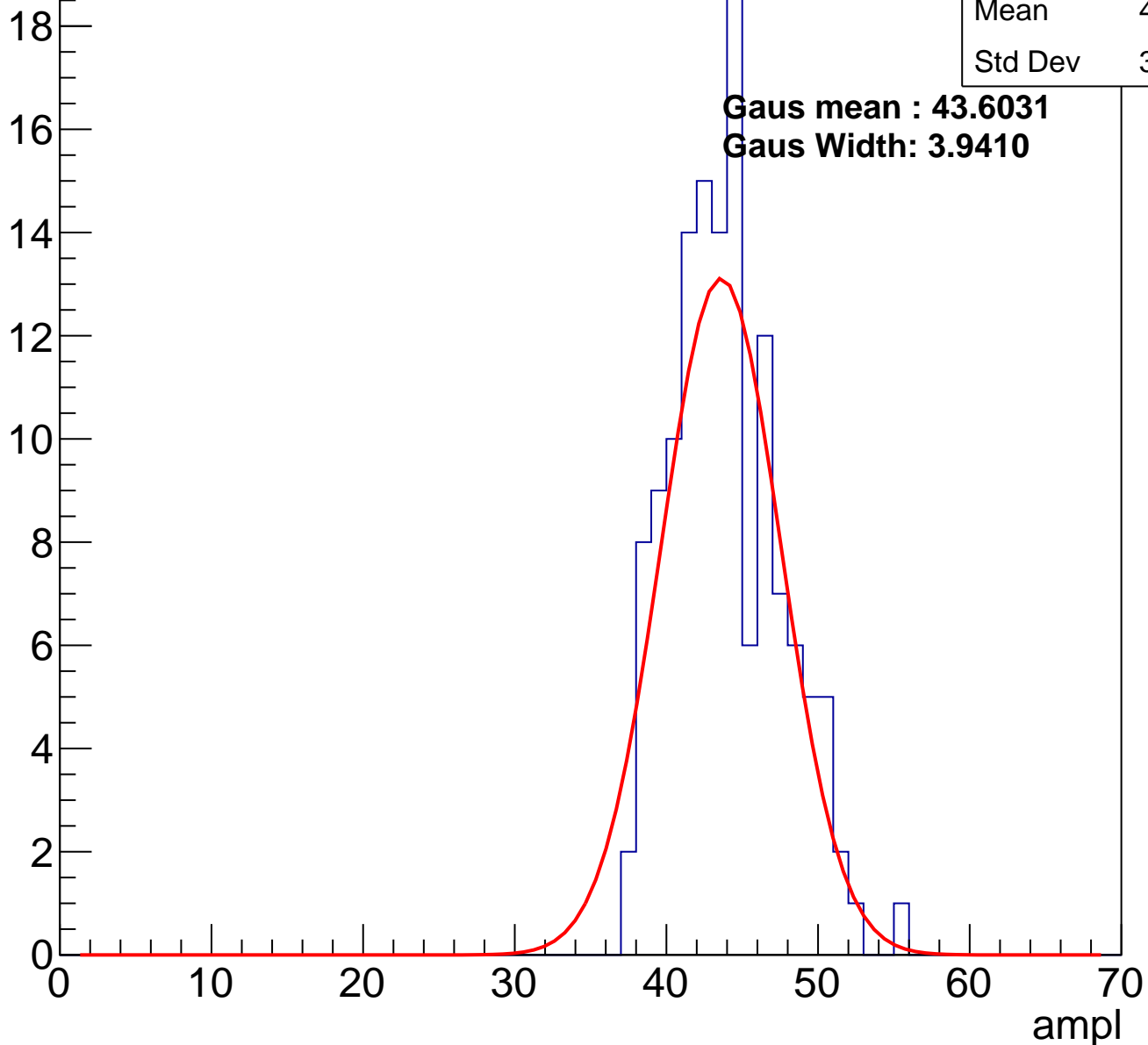
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 43.49 |
| Std Dev | 3.573 |

Gaus mean : 43.6031

Gaus Width: 3.9410

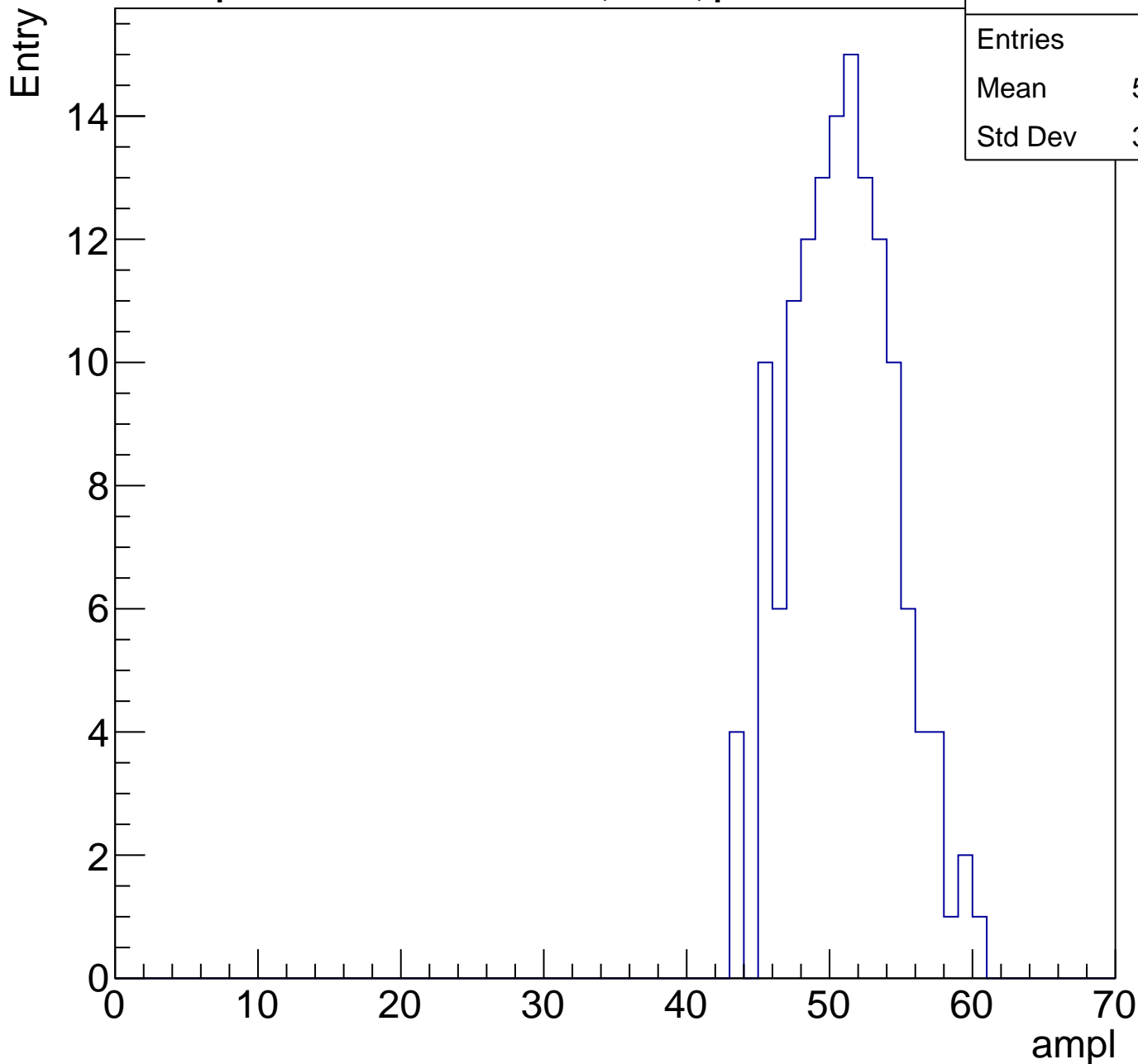
Entry



B0L101S, U22-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 50.46 |
| Std Dev | 3.636 |



B0L101S, U22-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

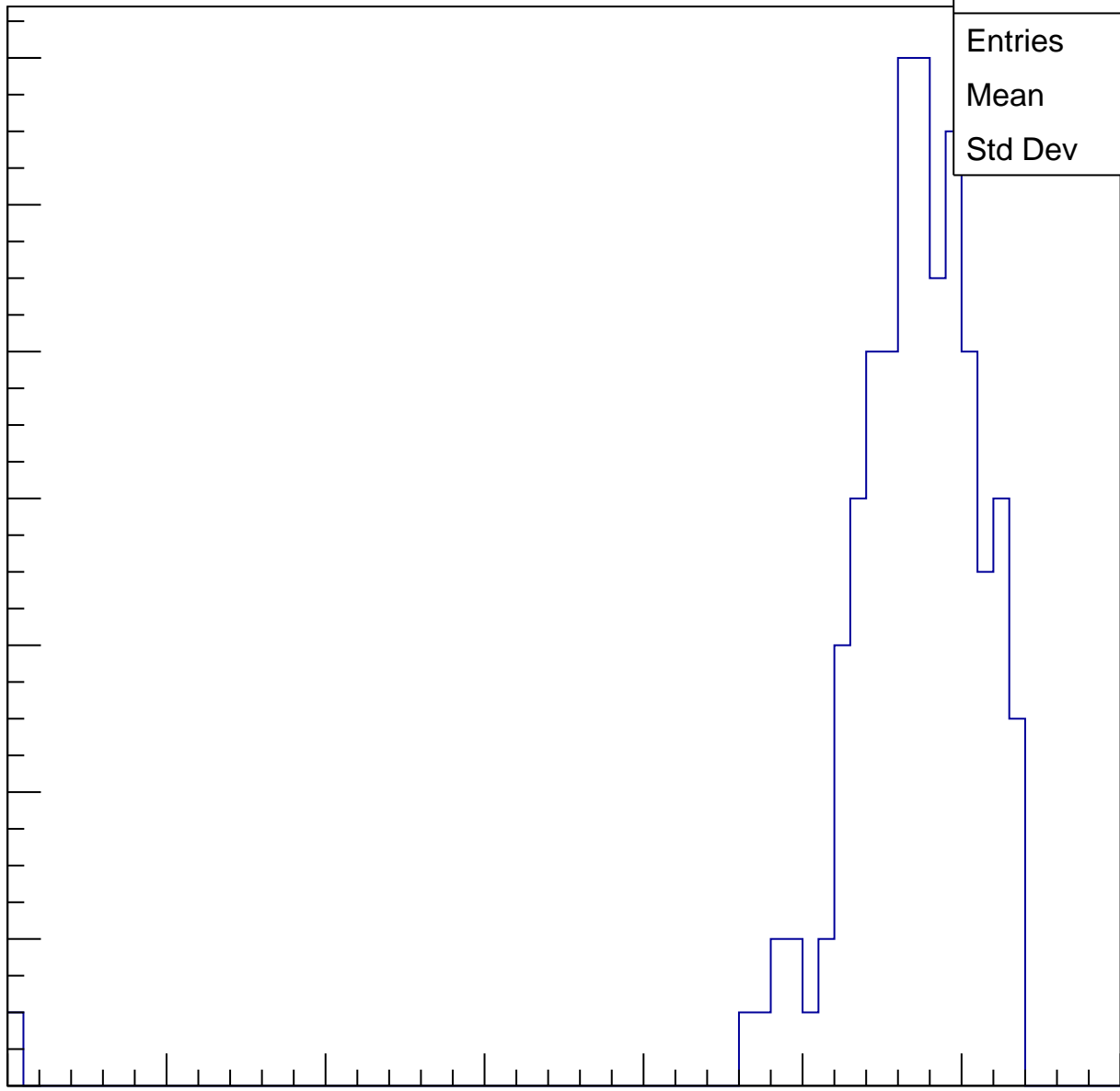
| | |
|---------|-------|
| Entries | 126 |
| Mean | 56.27 |
| Std Dev | 6.224 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch98, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 66 |
| Mean | 59.45 |
| Std Dev | 7.703 |

B0L101S, U22-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

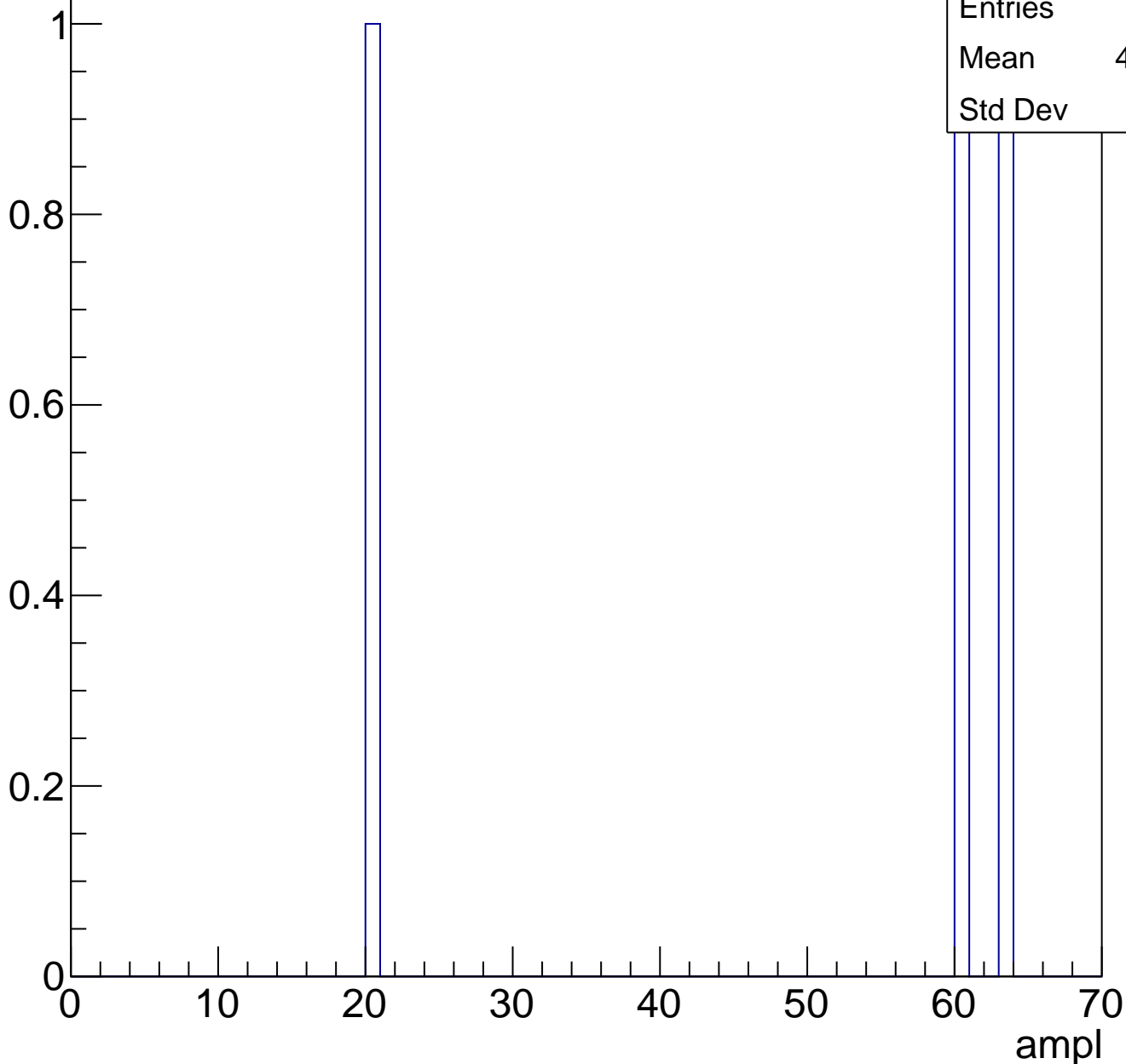
Entry



B0L101S, U22-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch99, adc0

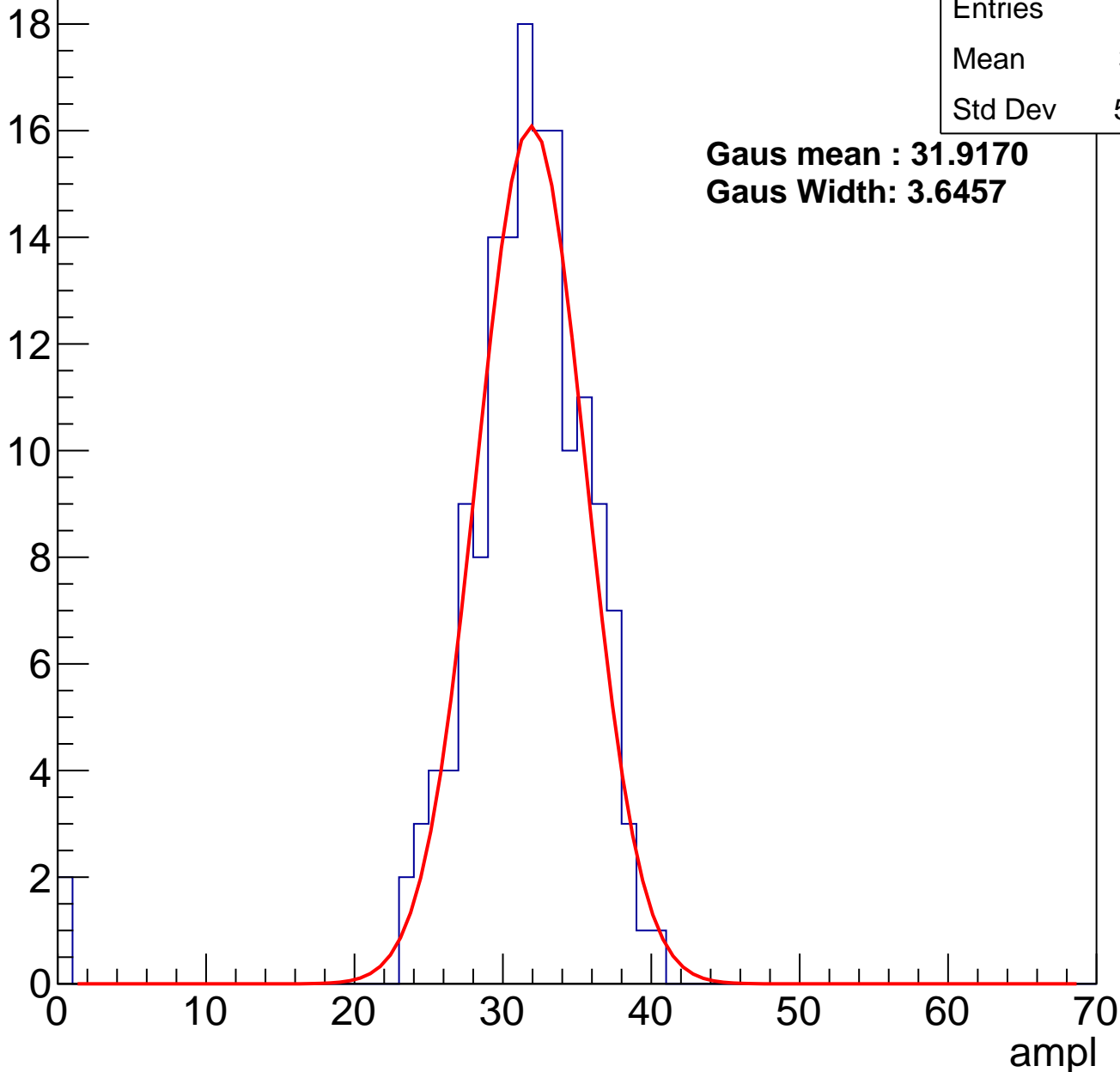
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 152 |
| Mean | 31.01 |
| Std Dev | 5.028 |

Gaus mean : 31.9170

Gaus Width: 3.6457

Entry



B0L101S, U22-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

122

Mean

38.09

Std Dev

3.214

Gaus mean : 38.2024

Gaus Width: 2.9731

0

10

20

30

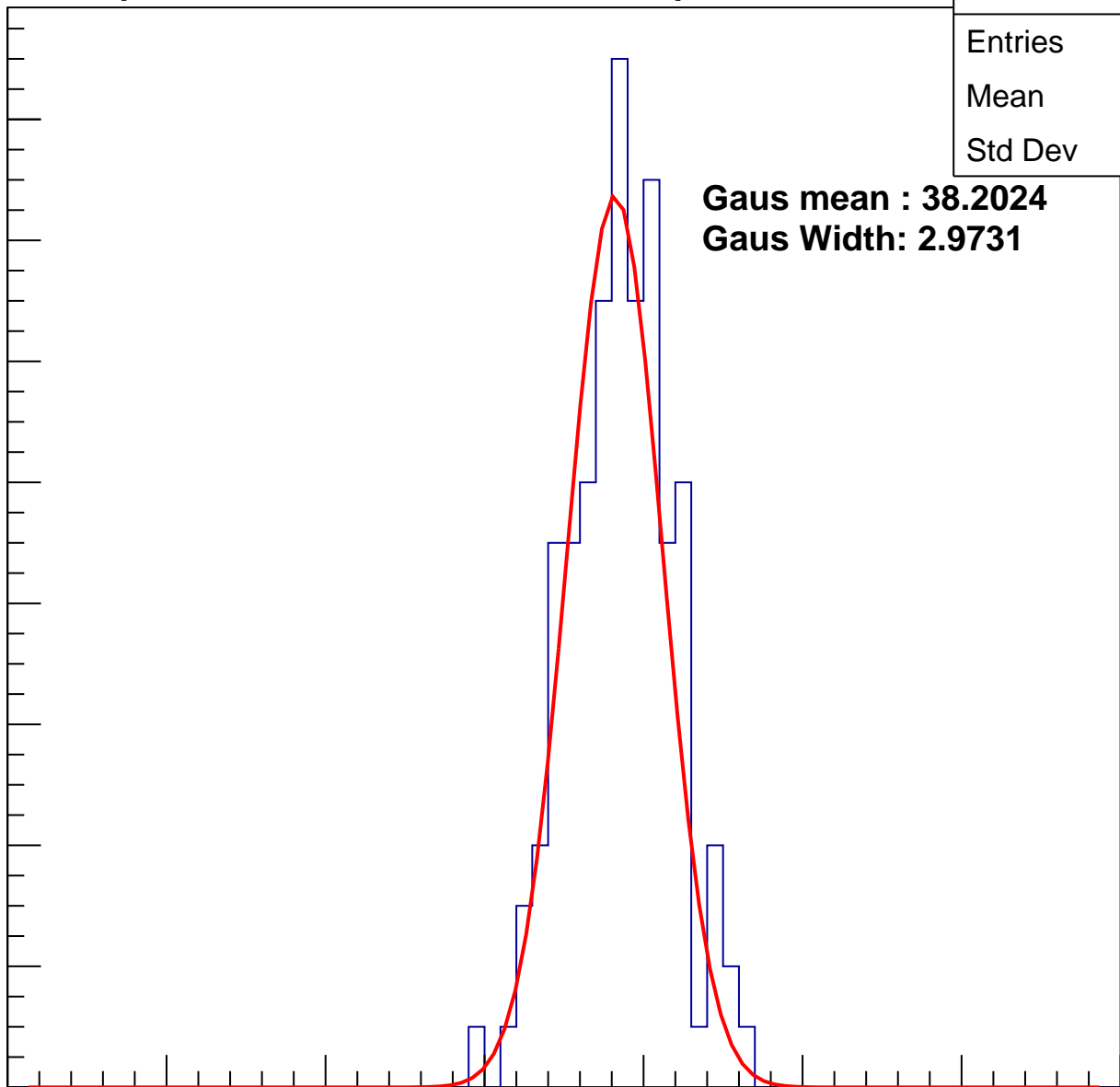
40

50

60

70

ampl



B0L101S, U22-ch99, adc2

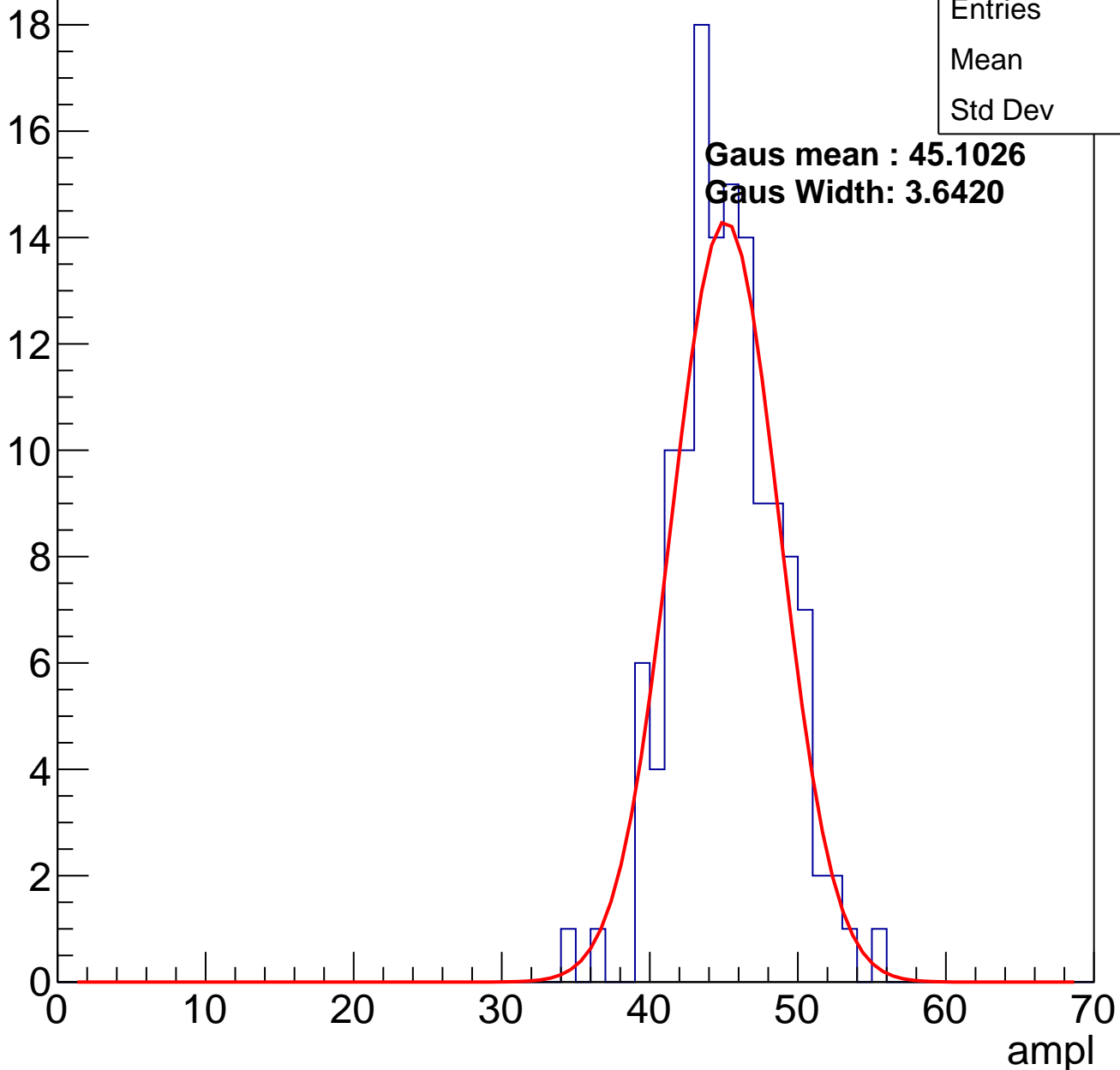
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 132 |
| Mean | 44.8 |
| Std Dev | 3.5 |

Gaus mean : 45.1026

Gaus Width: 3.6420

Entry



B0L101S, U22-ch99, adc3

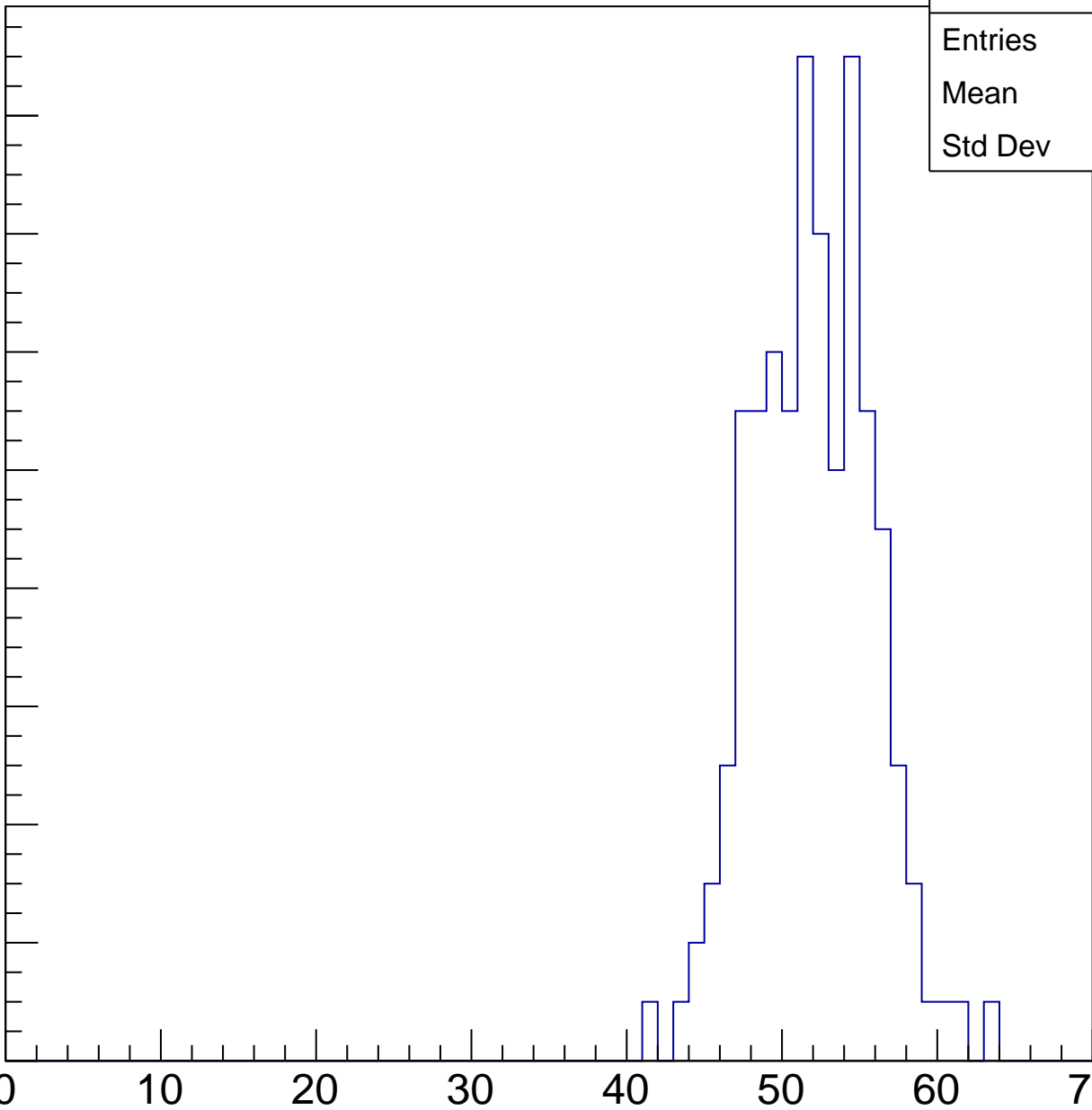
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 147 |
| Mean | 51.52 |
| Std Dev | 3.791 |

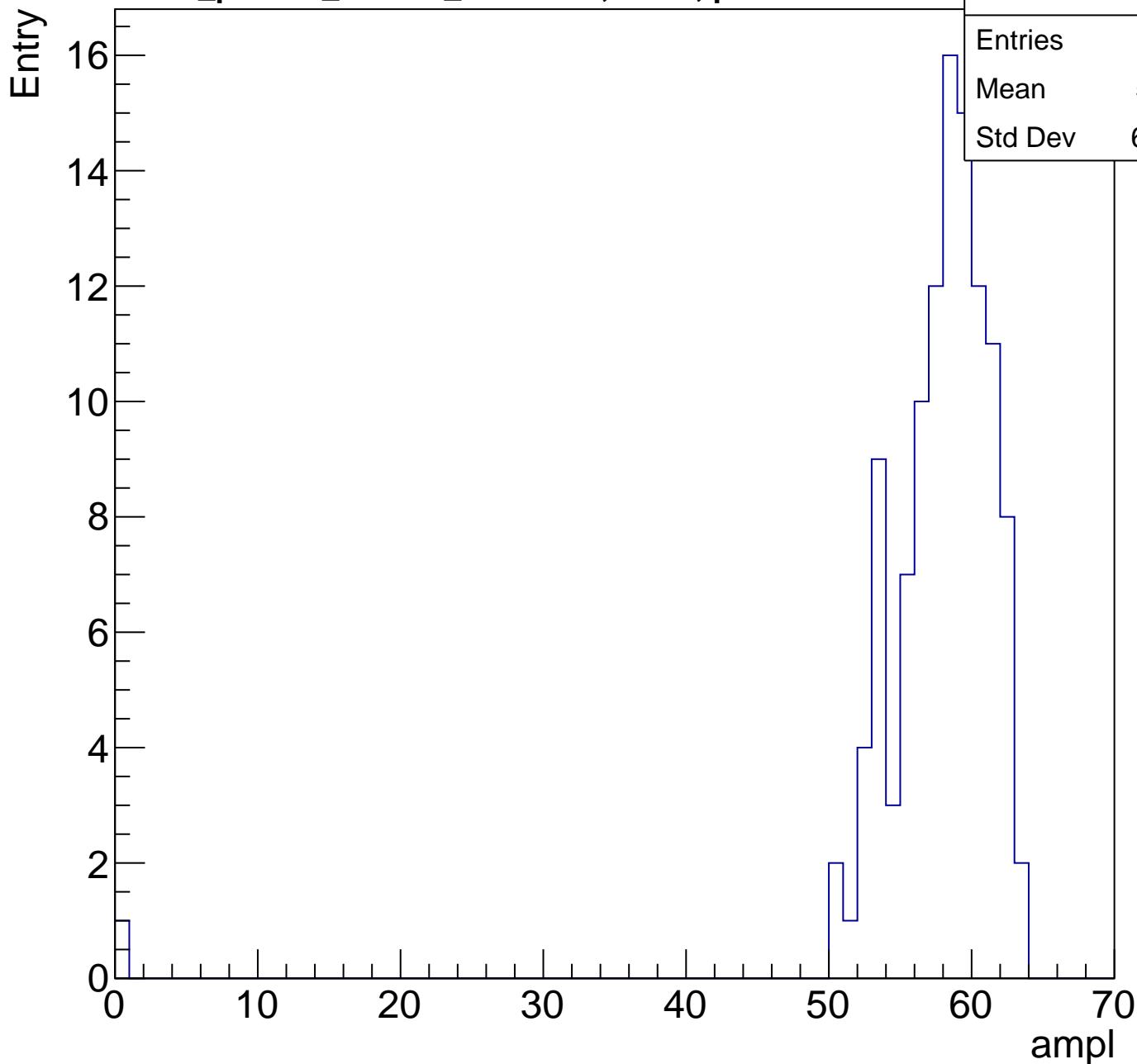
ampl



B0L101S, U22-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 113 |
| Mean | 57.11 |
| Std Dev | 6.185 |



B0L101S, U22-ch99, adc5

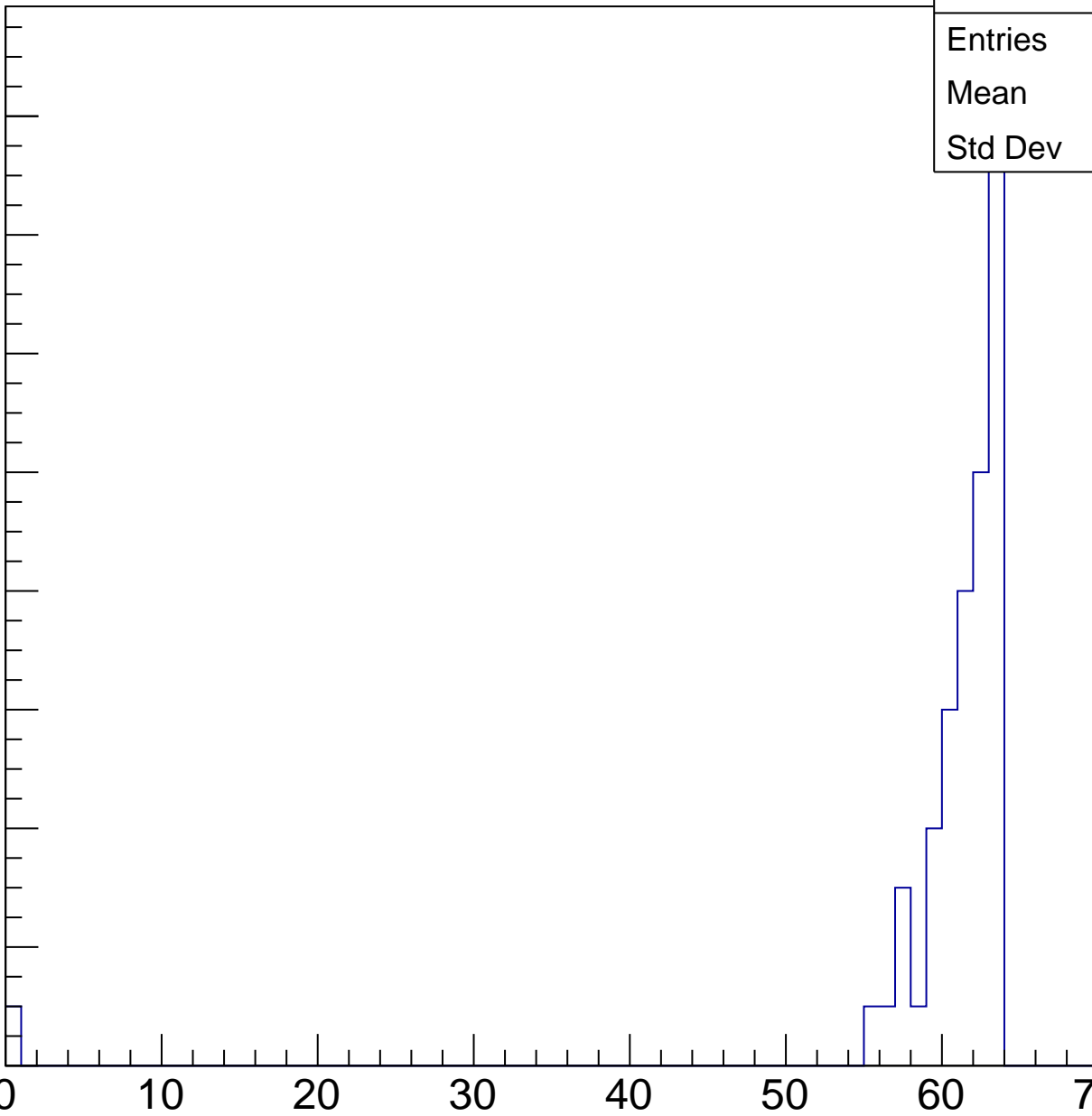
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.9 |
| Std Dev | 8.634 |

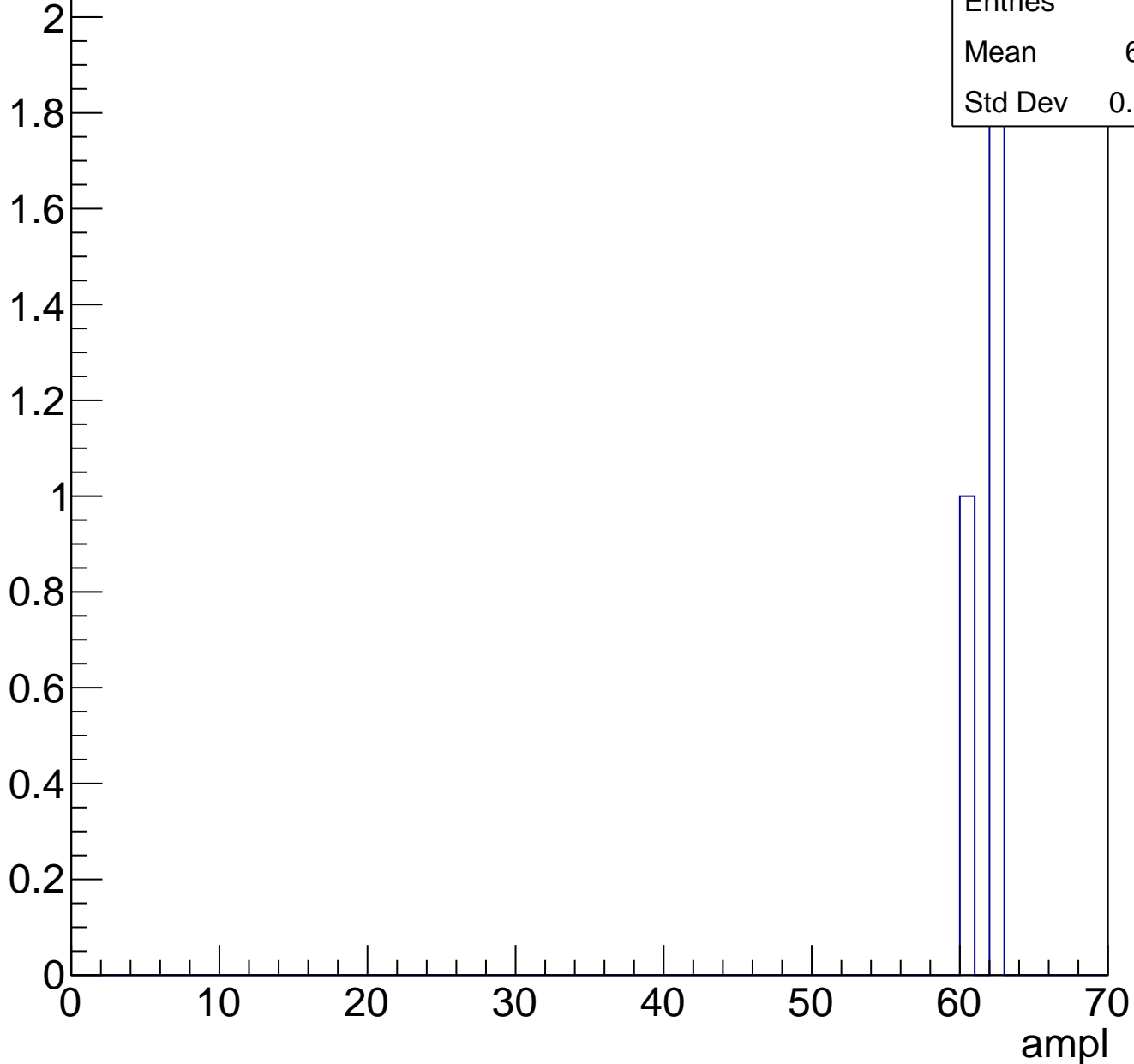
ampl



B0L101S, U22-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 27.39 |
| Std Dev | 5.596 |

Gaus mean : 28.6974

Gaus Width: 4.0945

Entry

12

10

8

6

4

2

0

0

10

20

30

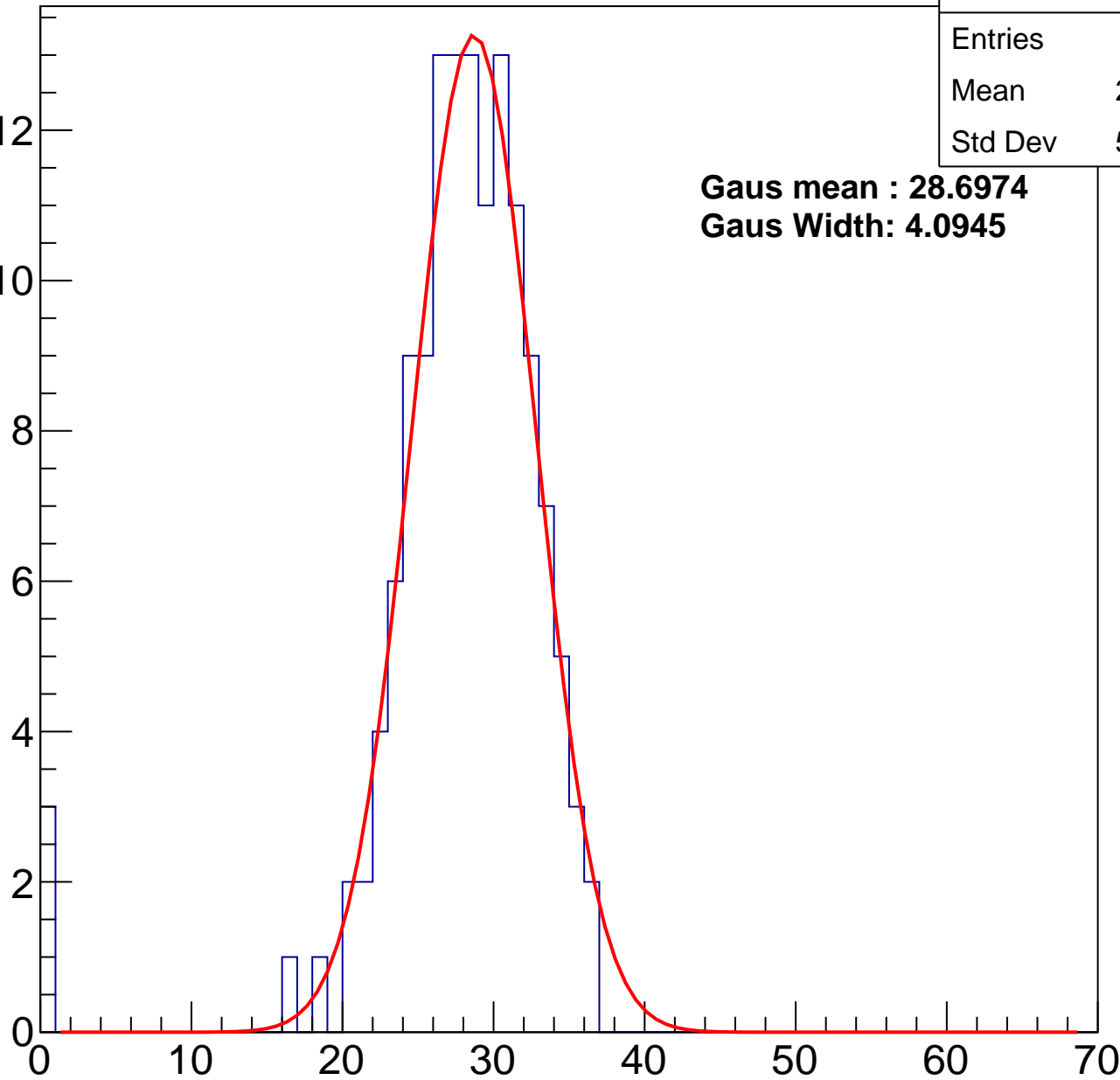
40

50

60

70

ampl



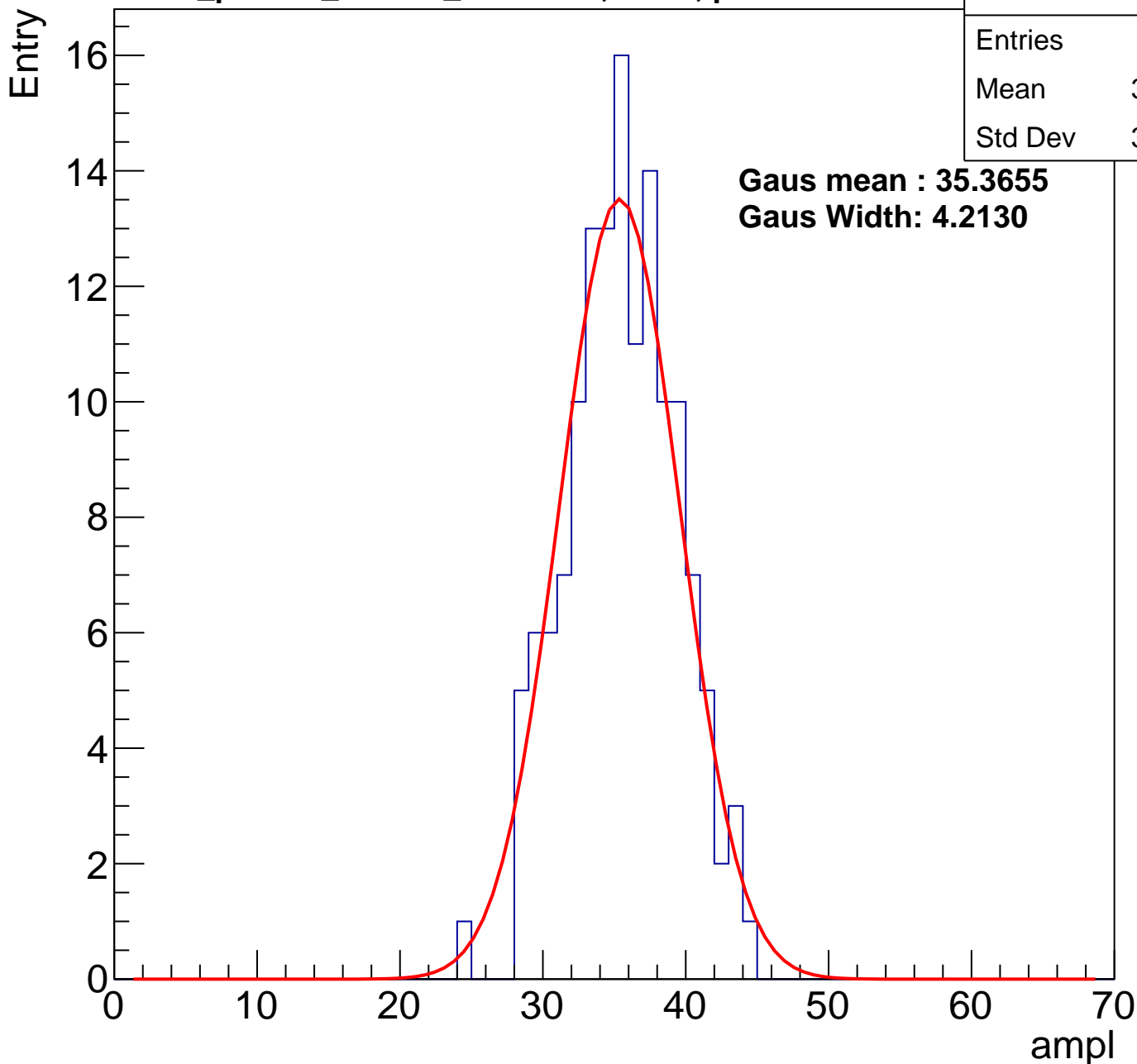
B0L101S, U22-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 140 |
| Mean | 35.09 |
| Std Dev | 3.818 |

Gaus mean : 35.3655

Gaus Width: 4.2130



B0L101S, U22-ch100, adc2

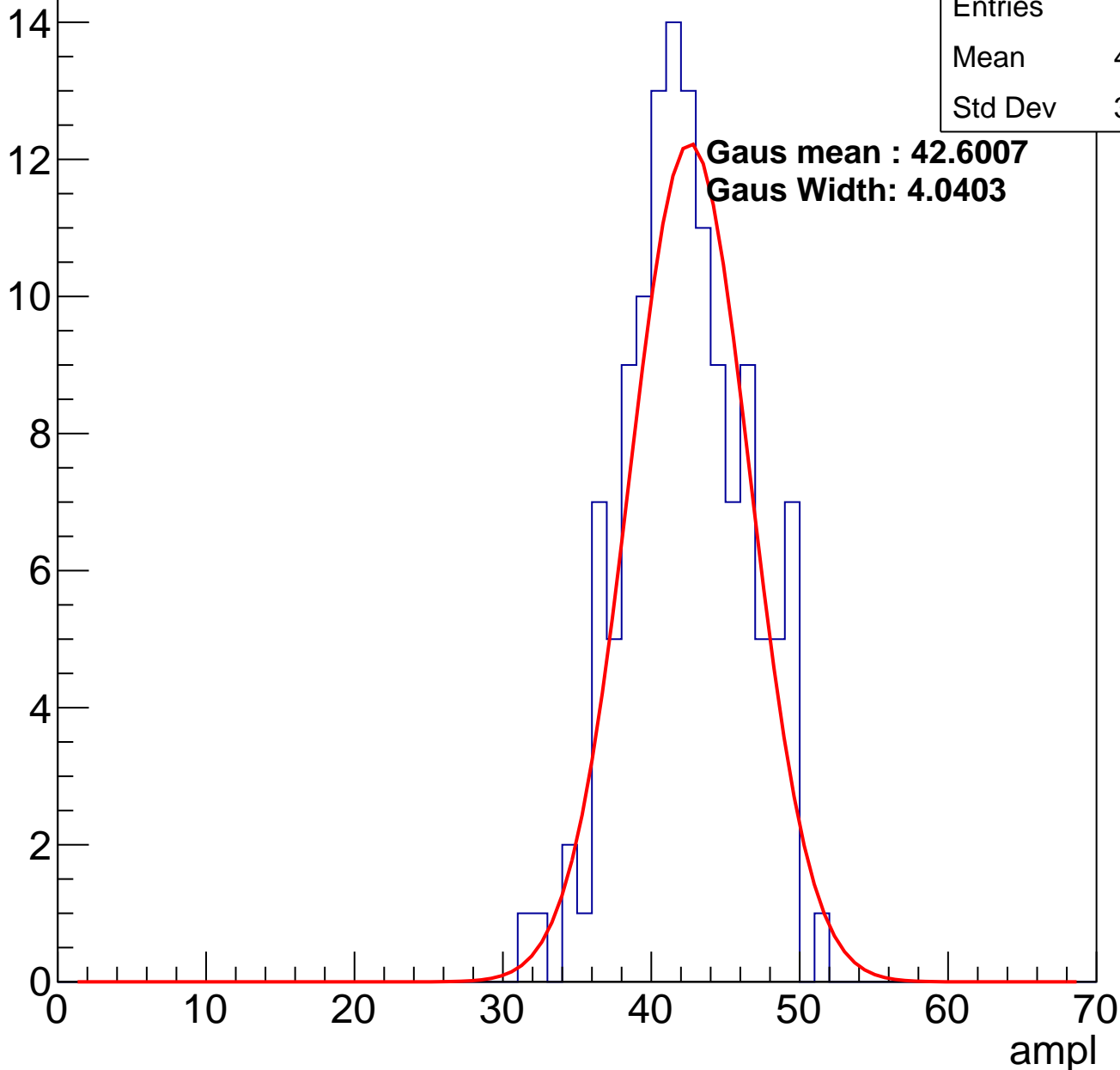
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 130 |
| Mean | 41.86 |
| Std Dev | 3.976 |

Gaus mean : 42.6007

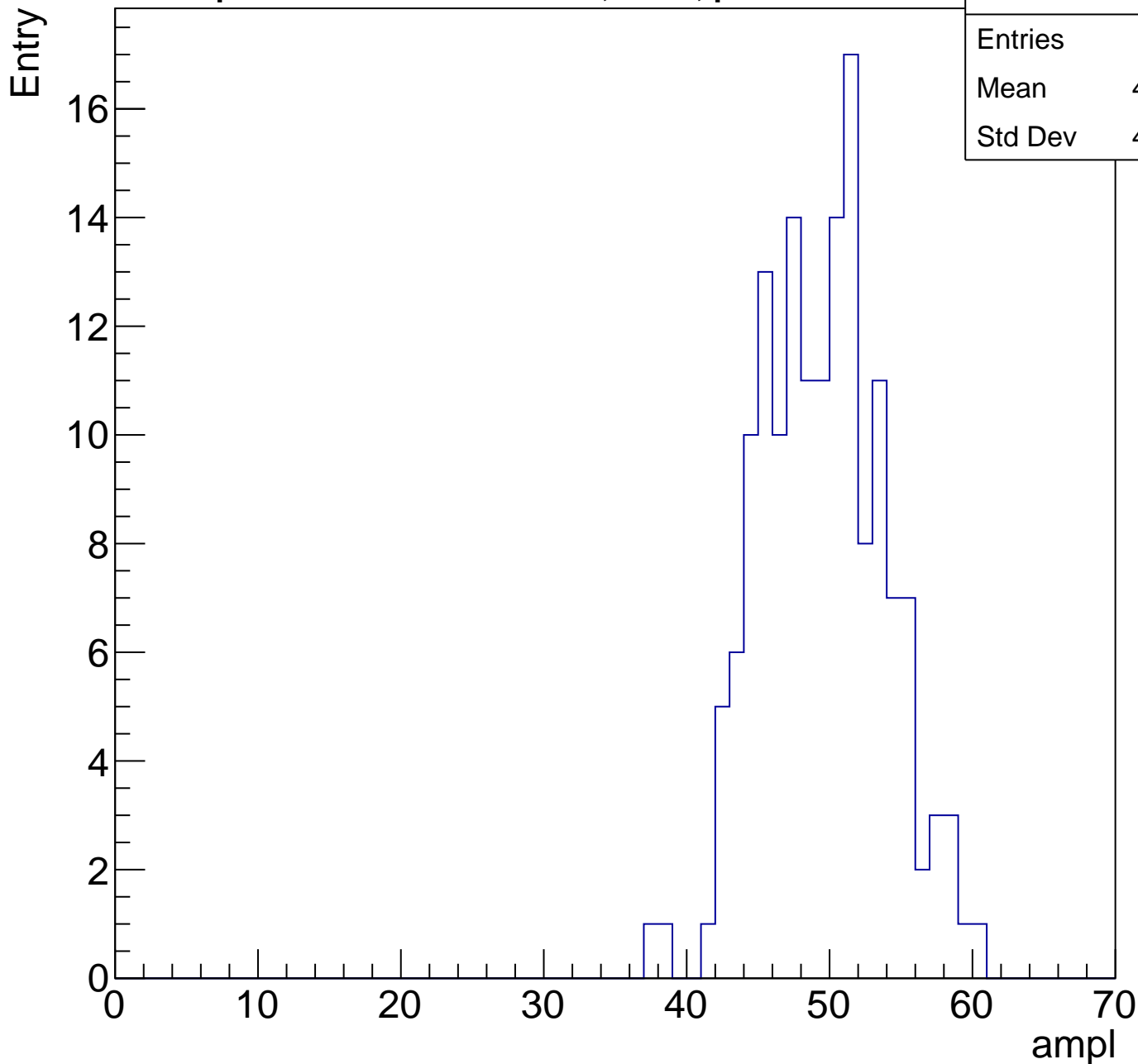
Gaus Width: 4.0403



B0L101S, U22-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 157 |
| Mean | 49.04 |
| Std Dev | 4.309 |



B0L101S, U22-ch100, adc4

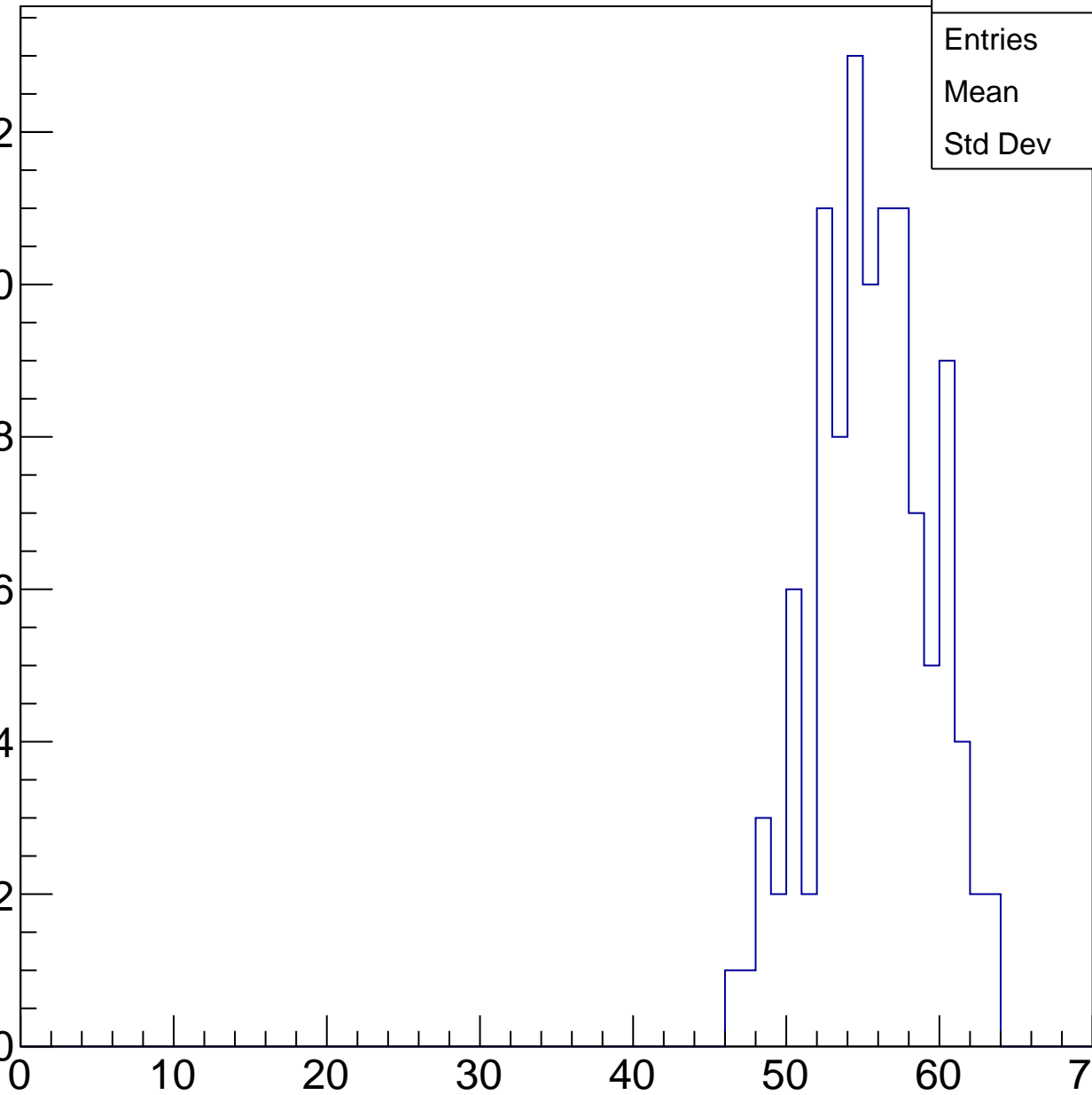
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 108 |
| Mean | 55.21 |
| Std Dev | 3.699 |

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U22-ch100, adc5

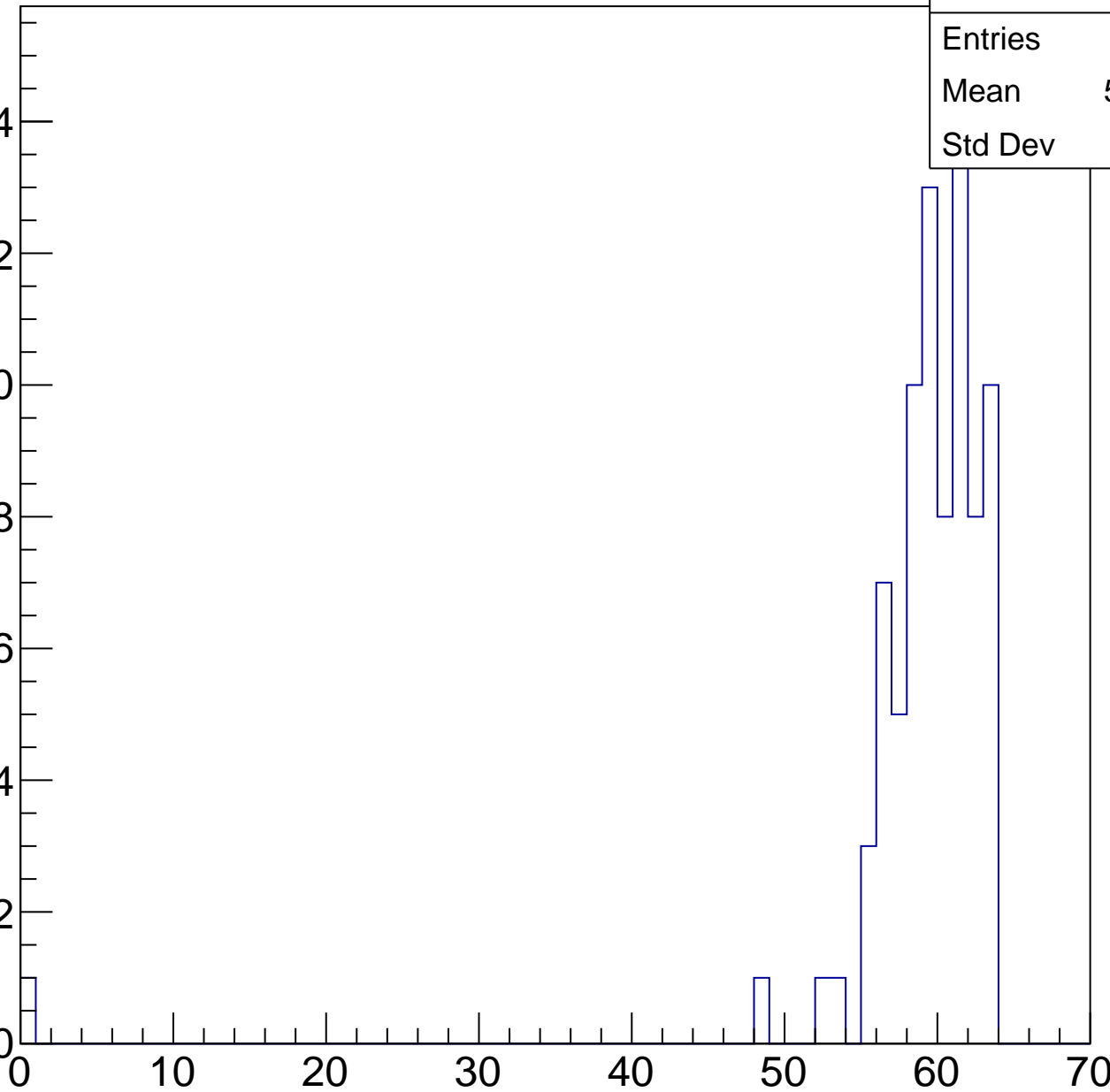
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 58.59 |
| Std Dev | 7.04 |

ampl

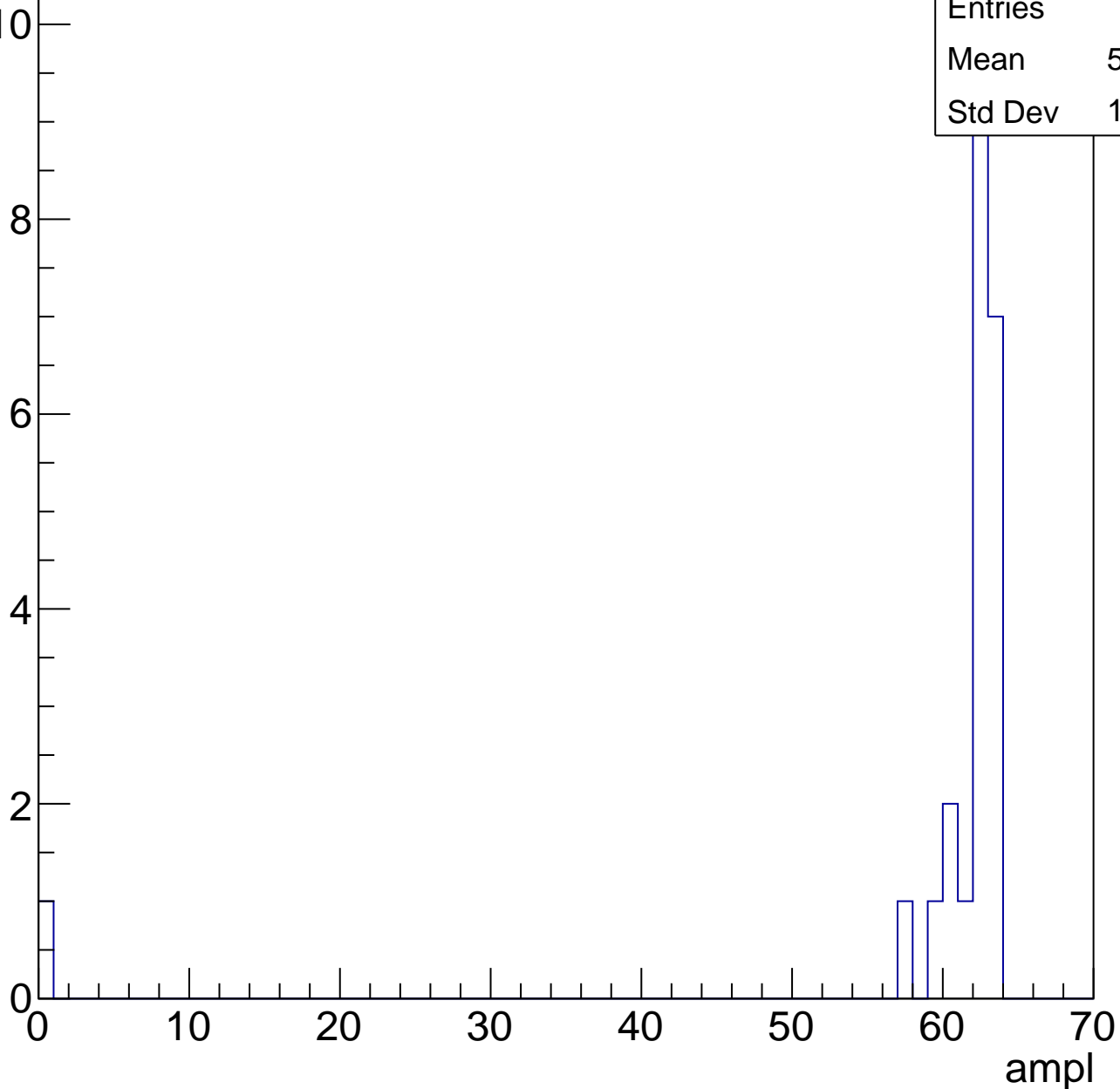


B0L101S, U22-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 23 |
| Mean | 59.04 |
| Std Dev | 12.67 |



B0L101S, U22-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B0L101S, U22-ch101, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 142 |
| Mean | 29.61 |
| Std Dev | 4.3 |

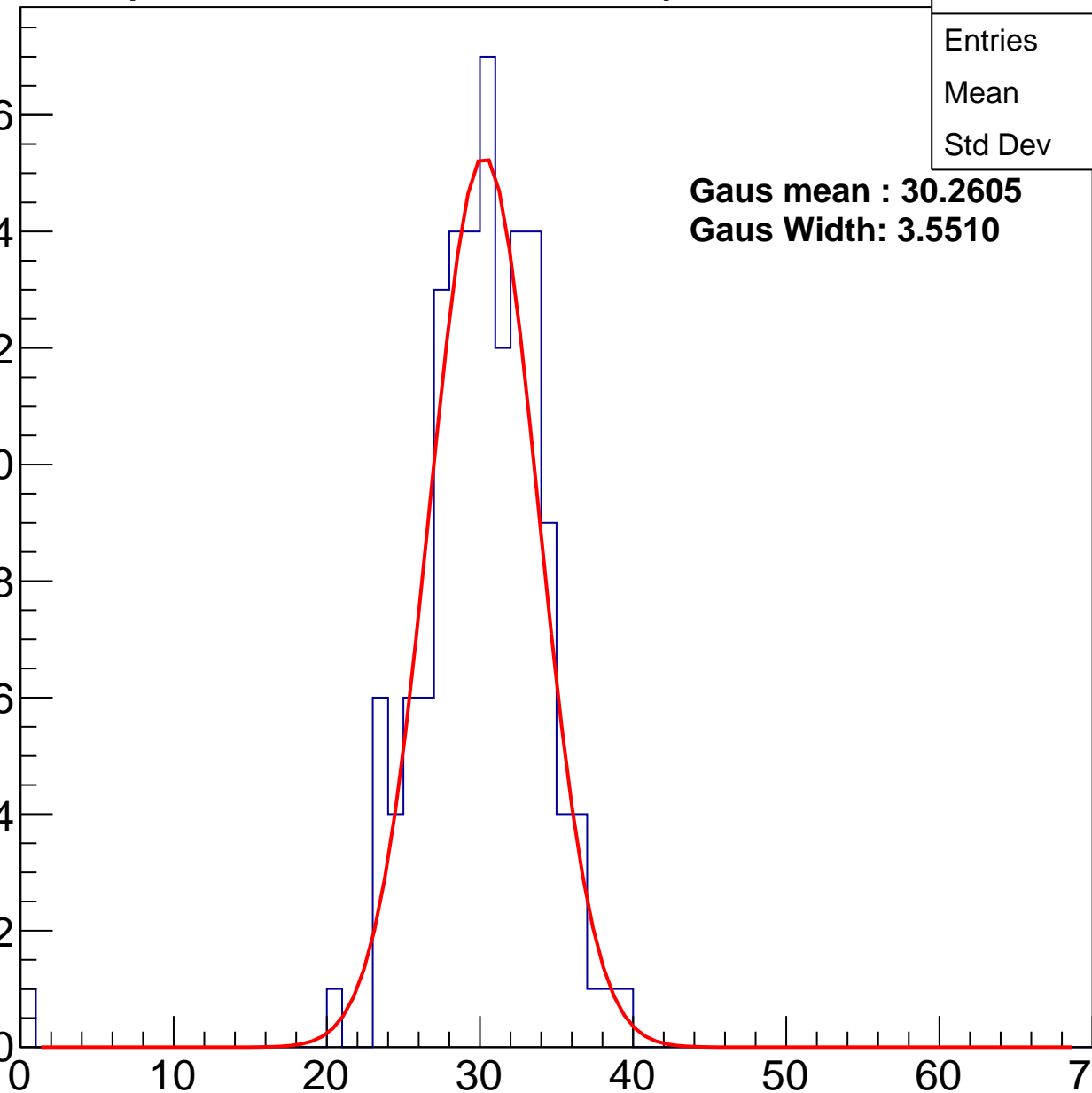
Gaus mean : 30.2605

Gaus Width: 3.5510

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch101, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 154 |
| Mean | 37.47 |
| Std Dev | 3.711 |

Gaus mean : 37.9544

Gaus Width: 3.6717

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

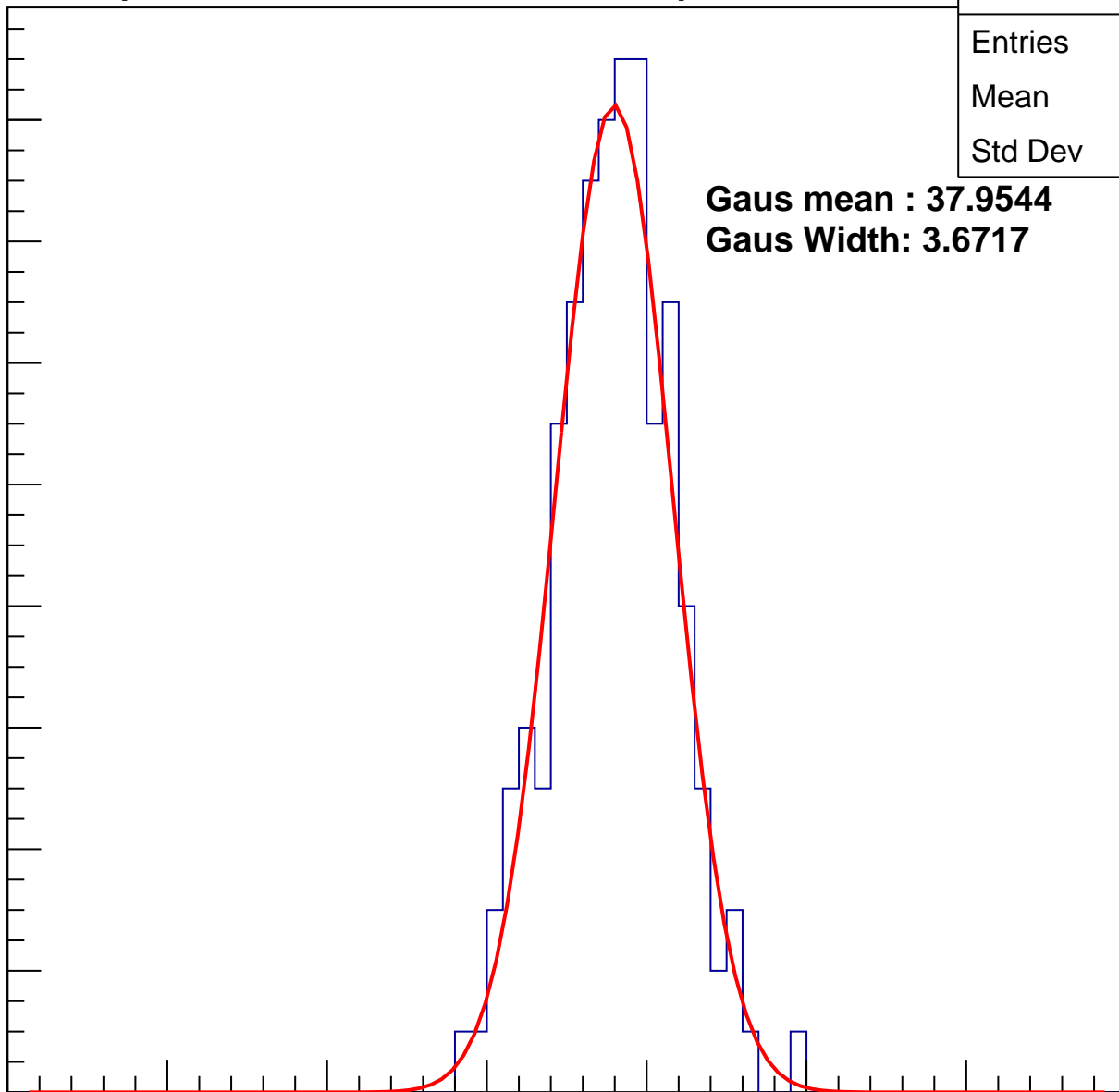
40

50

60

70

ampl



B0L101S, U22-ch101, adc2

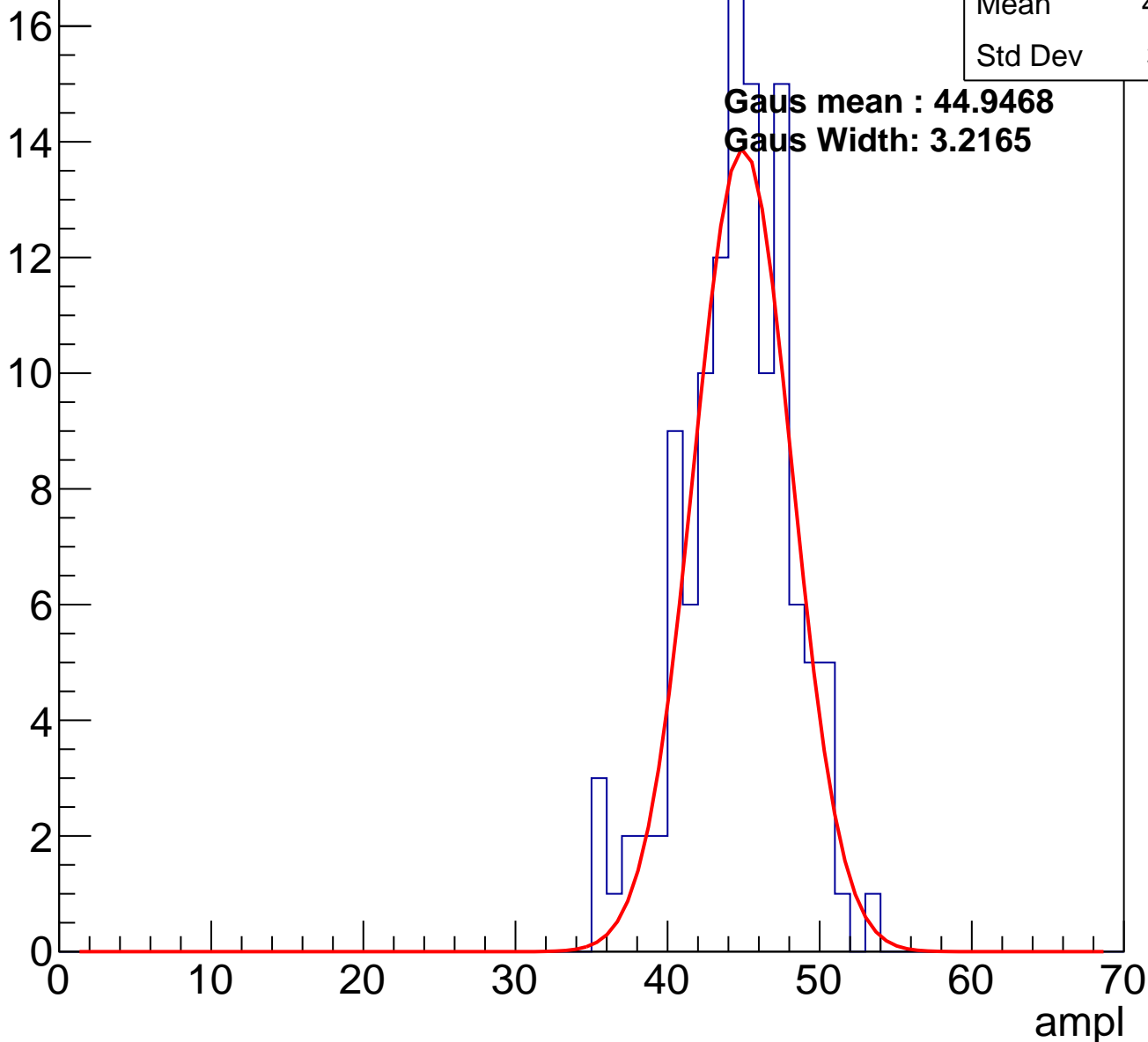
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 122 |
| Mean | 44.15 |
| Std Dev | 3.501 |

Gaus mean : 44.9468

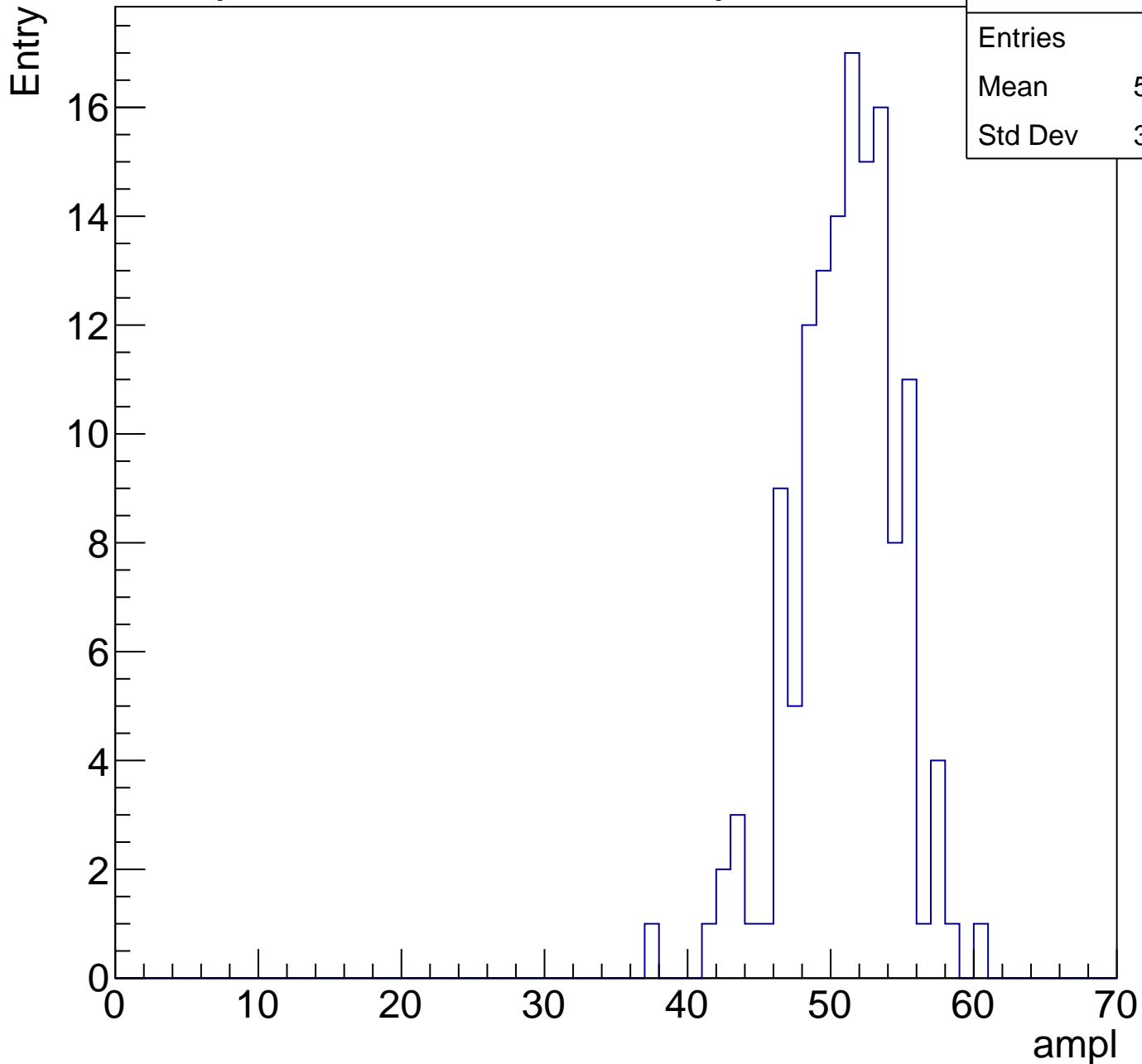
Gaus Width: 3.2165



B0L101S, U22-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 136 |
| Mean | 50.56 |
| Std Dev | 3.652 |



B0L101S, U22-ch101, adc4

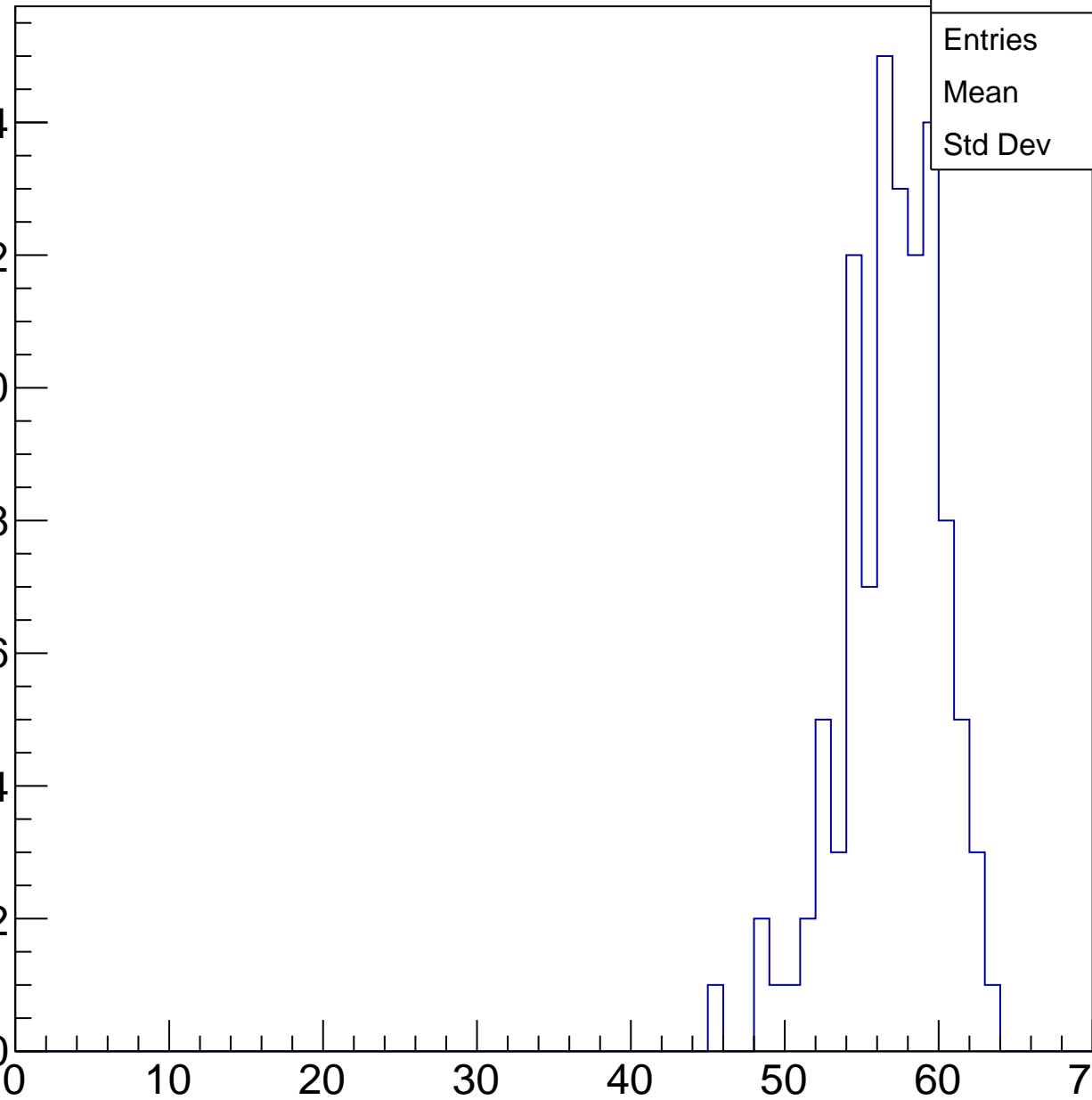
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 105 |
| Mean | 56.49 |
| Std Dev | 3.263 |

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch101, adc5

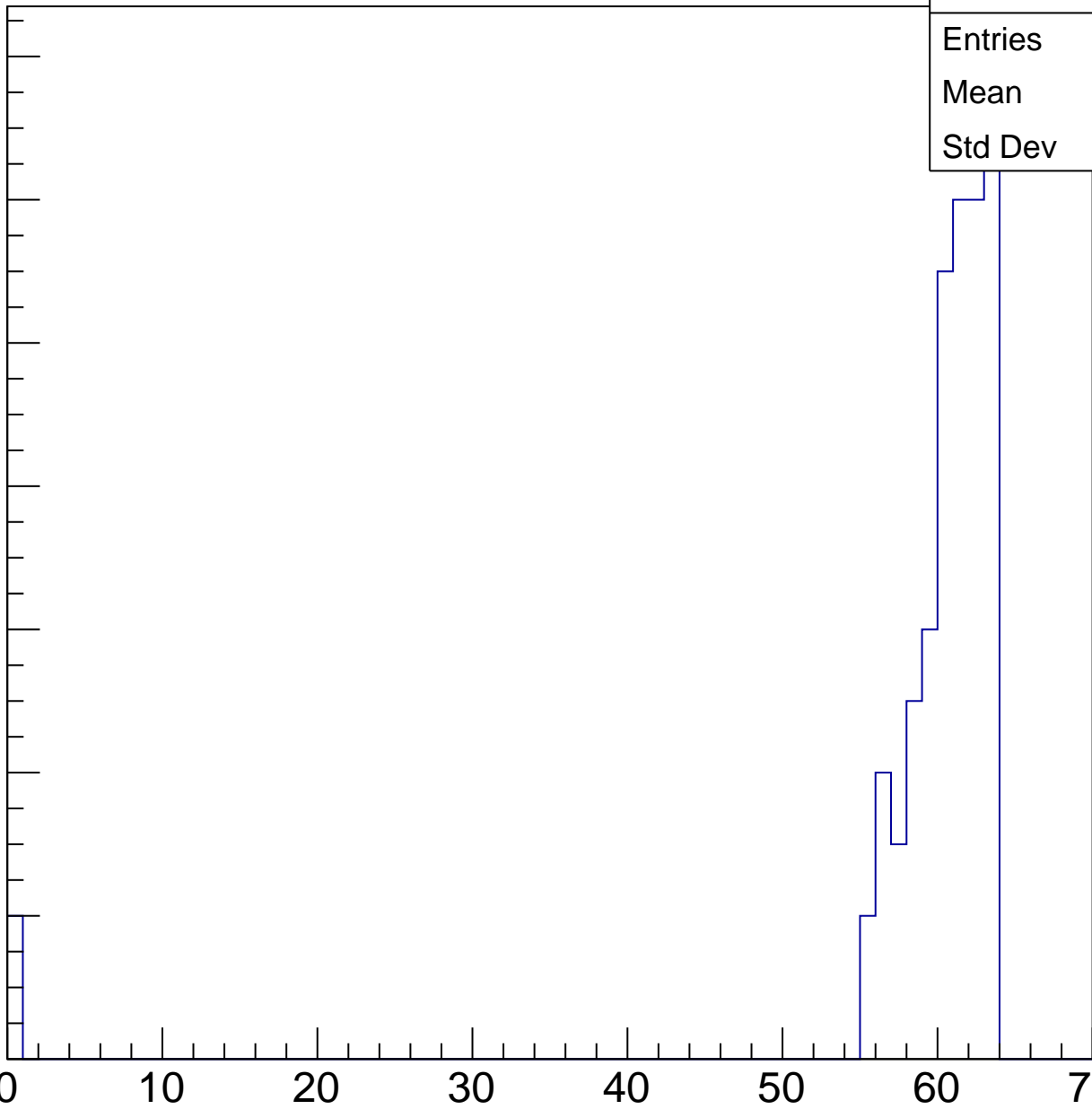
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 58.69 |
| Std Dev | 10.23 |

ampl

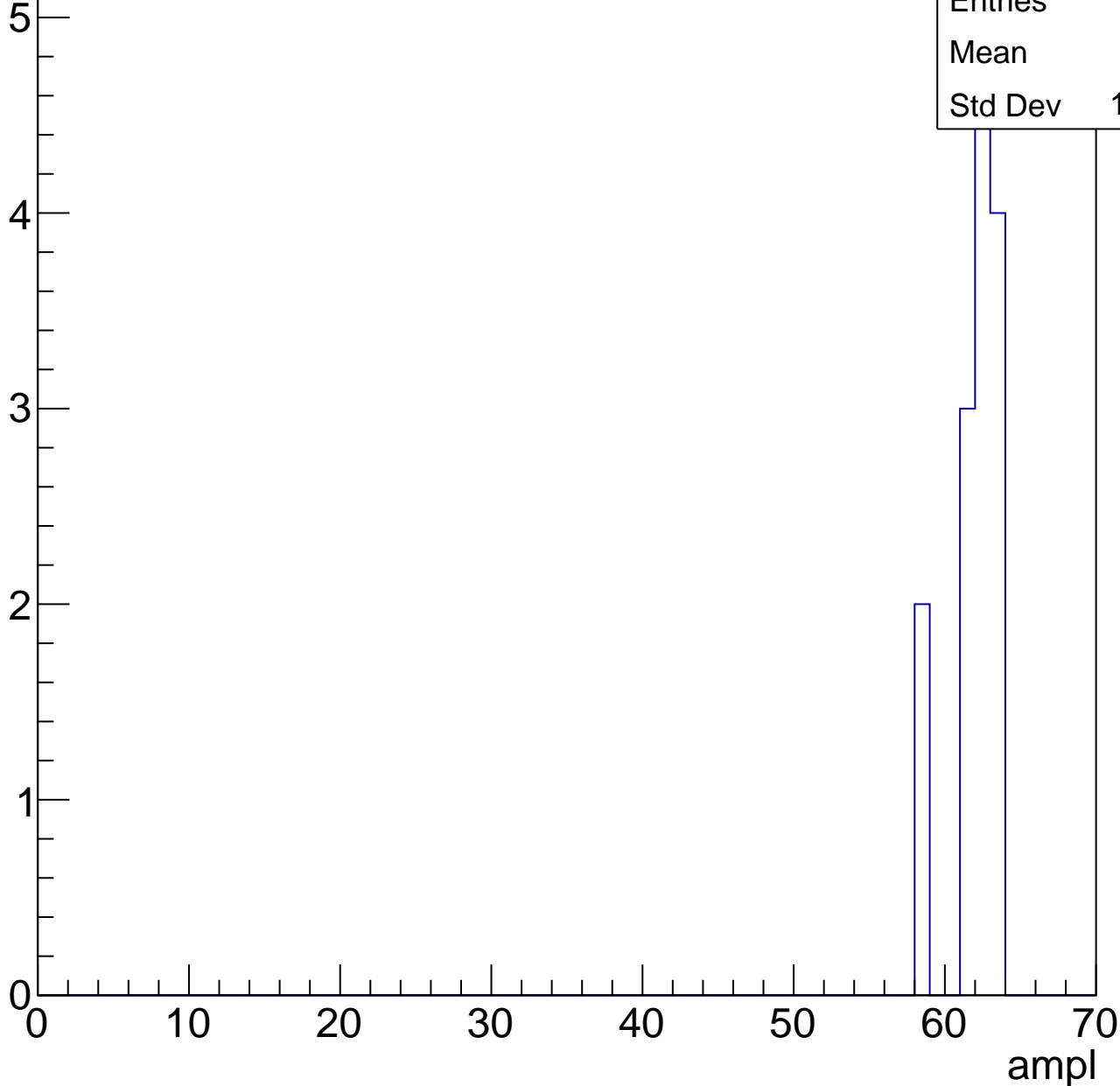


B0L101S, U22-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.5 |
| Std Dev | 1.592 |



B0L101S, U22-ch101, adc7

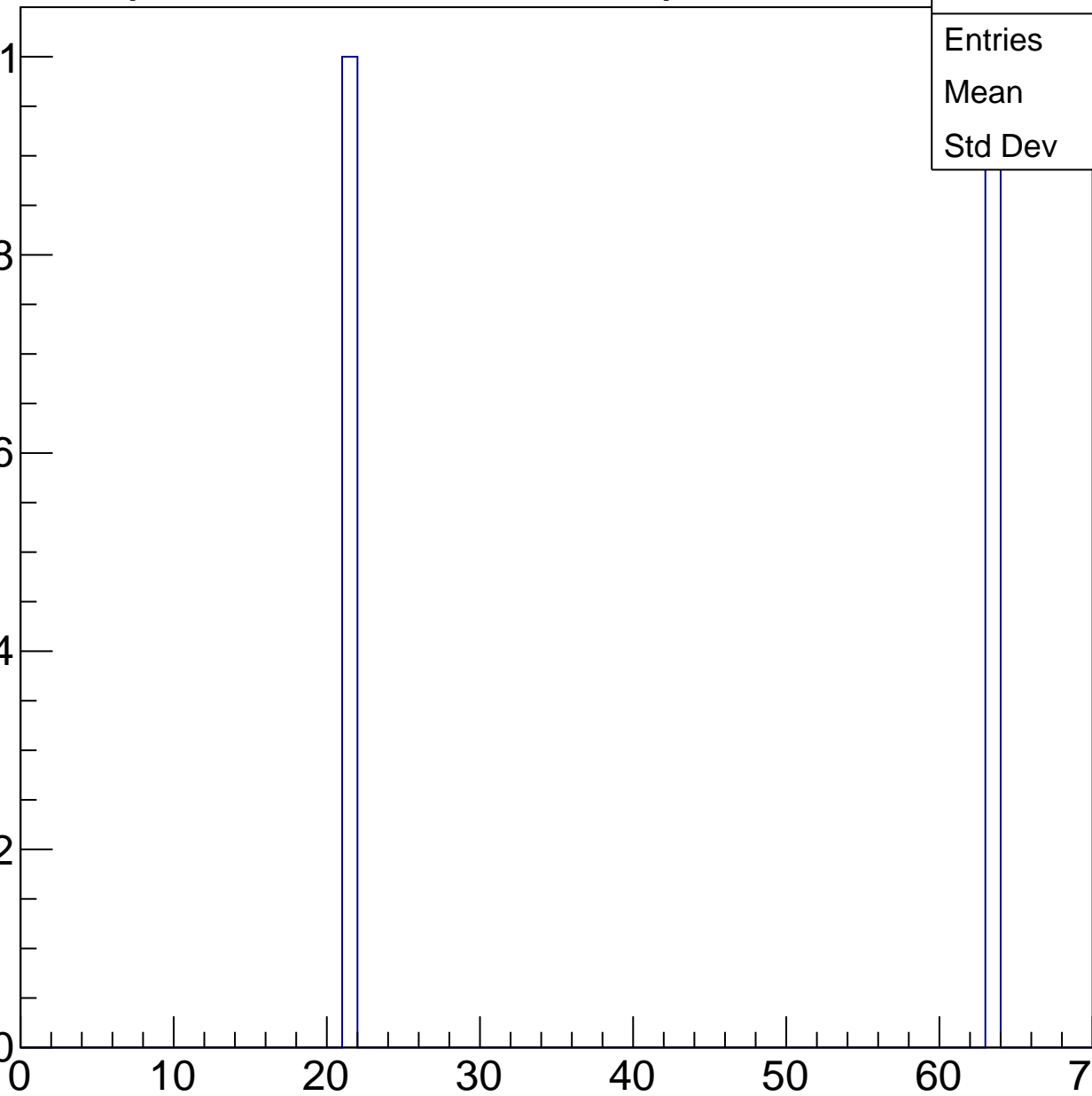
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 42 |
| Std Dev | 21 |

ampl



B0L101S, U22-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 29.77 |
| Std Dev | 5.137 |

Gaus mean : 29.6713

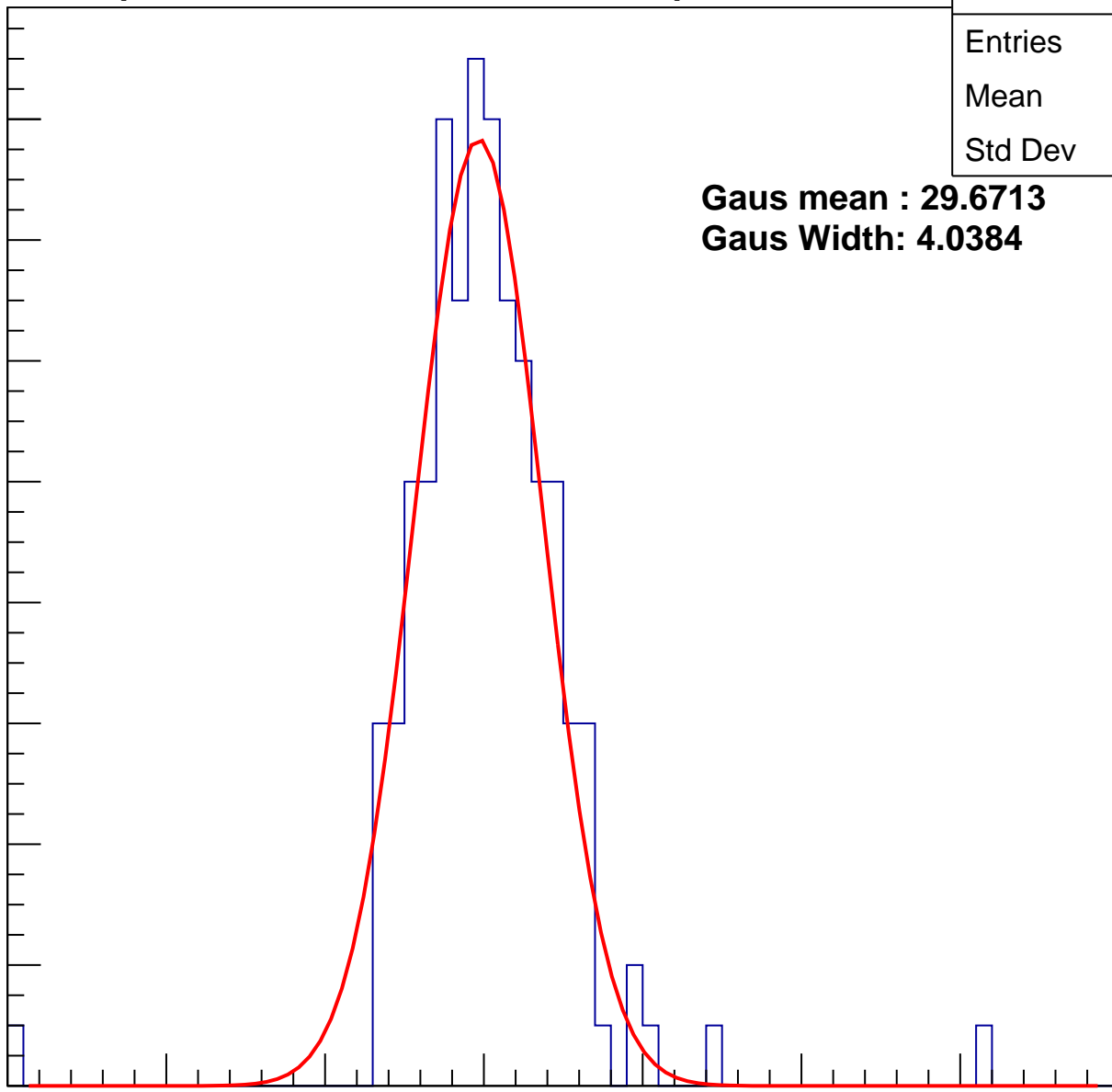
Gaus Width: 4.0384

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch102, adc1

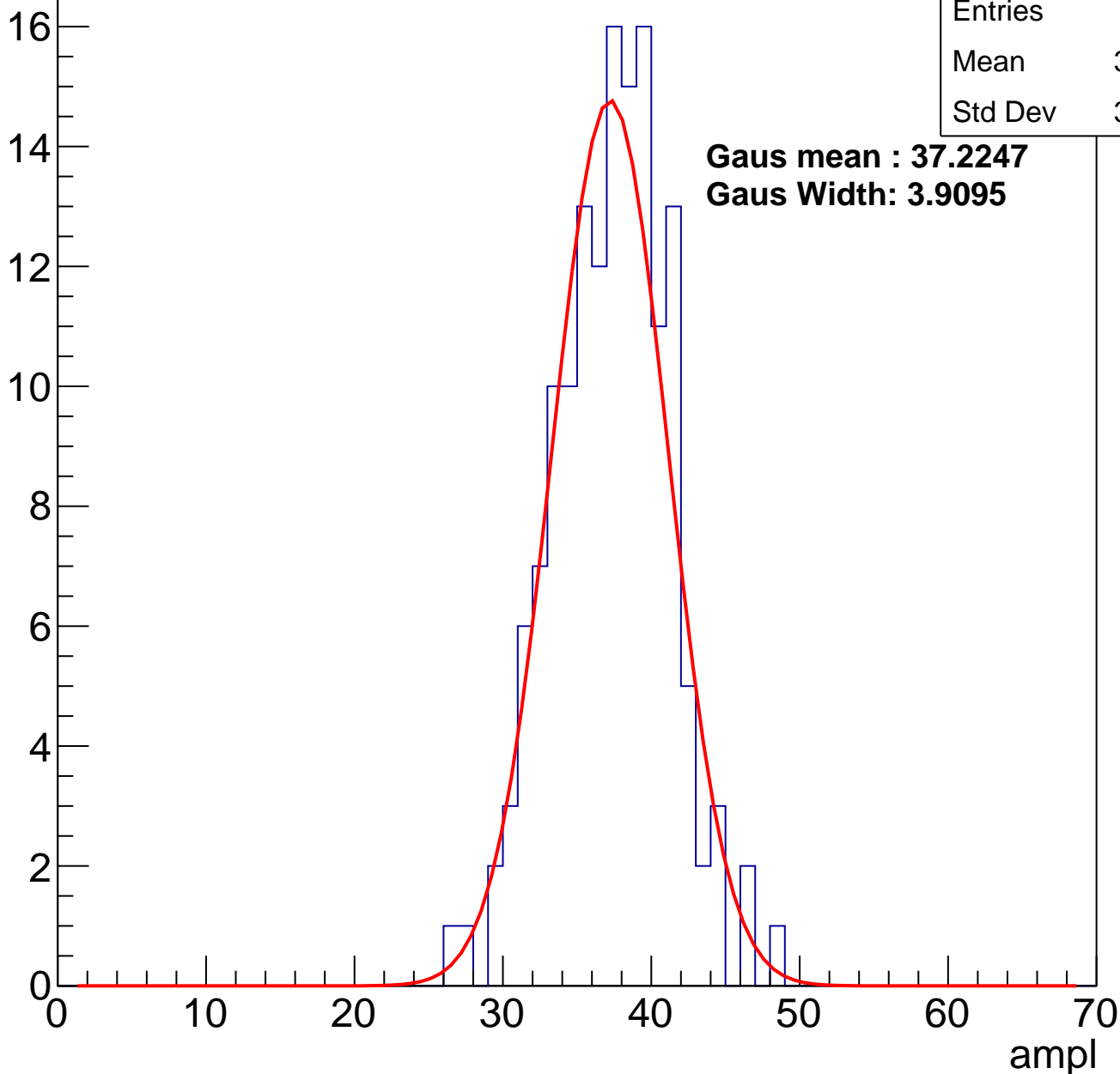
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 36.88 |
| Std Dev | 3.853 |

Gaus mean : 37.2247

Gaus Width: 3.9095

Entry



B0L101S, U22-ch102, adc2

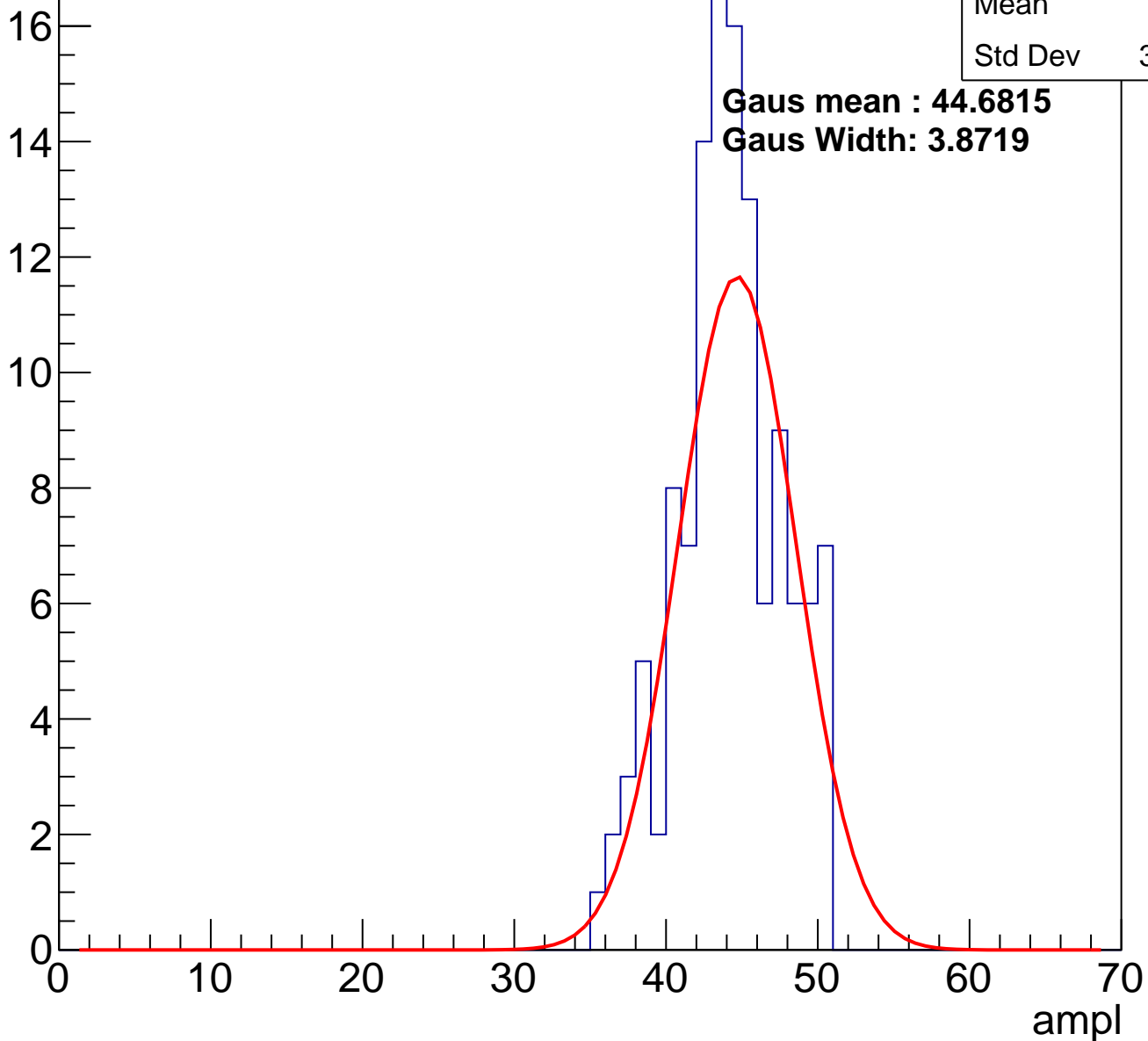
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 122 |
| Mean | 43.7 |
| Std Dev | 3.454 |

Gaus mean : 44.6815

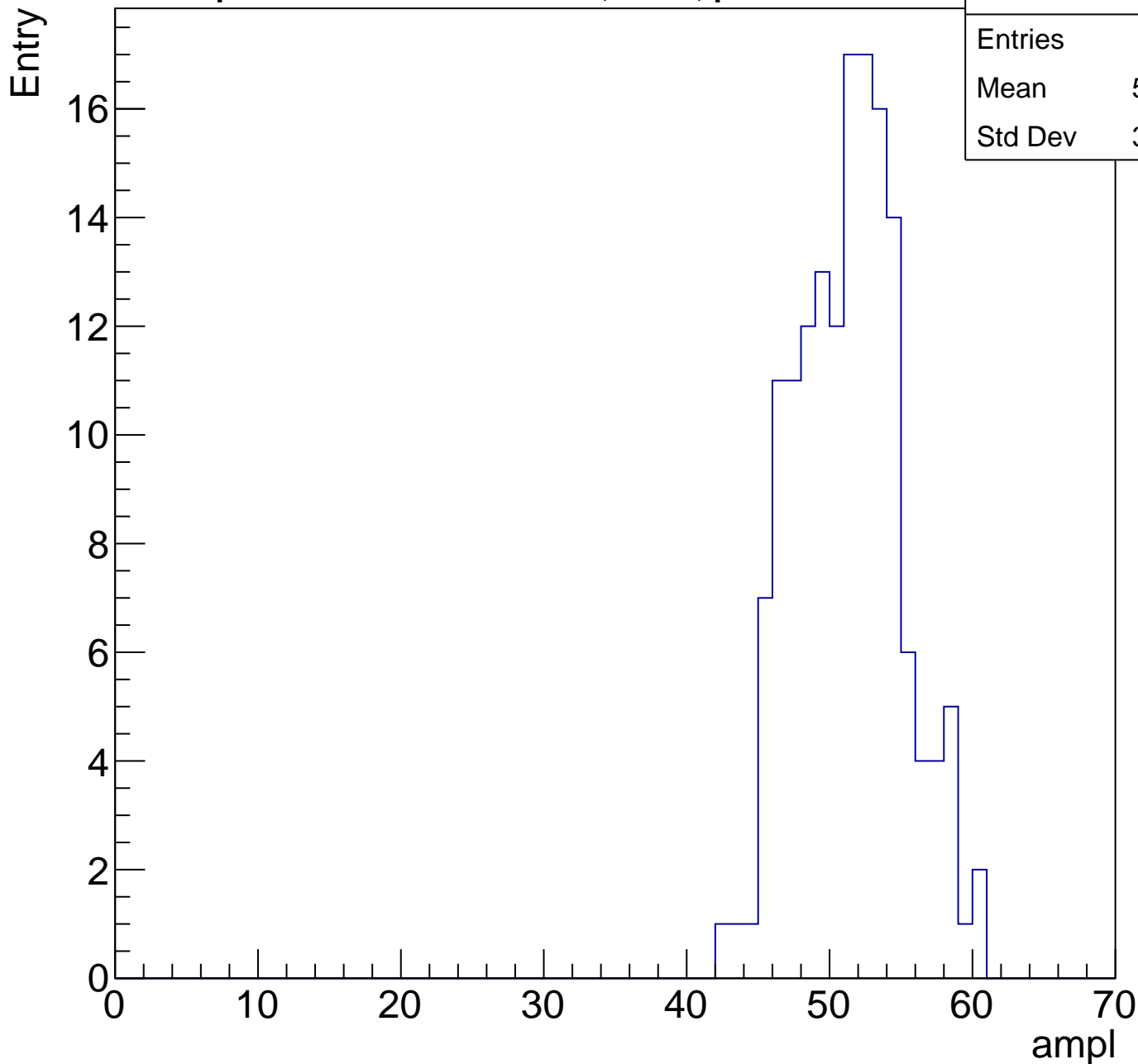
Gaus Width: 3.8719



B0L101S, U22-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 50.88 |
| Std Dev | 3.667 |



B0L101S, U22-ch102, adc4

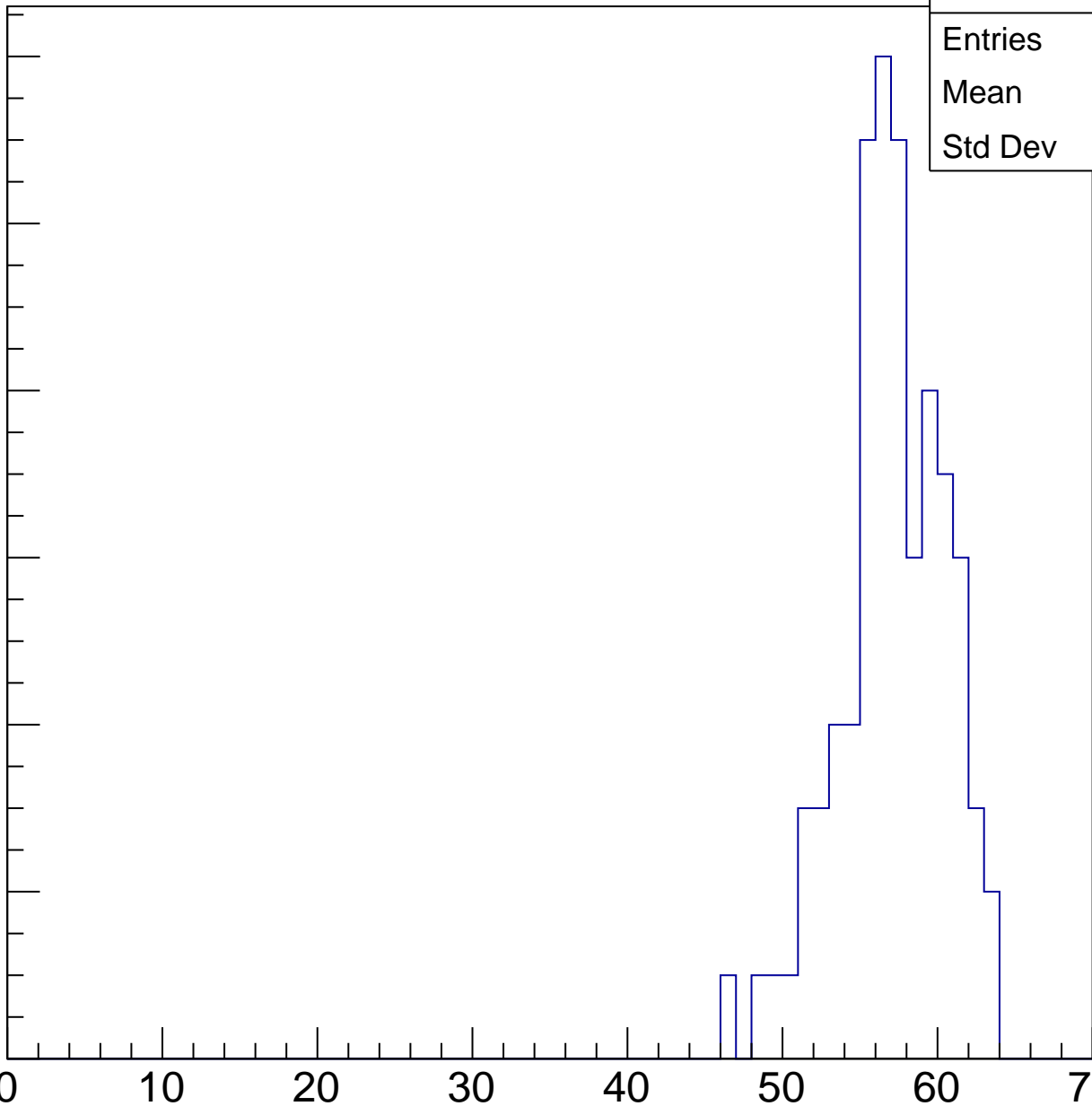
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 84 |
| Mean | 56.57 |
| Std Dev | 3.42 |

ampl



B0L101S, U22-ch102, adc5

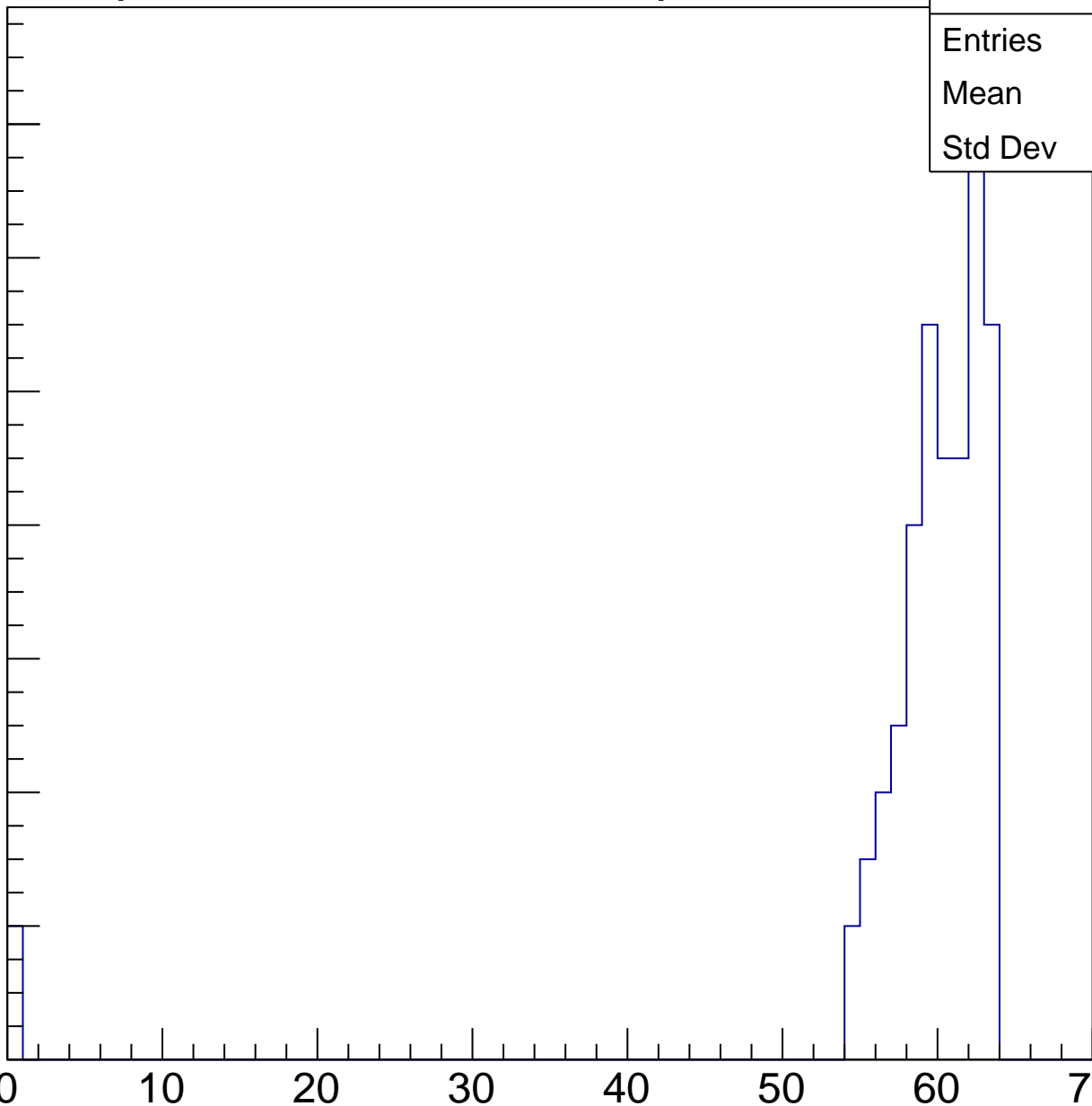
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 58.32 |
| Std Dev | 9.707 |

ampl

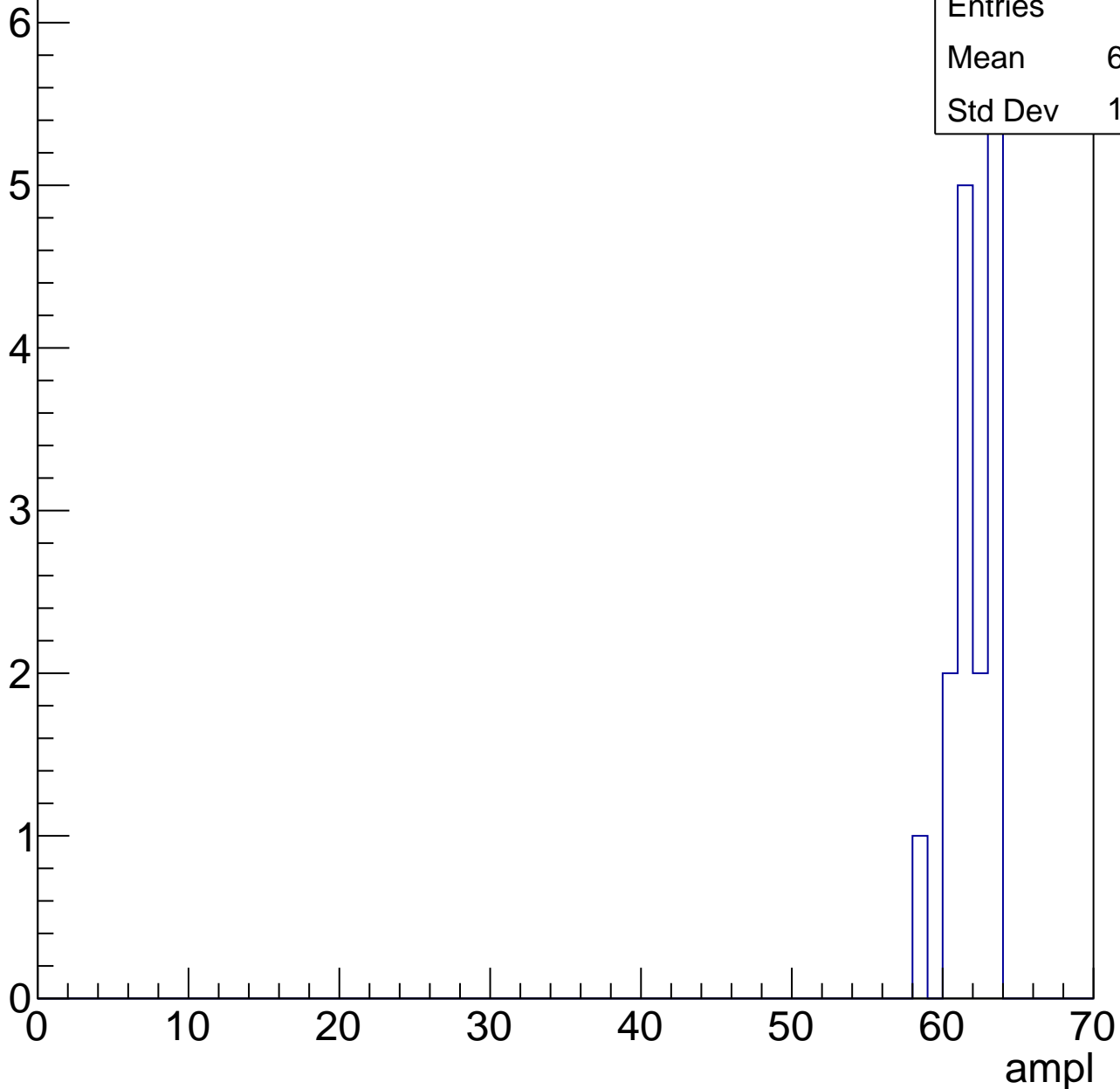


B0L101S, U22-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

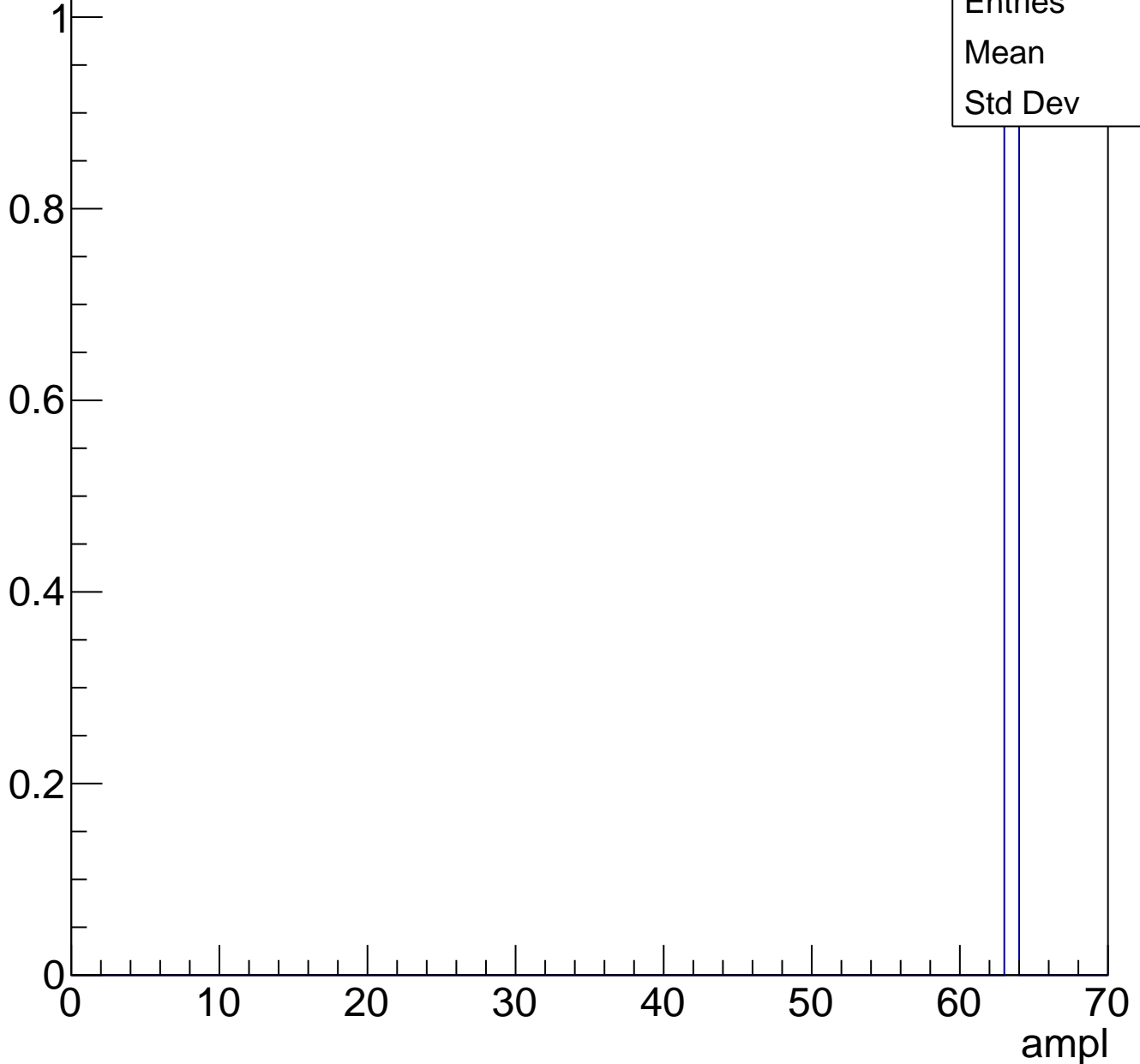
| | |
|---------|-------|
| Entries | 16 |
| Mean | 61.56 |
| Std Dev | 1.413 |



B0L101S, U22-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B0L101S, U22-ch103, adc0

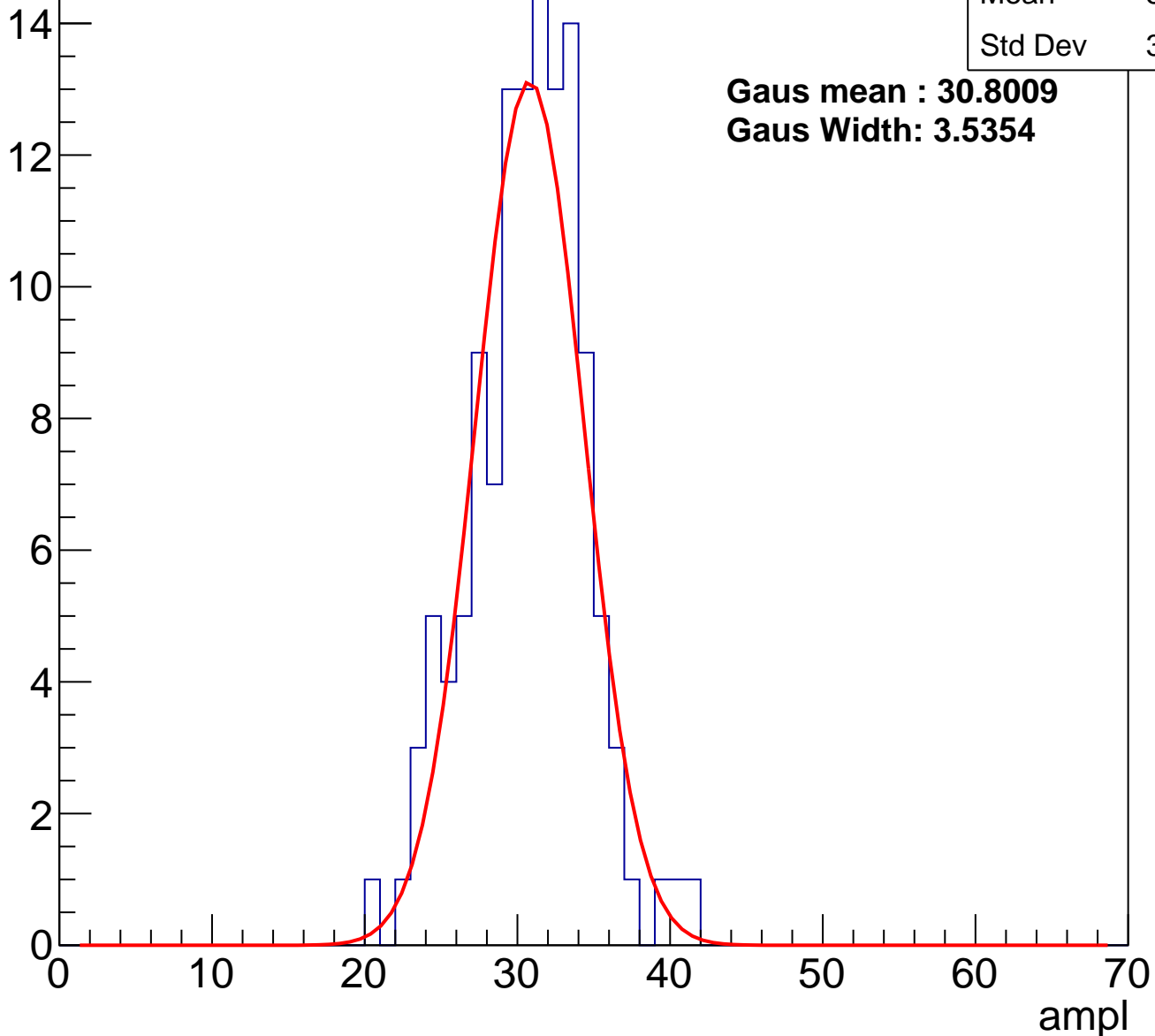
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 30.29 |
| Std Dev | 3.707 |

Gaus mean : 30.8009

Gaus Width: 3.5354

Entry



B0L101S, U22-ch103, adc1

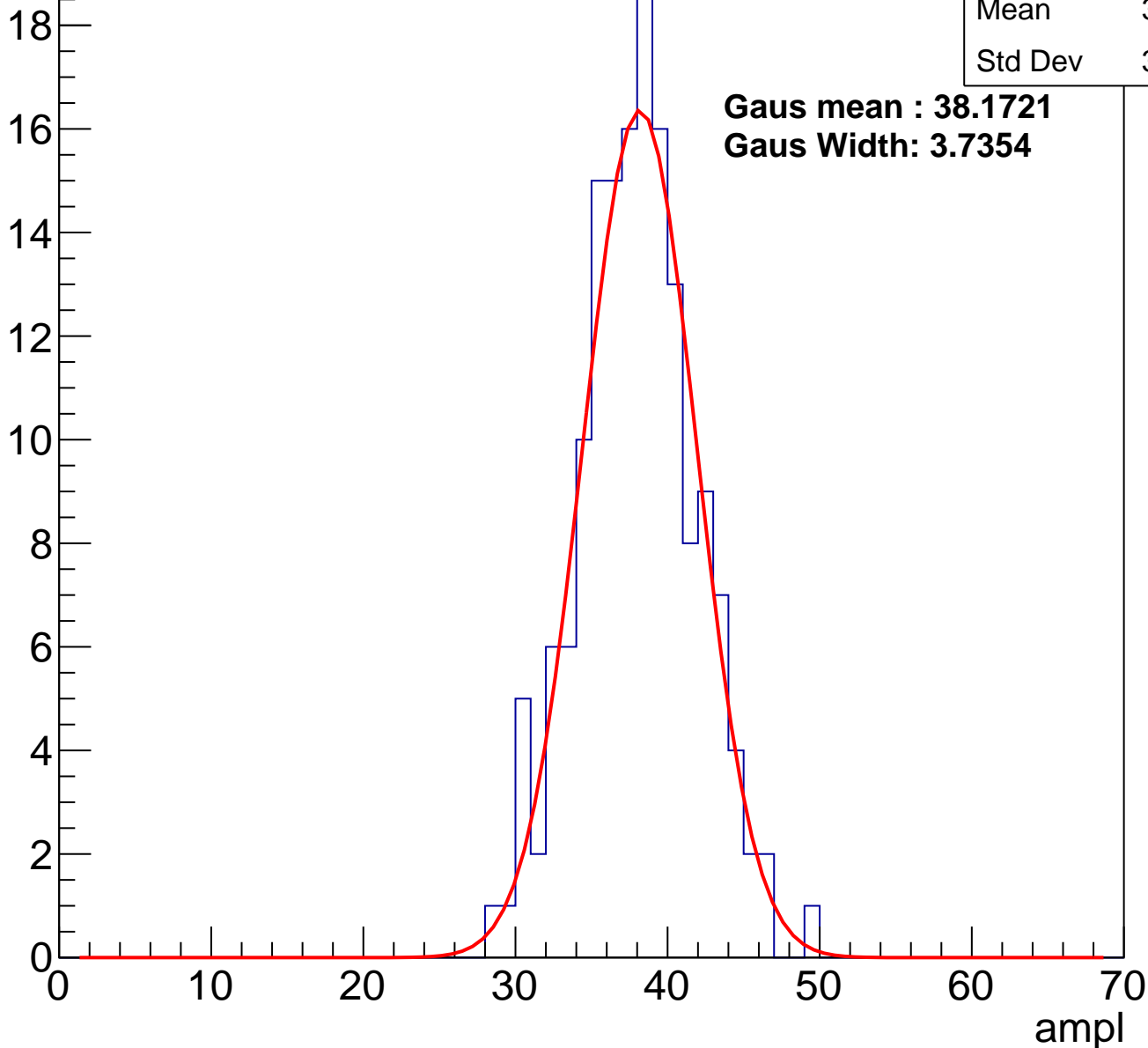
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 37.57 |
| Std Dev | 3.779 |

Gaus mean : 38.1721

Gaus Width: 3.7354

Entry



B0L101S, U22-ch103, adc2

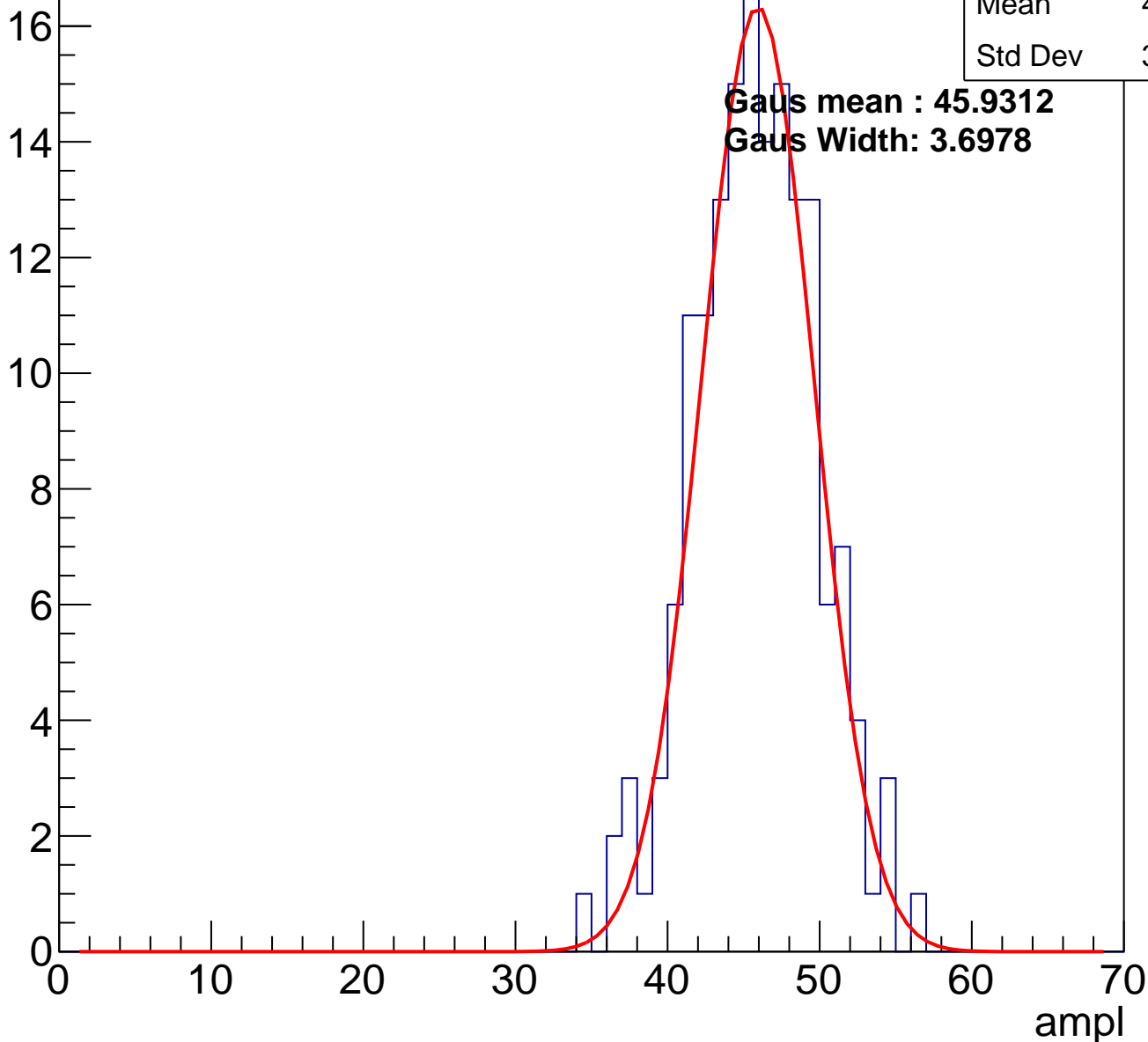
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 160 |
| Mean | 45.34 |
| Std Dev | 3.977 |

Gaus mean : 45.9312

Gaus Width: 3.6978

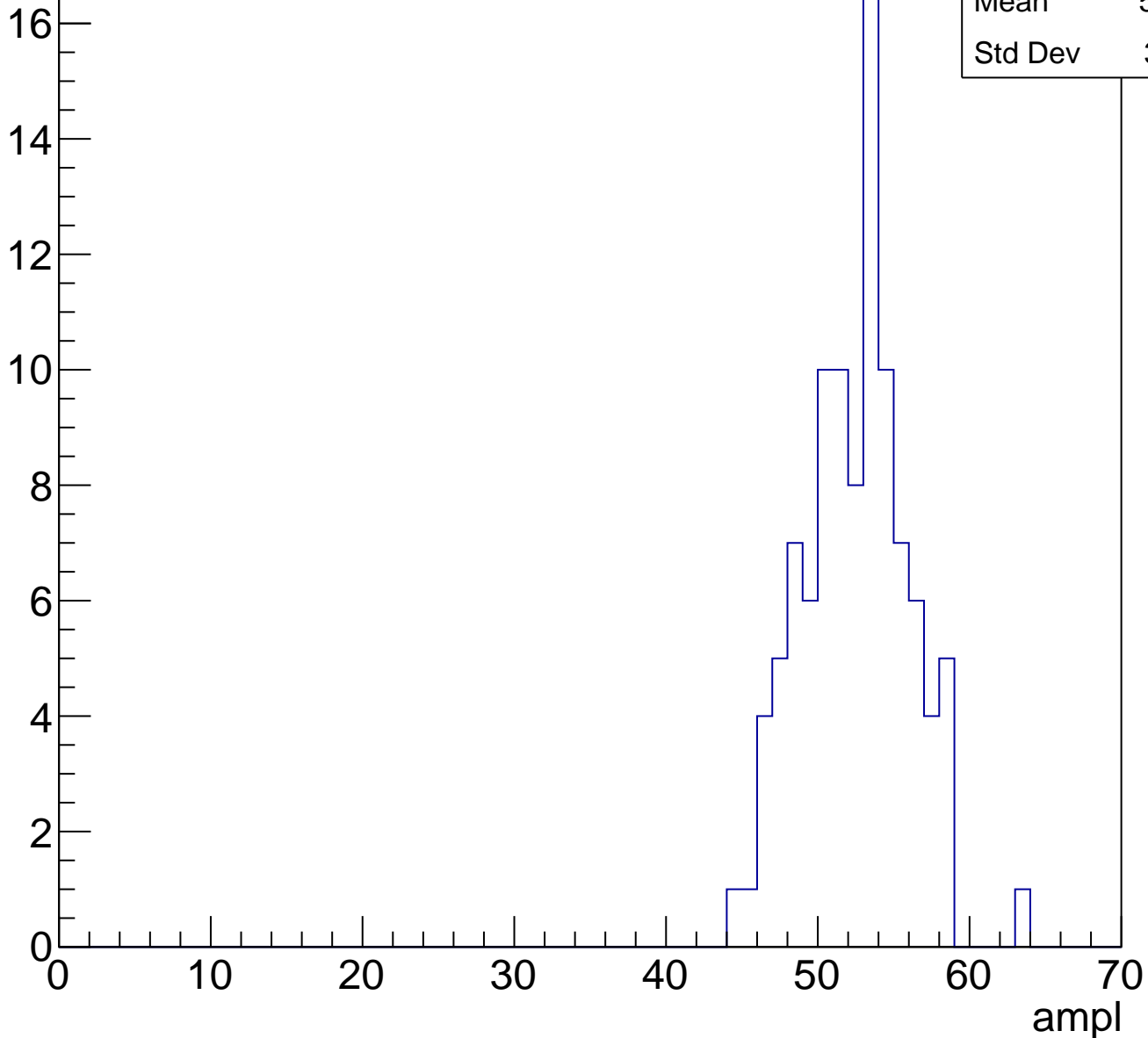


B0L101S, U22-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 102 |
| Mean | 52.03 |
| Std Dev | 3.451 |

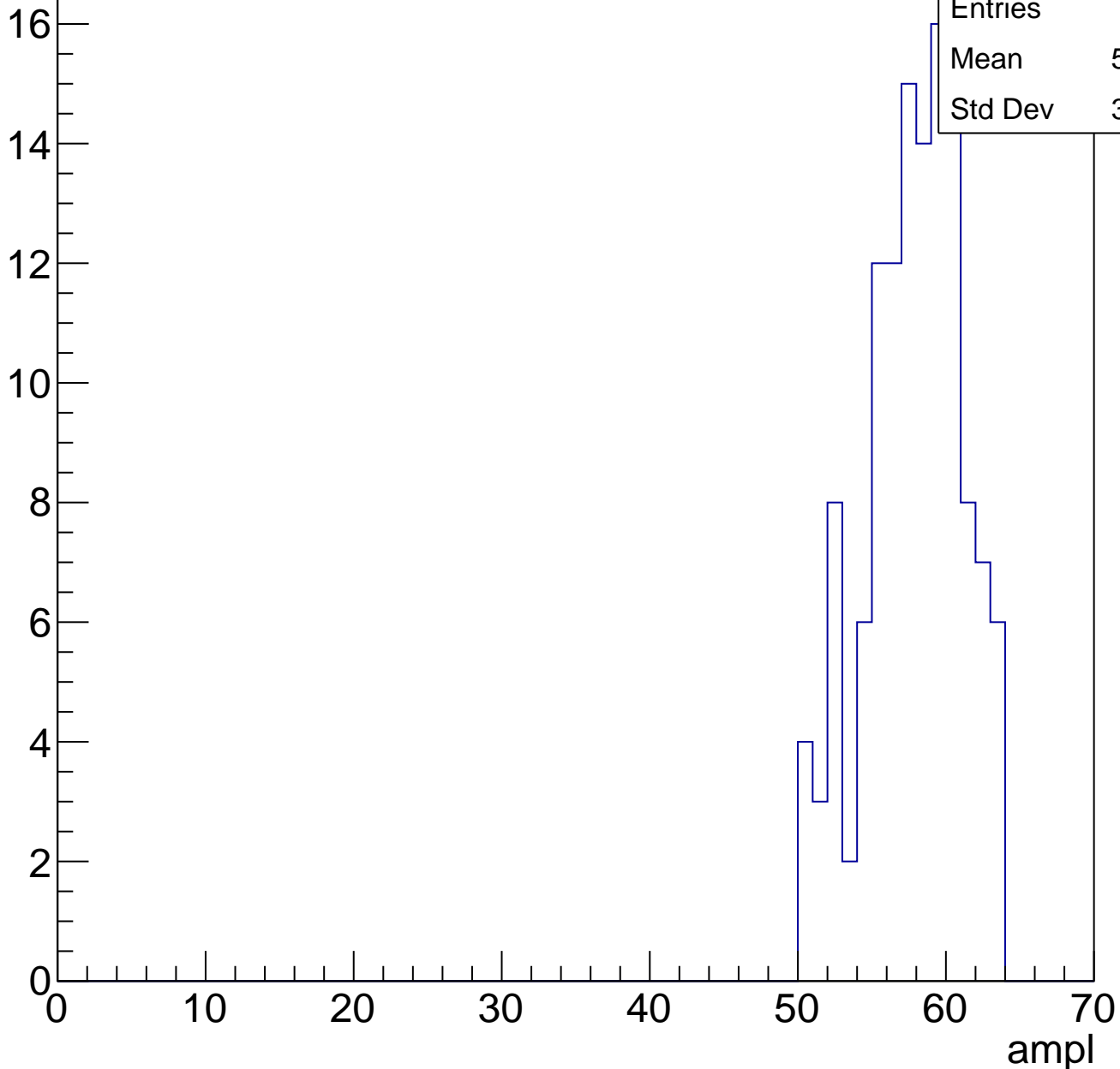


B0L101S, U22-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 128 |
| Mean | 57.36 |
| Std Dev | 3.266 |

Entry



B0L101S, U22-ch103, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|------|
| Entries | 53 |
| Mean | 58.7 |
| Std Dev | 11.8 |

B0L101S, U22-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch103, adc7

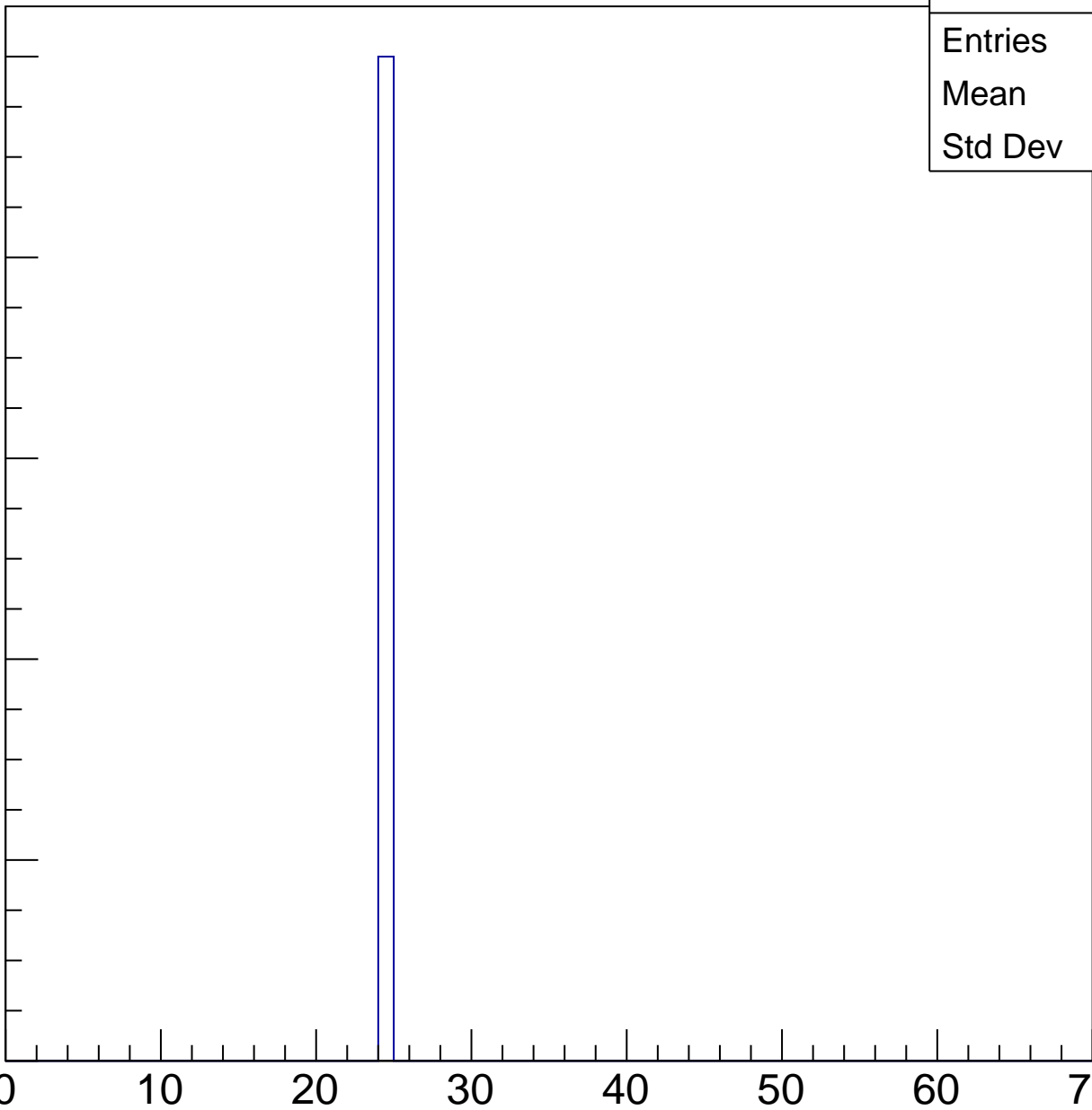
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl



B0L101S, U22-ch104, adc0

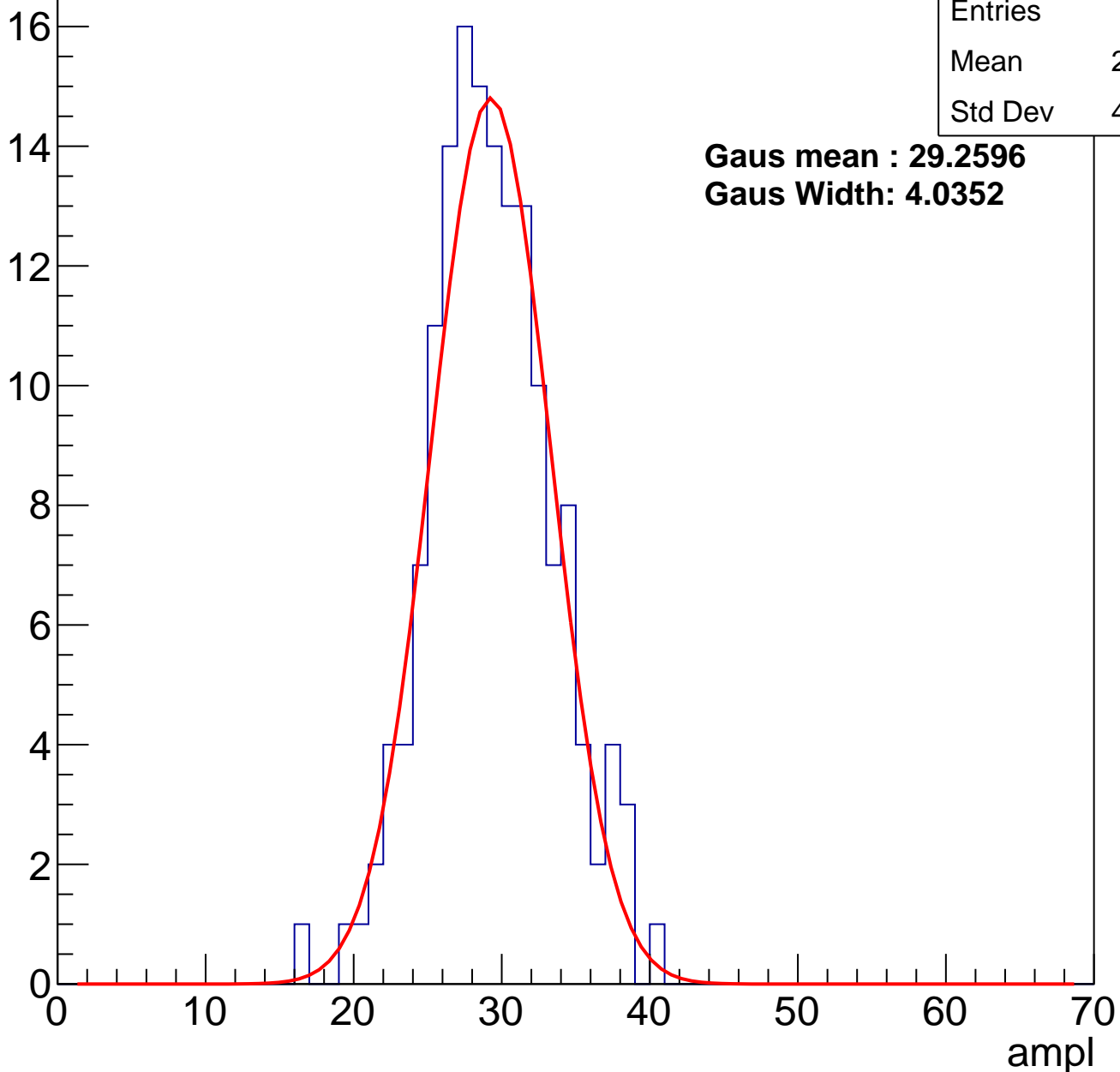
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 155 |
| Mean | 28.85 |
| Std Dev | 4.173 |

Gaus mean : 29.2596

Gaus Width: 4.0352

Entry



B0L101S, U22-ch104, adc1

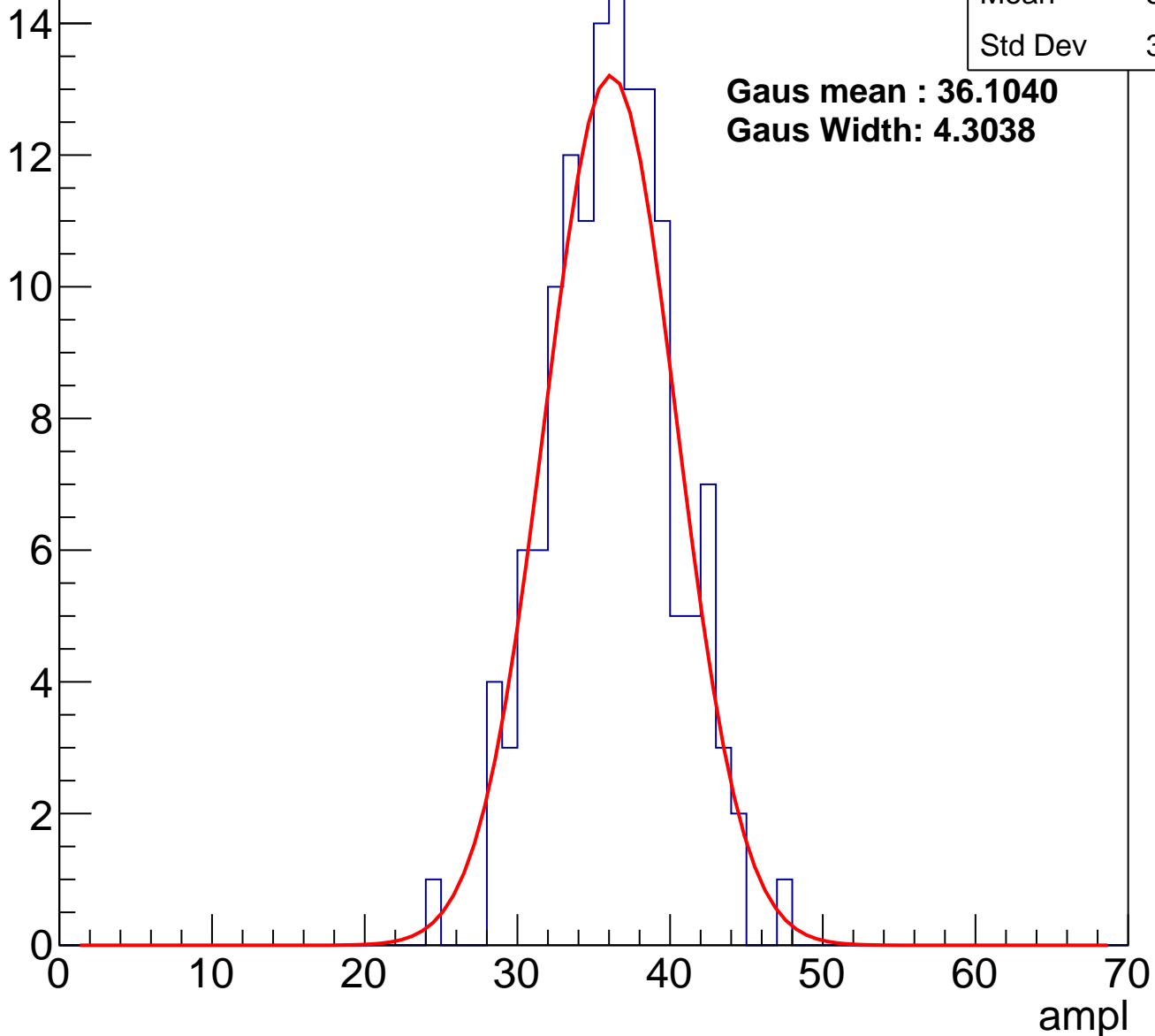
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 142 |
| Mean | 35.75 |
| Std Dev | 3.978 |

Gaus mean : 36.1040

Gaus Width: 4.3038

Entry



B0L101S, U22-ch104, adc2

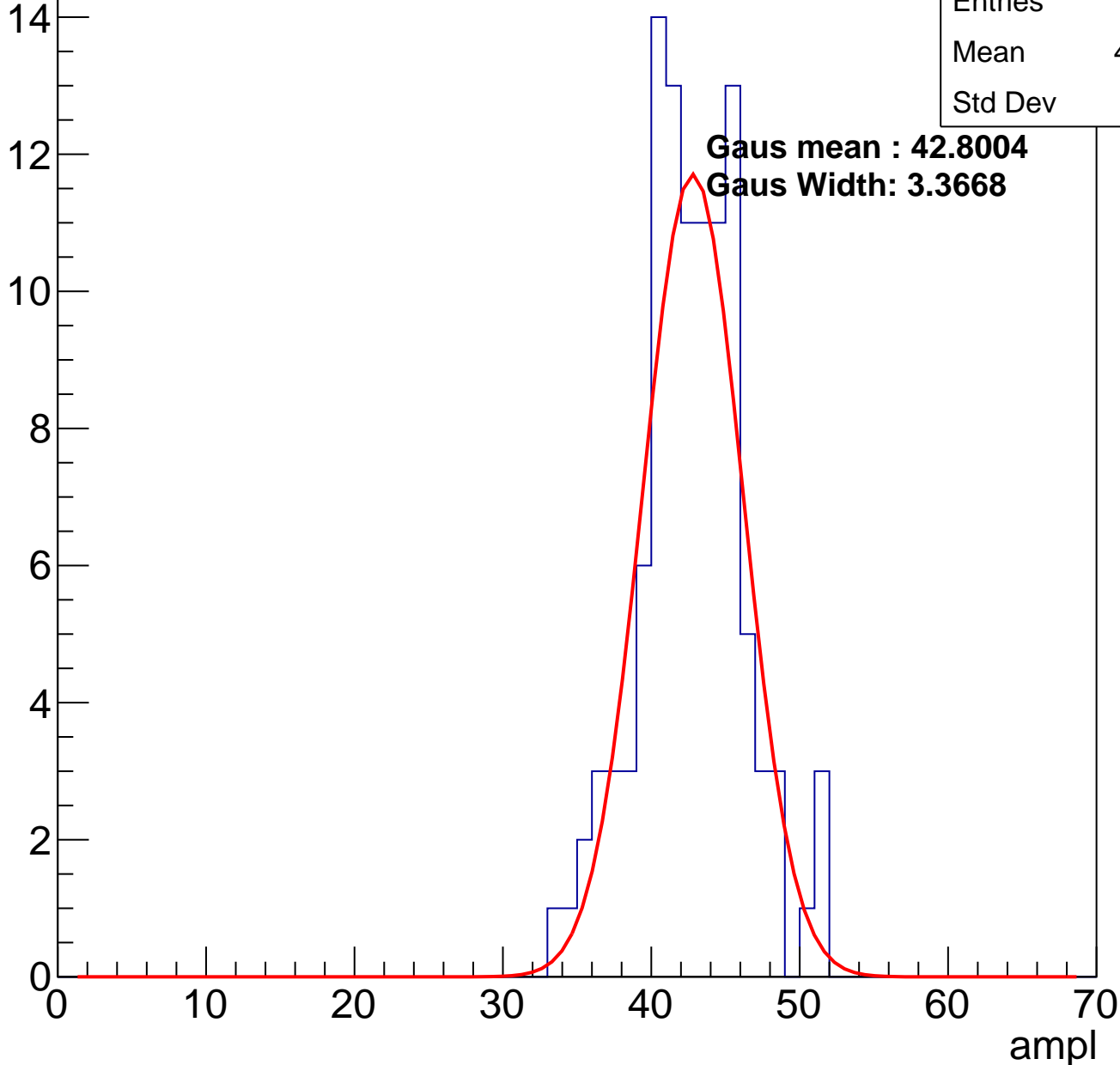
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 107 |
| Mean | 42.23 |
| Std Dev | 3.53 |

Gaus mean : 42.8004

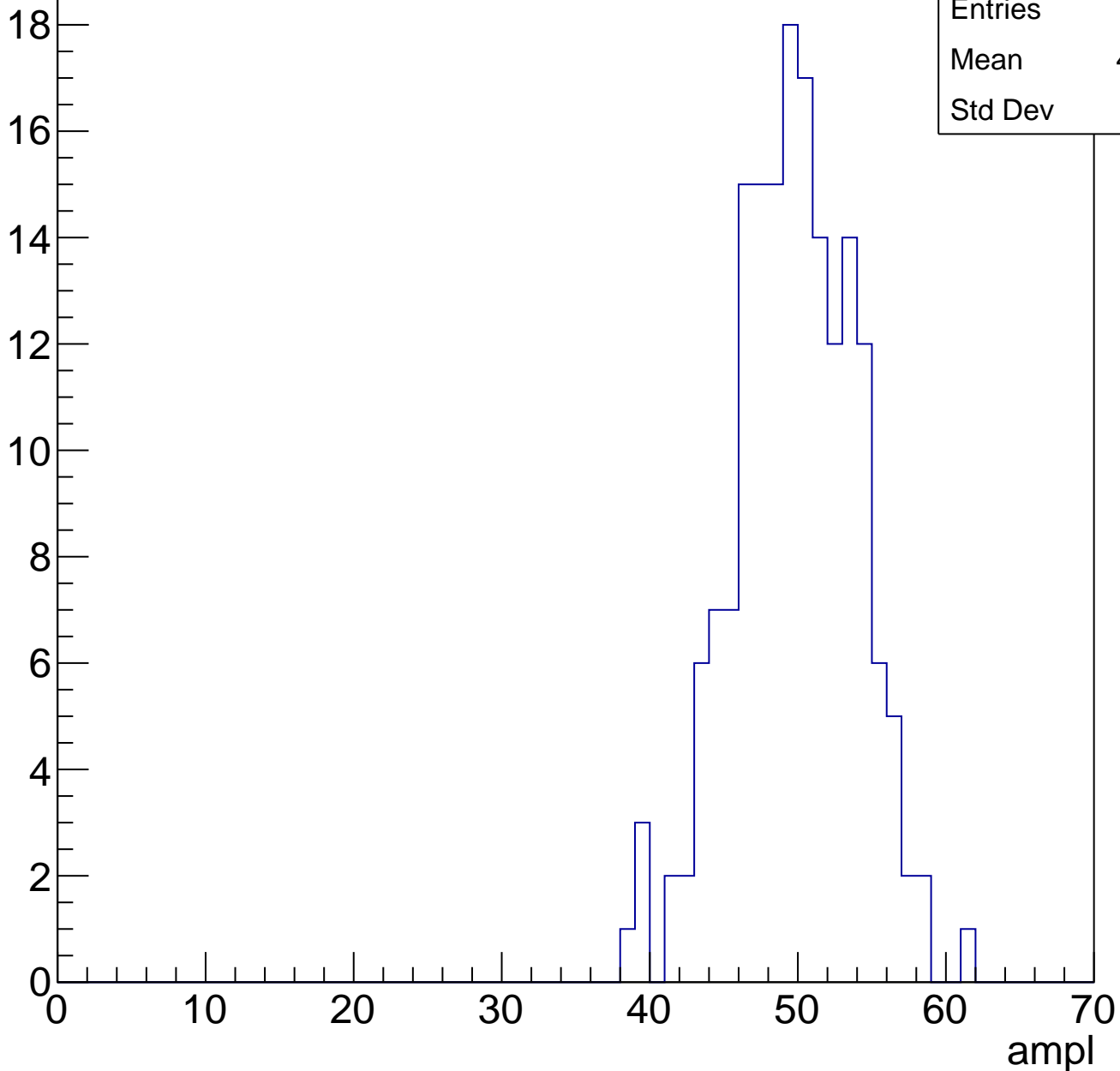
Gaus Width: 3.3668



B0L101S, U22-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

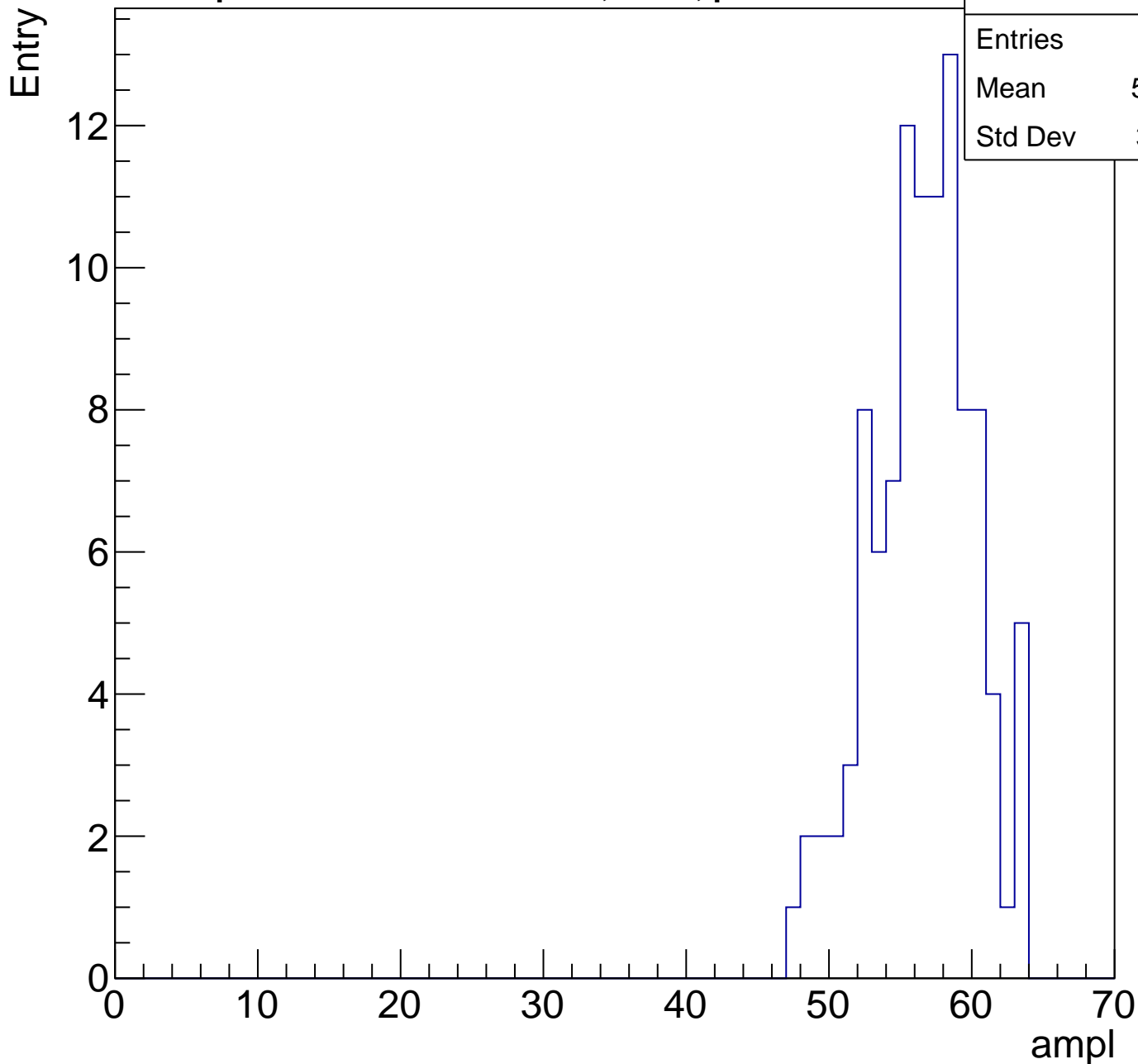
Entry



B0L101S, U22-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 104 |
| Mean | 56.12 |
| Std Dev | 3.561 |



B0L101S, U22-ch104, adc5

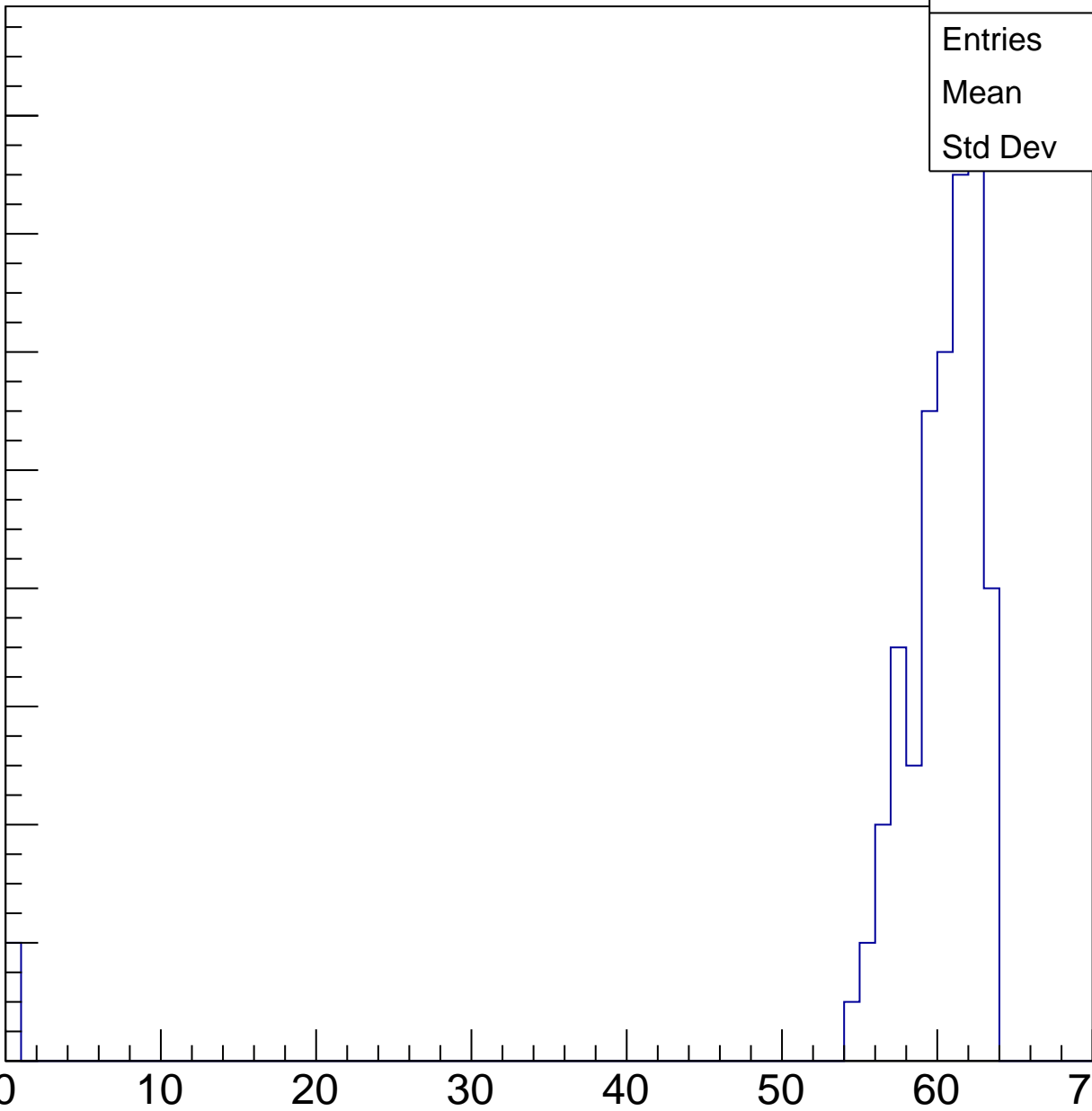
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 84 |
| Mean | 58.56 |
| Std Dev | 9.402 |

ampl

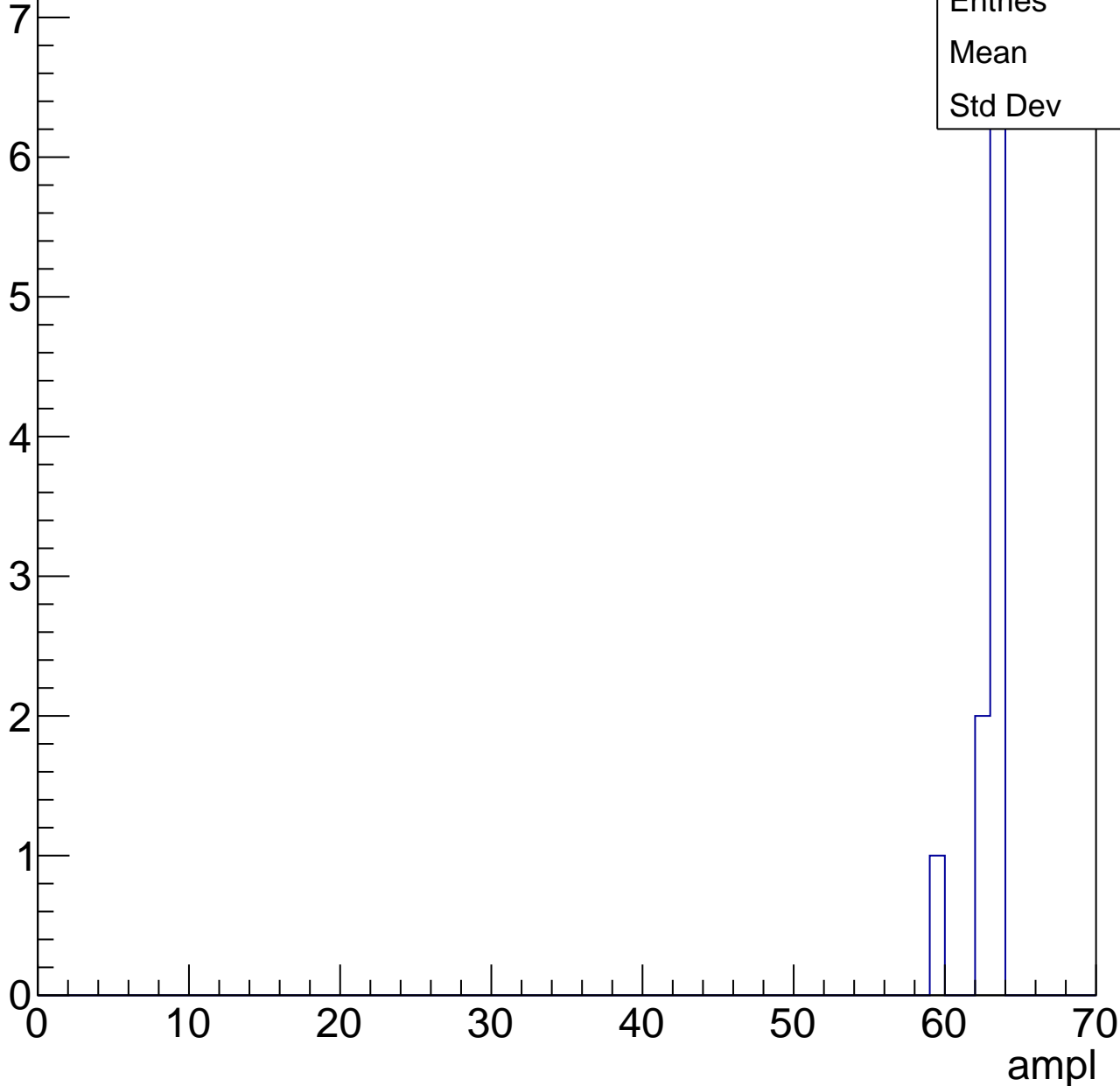


B0L101S, U22-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|------|
| Entries | 10 |
| Mean | 62.4 |
| Std Dev | 1.2 |



B0L101S, U22-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch105, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 164 |
| Mean | 28.78 |
| Std Dev | 5.194 |

Gaus mean : 29.9977

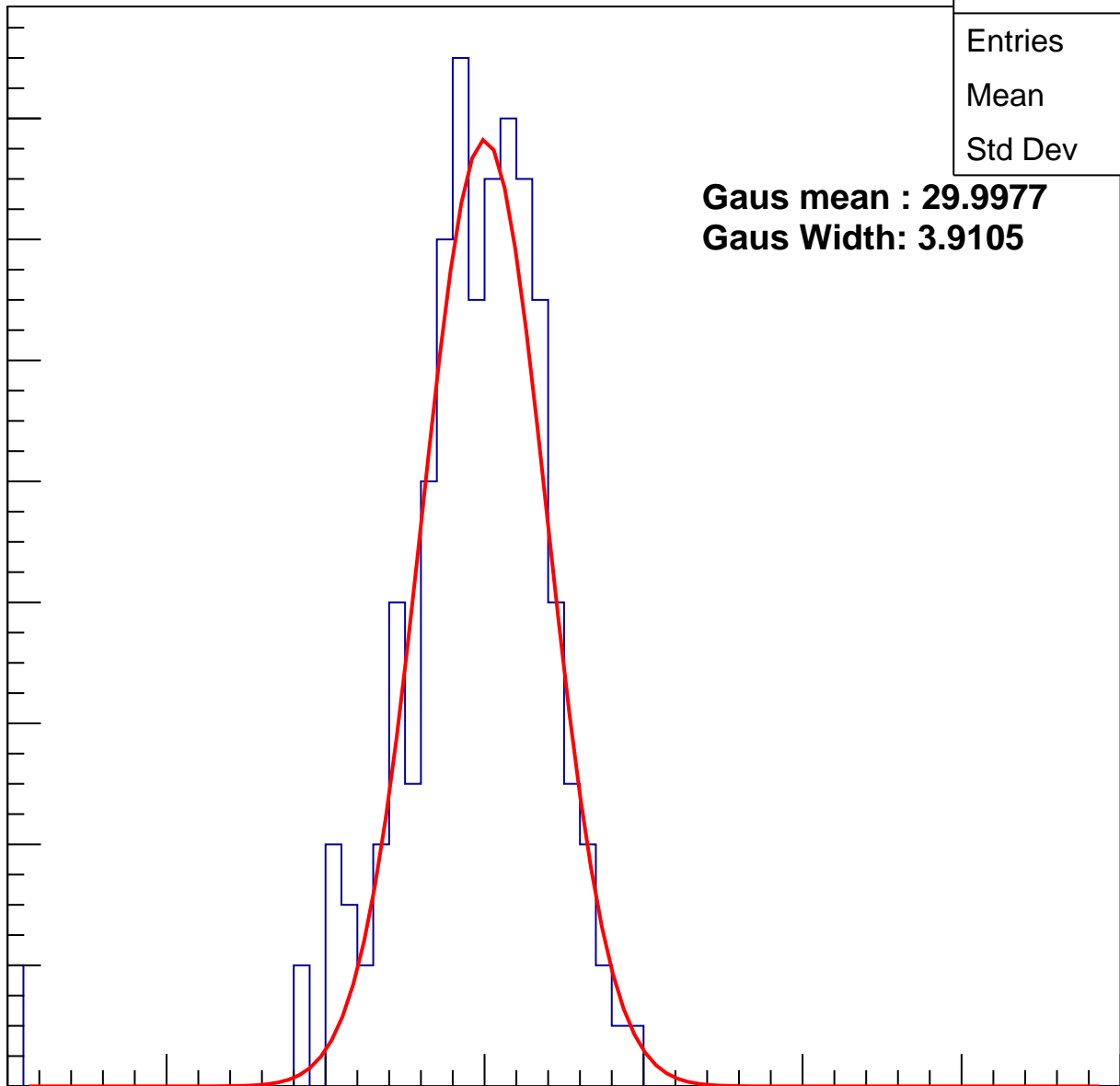
Gaus Width: 3.9105

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch105, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

147

Mean

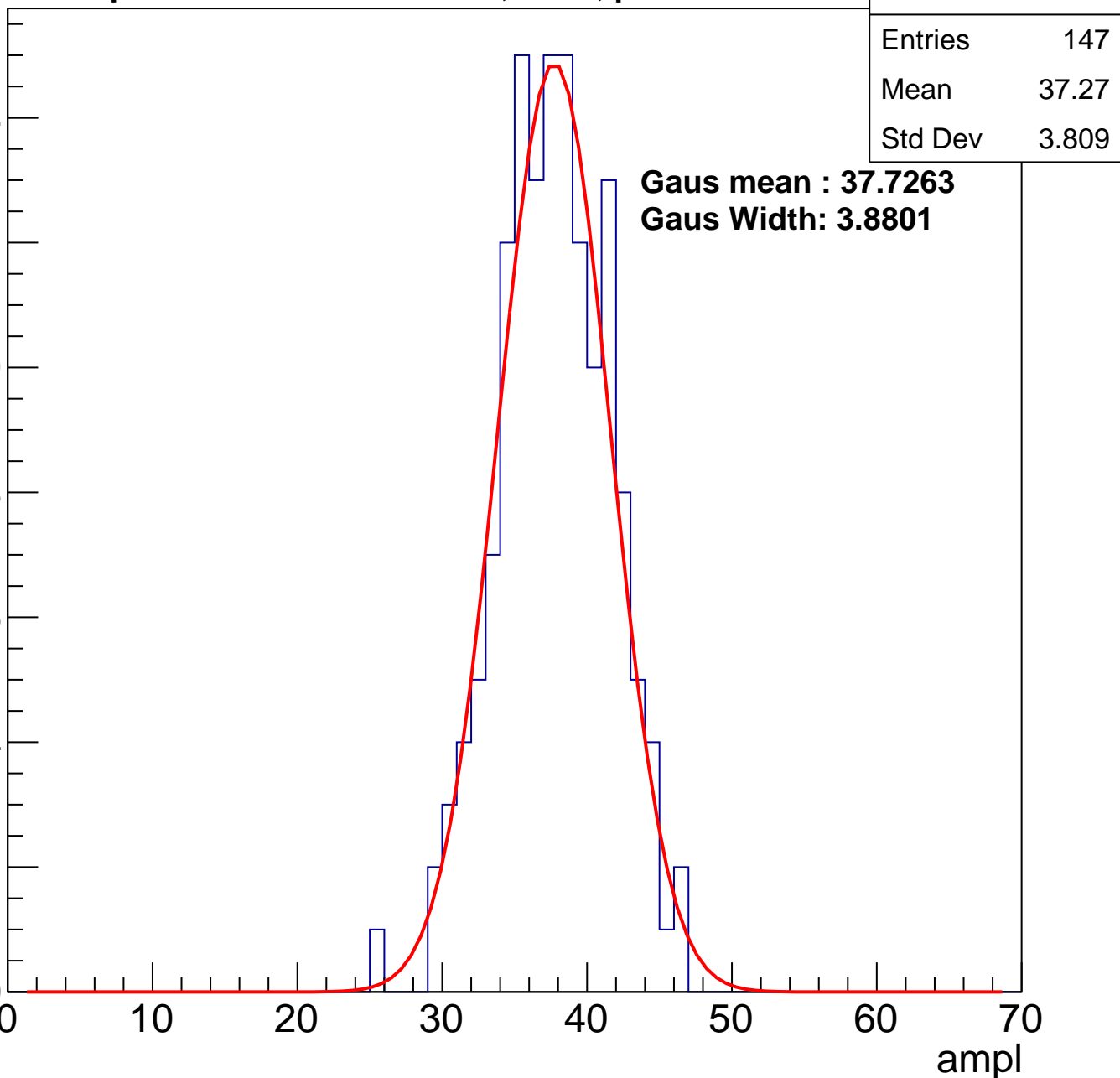
37.27

Std Dev

3.809

Gaus mean : 37.7263

Gaus Width: 3.8801



B0L101S, U22-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

139

Mean

44.29

Std Dev

3.874

Gaus mean : 44.4758

Gaus Width: 4.0665

0

10

20

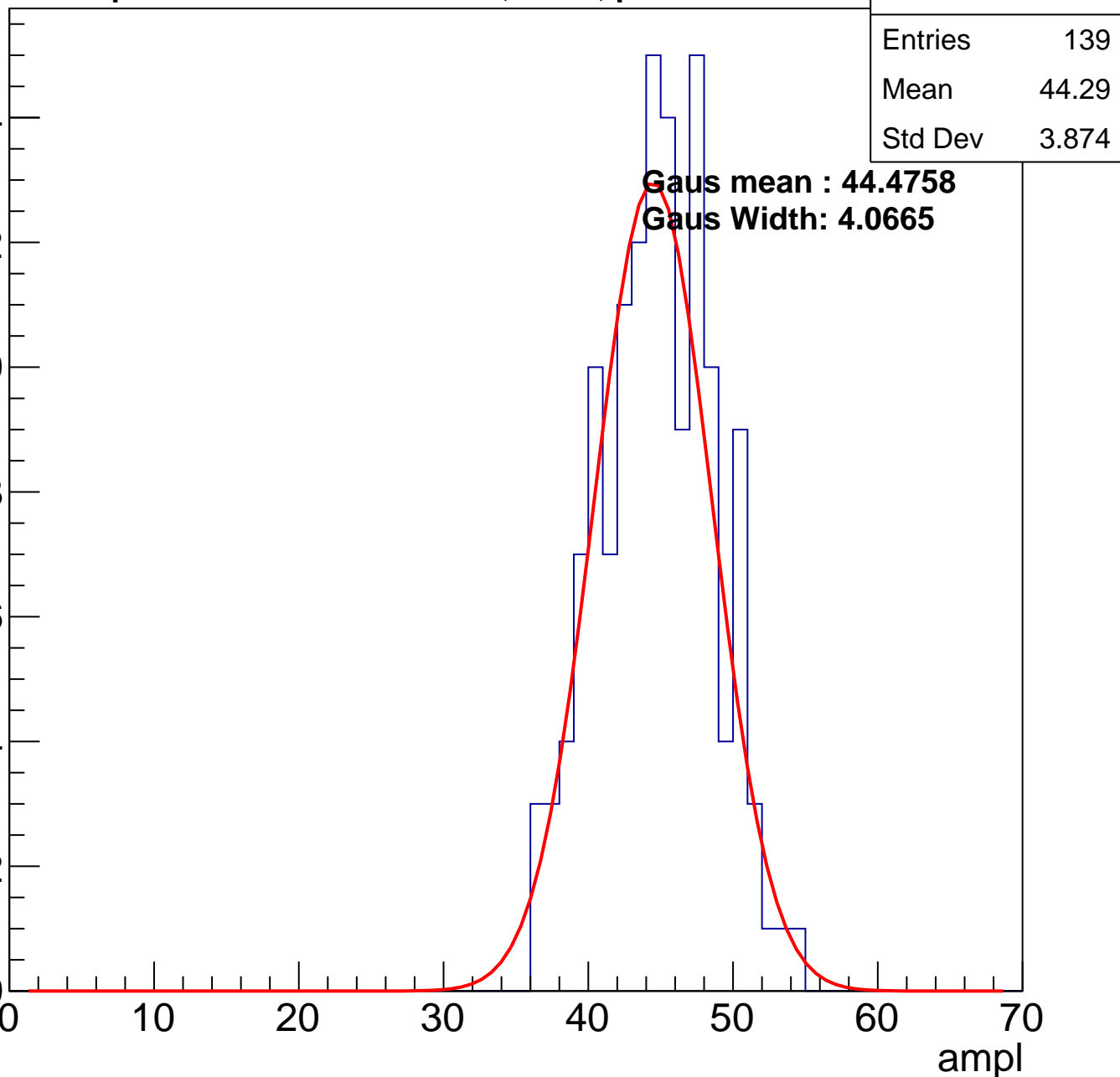
30

40

50

60

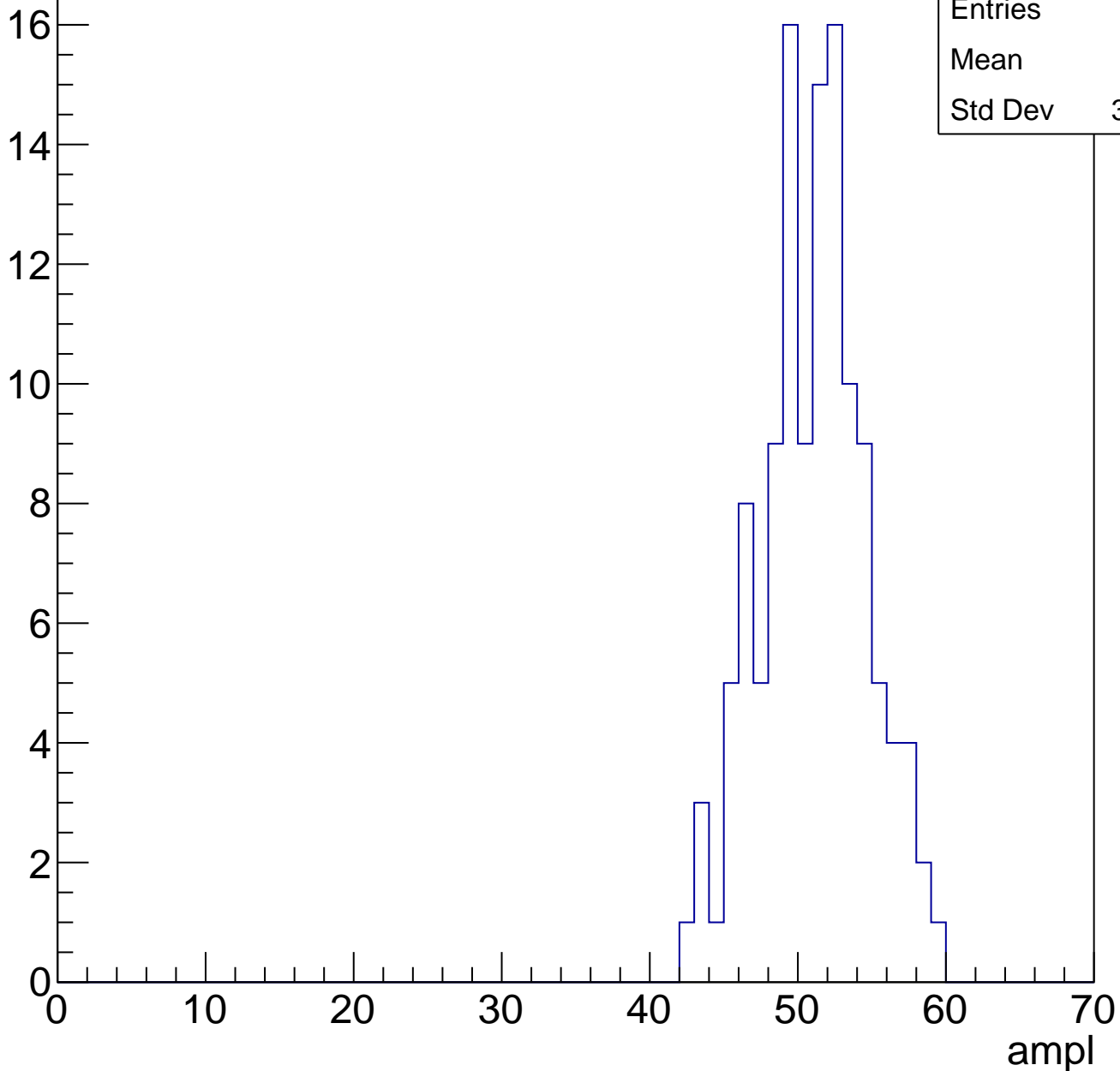
ampl



B0L101S, U22-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 123 |
| Mean | 50.6 |
| Std Dev | 3.538 |

B0L101S, U22-ch105, adc4

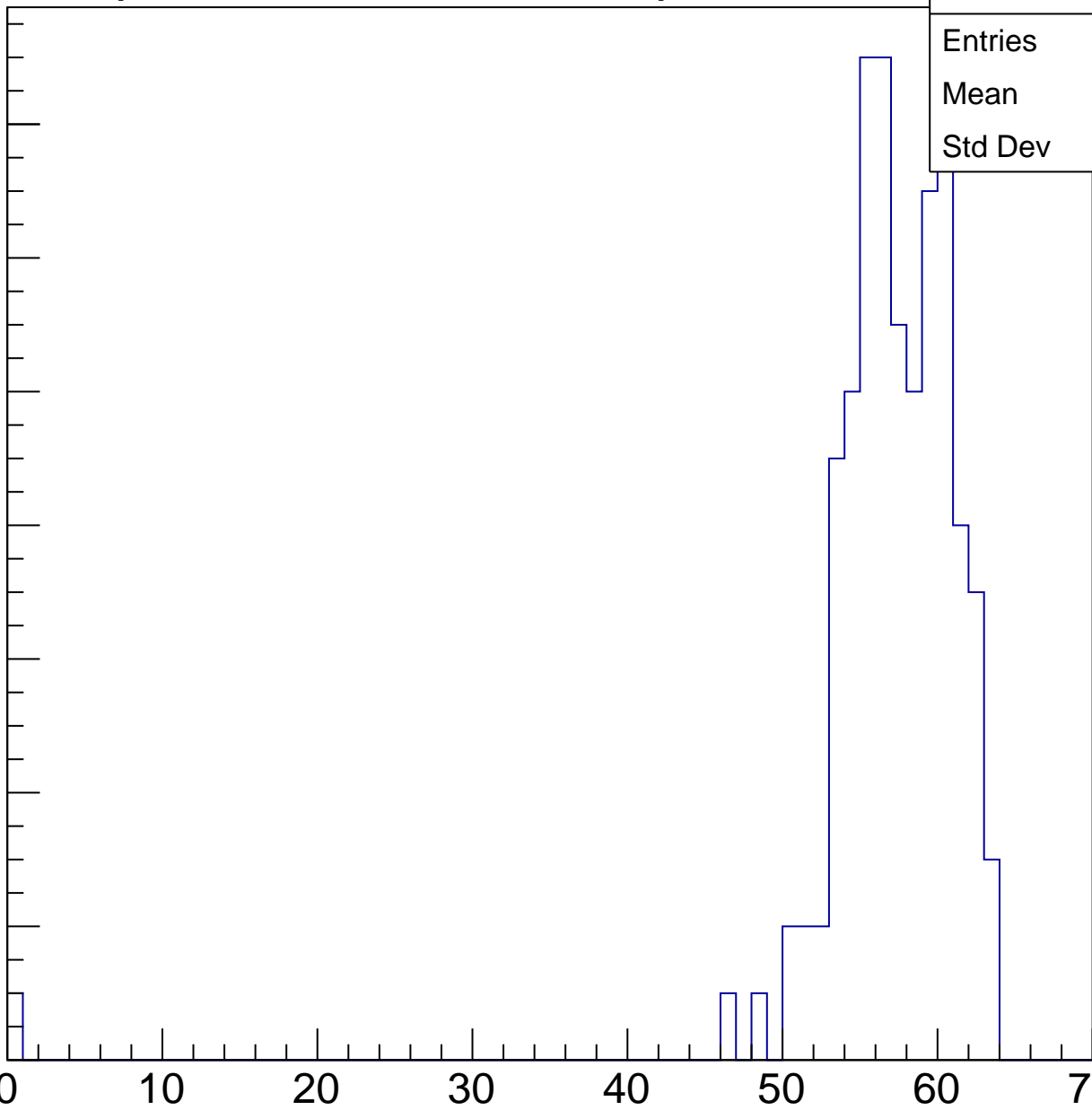
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 125 |
| Mean | 56.54 |
| Std Dev | 6.041 |

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch105, adc5

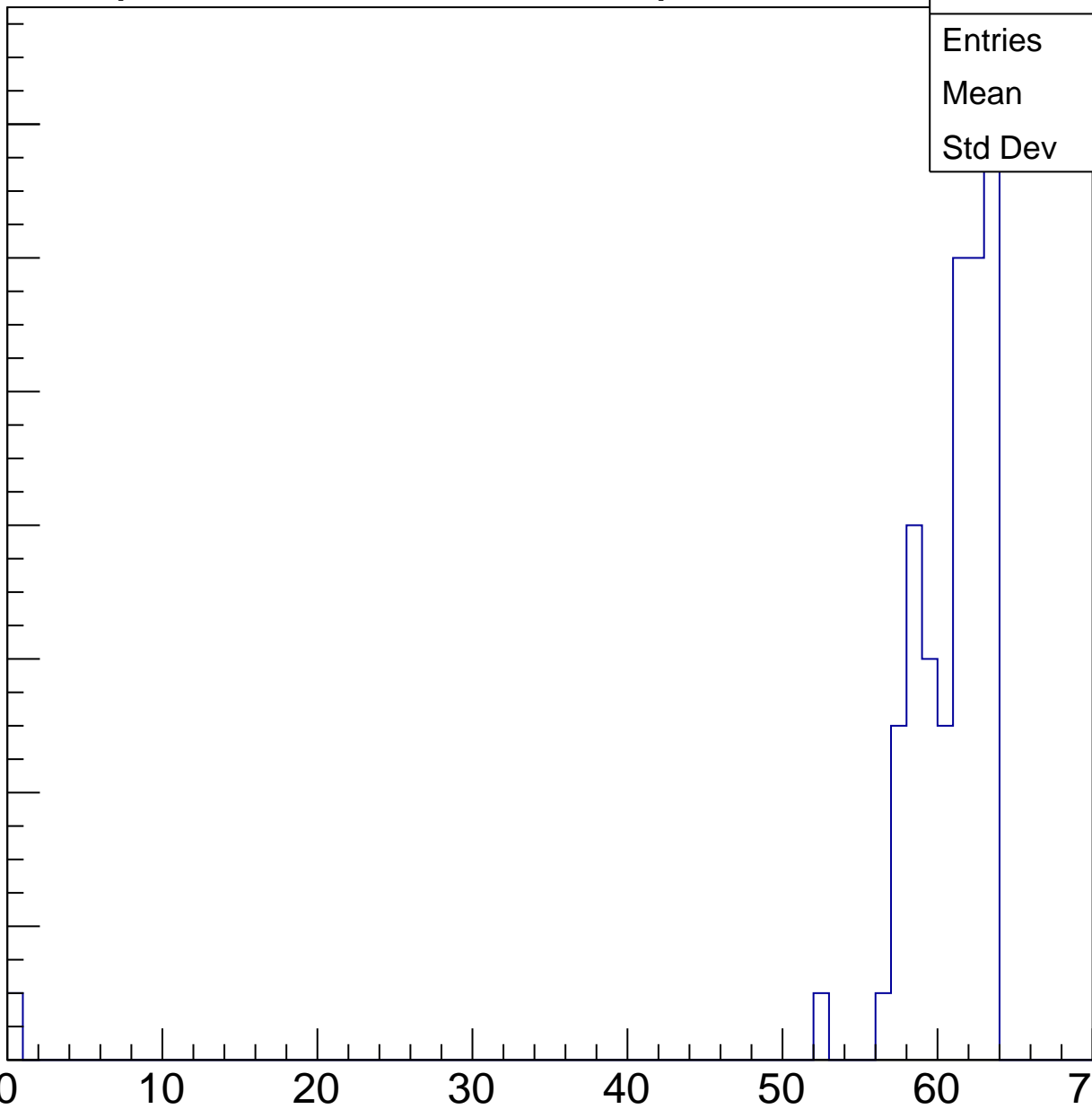
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 66 |
| Mean | 59.58 |
| Std Dev | 7.732 |

ampl



B0L101S, U22-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

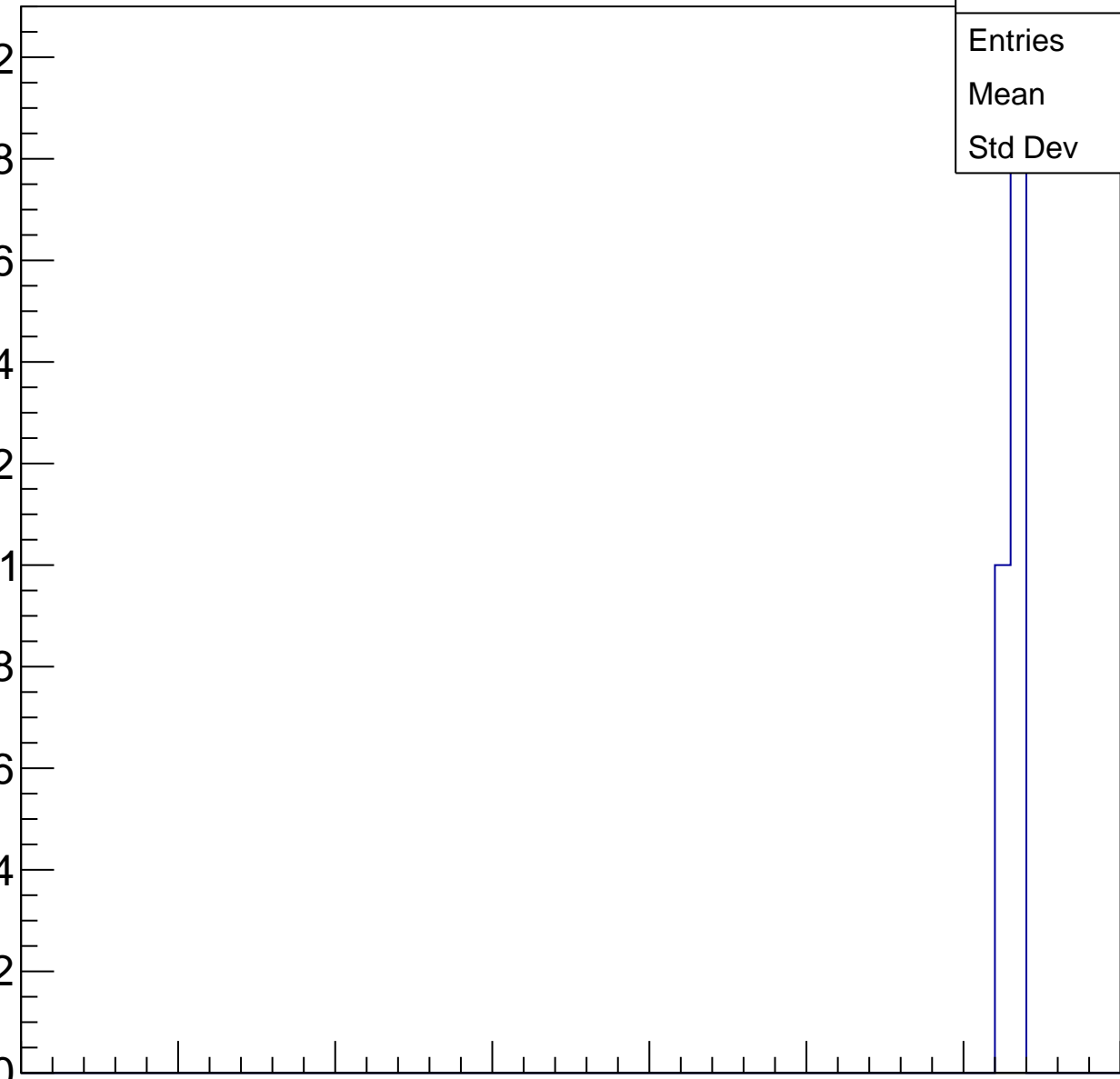
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch106, adc0

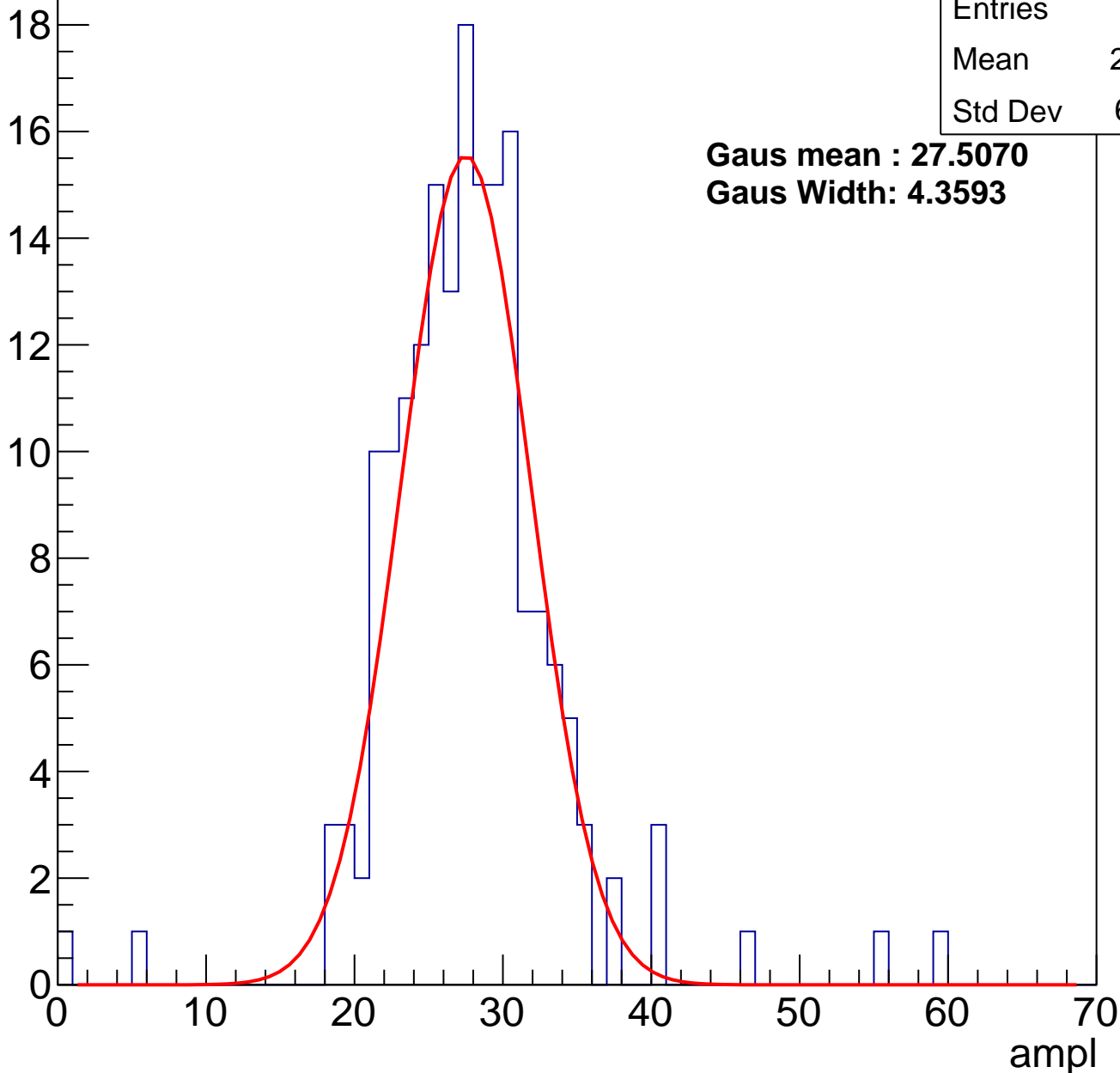
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 27.23 |
| Std Dev | 6.101 |

Gaus mean : 27.5070

Gaus Width: 4.3593

Entry



B0L101S, U22-ch106, adc1

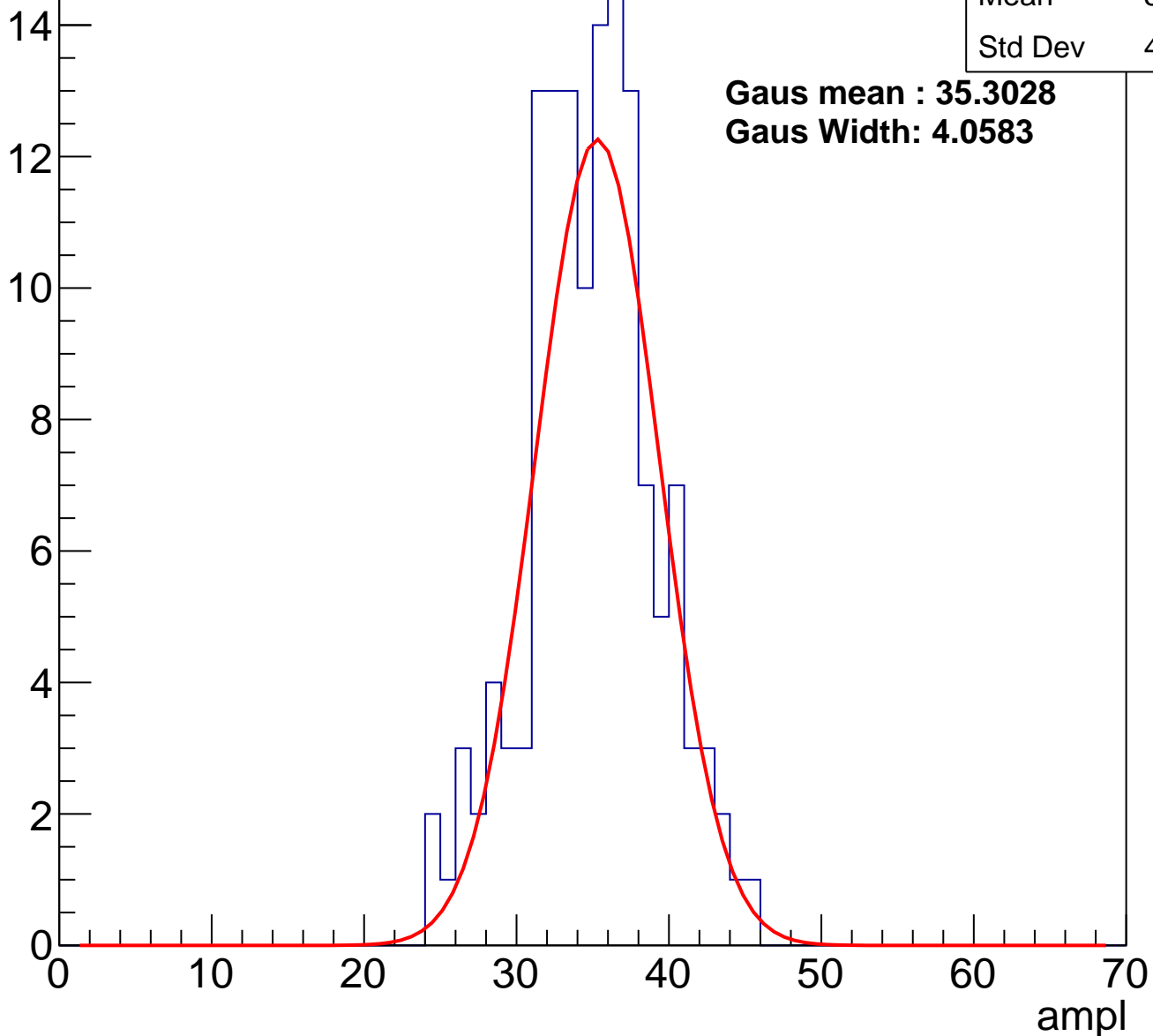
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 138 |
| Mean | 34.48 |
| Std Dev | 4.162 |

Gaus mean : 35.3028

Gaus Width: 4.0583

Entry



B0L101S, U22-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

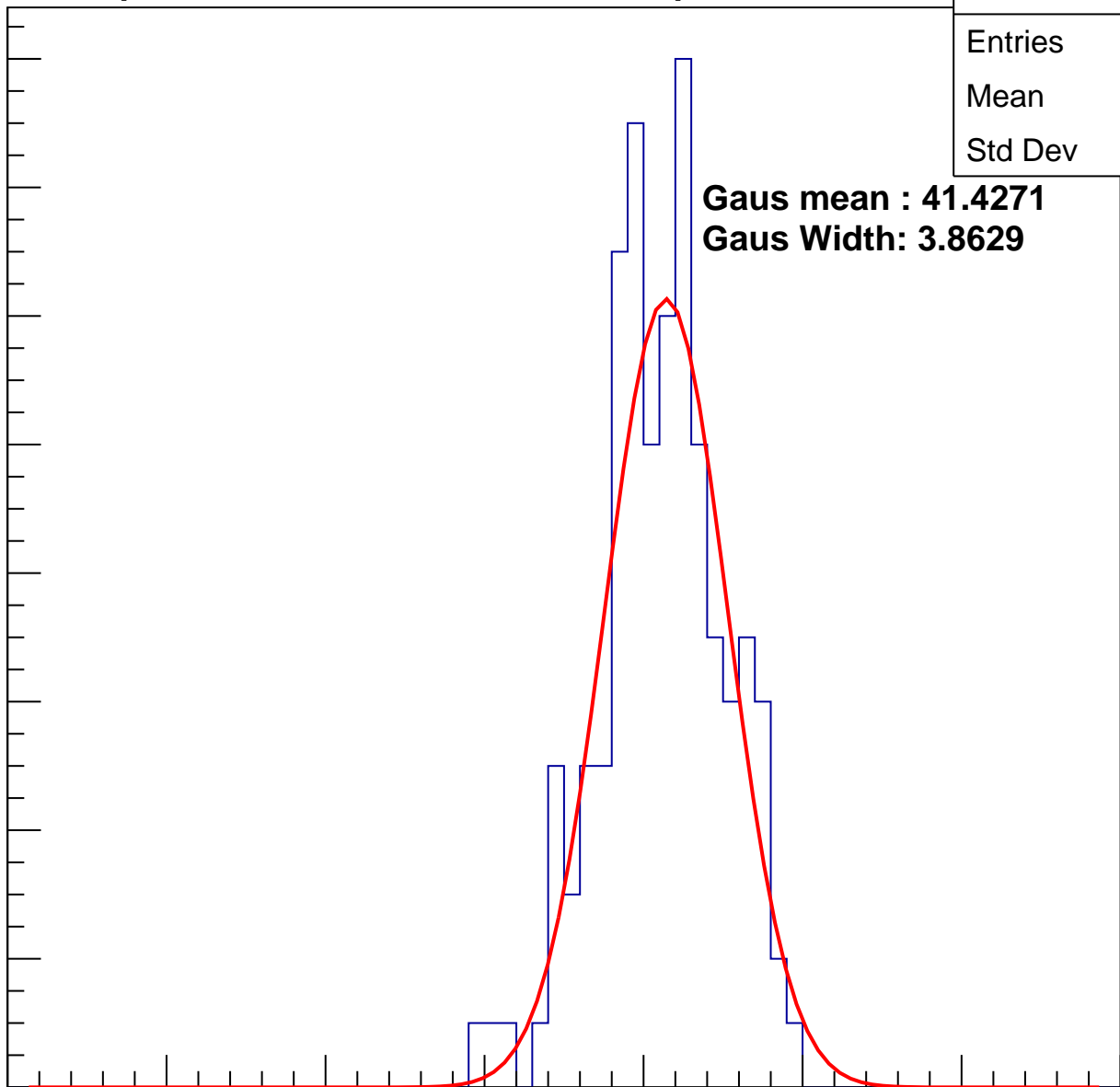
| | |
|---------|-------|
| Entries | 127 |
| Mean | 40.65 |
| Std Dev | 3.895 |

Gaus mean : 41.4271

Gaus Width: 3.8629

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch106, adc3

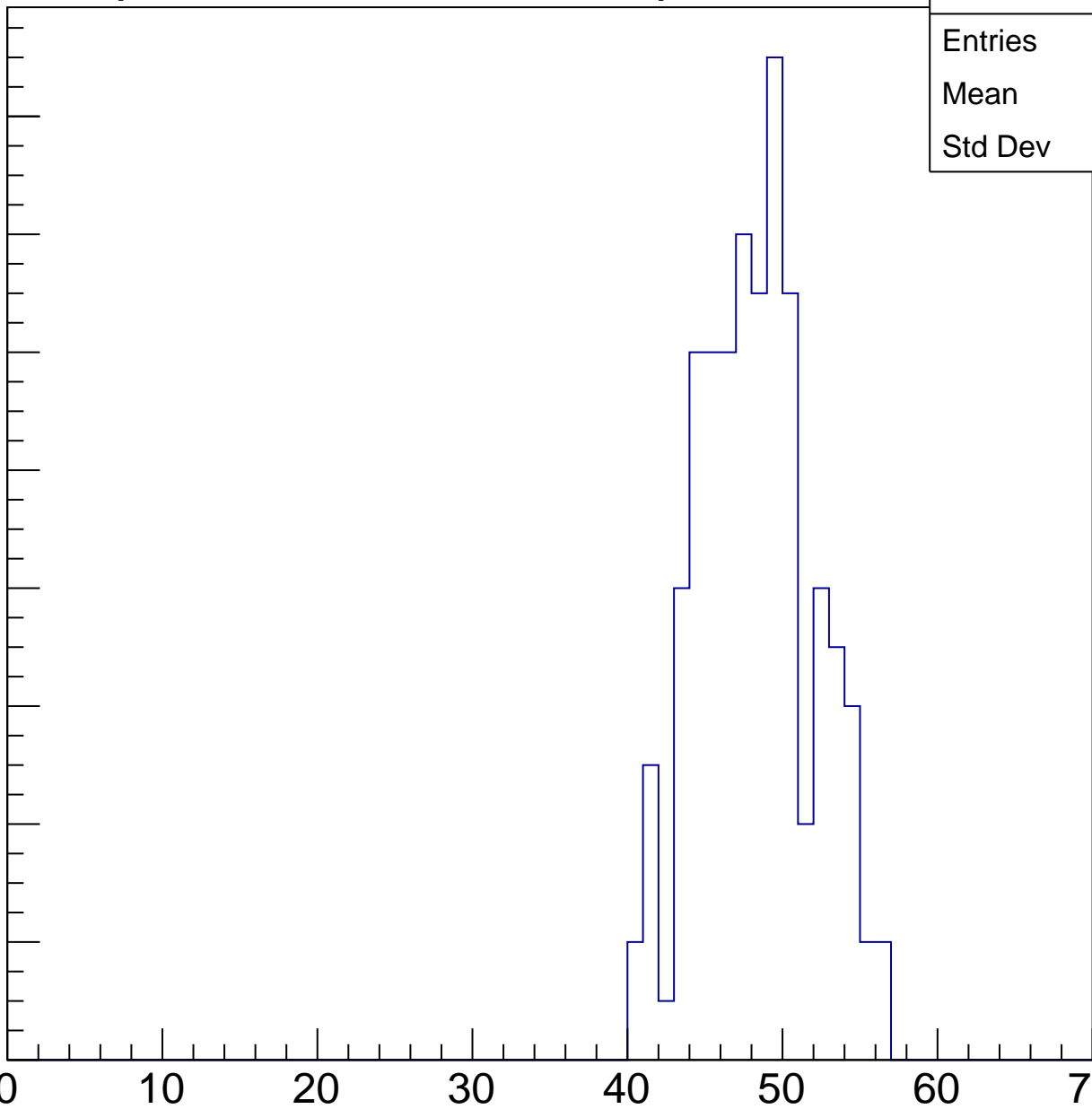
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 138 |
| Mean | 47.78 |
| Std Dev | 3.626 |

ampl

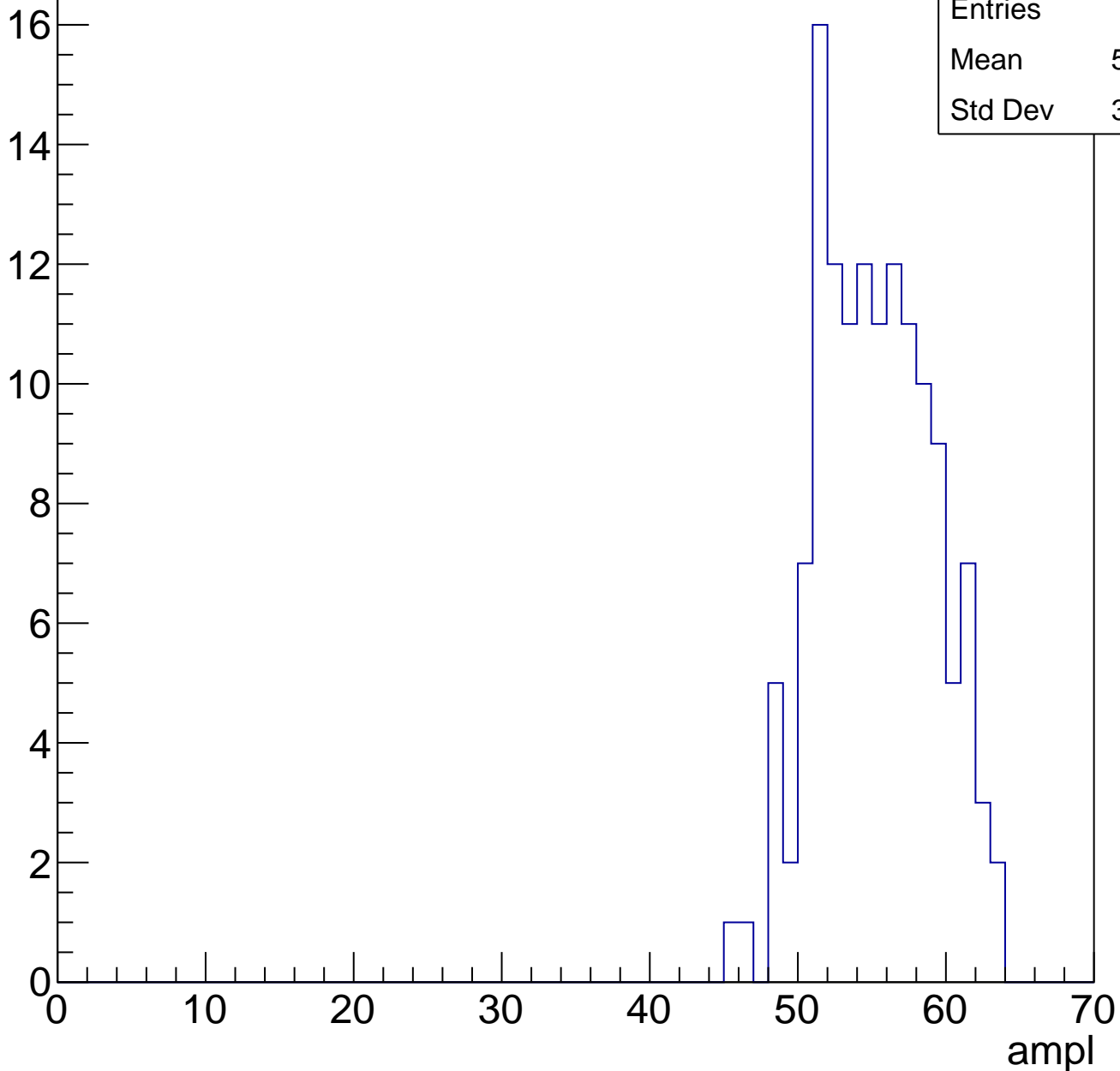


B0L101S, U22-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 137 |
| Mean | 54.77 |
| Std Dev | 3.847 |



B0L101S, U22-ch106, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

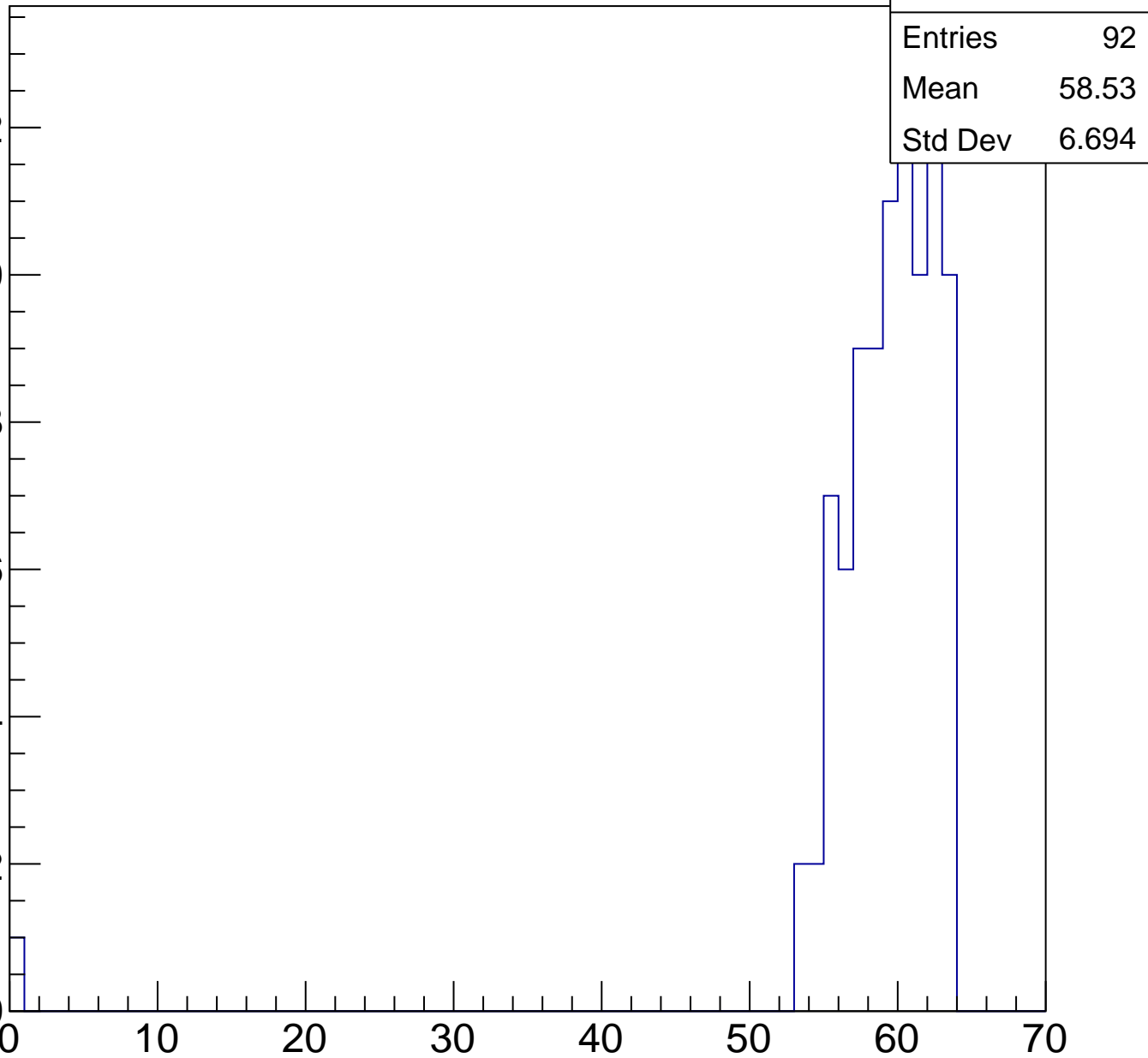
4

2

0

| | |
|---------|-------|
| Entries | 92 |
| Mean | 58.53 |
| Std Dev | 6.694 |

ampl

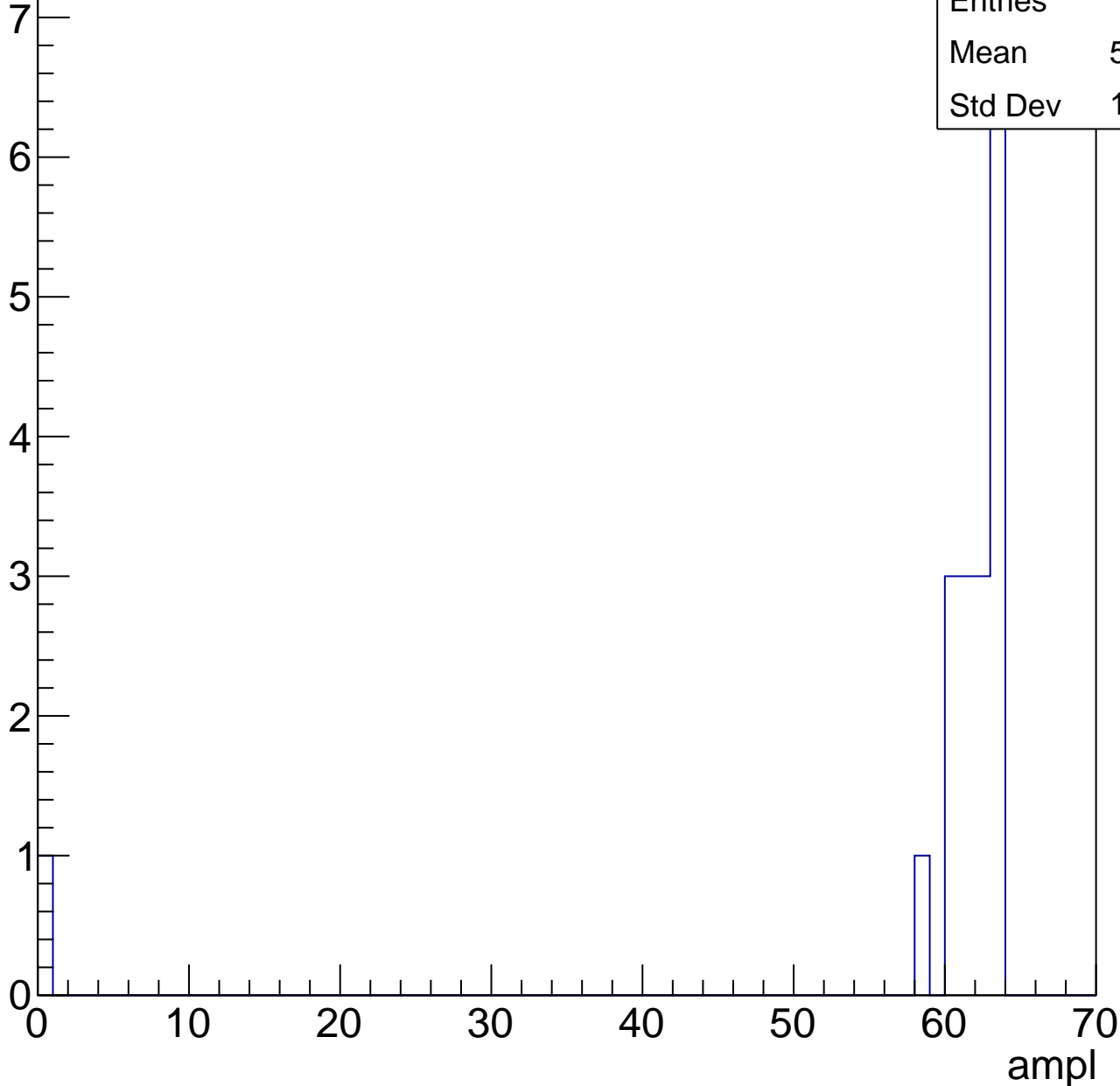


B0L101S, U22-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

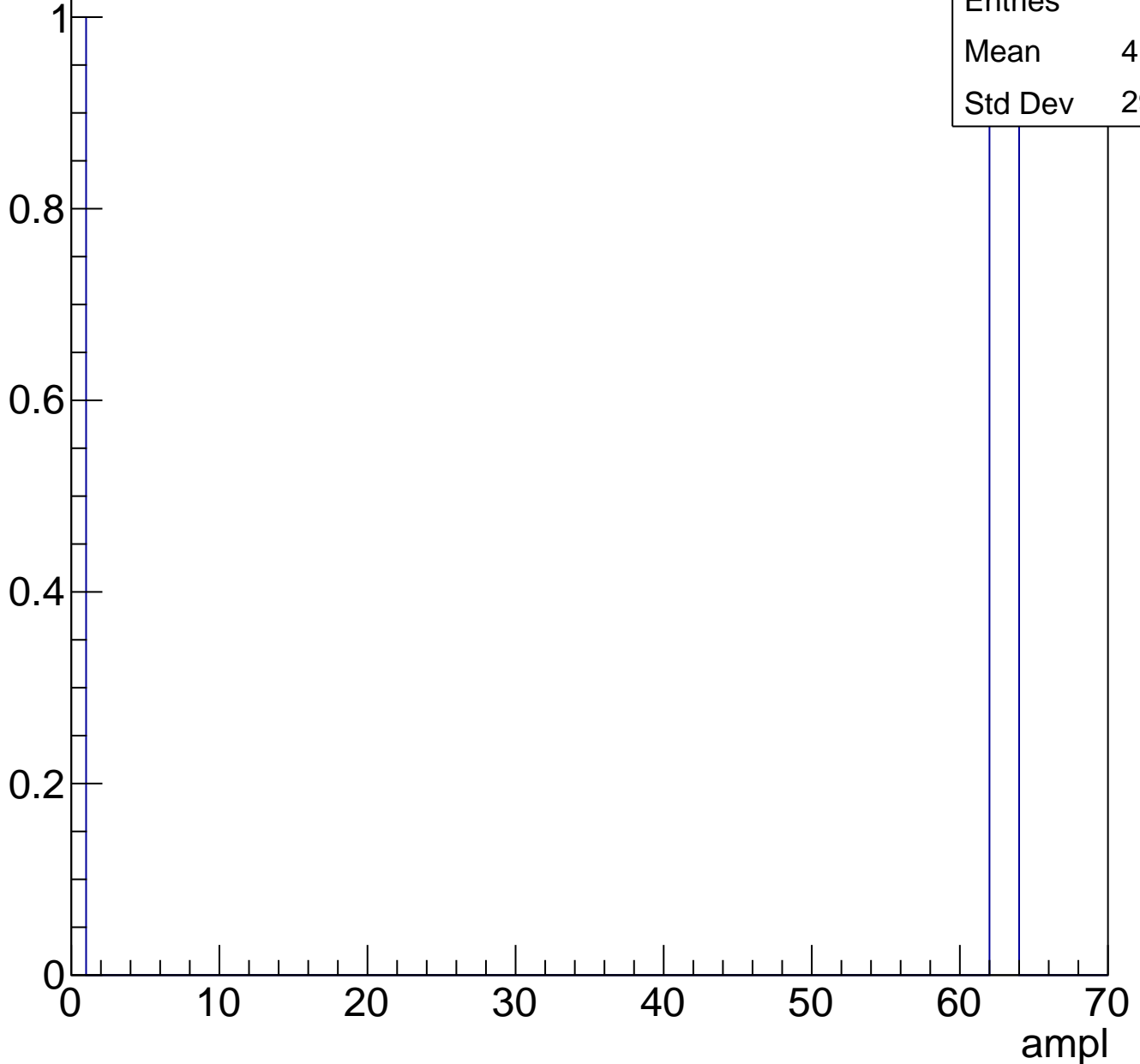
| | |
|---------|-------|
| Entries | 18 |
| Mean | 58.22 |
| Std Dev | 14.19 |



B0L101S, U22-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 162 |
| Mean | 28.52 |
| Std Dev | 4.52 |

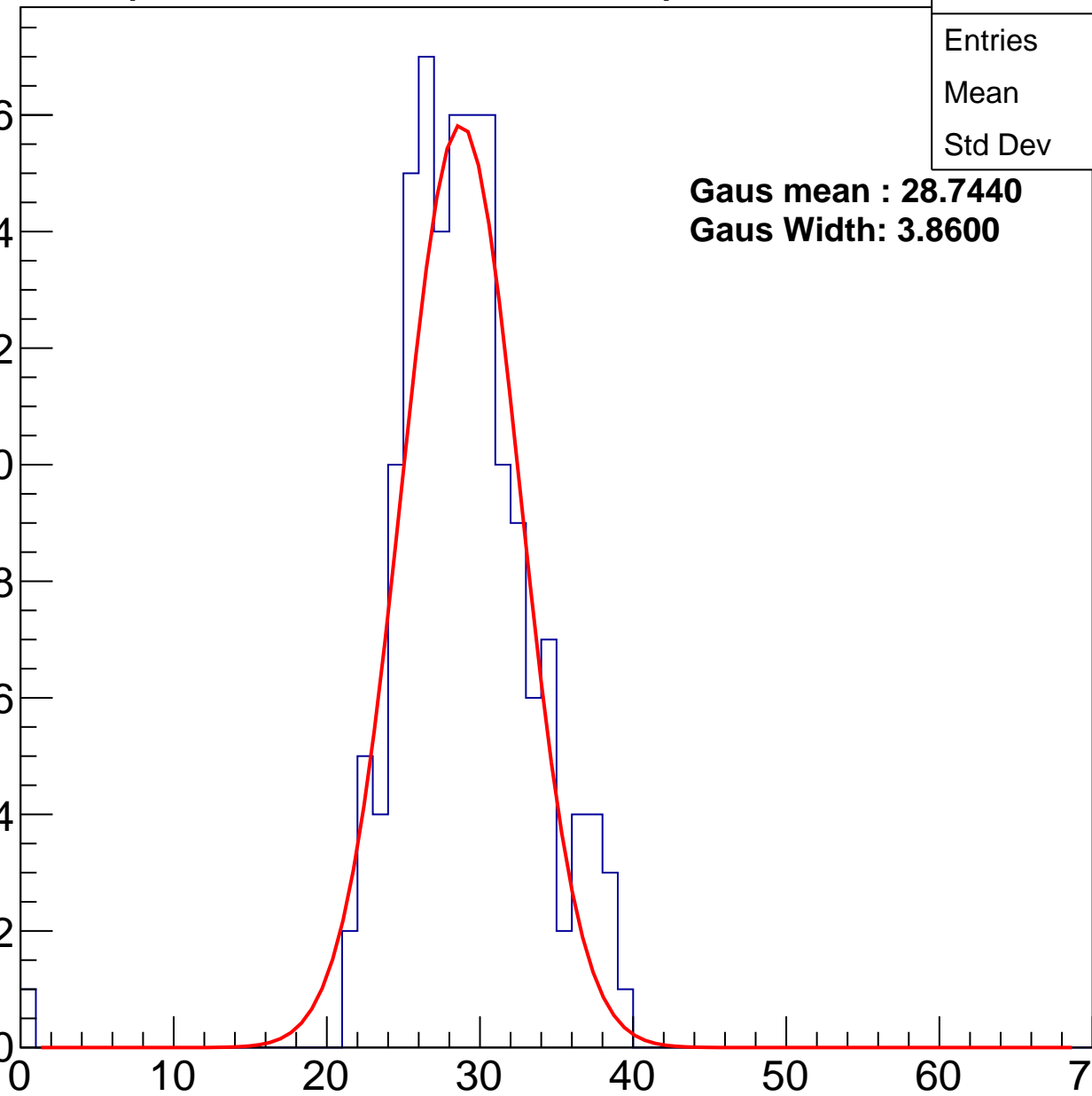
Gaus mean : 28.7440

Gaus Width: 3.8600

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U22-ch107, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 35.37 |
| Std Dev | 3.59 |

Gaus mean : 35.9241

Gaus Width: 3.7586

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

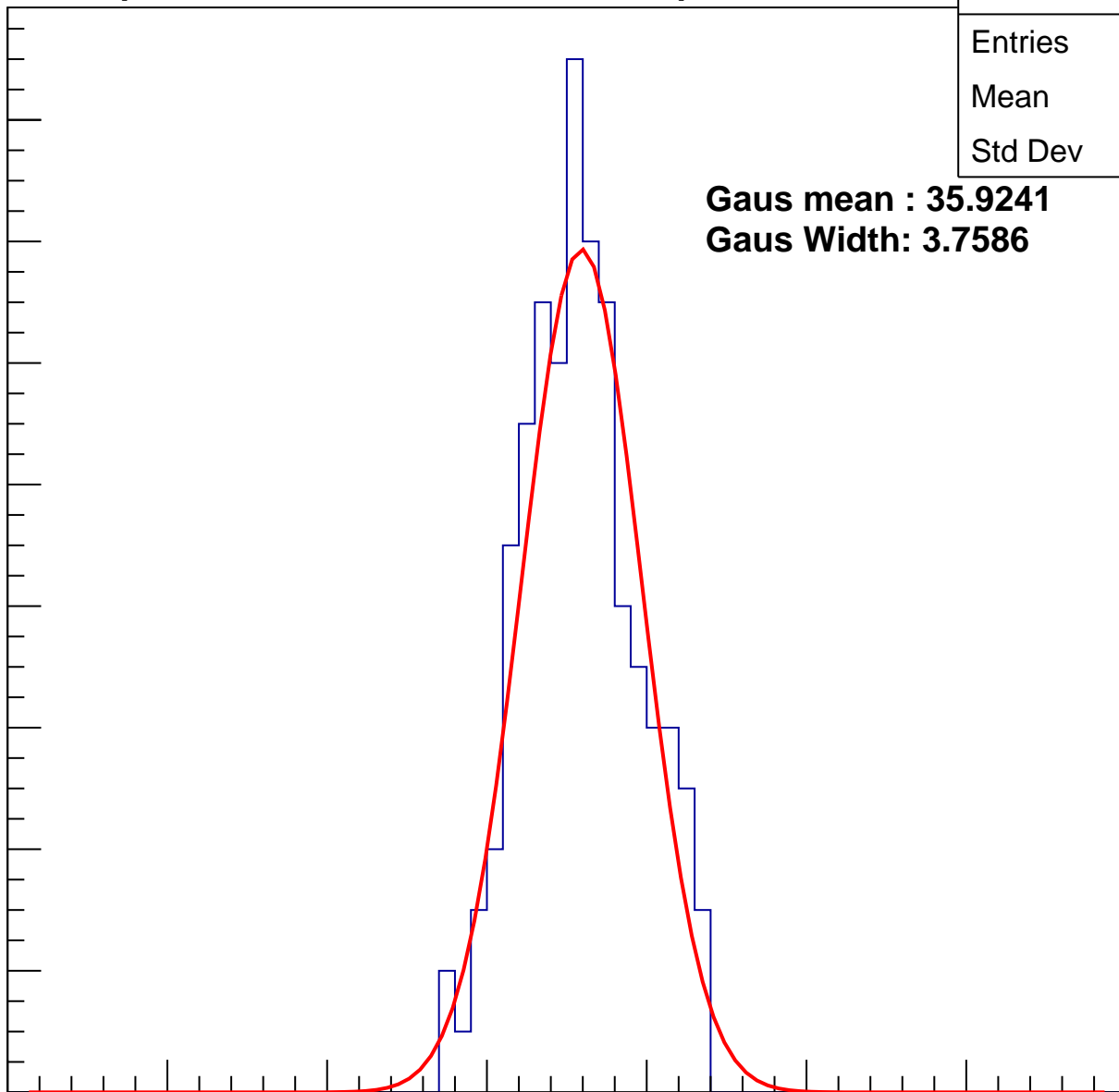
40

50

60

70

ampl



B0L101S, U22-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 144 |
| Mean | 42.84 |
| Std Dev | 3.588 |

Gaus mean : 43.4115

Gaus Width: 3.6021

0

2

4

6

8

10

12

14

B0L101S, U22-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 157 |
| Mean | 50.19 |
| Std Dev | 4.095 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

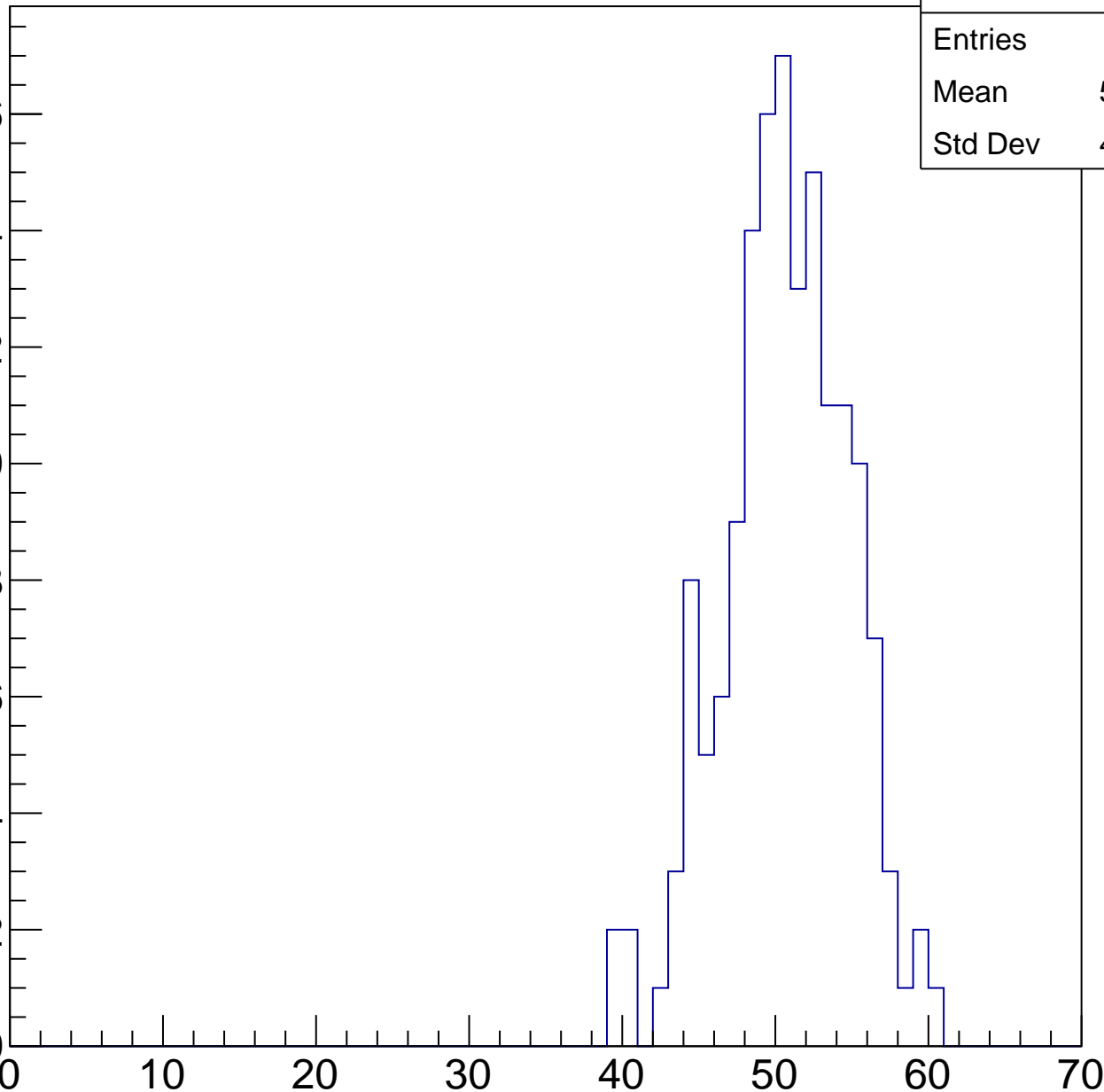
30

40

50

60

ampl

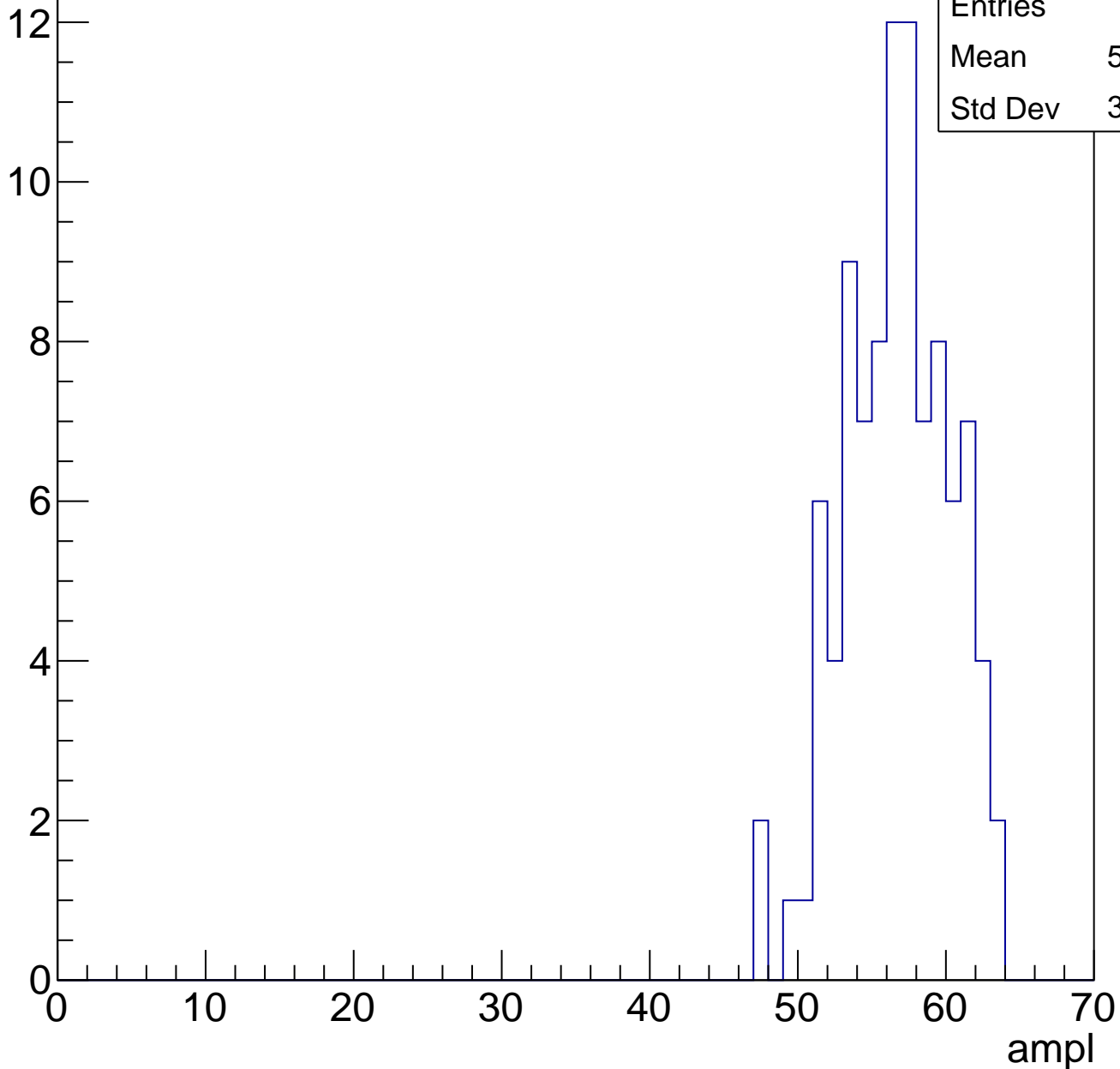


B0L101S, U22-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 96 |
| Mean | 56.22 |
| Std Dev | 3.533 |

Entry



B0L101S, U22-ch107, adc5

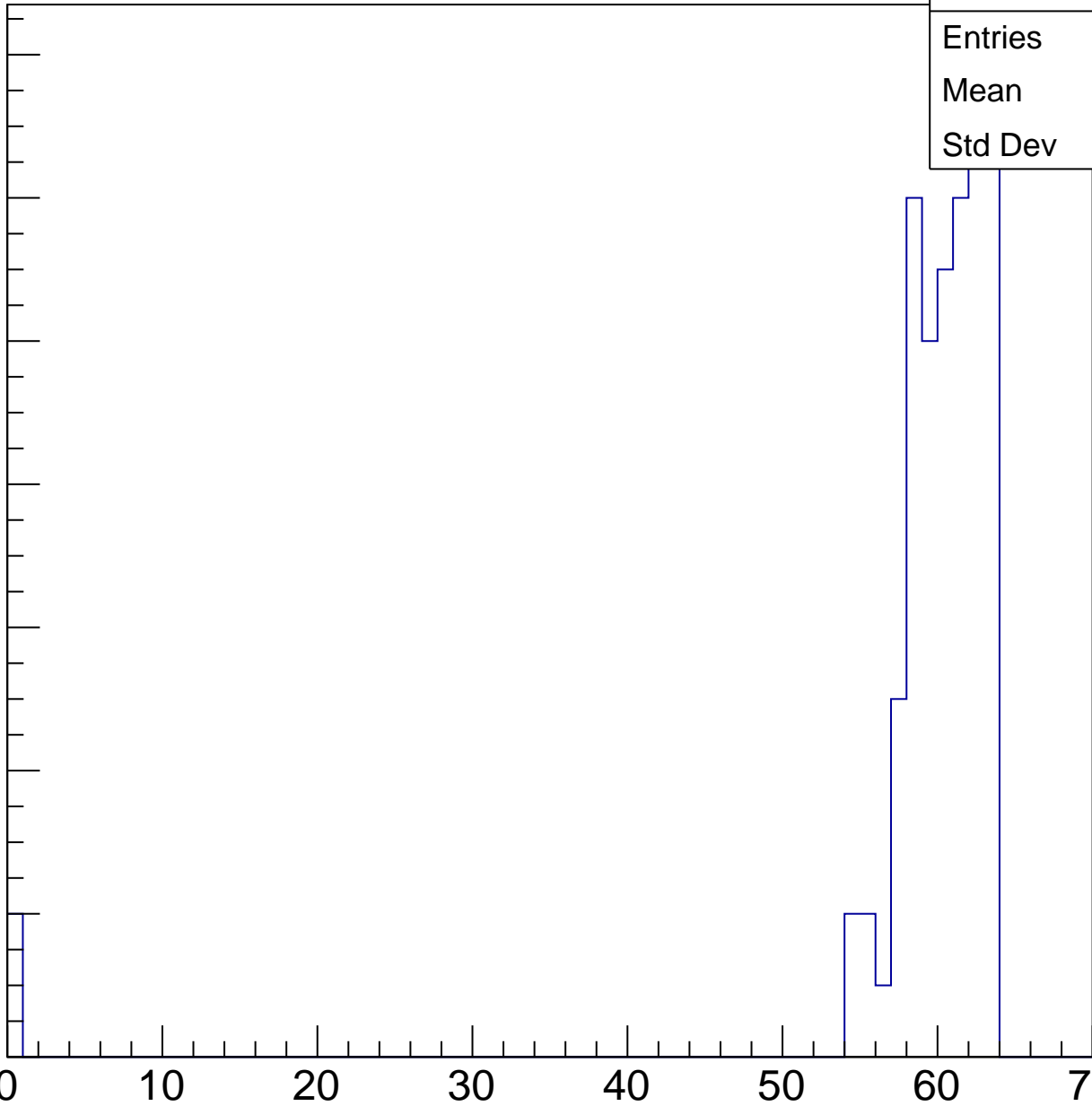
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 58.67 |
| Std Dev | 9.384 |

ampl



B0L101S, U22-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

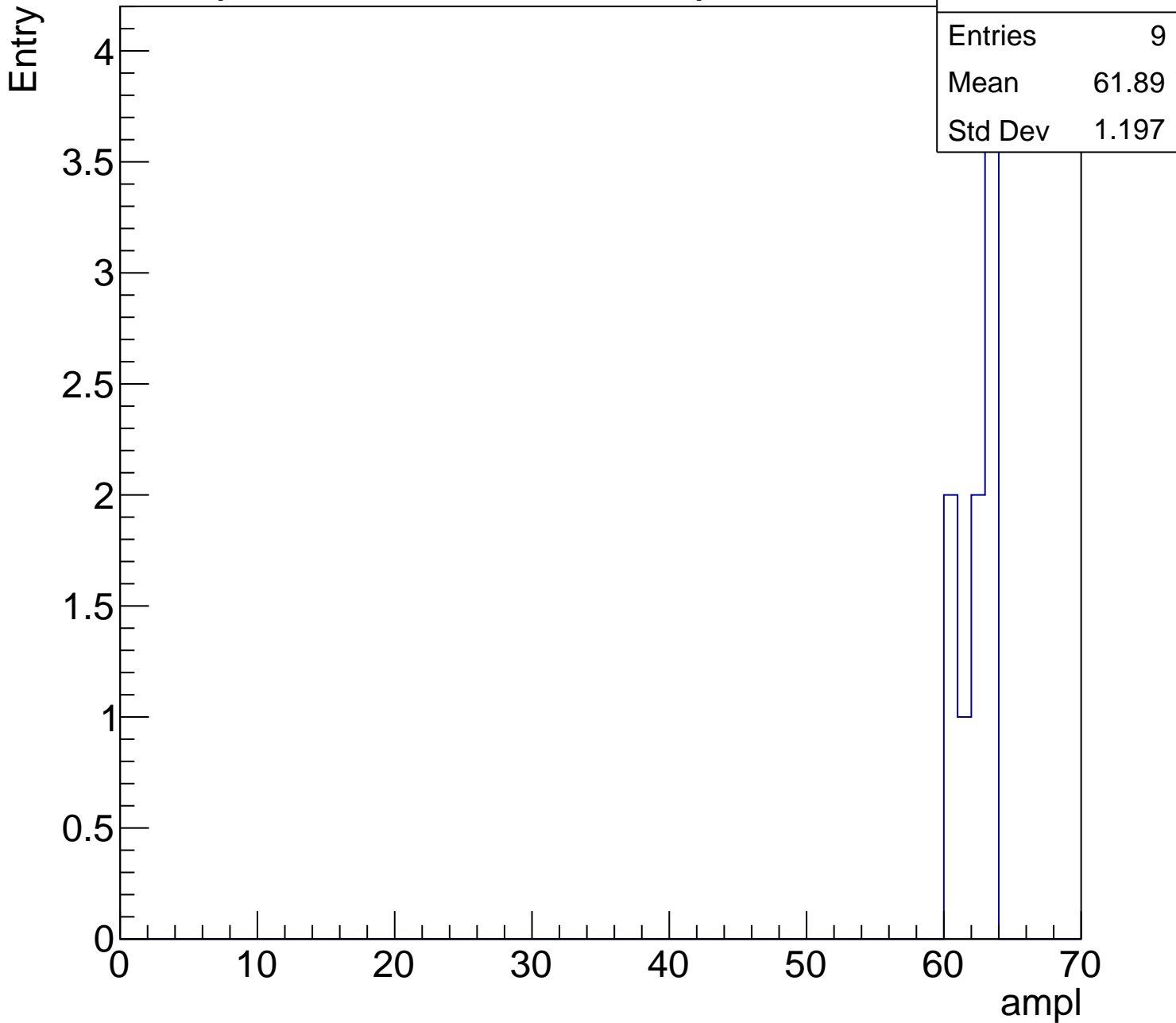
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 9 |
| Mean | 61.89 |
| Std Dev | 1.197 |

ampl

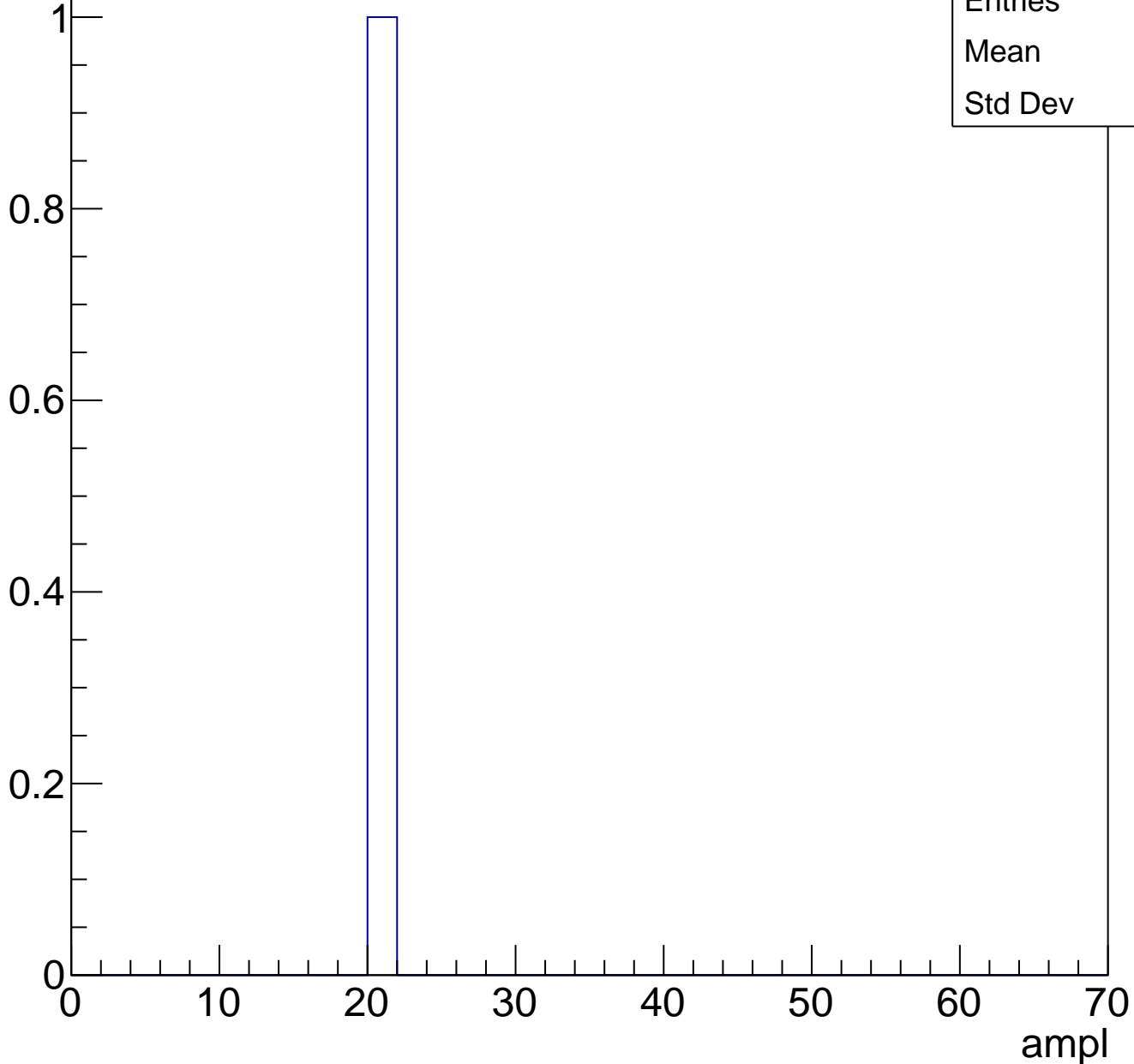
0 10 20 30 40 50 60 70



B0L101S, U22-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch108, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 28.13 |
| Std Dev | 5.101 |

Gaus mean : 28.9180

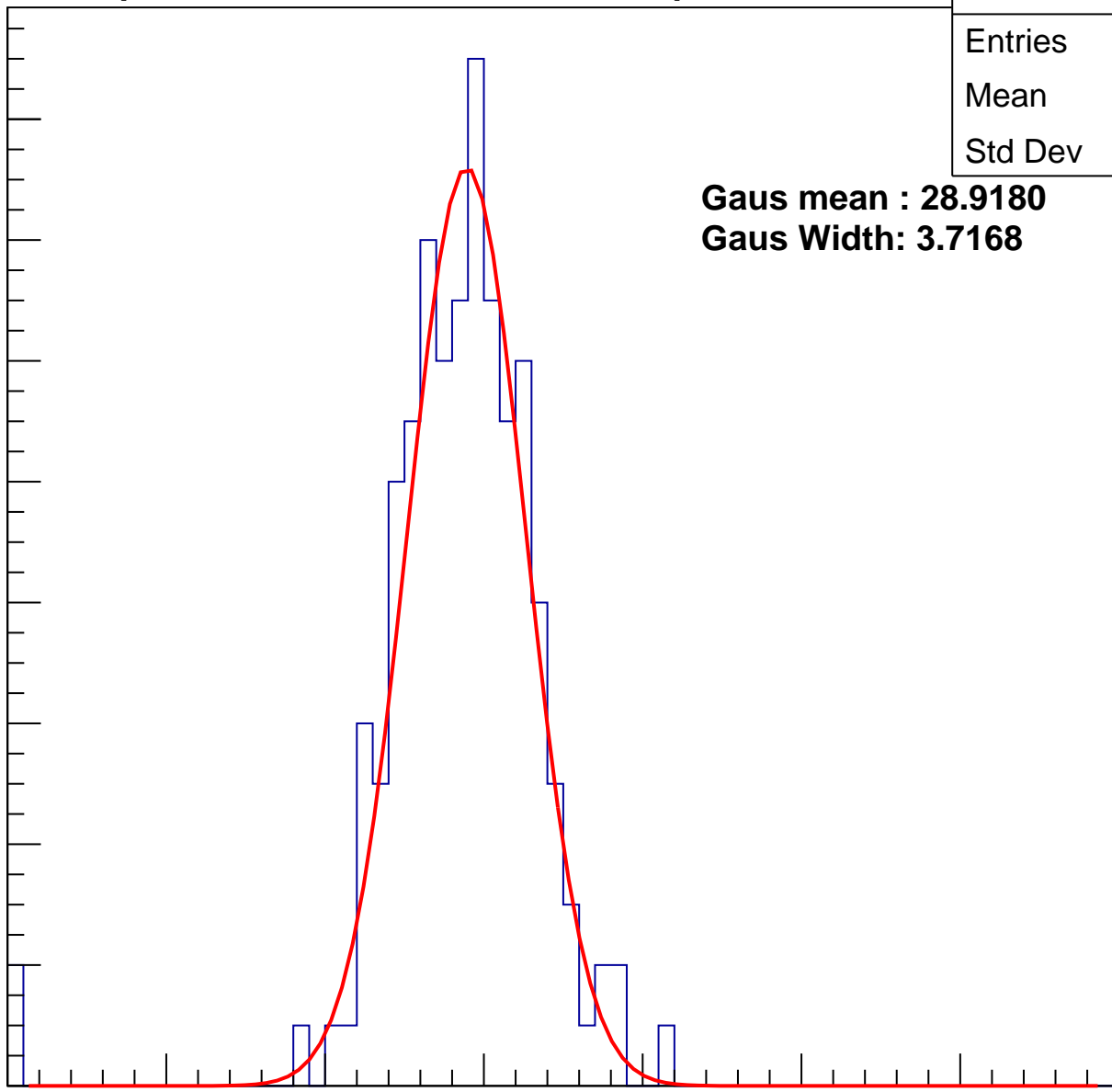
Gaus Width: 3.7168

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



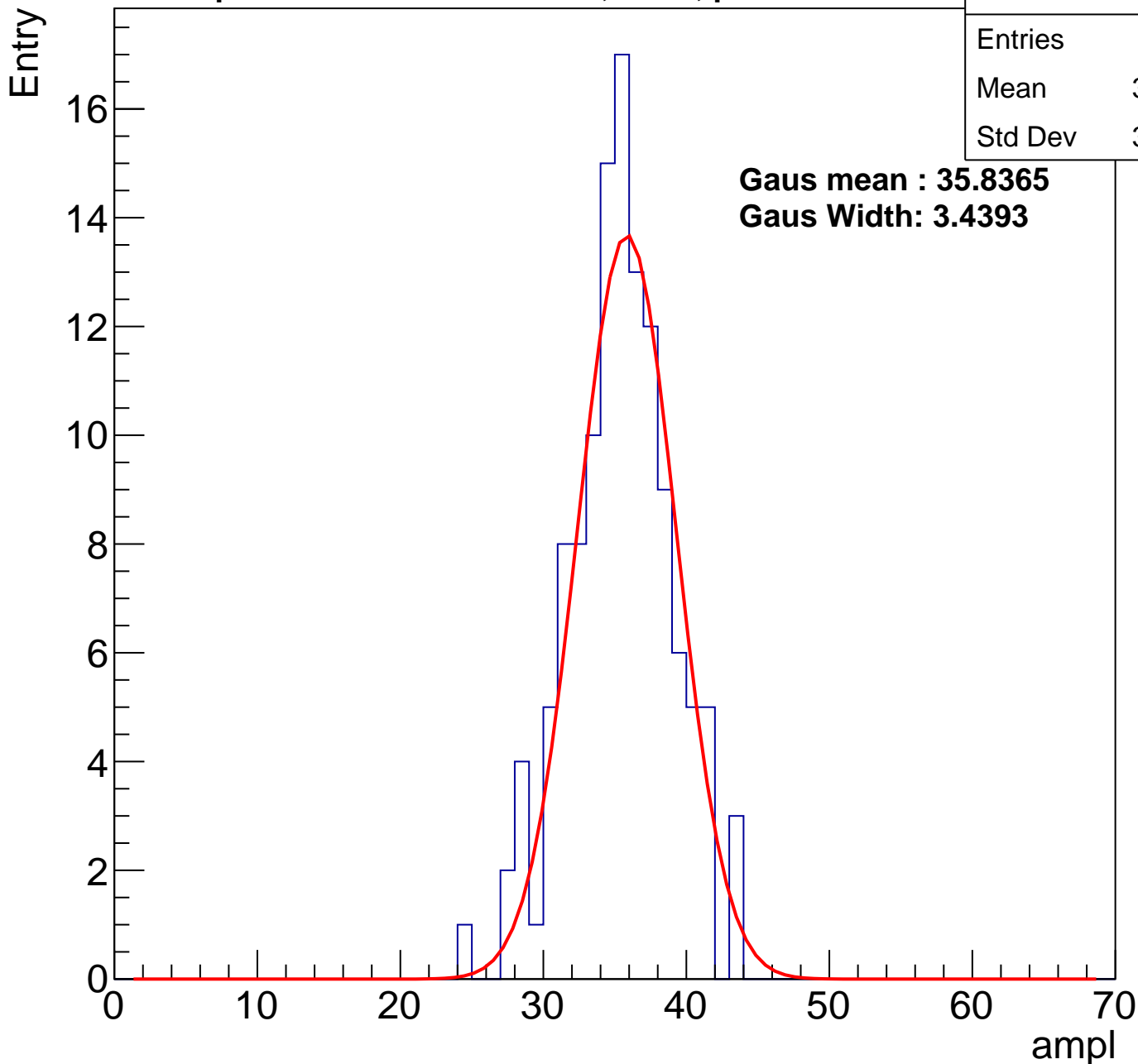
B0L101S, U22-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 124 |
| Mean | 34.92 |
| Std Dev | 3.587 |

Gaus mean : 35.8365

Gaus Width: 3.4393



B0L101S, U22-ch108, adc2

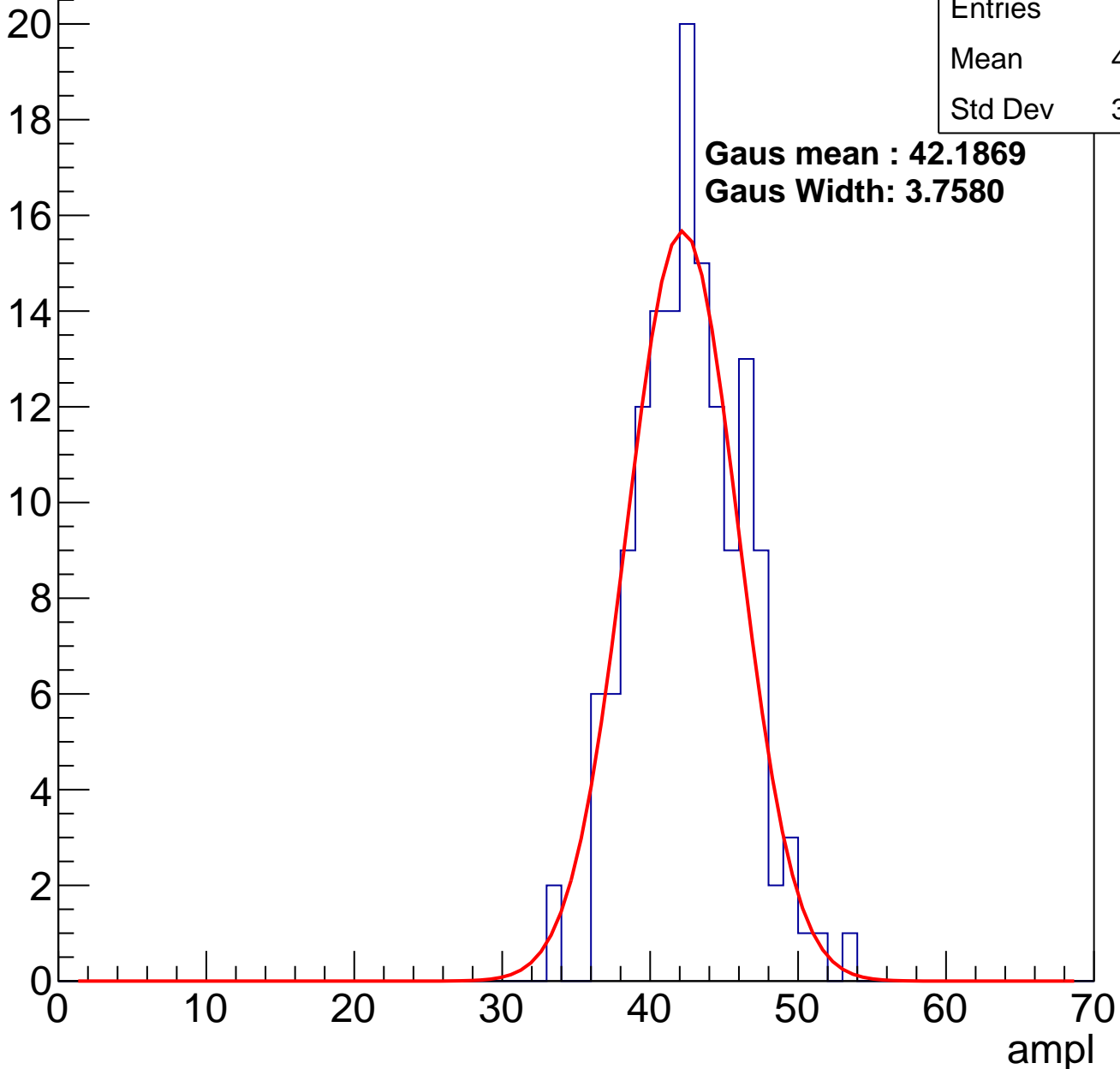
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 149 |
| Mean | 42.17 |
| Std Dev | 3.578 |

Gaus mean : 42.1869

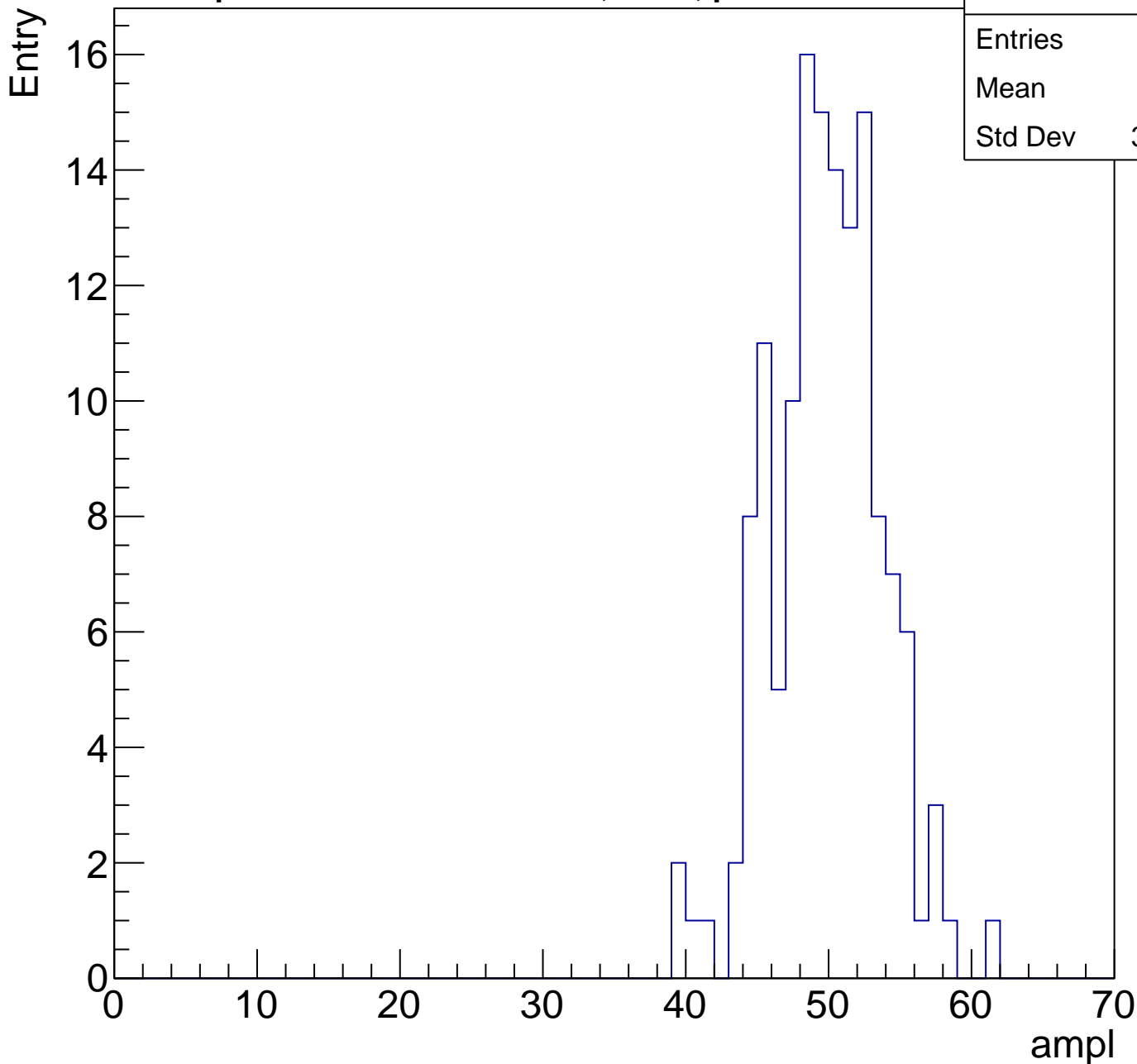
Gaus Width: 3.7580



B0L101S, U22-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

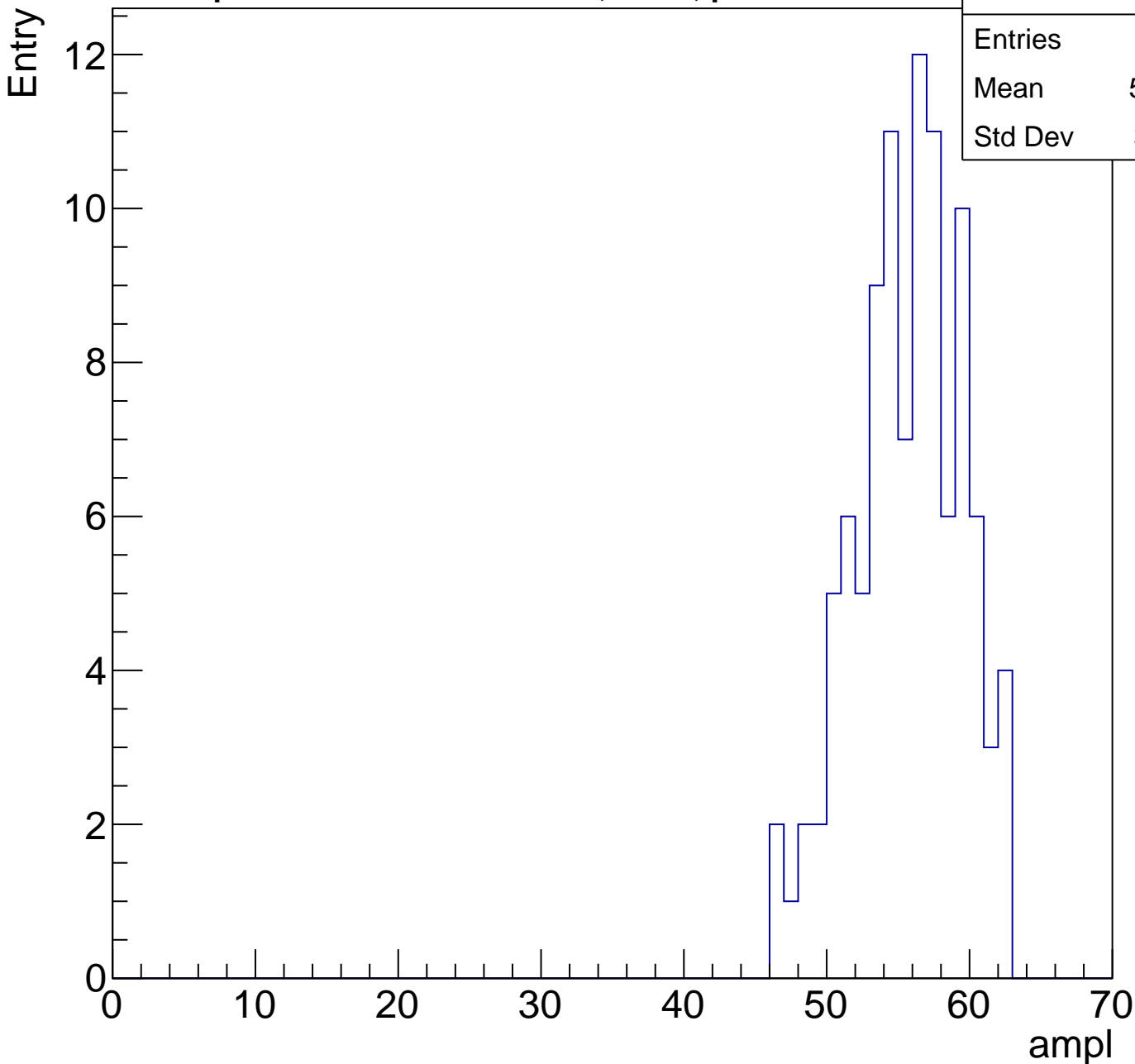
| | |
|---------|-------|
| Entries | 140 |
| Mean | 49.4 |
| Std Dev | 3.834 |



B0L101S, U22-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 102 |
| Mean | 55.23 |
| Std Dev | 3.741 |



B0L101S, U22-ch108, adc5

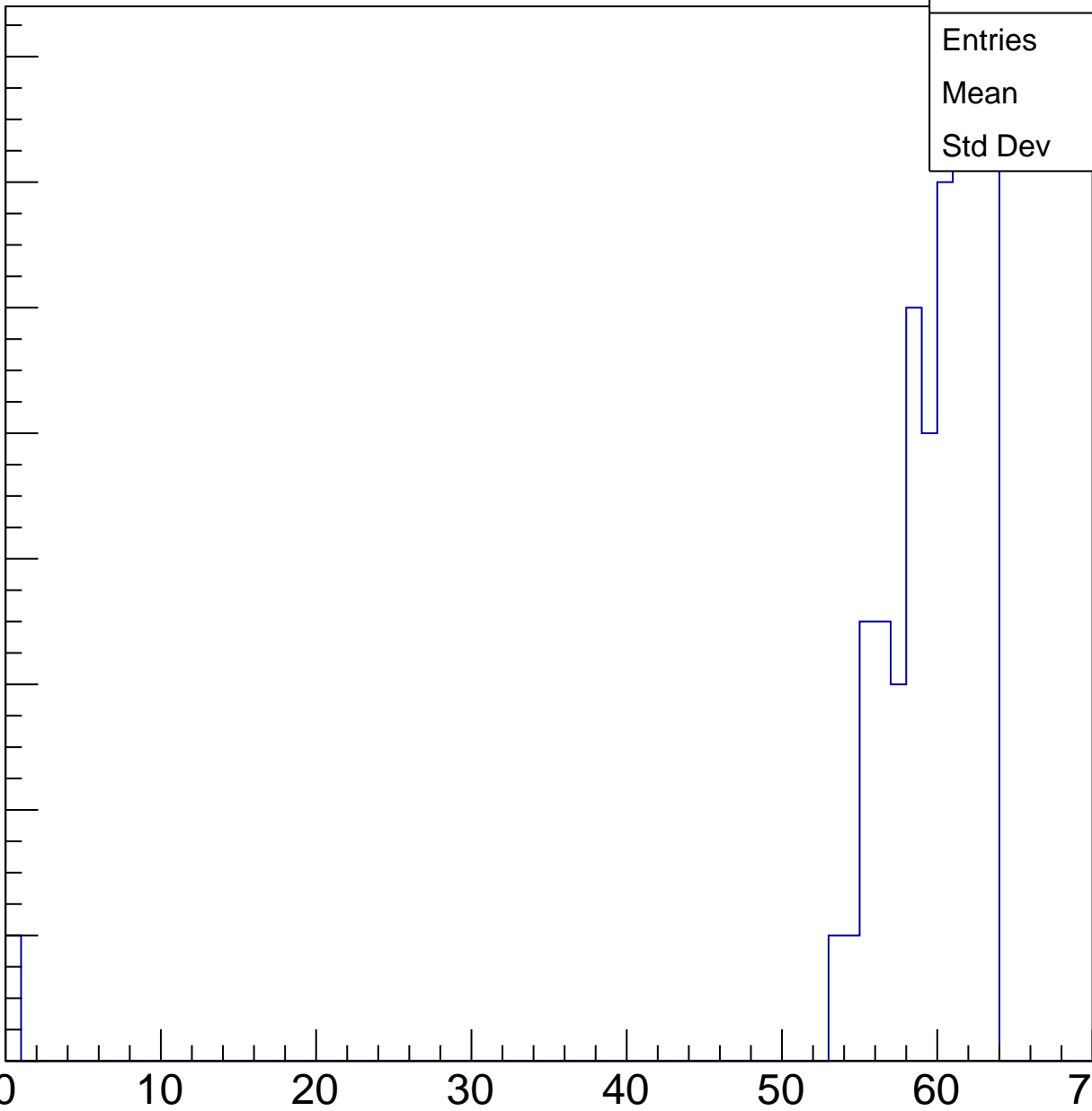
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 108 |
| Mean | 58.43 |
| Std Dev | 8.451 |

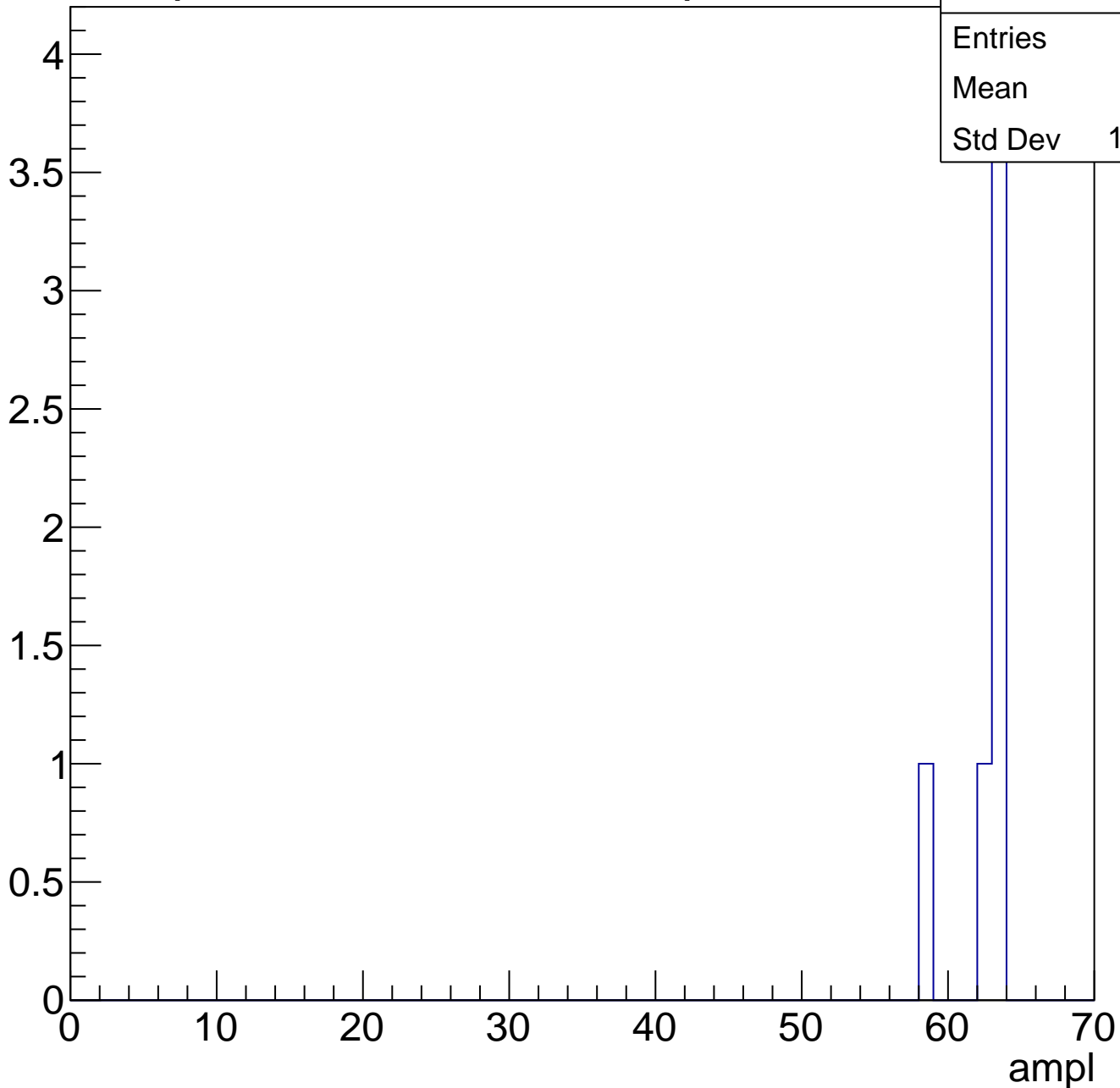
ampl



B0L101S, U22-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch109, adc0

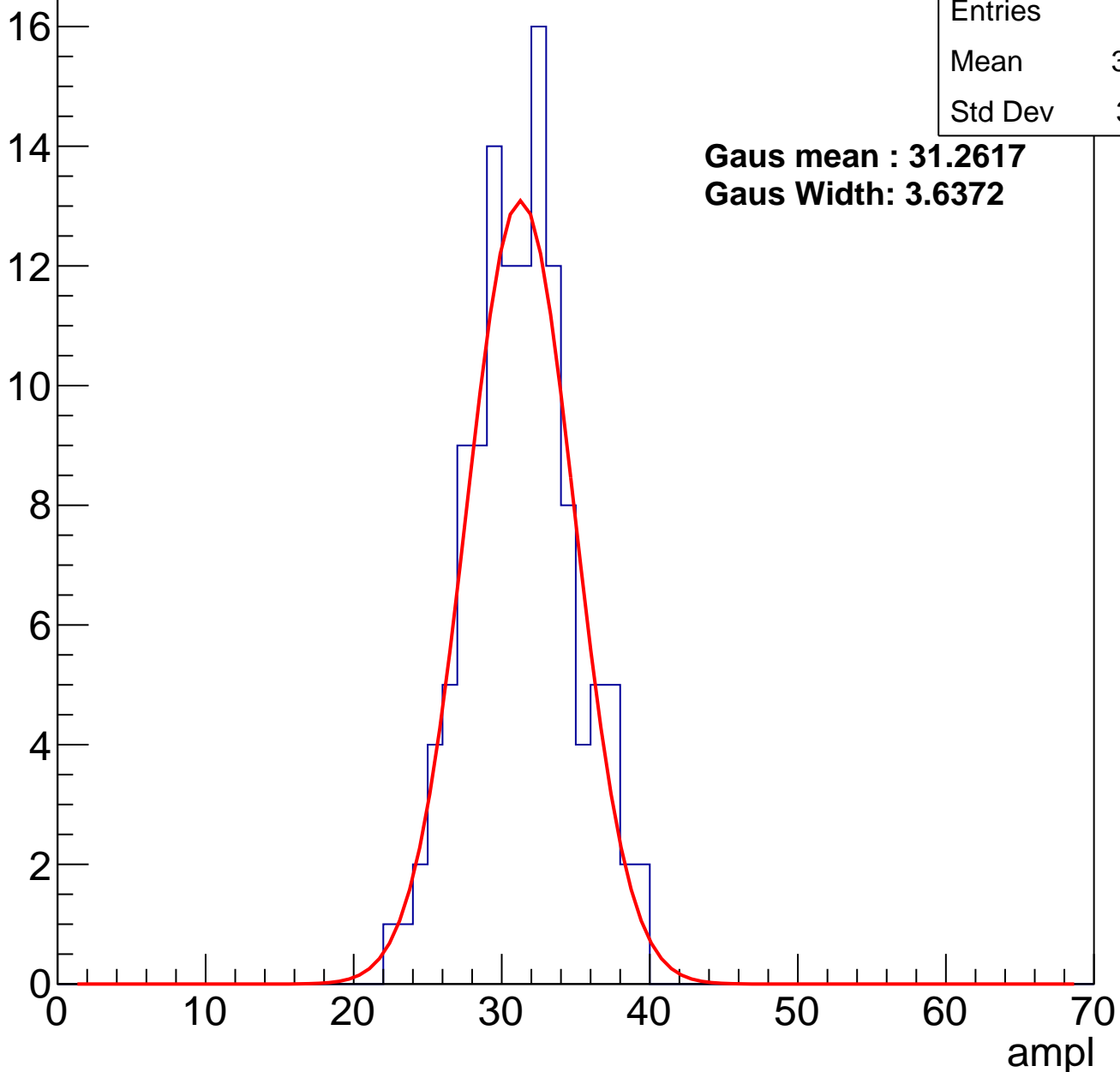
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 30.85 |
| Std Dev | 3.531 |

Gaus mean : 31.2617

Gaus Width: 3.6372

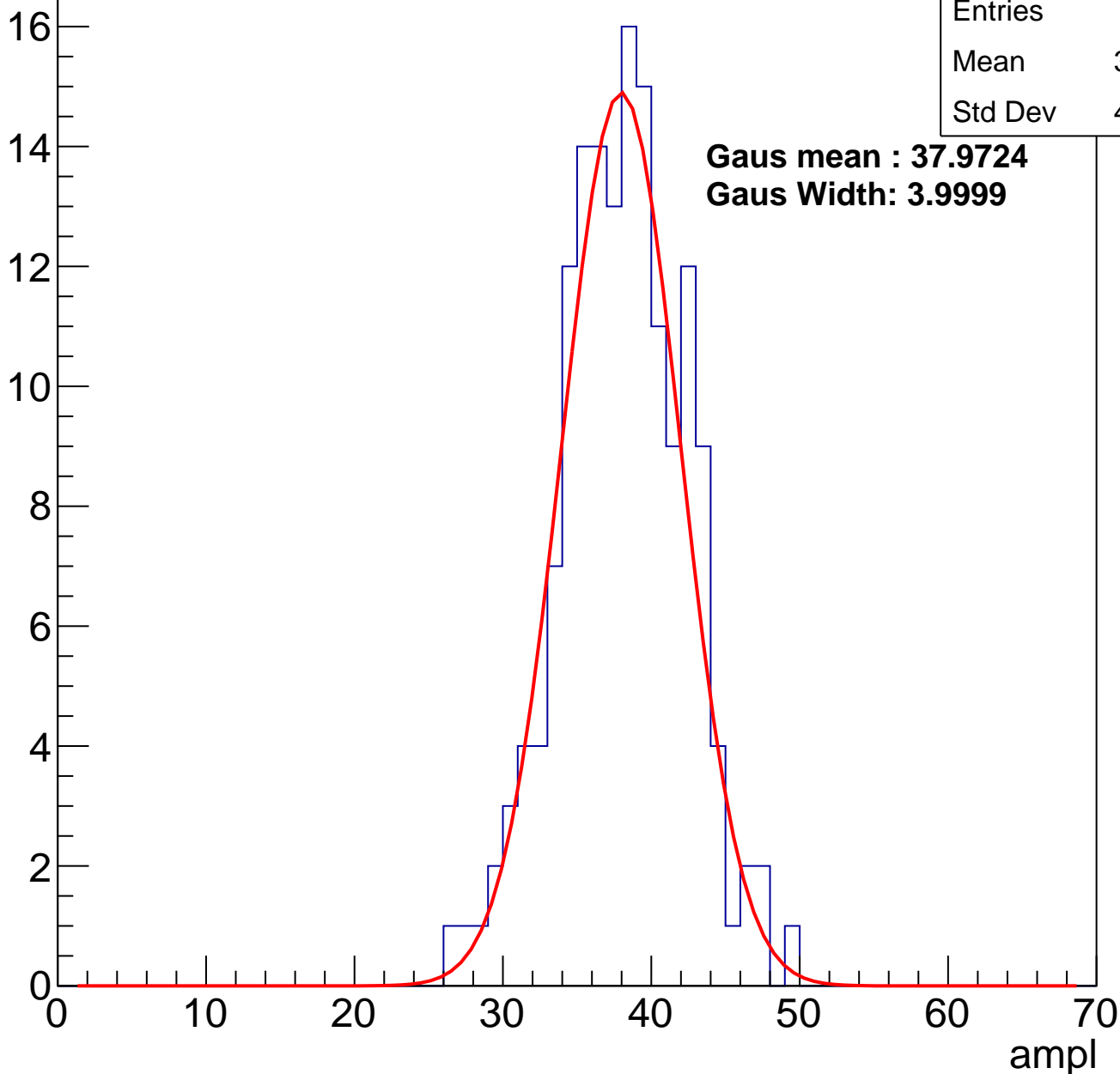
Entry



B0L101S, U22-ch109, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch109, adc2

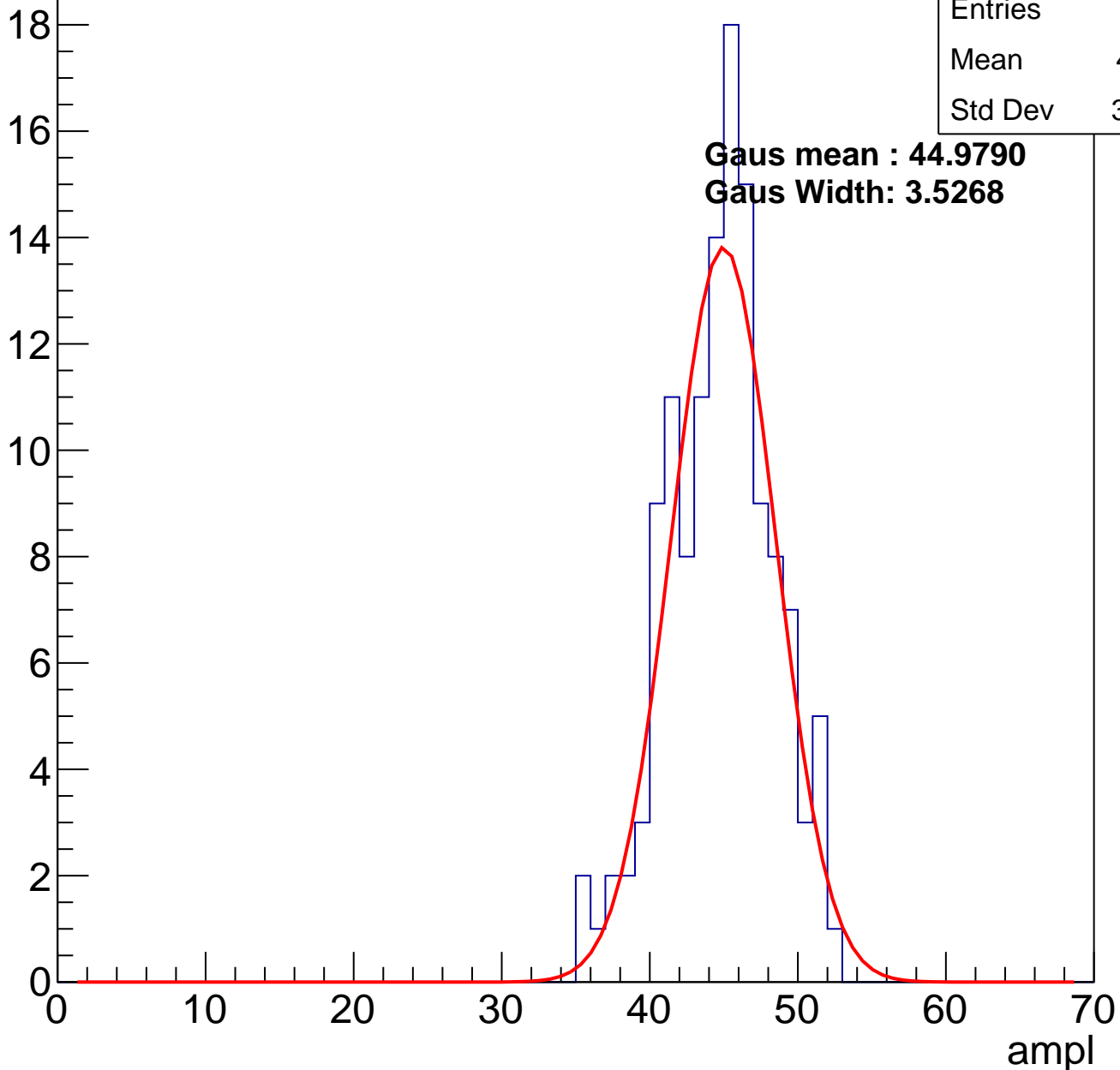
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 129 |
| Mean | 44.31 |
| Std Dev | 3.548 |

Gaus mean : 44.9790

Gaus Width: 3.5268

Entry



B0L101S, U22-ch109, adc3

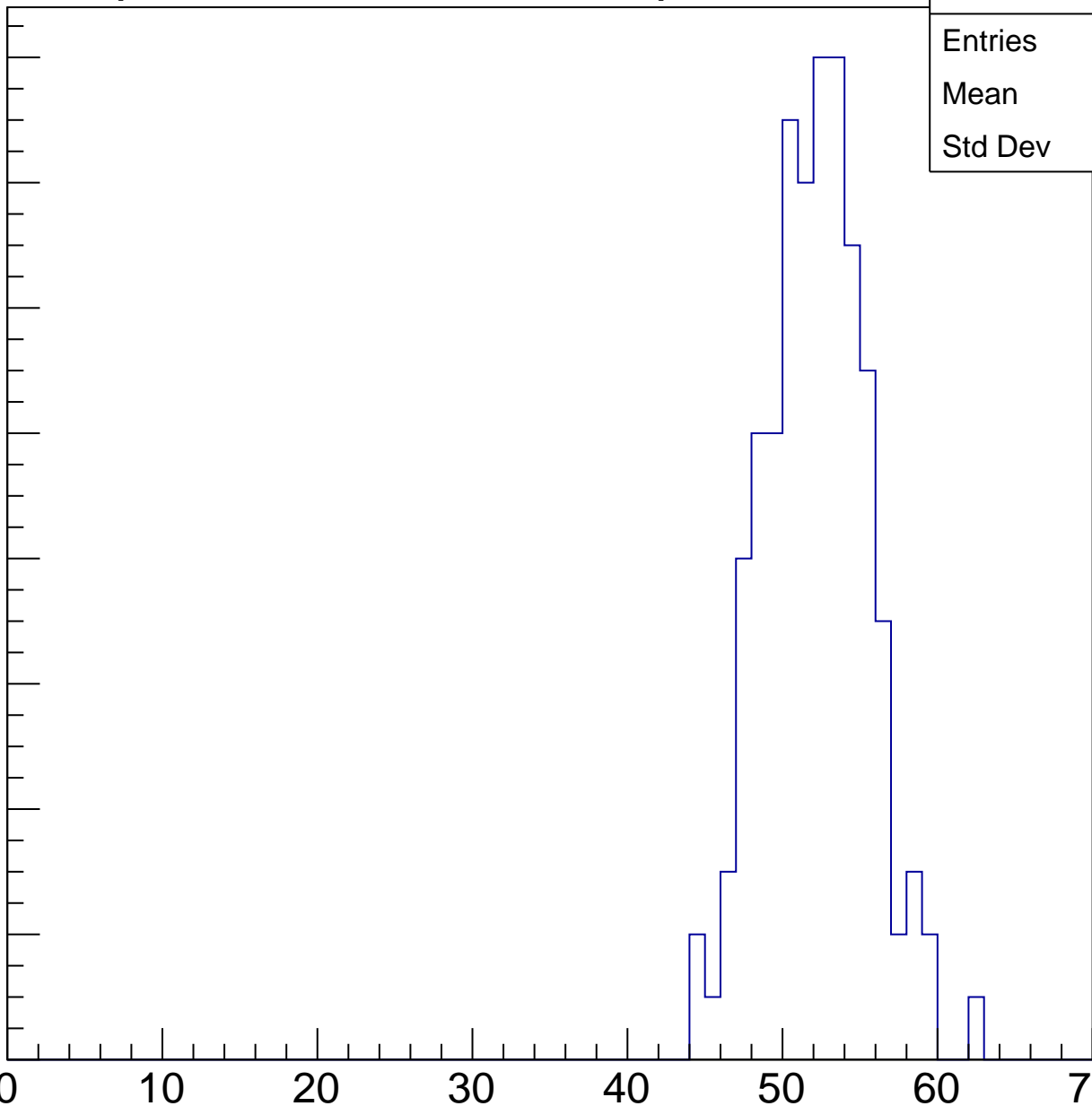
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 51.7 |
| Std Dev | 3.269 |

ampl



B0L101S, U22-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

126

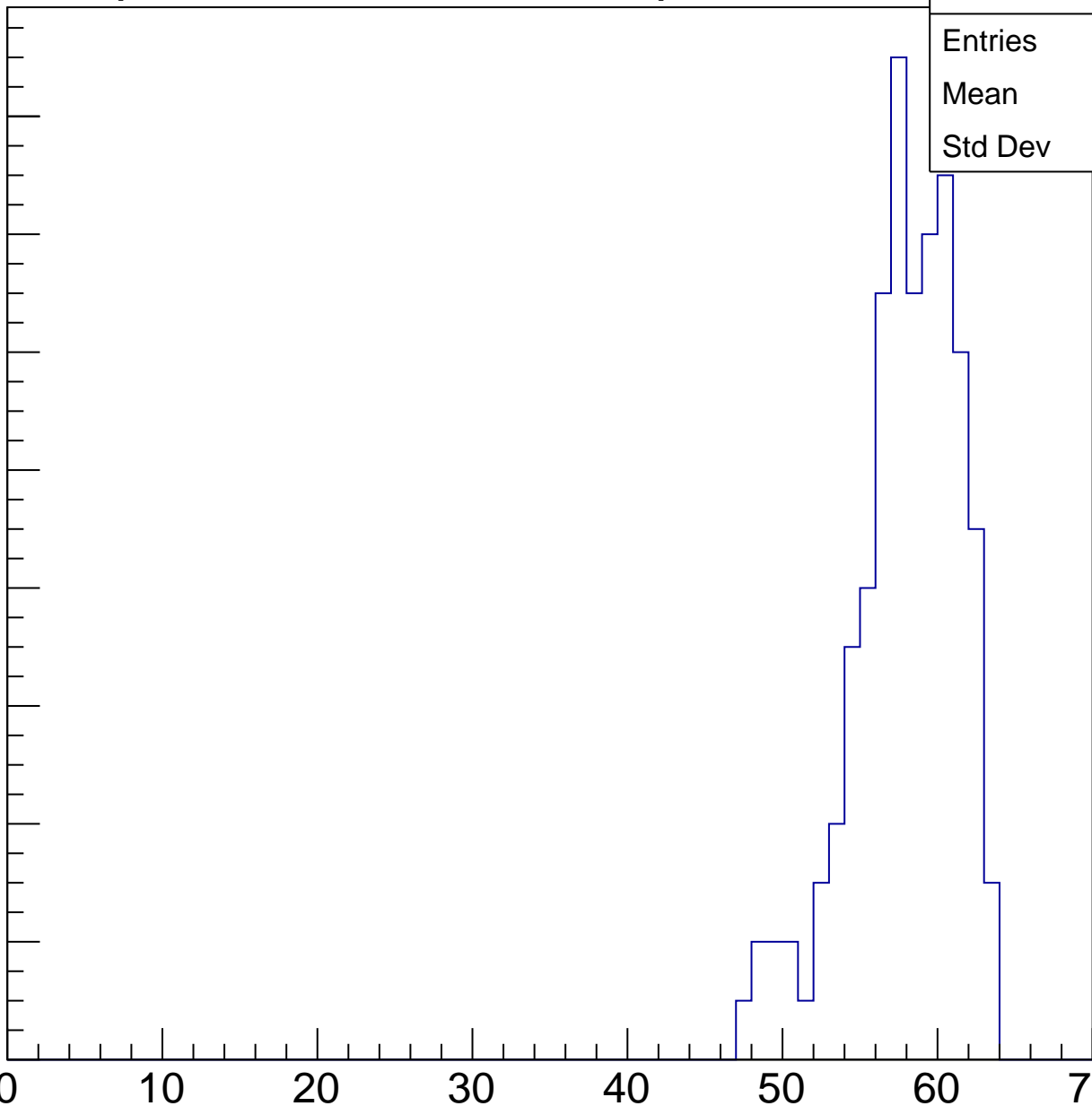
Mean

57.41

Std Dev

3.426

ampl



B0L101S, U22-ch109, adc5

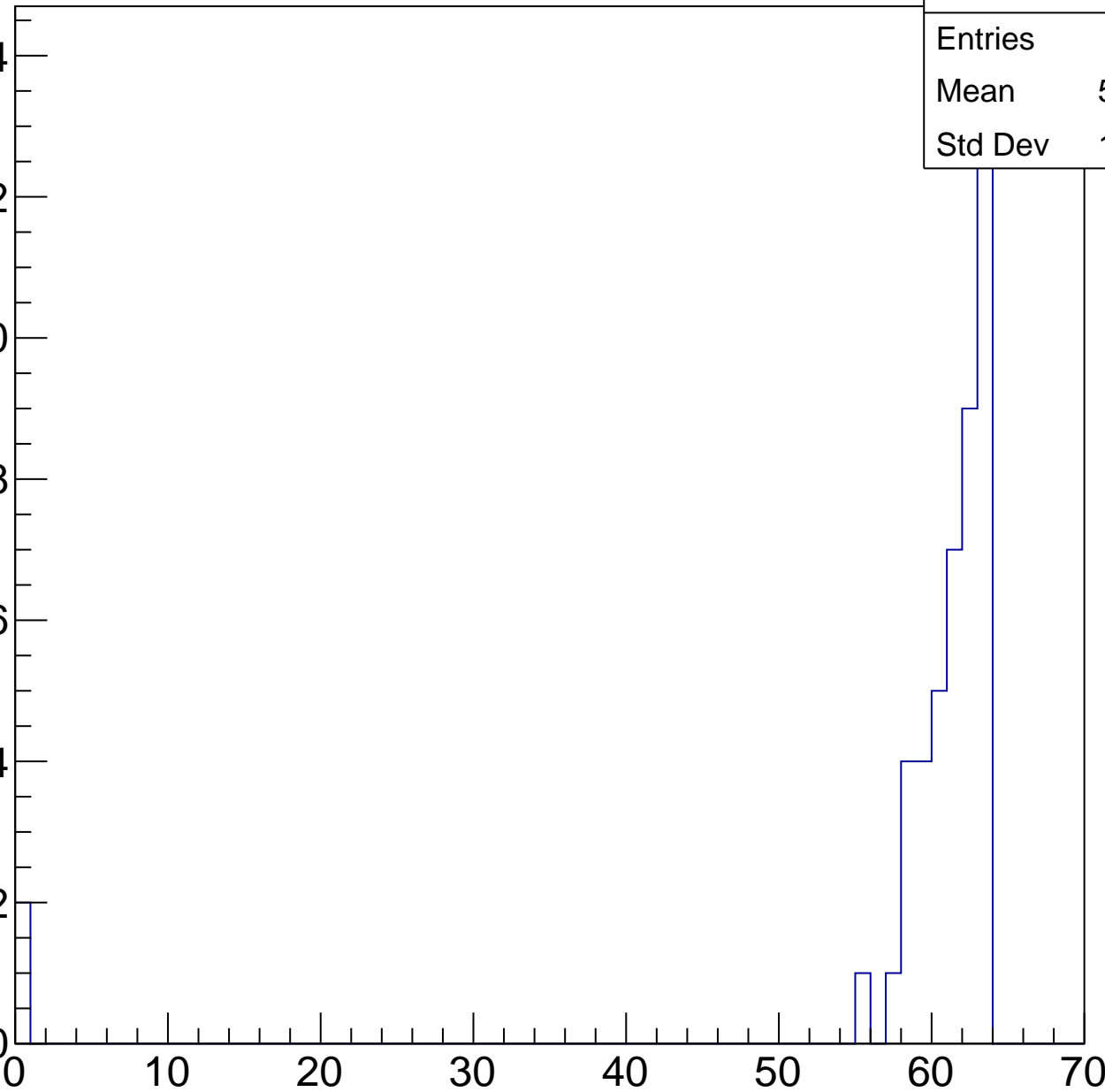
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.45 |
| Std Dev | 12.47 |

Entry

14
12
10
8
6
4
2
0

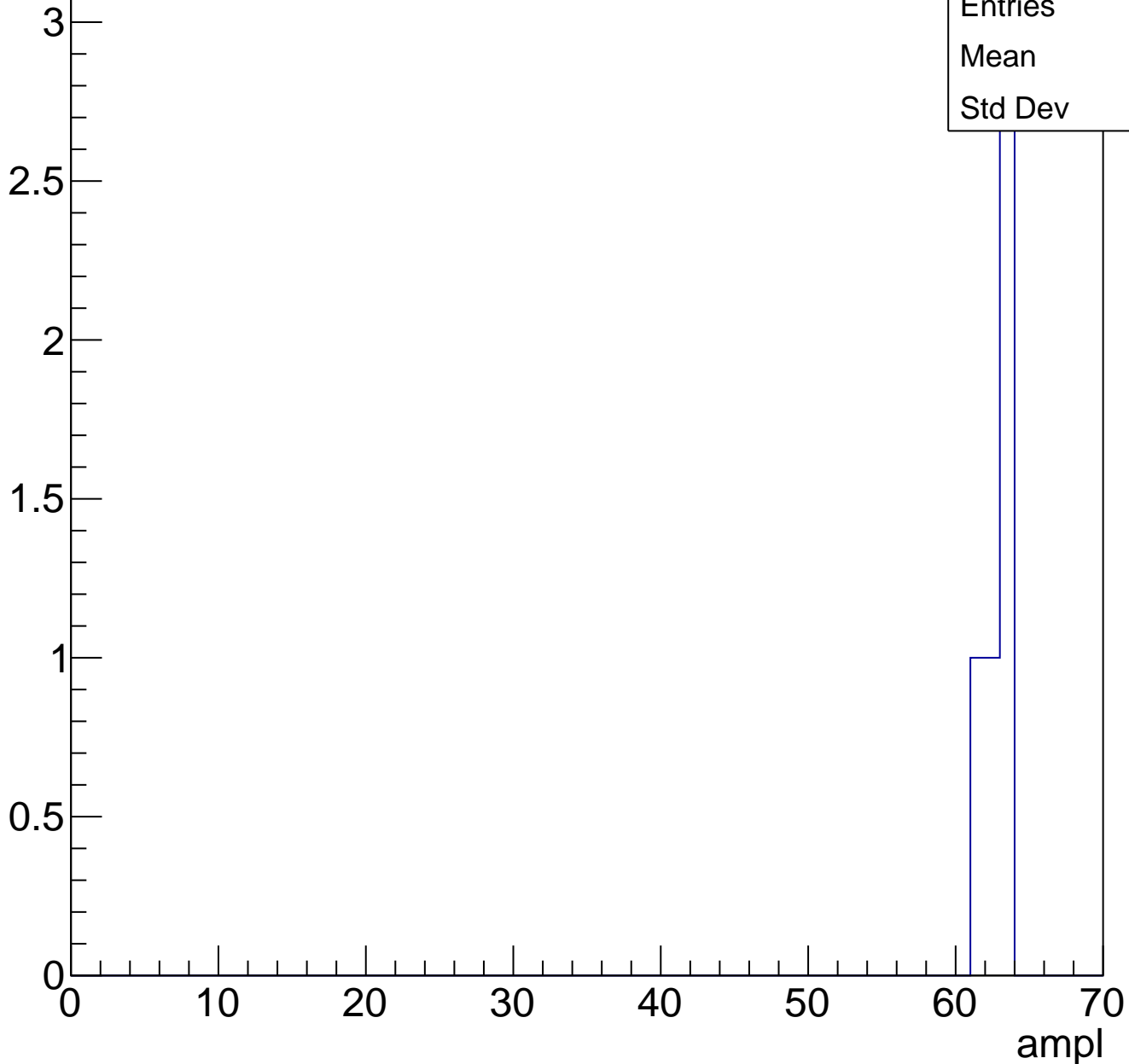
ampl



B0L101S, U22-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch110, adc0

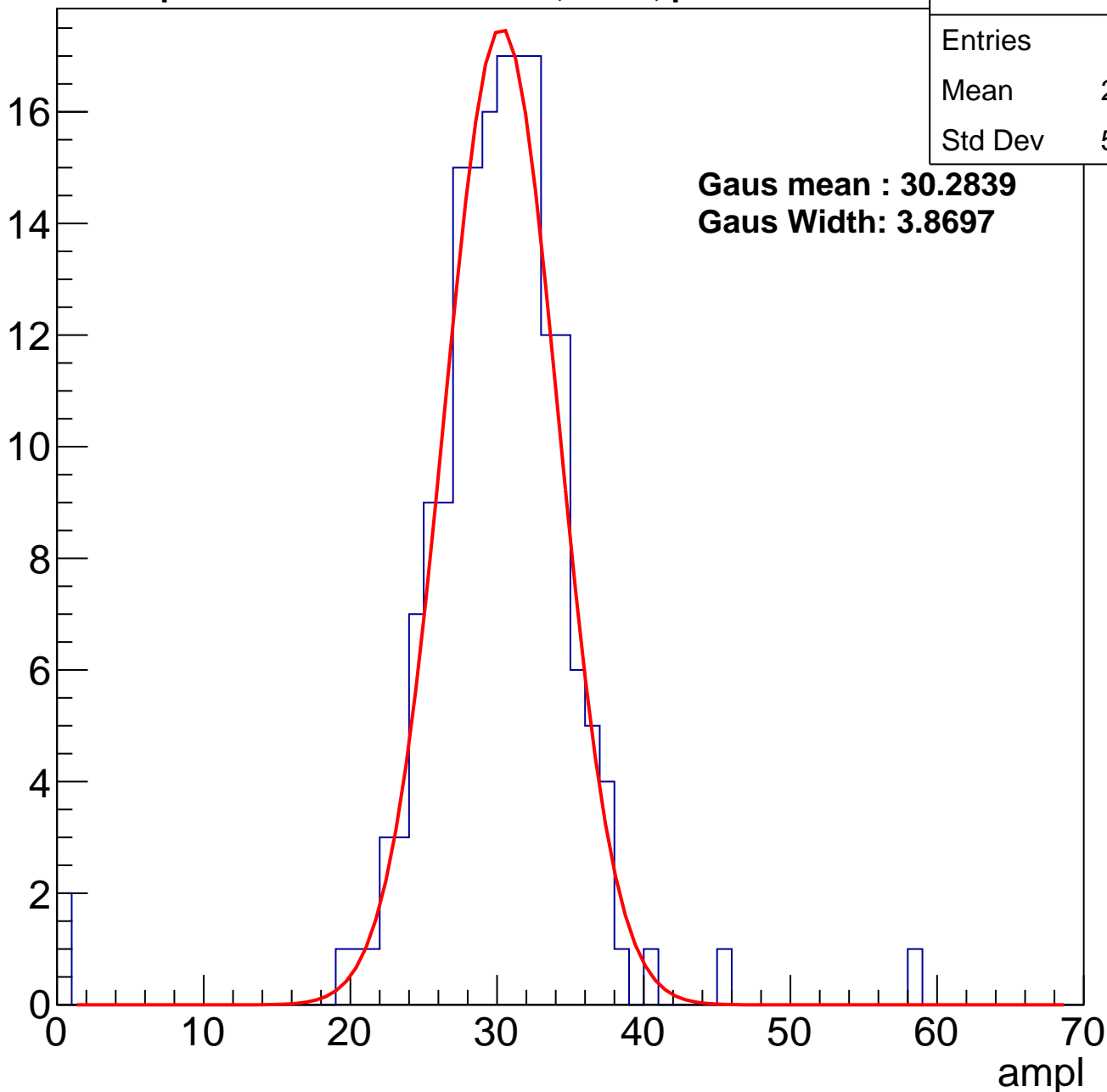
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 176 |
| Mean | 29.63 |
| Std Dev | 5.505 |

Gaus mean : 30.2839

Gaus Width: 3.8697

Entry



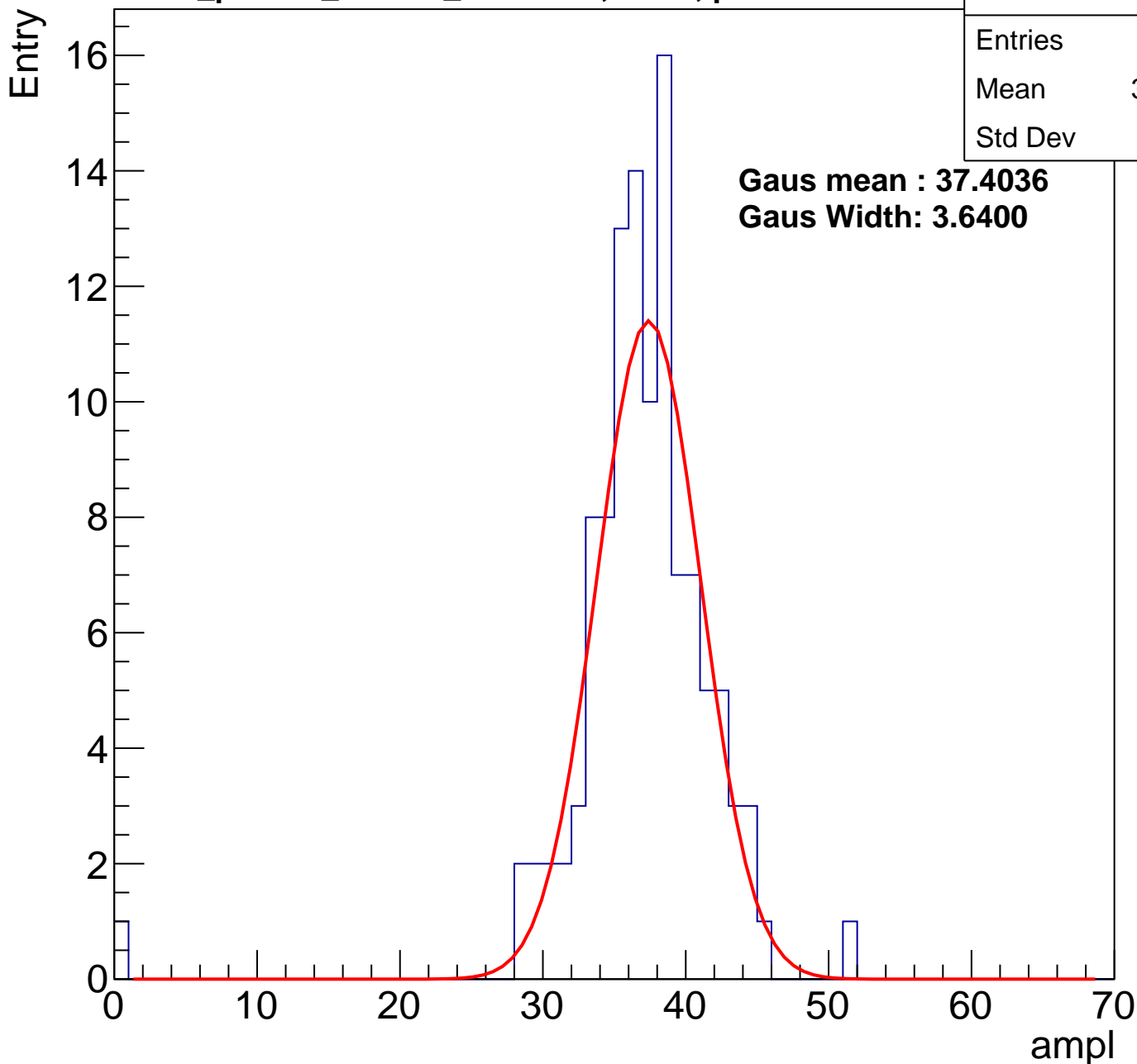
B0L101S, U22-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 113 |
| Mean | 36.55 |
| Std Dev | 5.13 |

Gaus mean : 37.4036

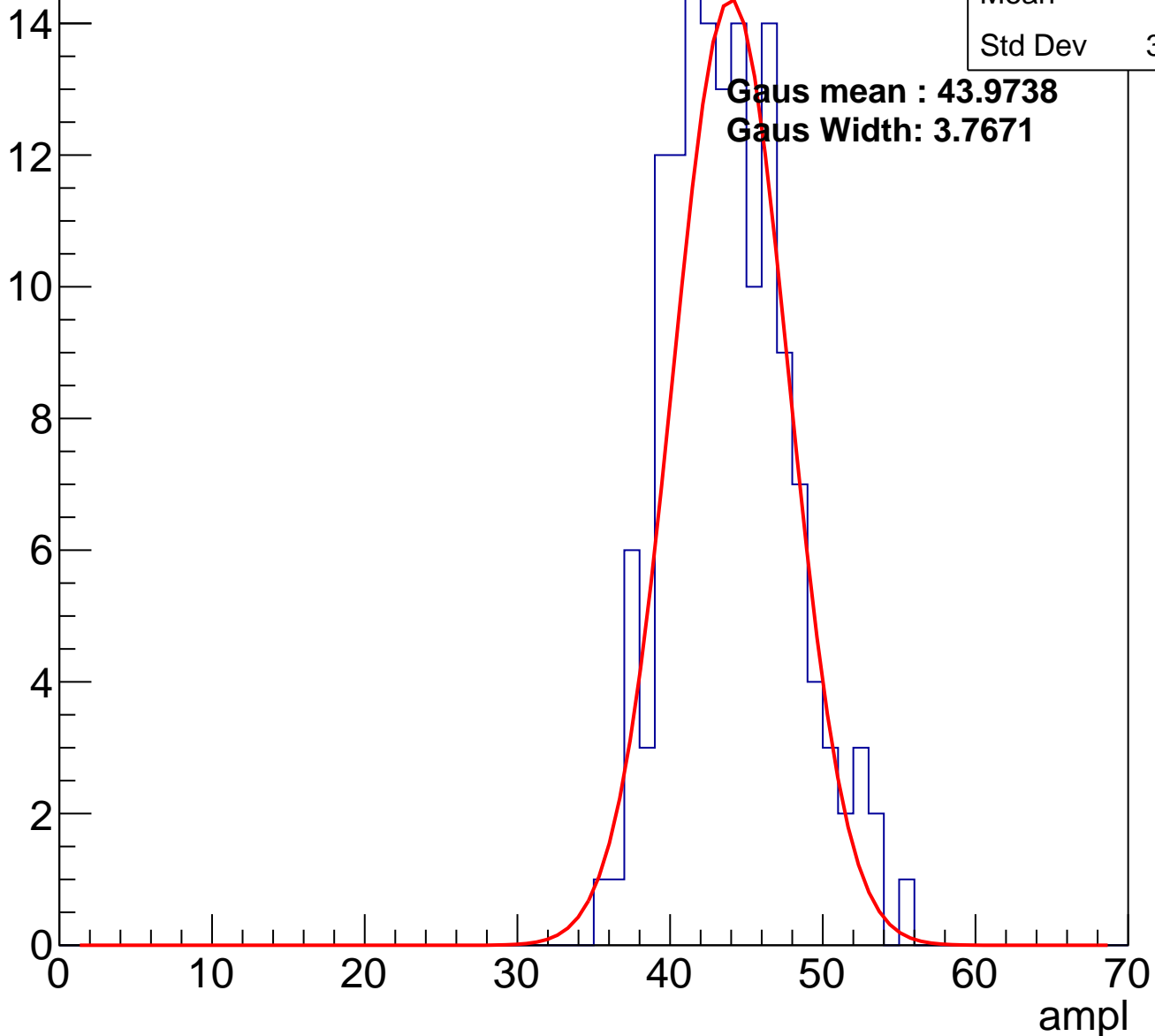
Gaus Width: 3.6400



B0L101S, U22-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 146 |
| Mean | 43.5 |
| Std Dev | 3.923 |

B0L101S, U22-ch110, adc3

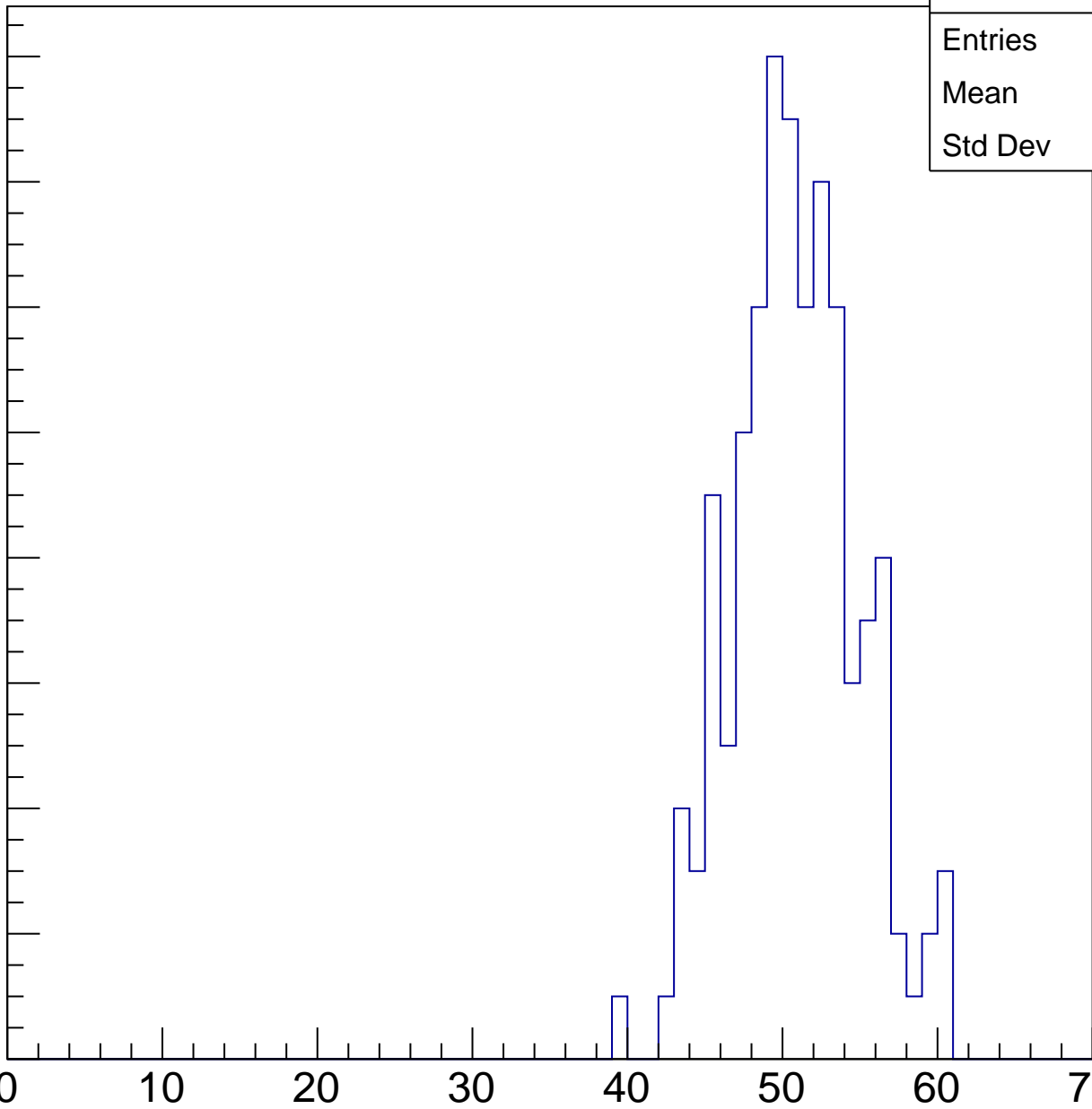
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 143 |
| Mean | 50.37 |
| Std Dev | 3.999 |

ampl



B0L101S, U22-ch110, adc4

calib_packv5_042523_0143.root, FC#1, port C1

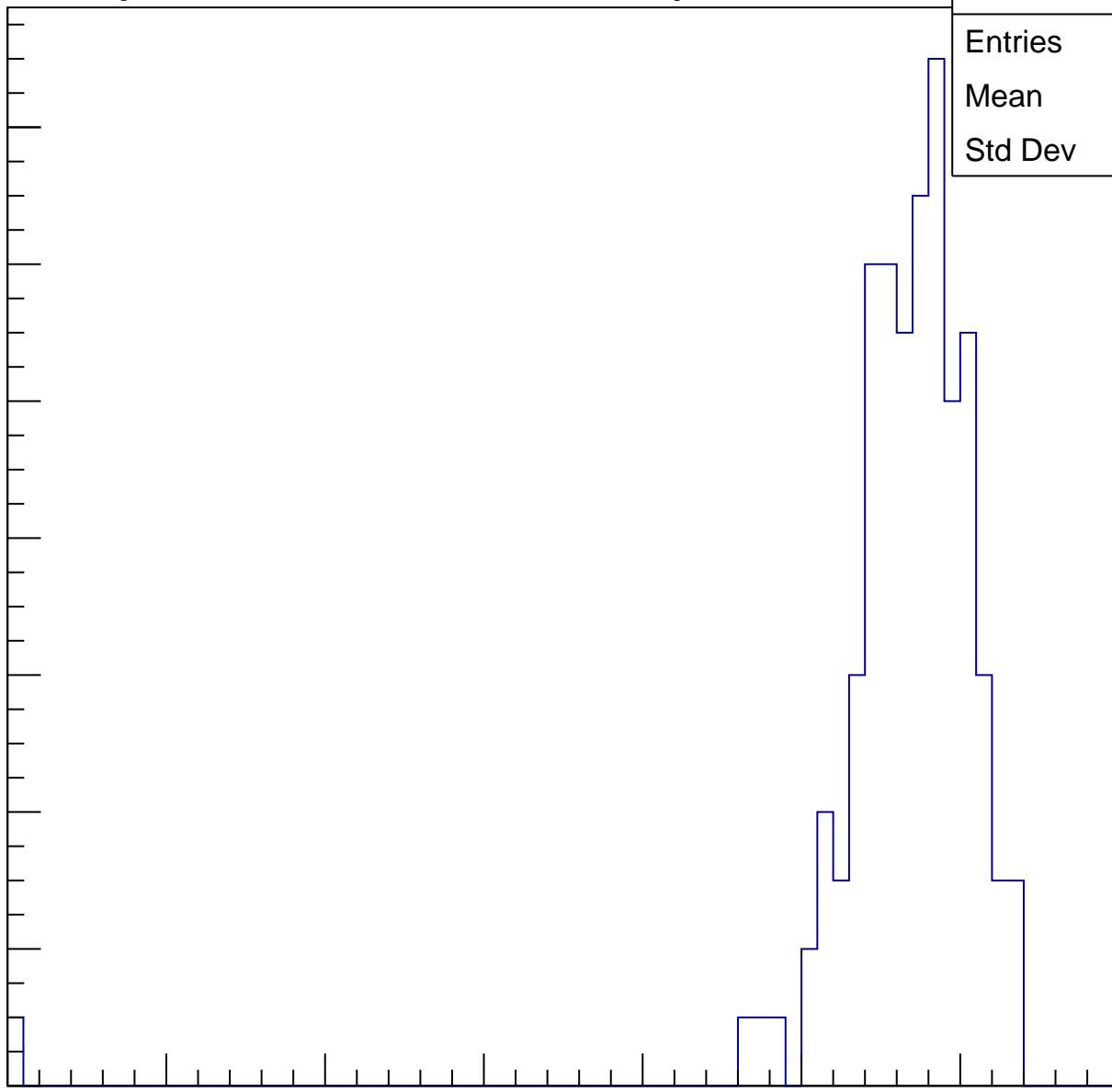
| | |
|---------|-------|
| Entries | 115 |
| Mean | 56.04 |
| Std Dev | 6.227 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch110, adc5

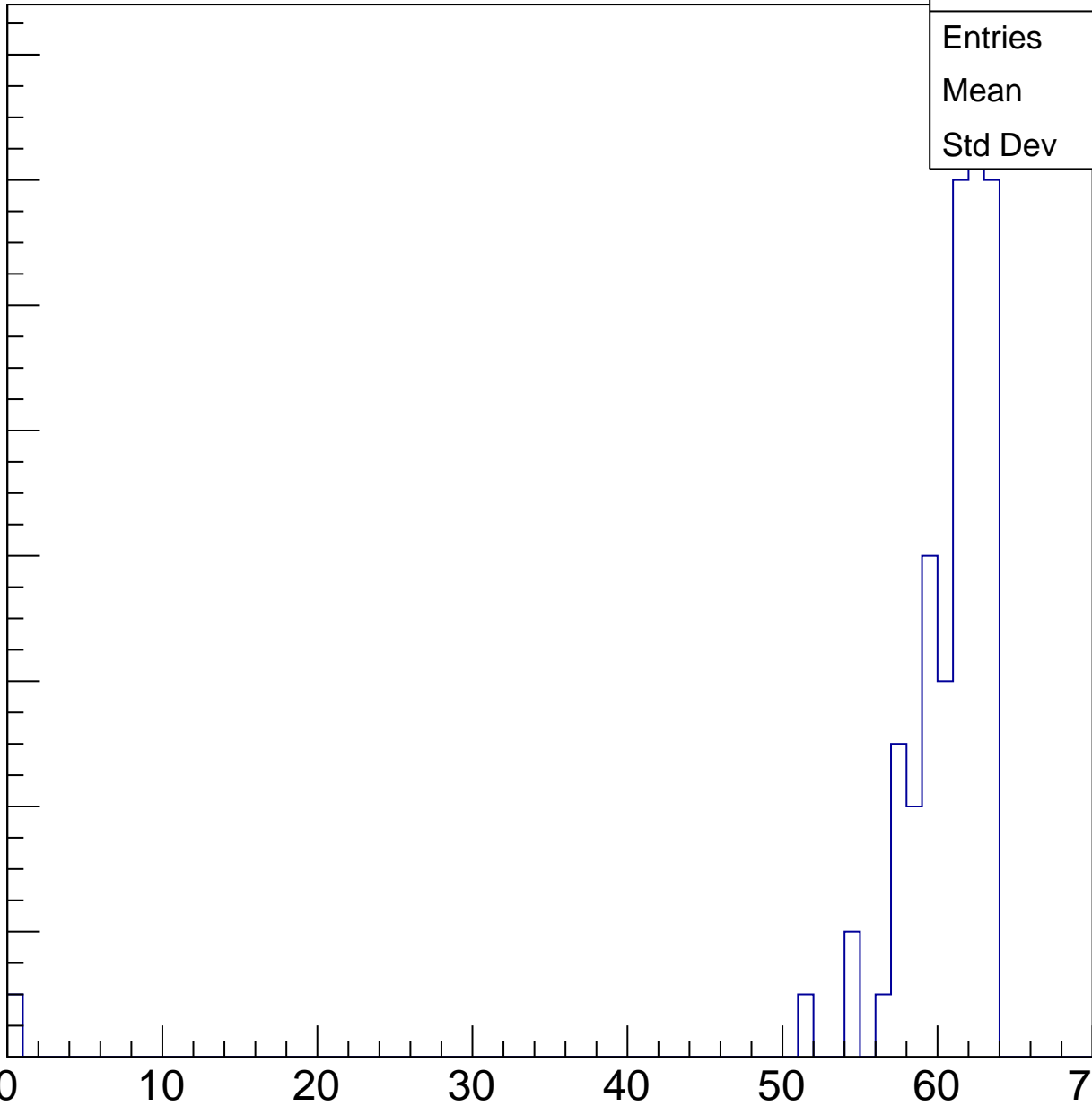
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 59.61 |
| Std Dev | 7.482 |

ampl



B0L101S, U22-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch111, adc0

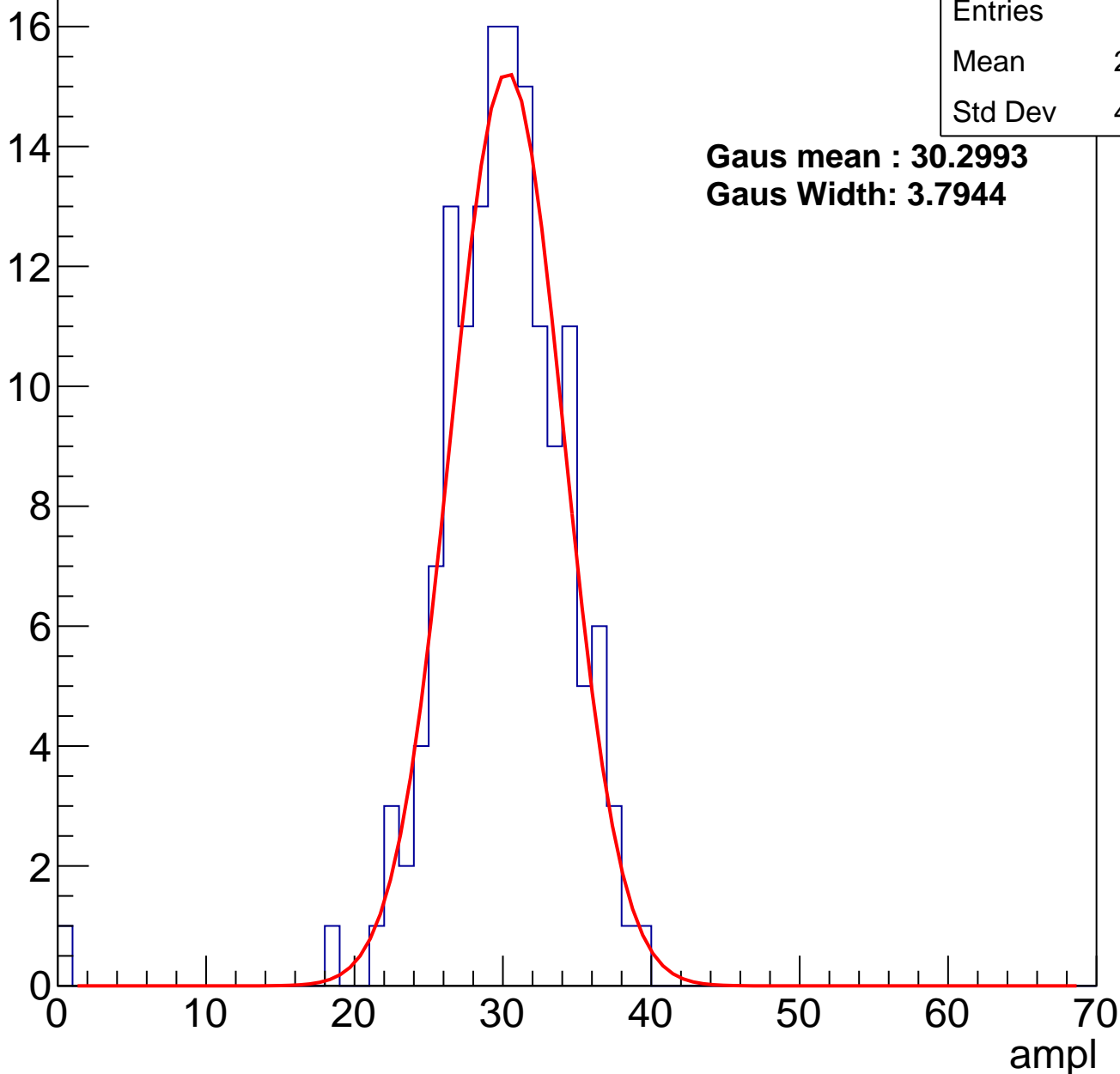
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 29.55 |
| Std Dev | 4.492 |

Gaus mean : 30.2993

Gaus Width: 3.7944

Entry



B0L101S, U22-ch111, adc1

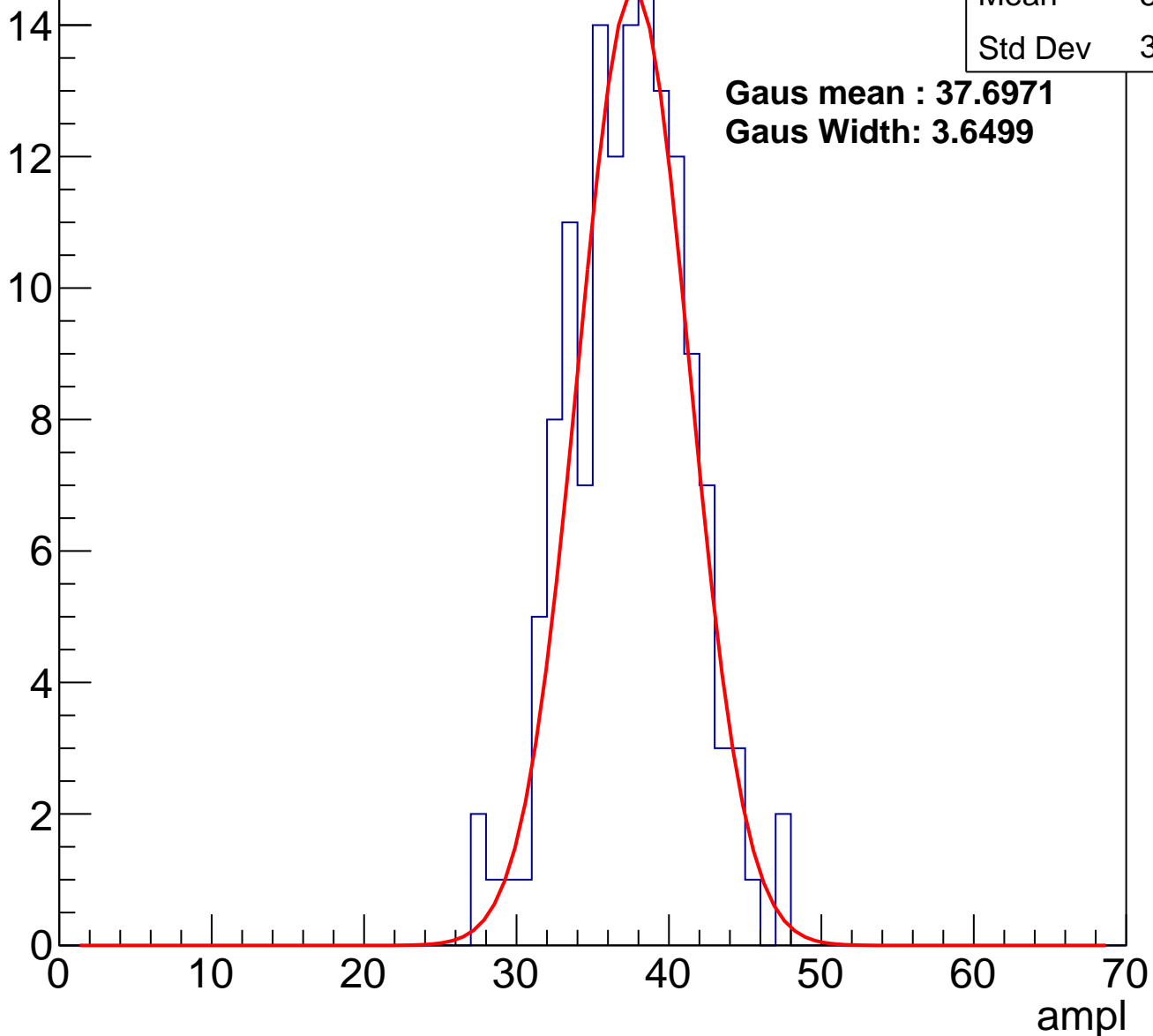
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 36.97 |
| Std Dev | 3.854 |

Gaus mean : 37.6971

Gaus Width: 3.6499

Entry



B0L101S, U22-ch111, adc2

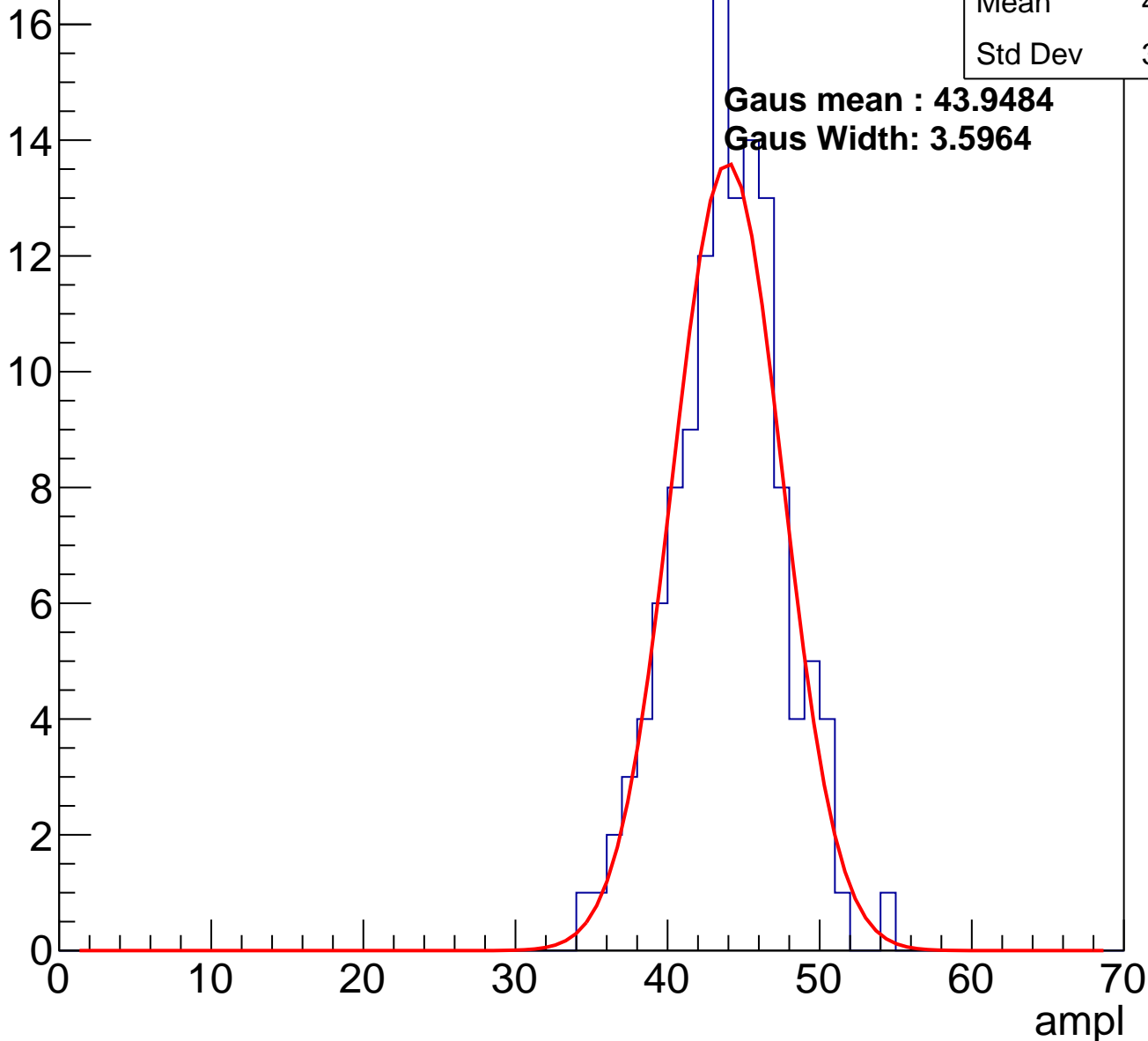
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 126 |
| Mean | 43.49 |
| Std Dev | 3.576 |

Gaus mean : 43.9484

Gaus Width: 3.5964



B0L101S, U22-ch111, adc3

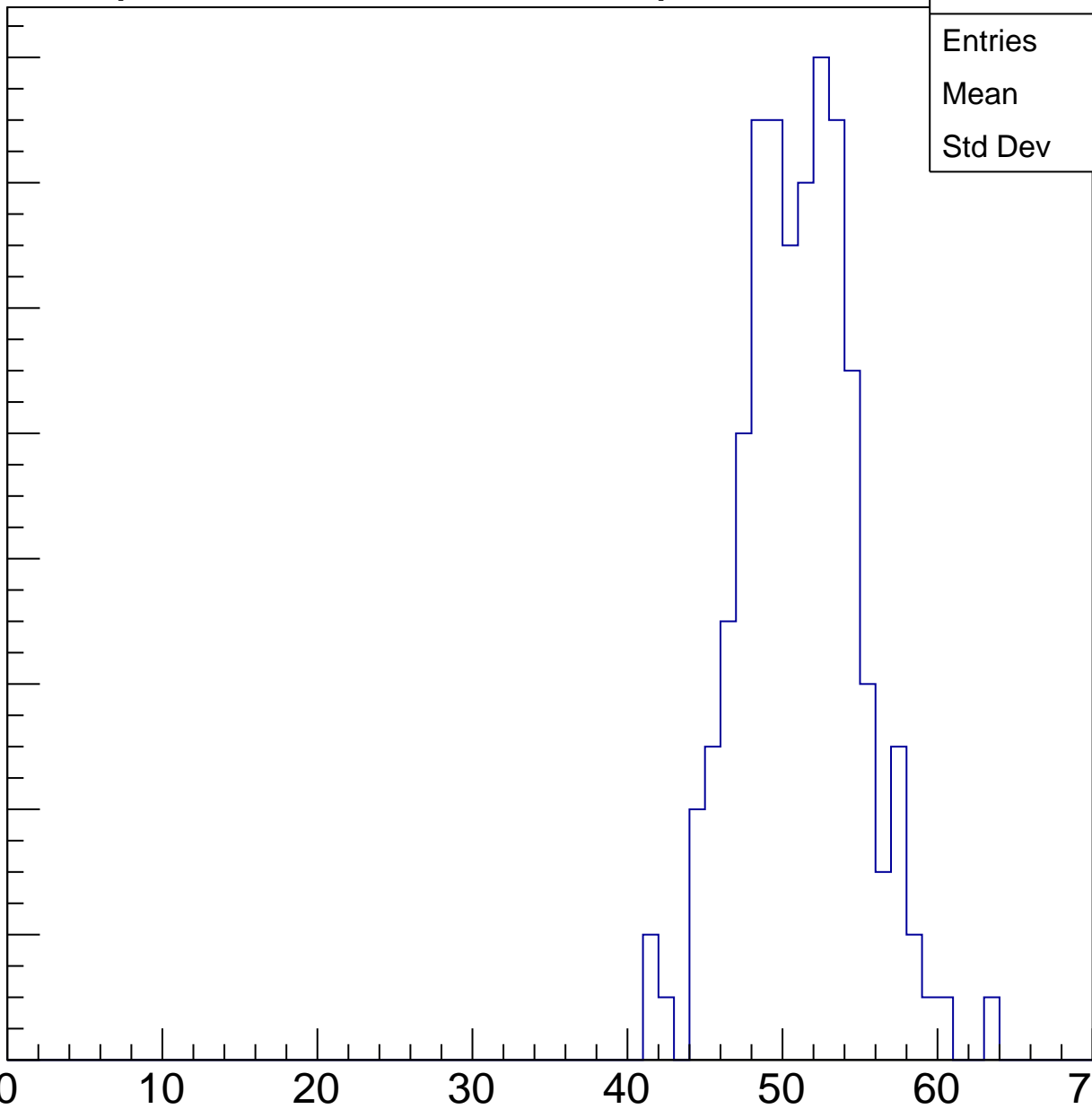
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 147 |
| Mean | 50.6 |
| Std Dev | 3.77 |

ampl



B0L101S, U22-ch111, adc4

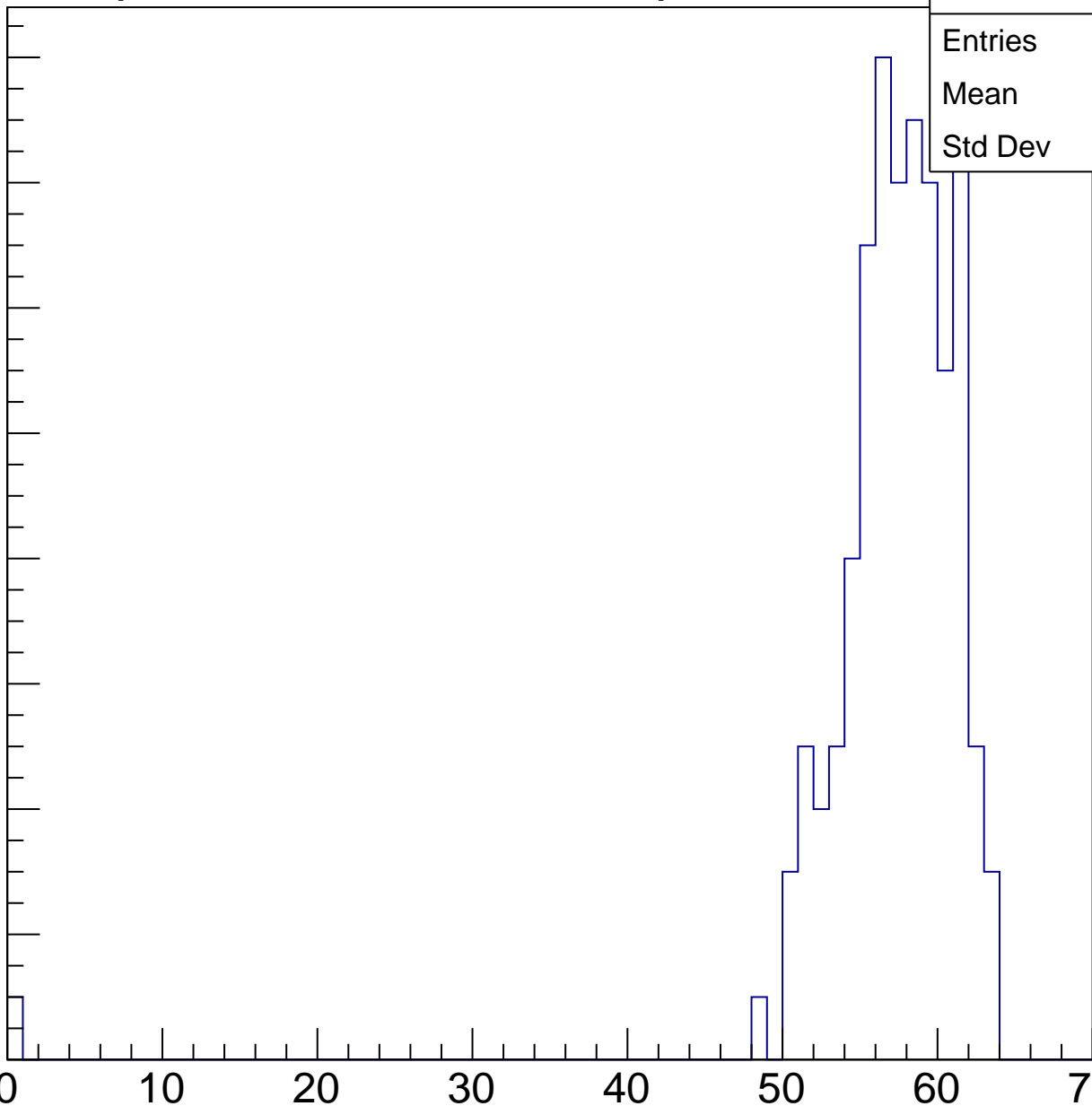
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 133 |
| Mean | 56.67 |
| Std Dev | 5.885 |

ampl



B0L101S, U22-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

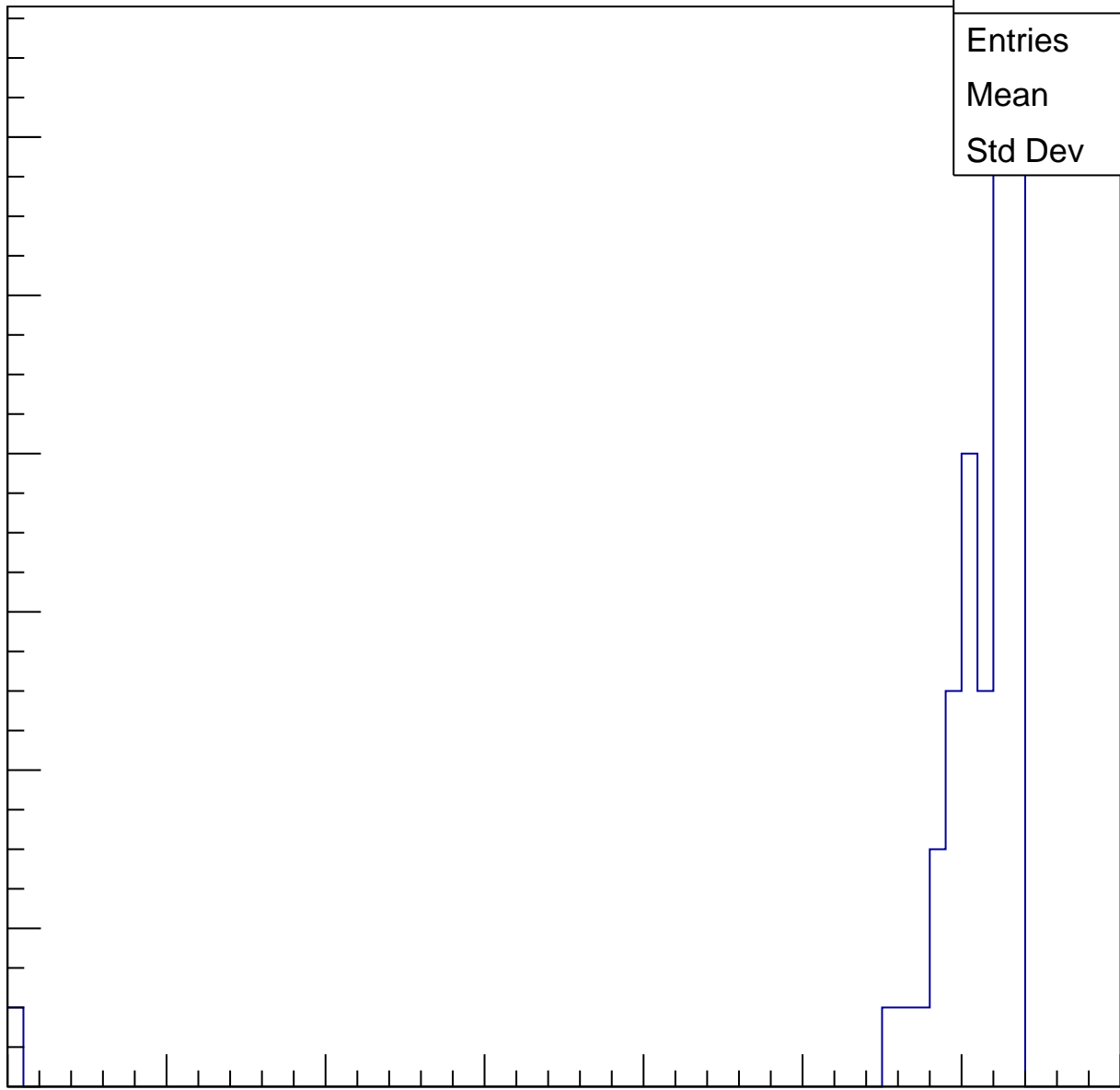
2

0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.75 |
| Std Dev | 8.675 |

ampl

0 10 20 30 40 50 60 70



B0L101S, U22-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch112, adc0

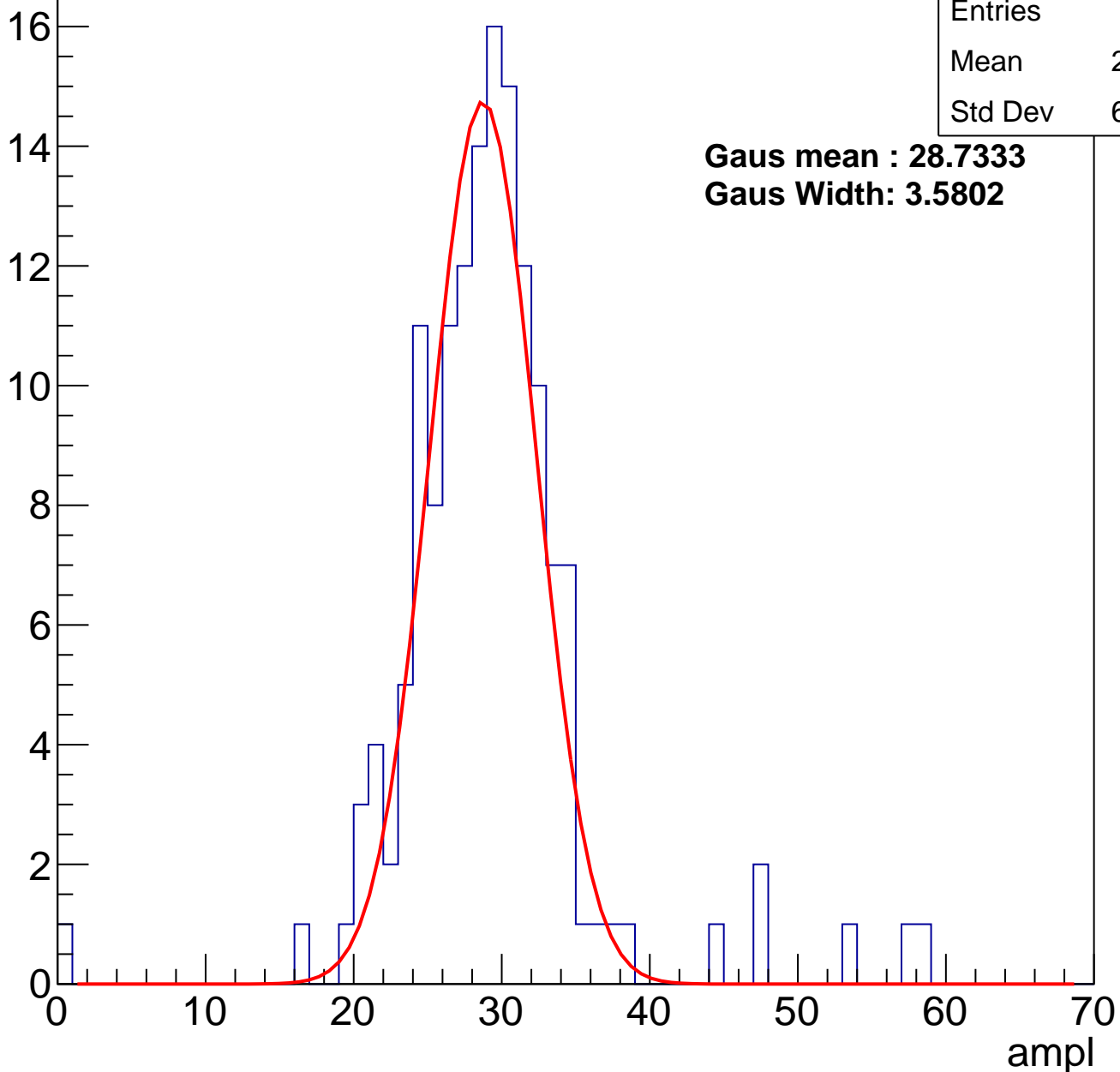
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 28.87 |
| Std Dev | 6.425 |

Gaus mean : 28.7333

Gaus Width: 3.5802

Entry



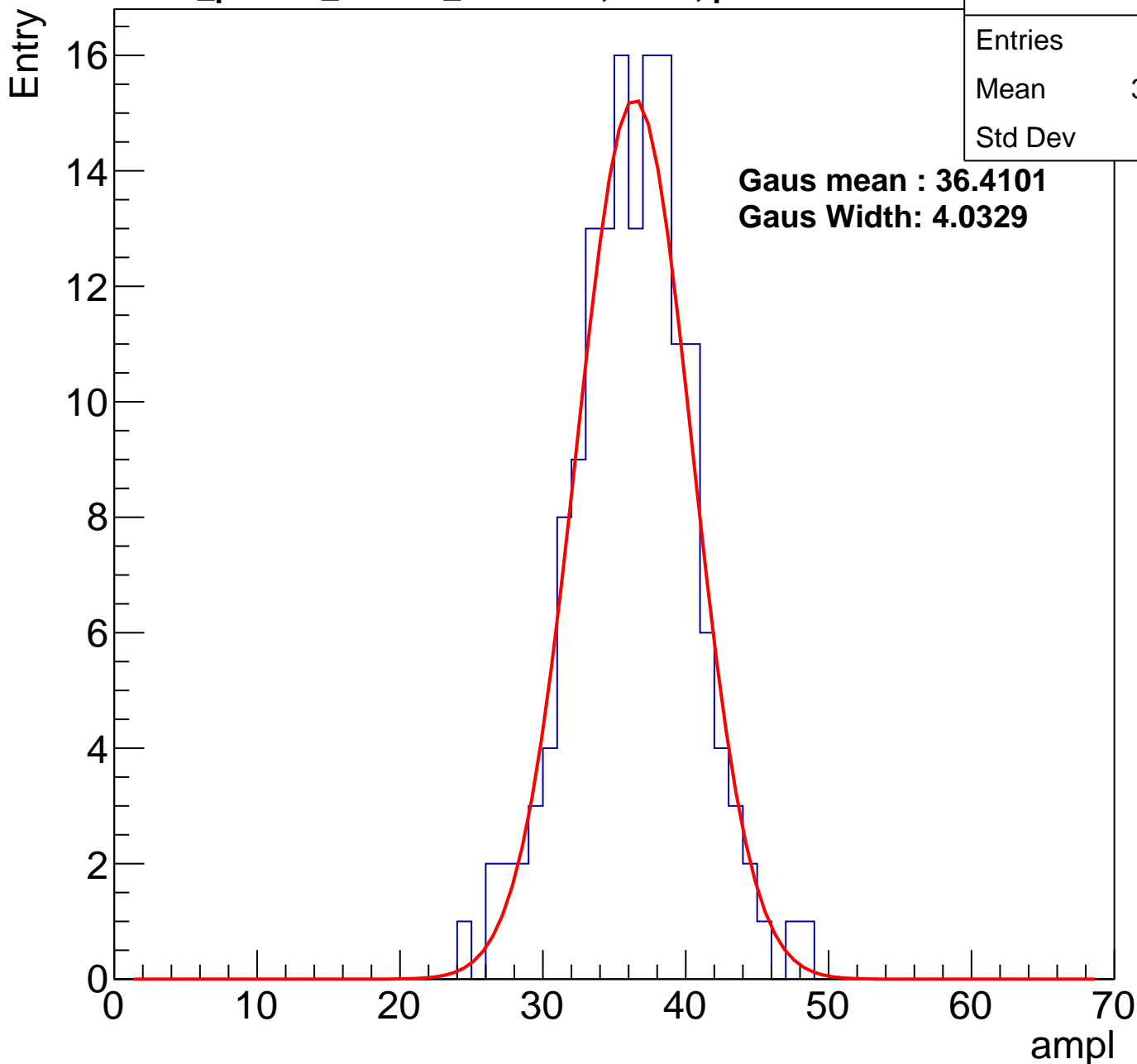
B0L101S, U22-ch112, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 35.87 |
| Std Dev | 4.15 |

Gaus mean : 36.4101

Gaus Width: 4.0329



B0L101S, U22-ch112, adc2

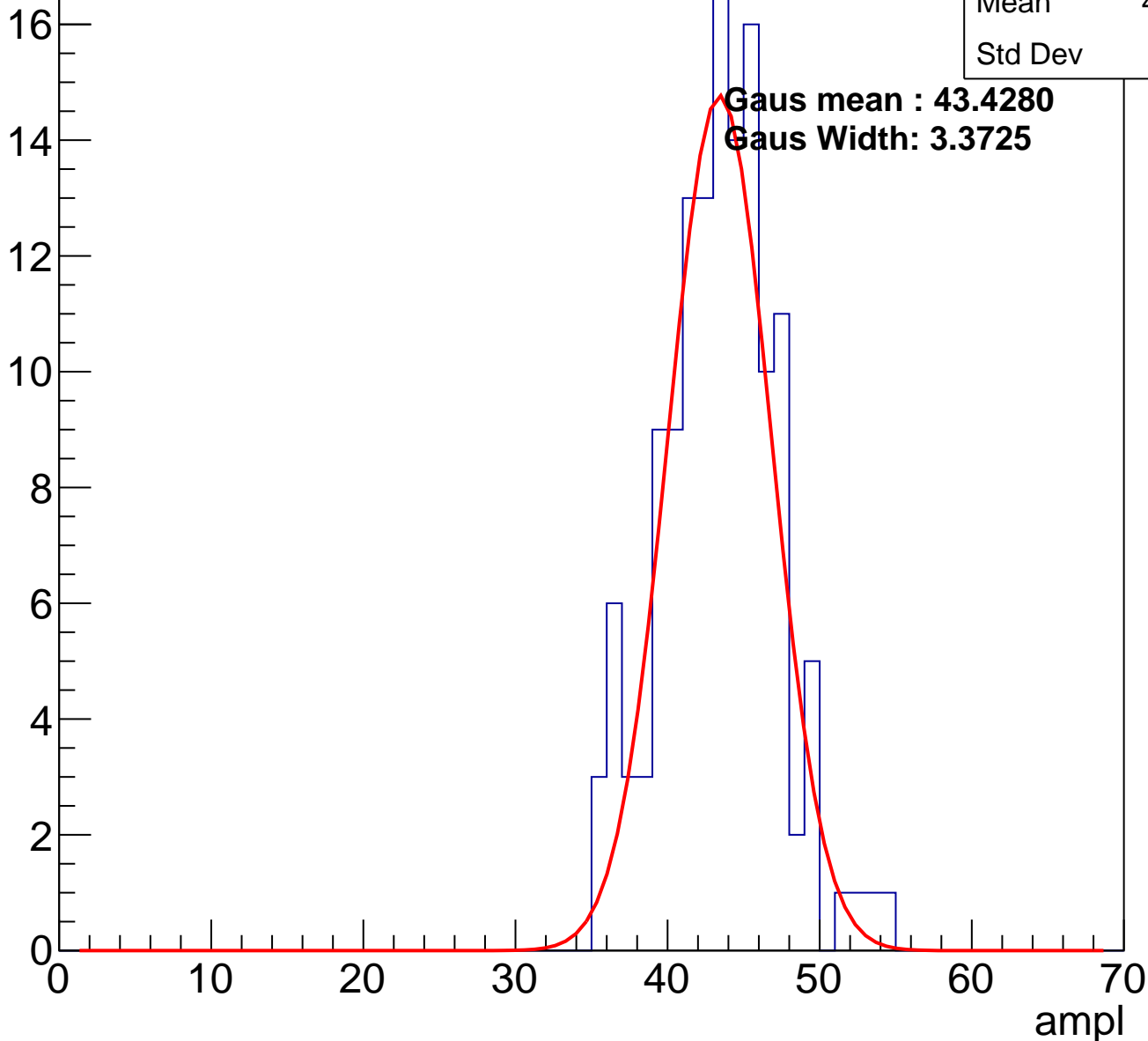
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 138 |
| Mean | 42.98 |
| Std Dev | 3.71 |

Gaus mean : 43.4280

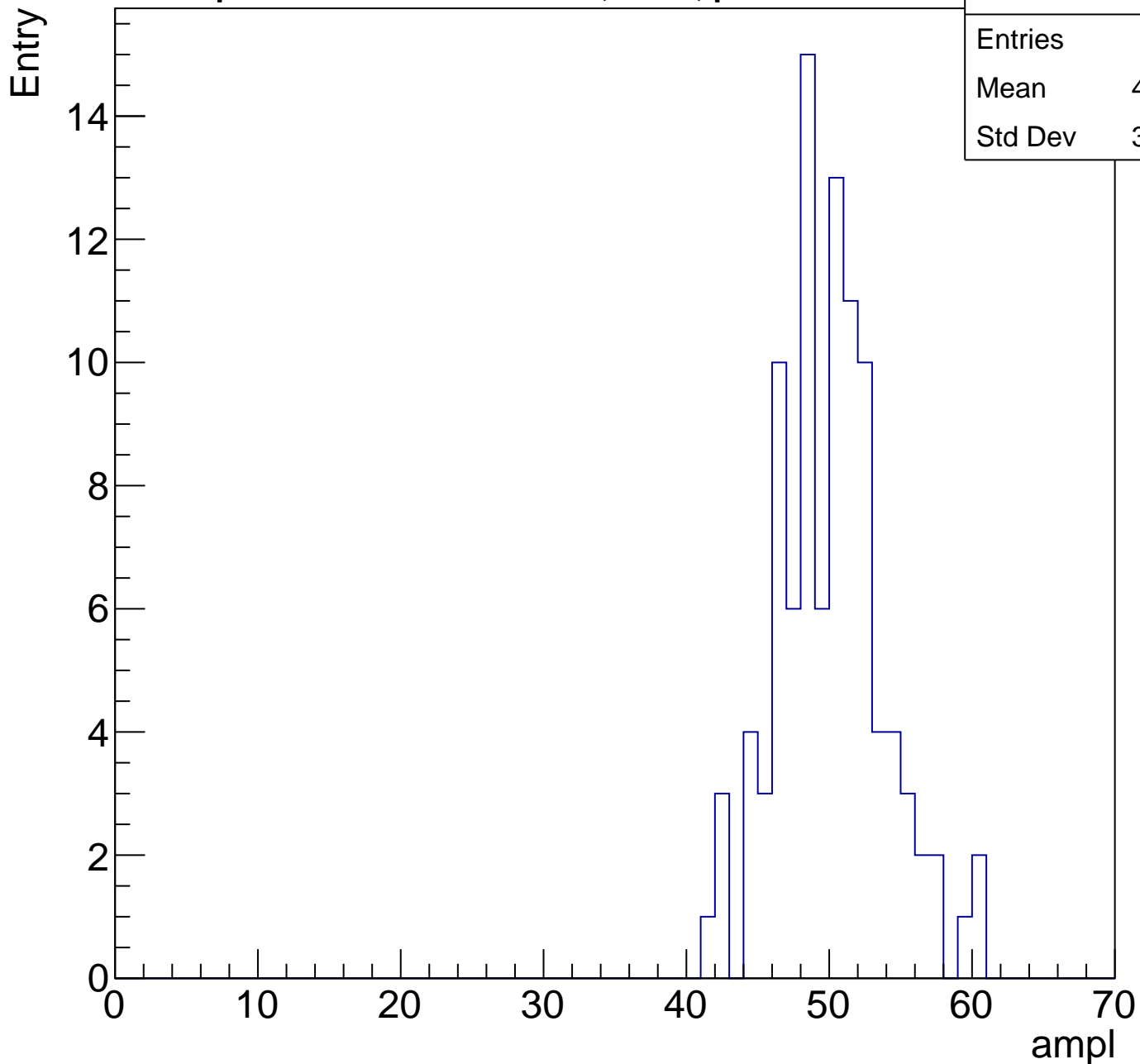
Gaus Width: 3.3725



B0L101S, U22-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

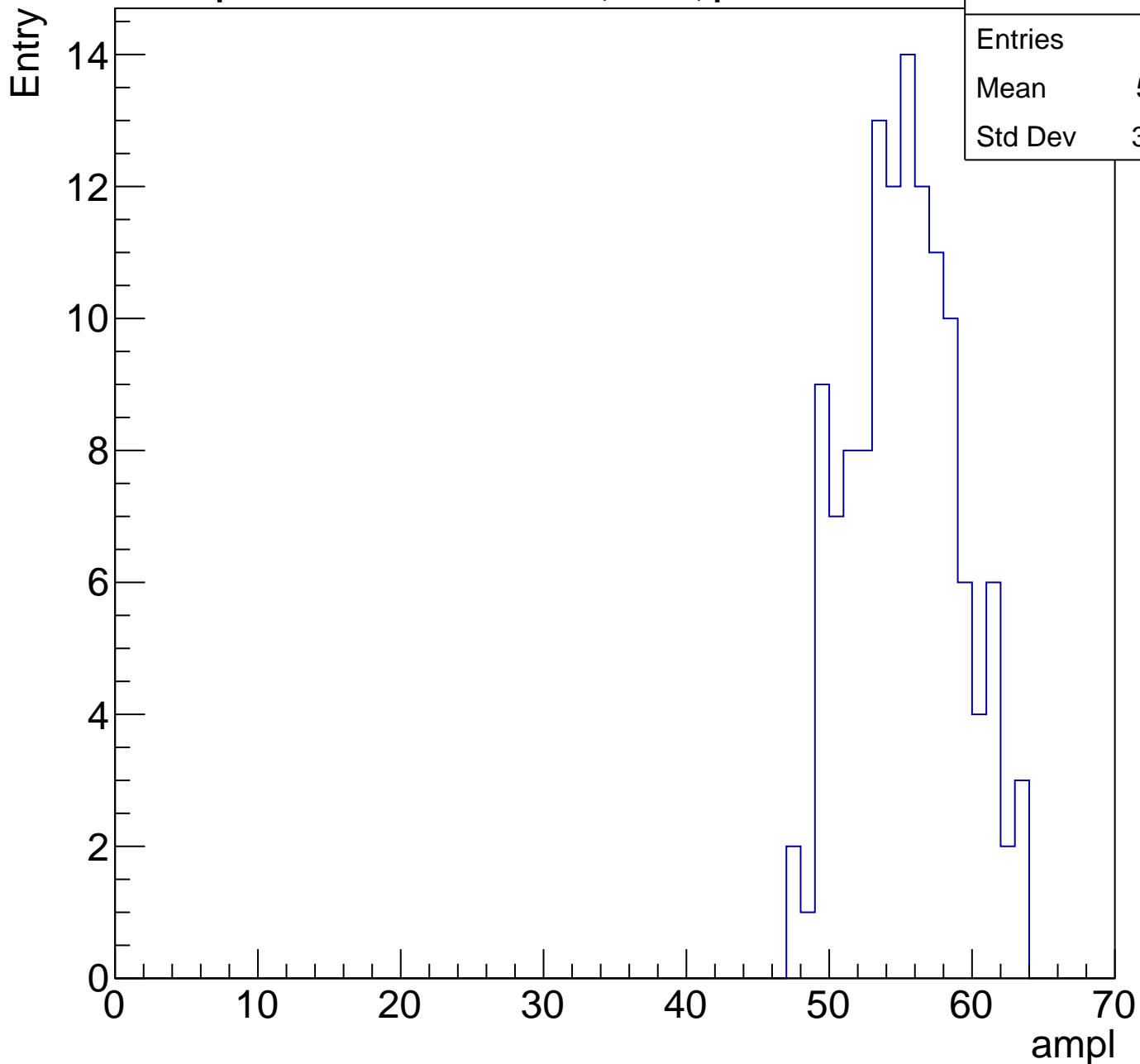
| | |
|---------|-------|
| Entries | 100 |
| Mean | 49.63 |
| Std Dev | 3.789 |



B0L101S, U22-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 128 |
| Mean | 54.81 |
| Std Dev | 3.733 |



B0L101S, U22-ch112, adc5

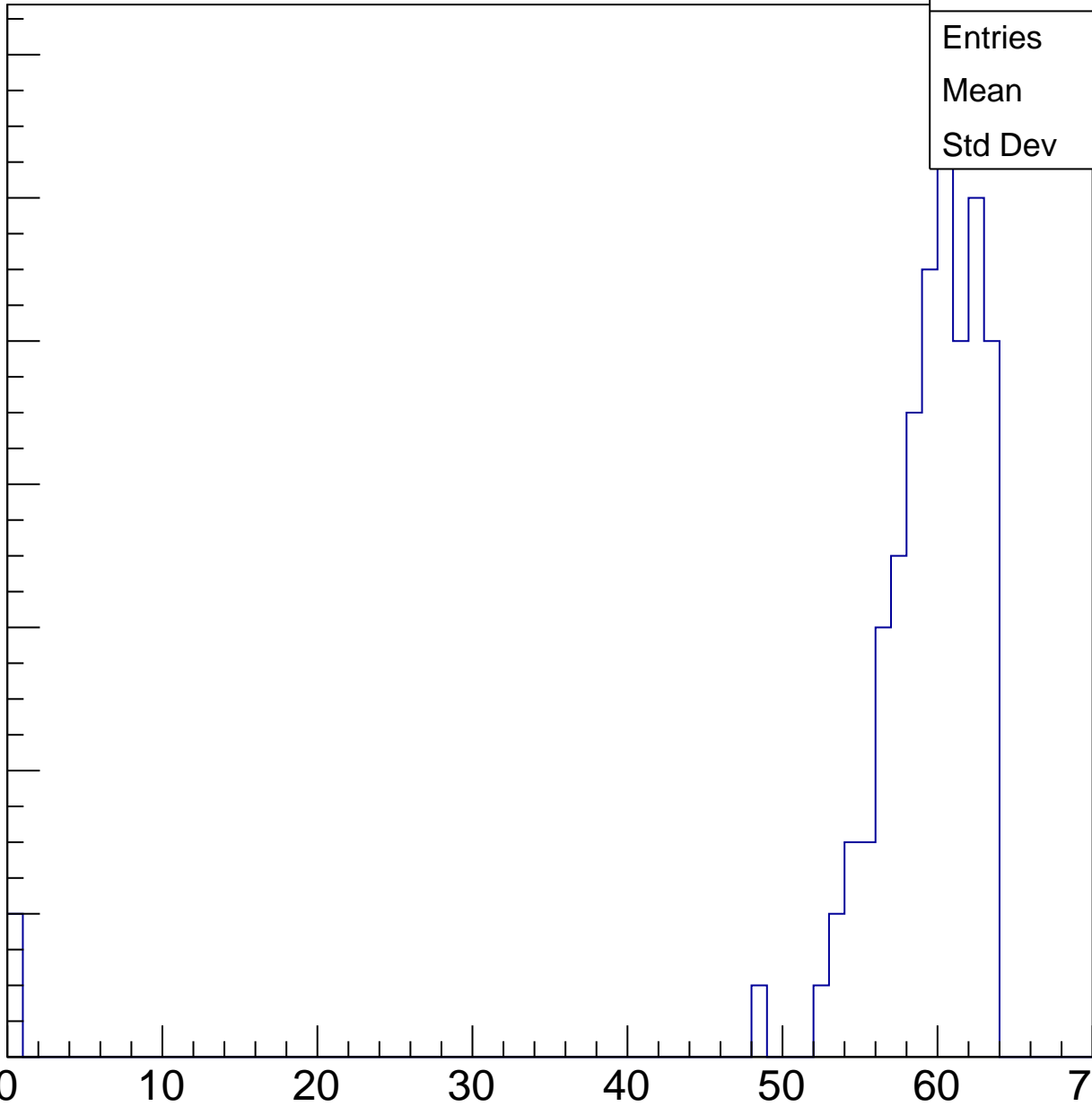
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 57.84 |
| Std Dev | 9.149 |

ampl

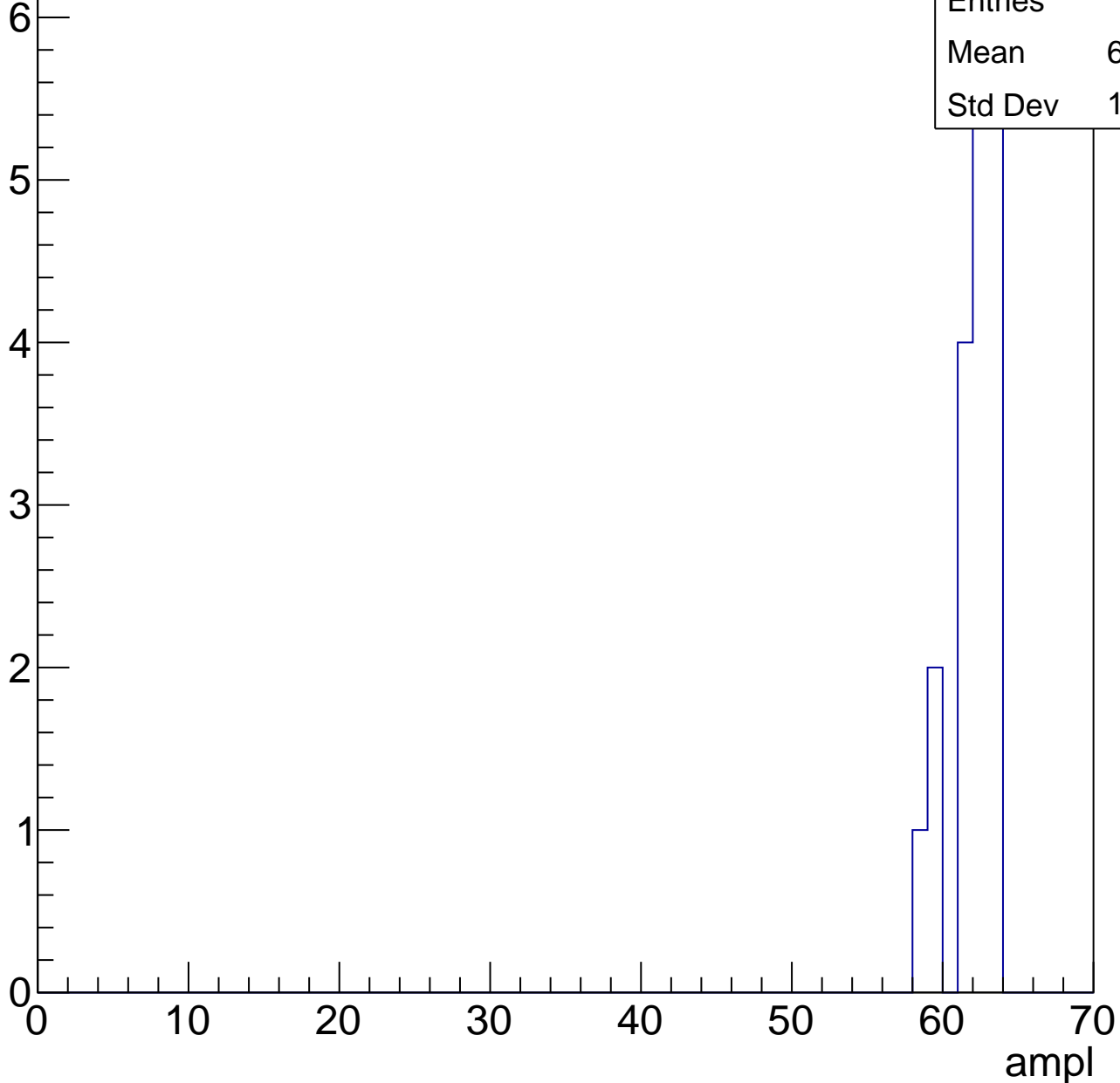


B0L101S, U22-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 19 |
| Mean | 61.58 |
| Std Dev | 1.462 |



B0L101S, U22-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B0L101S, U22-ch113, adc0

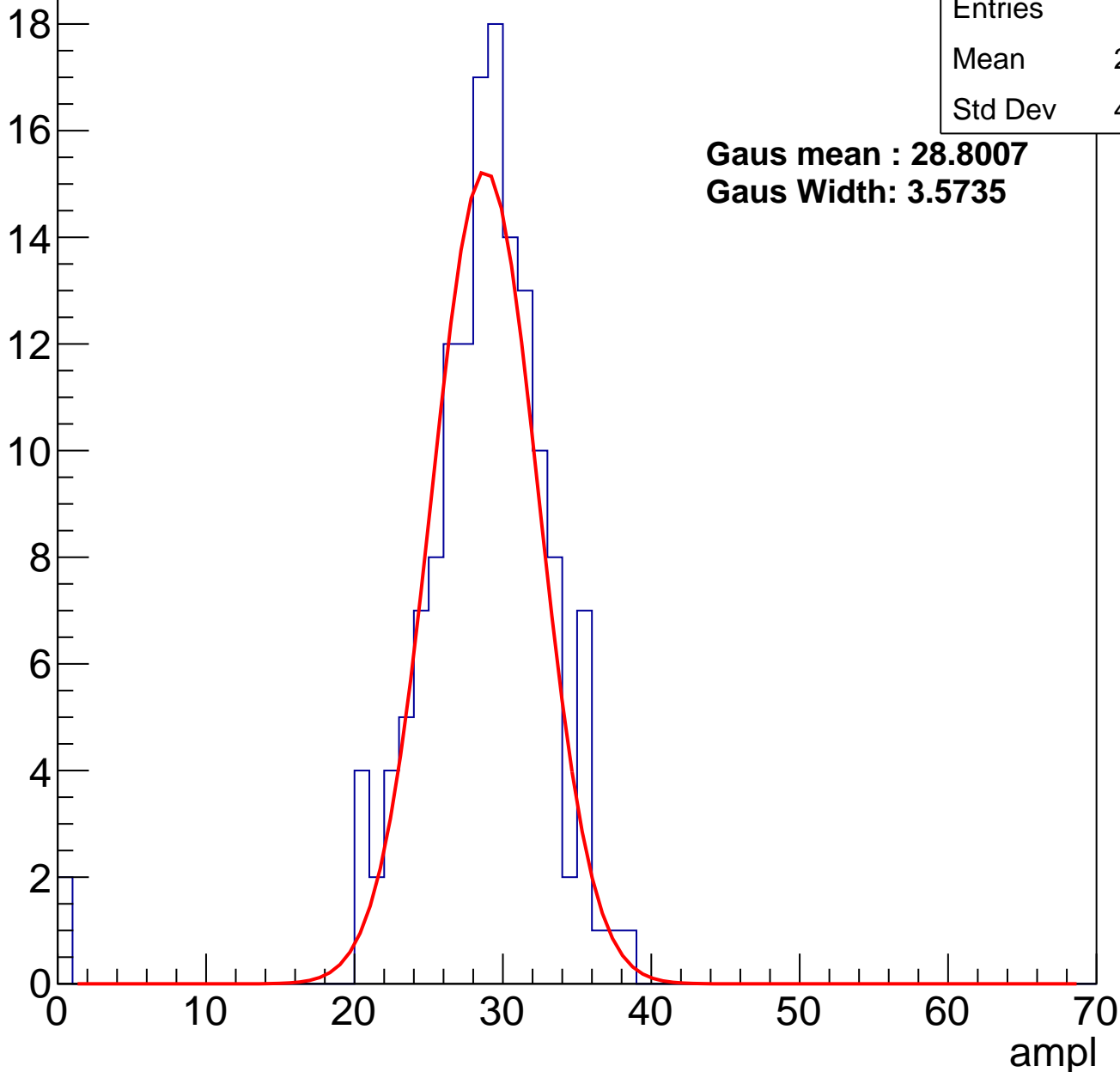
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 148 |
| Mean | 28.09 |
| Std Dev | 4.962 |

Gaus mean : 28.8007

Gaus Width: 3.5735

Entry



B0L101S, U22-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 142 |
| Mean | 35.97 |
| Std Dev | 3.911 |

Gaus mean : 36.4579

Gaus Width: 4.0412

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

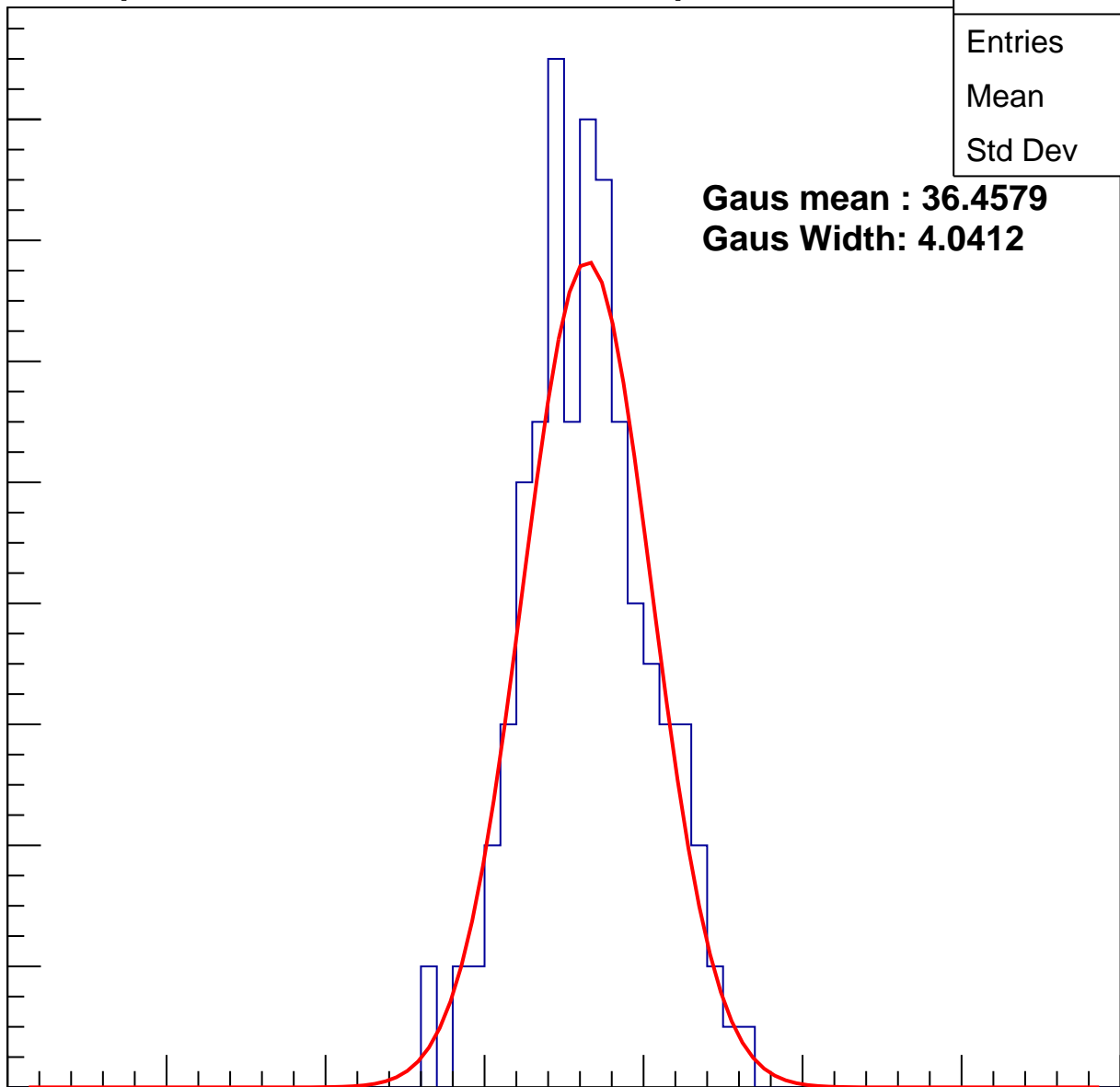
40

50

60

70

ampl



B0L101S, U22-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 135 |
| Mean | 42.36 |
| Std Dev | 3.497 |

Gaus mean : 42.8766

Gaus Width: 3.5746

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U22-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

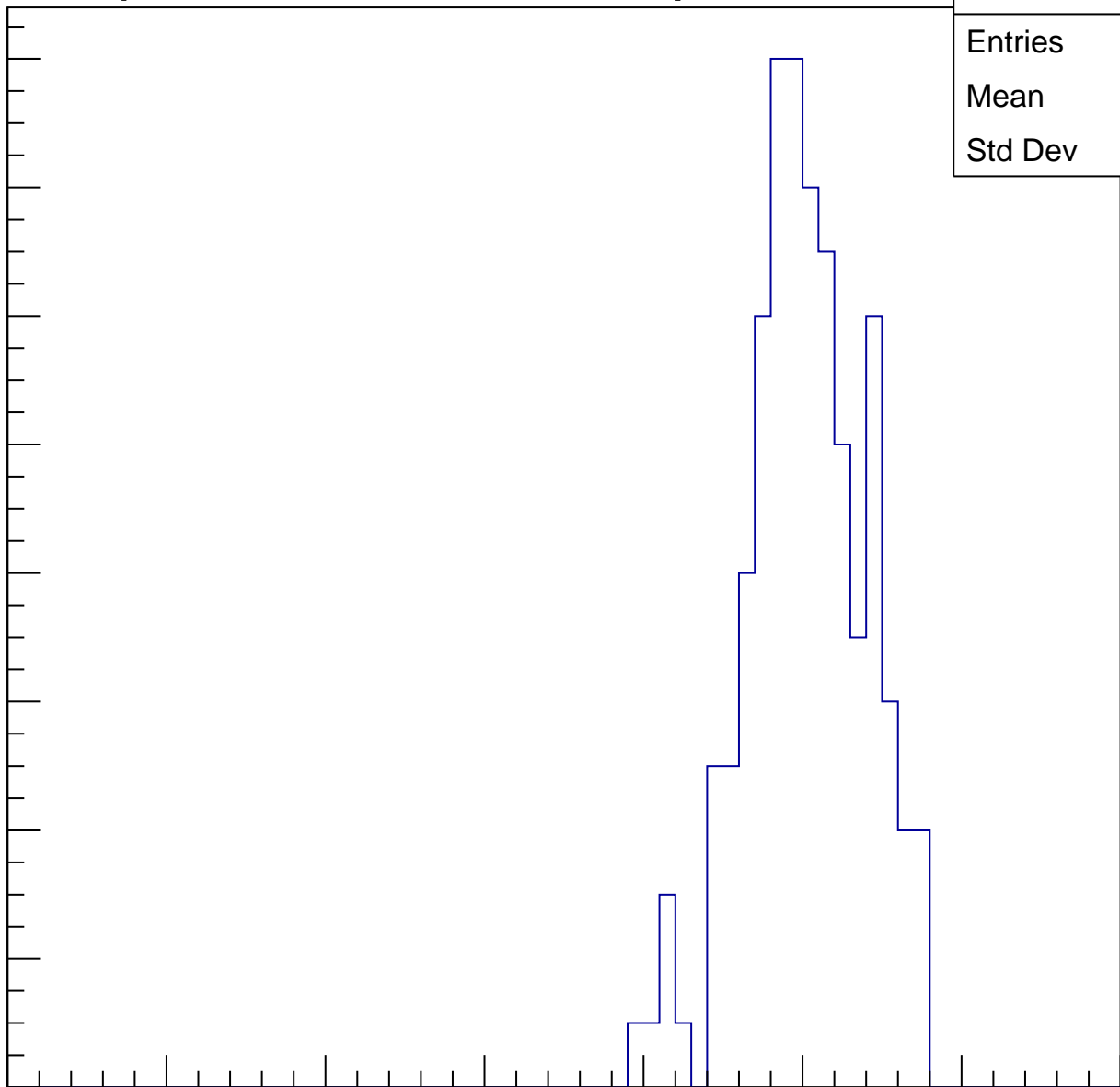
| | |
|---------|-------|
| Entries | 138 |
| Mean | 49.69 |
| Std Dev | 3.743 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 114 |
| Mean | 55.18 |
| Std Dev | 6.223 |

Entry

12

10

8

6

4

2

0

0

10

20

30

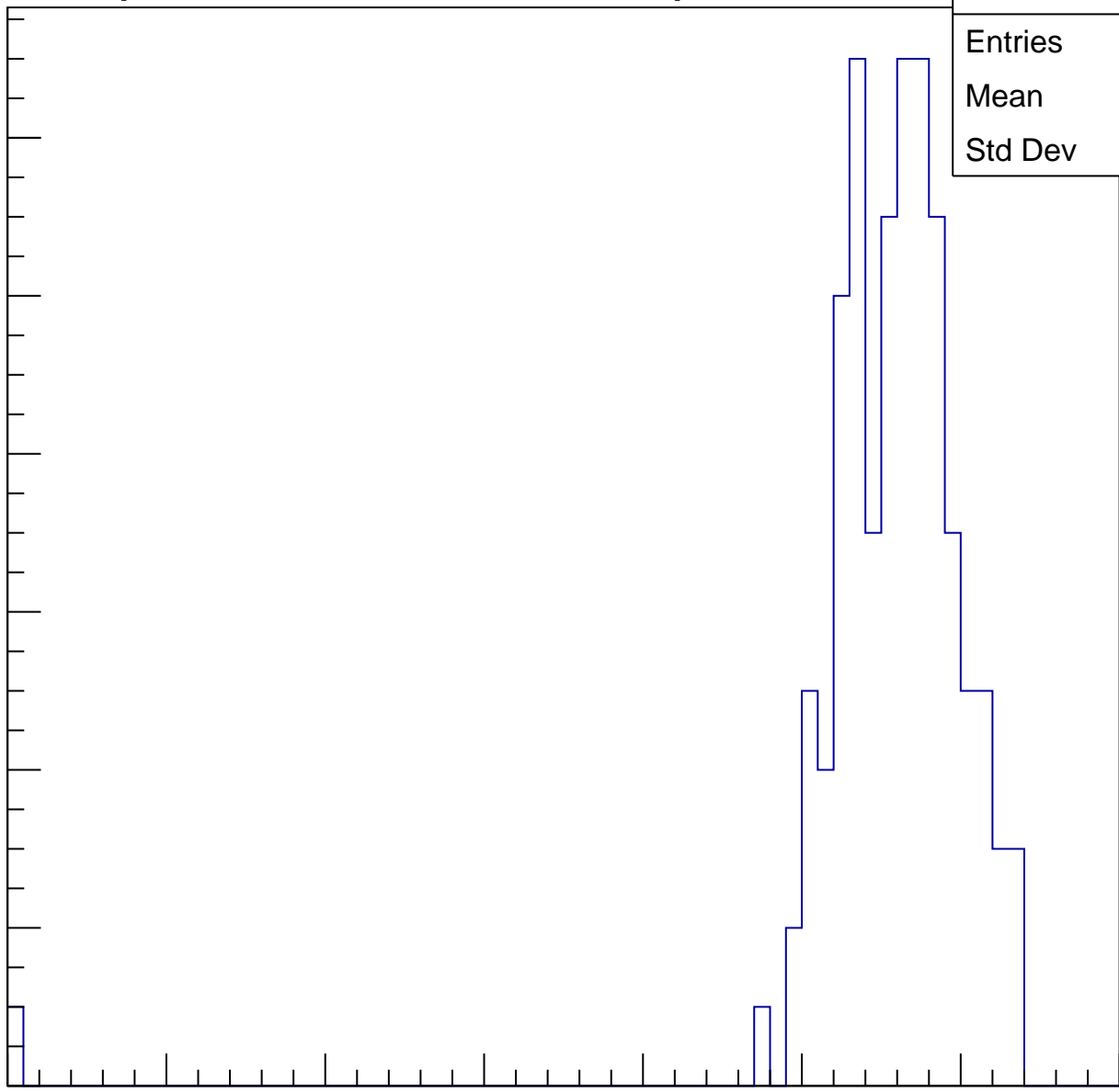
40

50

60

70

ampl



B0L101S, U22-ch113, adc5

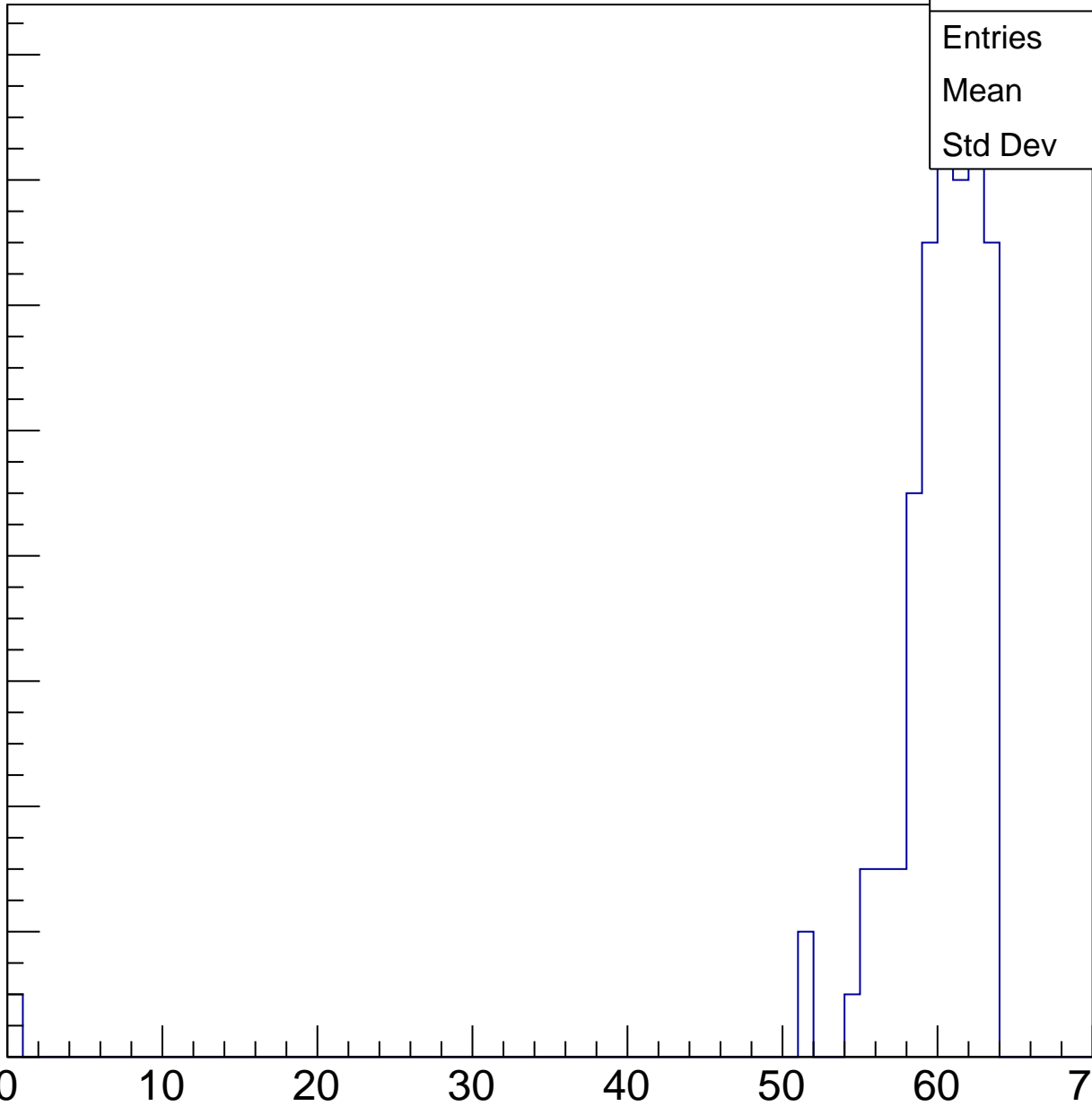
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 93 |
| Mean | 59.29 |
| Std Dev | 6.676 |

ampl



B0L101S, U22-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6 |
| Std Dev | 8.485 |

B0L101S, U22-ch114, adc0

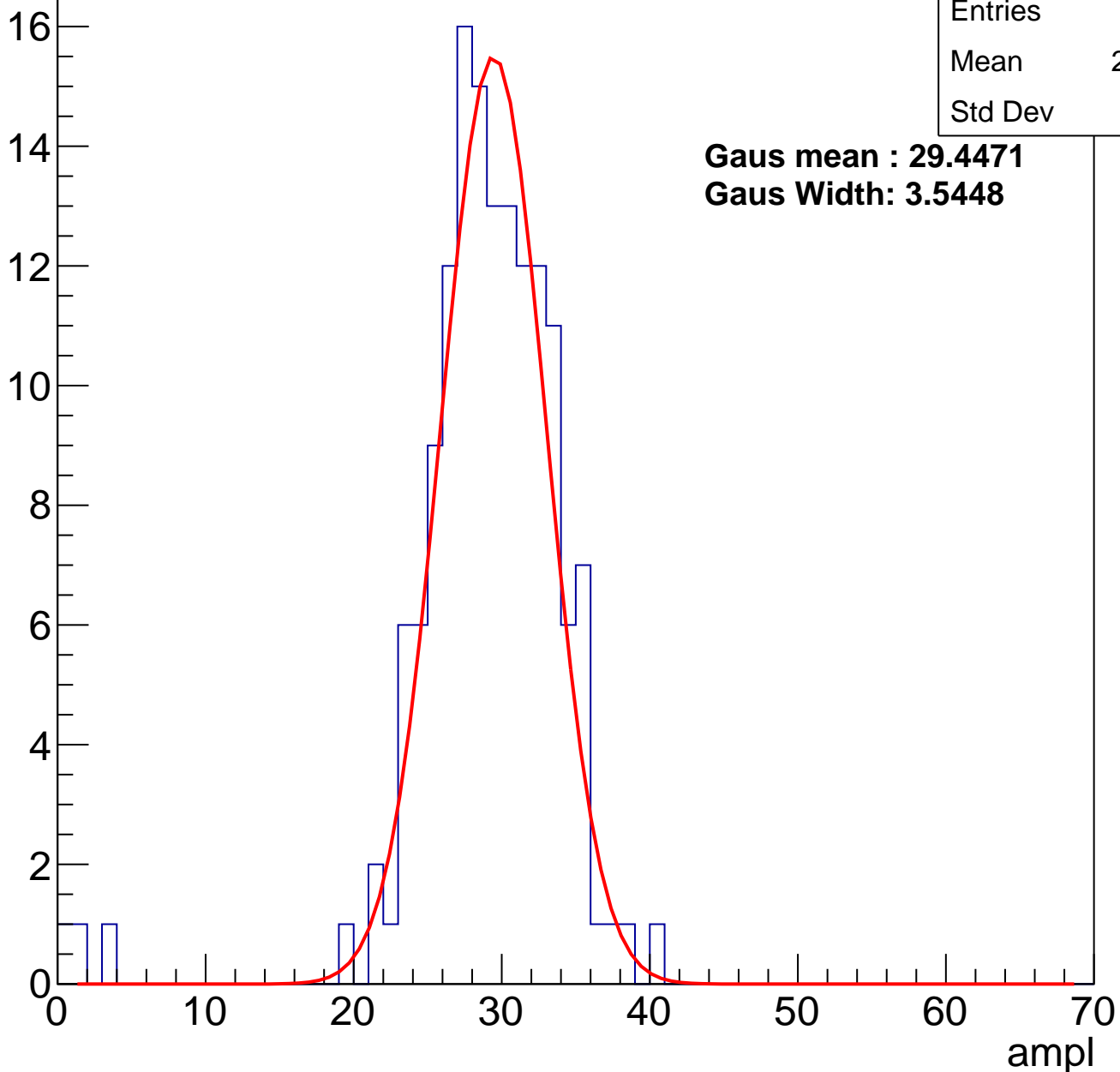
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 149 |
| Mean | 28.48 |
| Std Dev | 5.37 |

Gaus mean : 29.4471

Gaus Width: 3.5448

Entry



B0L101S, U22-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 36.19 |
| Std Dev | 5.332 |

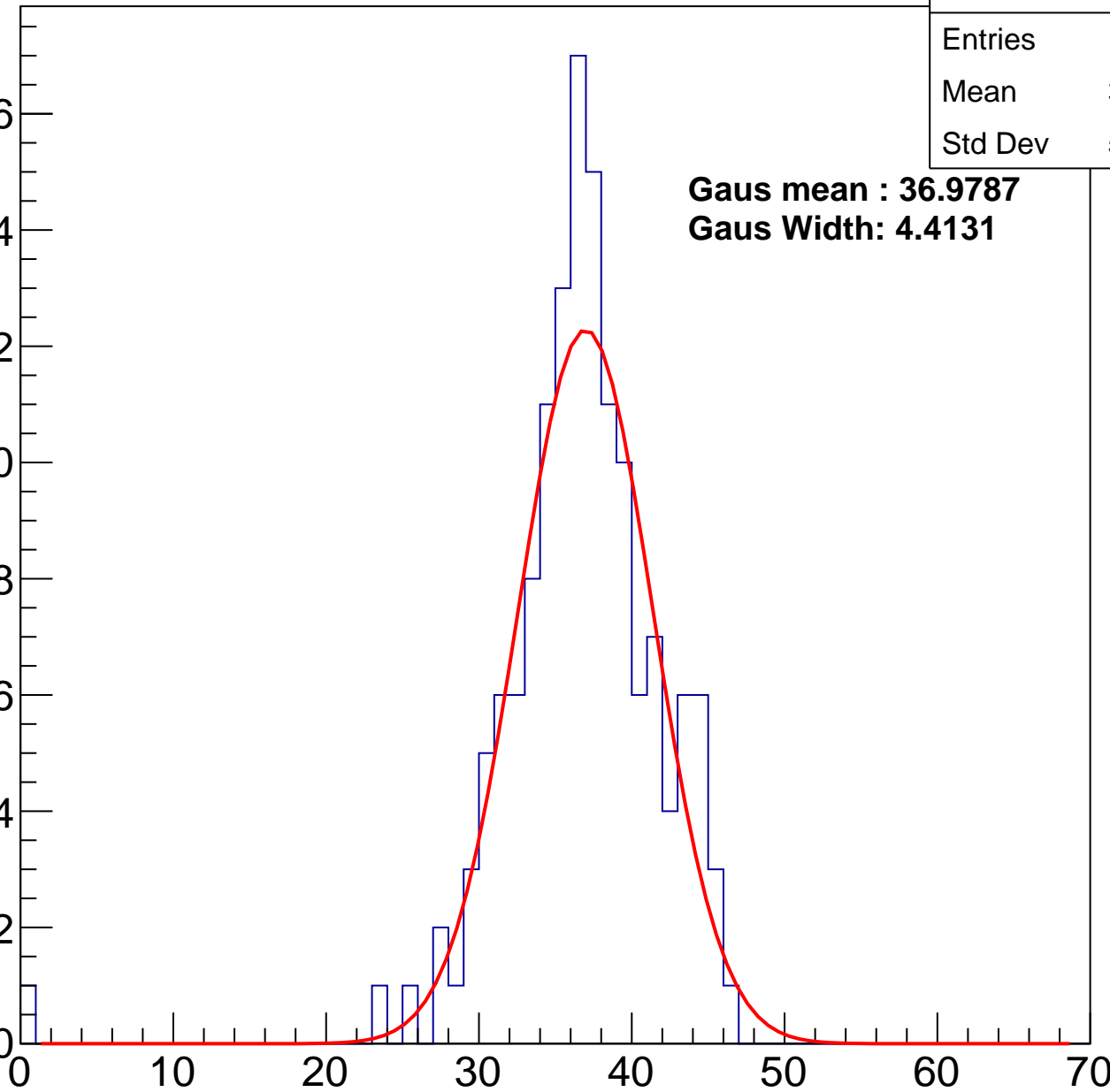
Gaus mean : 36.9787

Gaus Width: 4.4131

Entry

16
14
12
10
8
6
4
2
0

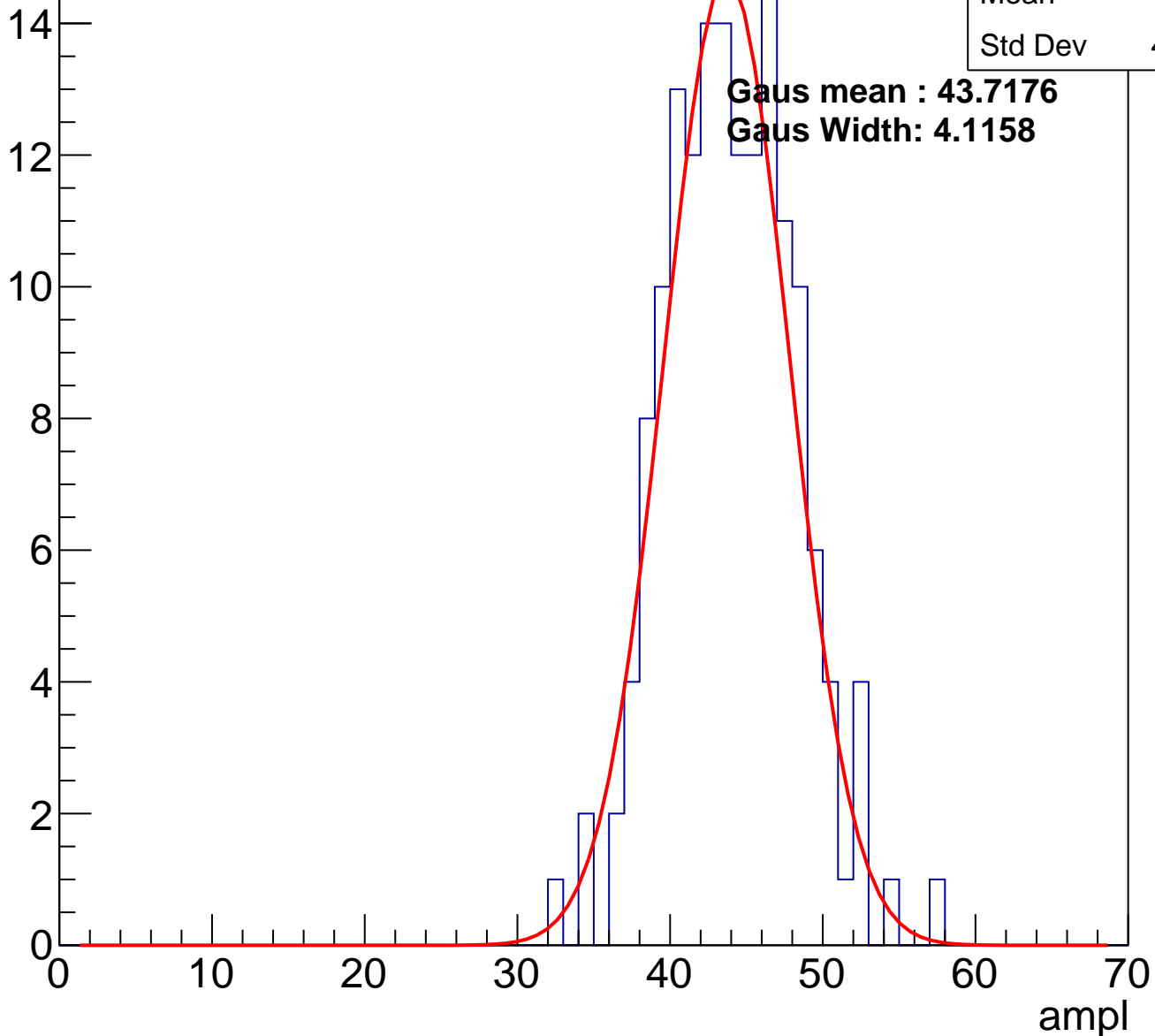
ampl



B0L101S, U22-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

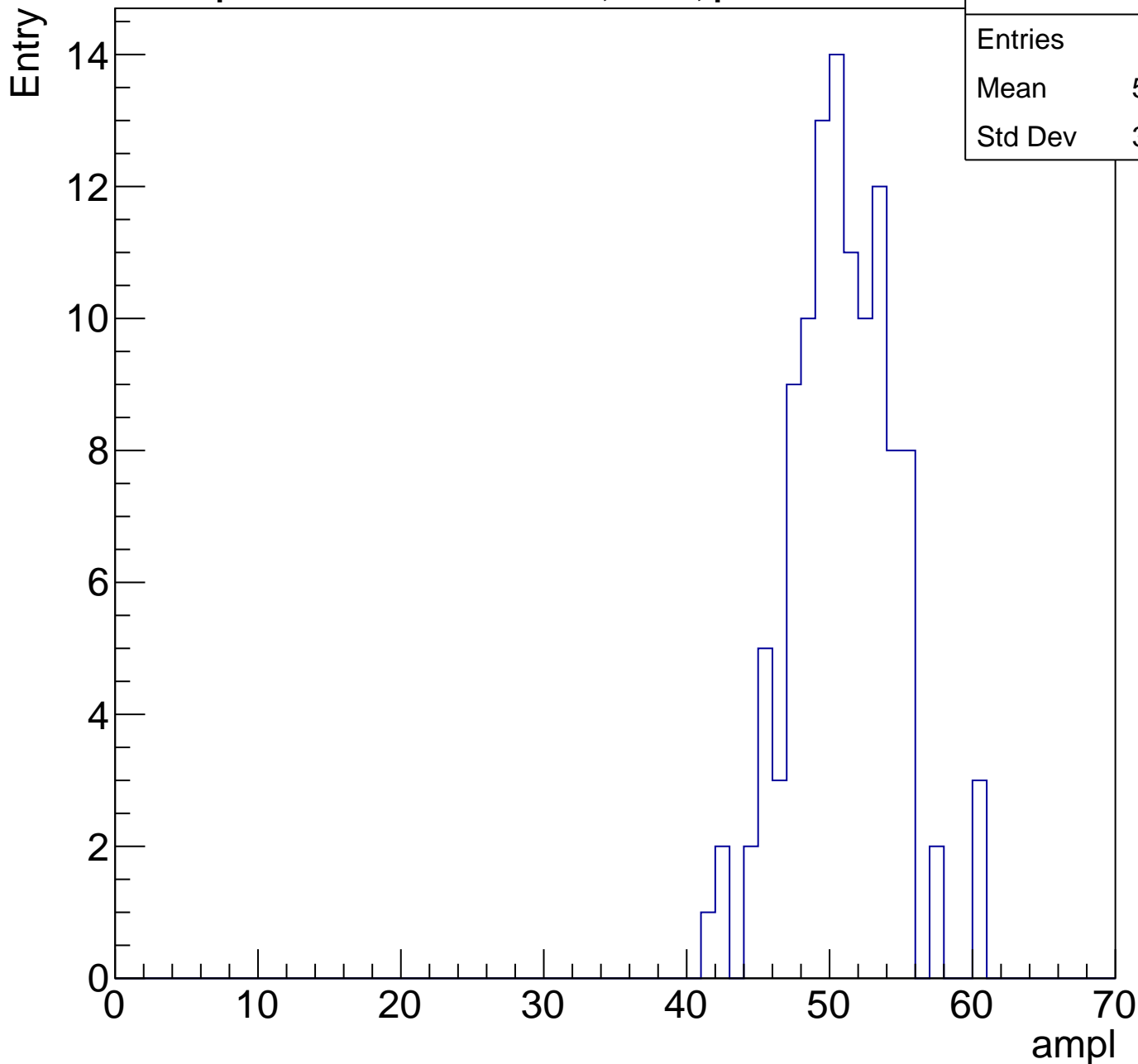
Entry



B0L101S, U22-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

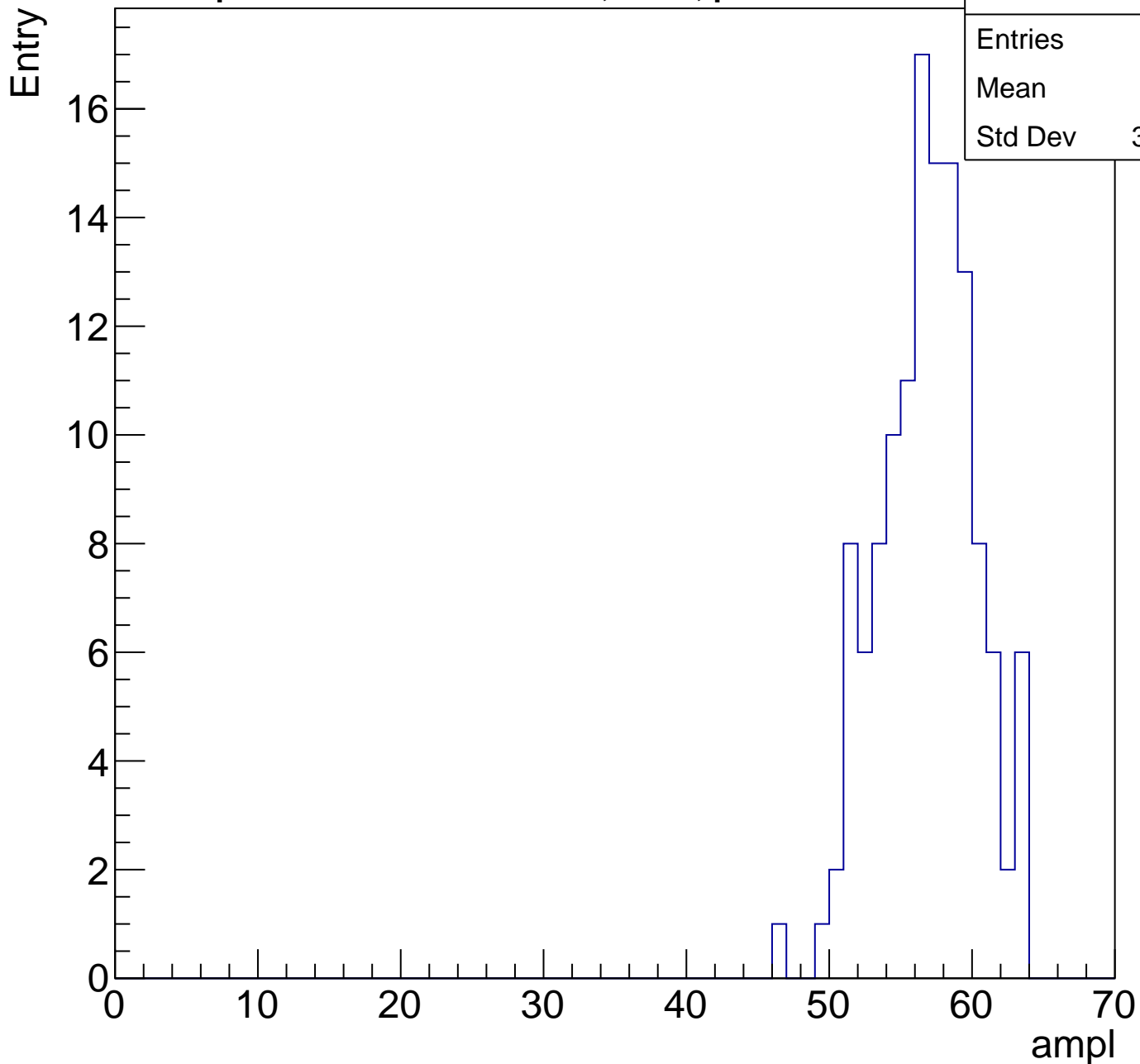
| | |
|---------|-------|
| Entries | 113 |
| Mean | 50.43 |
| Std Dev | 3.584 |



B0L101S, U22-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 129 |
| Mean | 56.4 |
| Std Dev | 3.358 |



B0L101S, U22-ch114, adc5

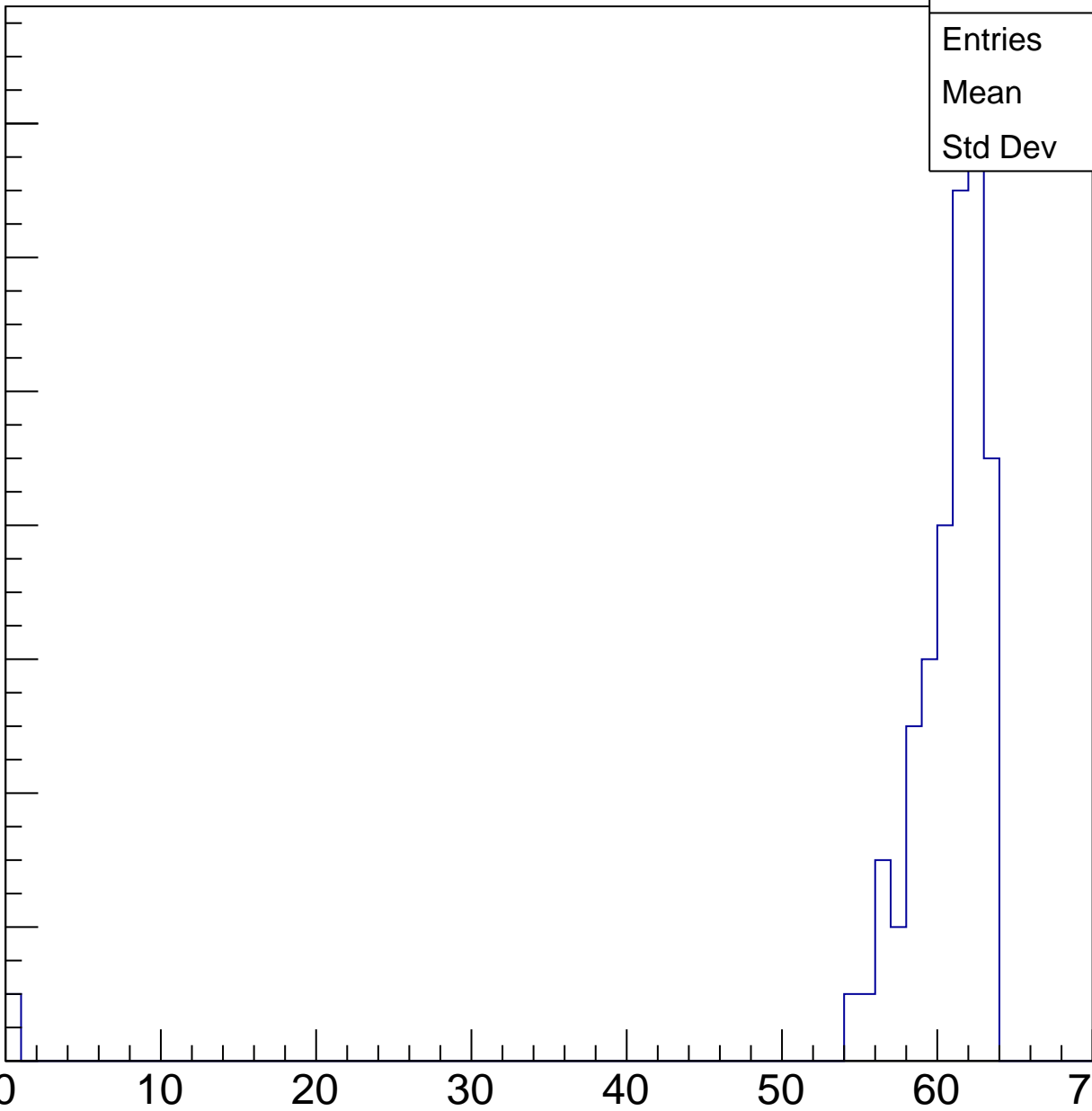
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 59.45 |
| Std Dev | 7.794 |

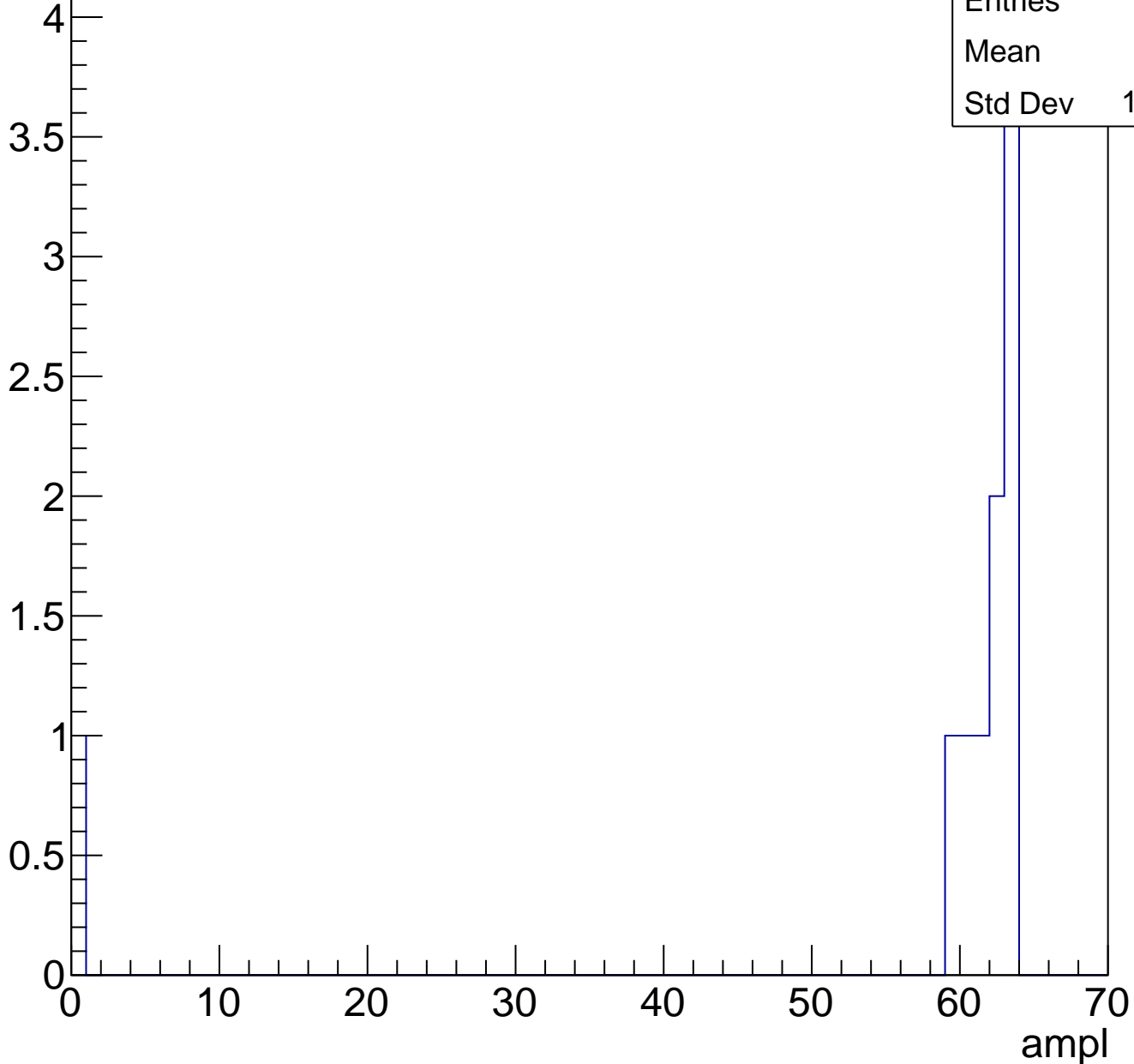
ampl



B0L101S, U22-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

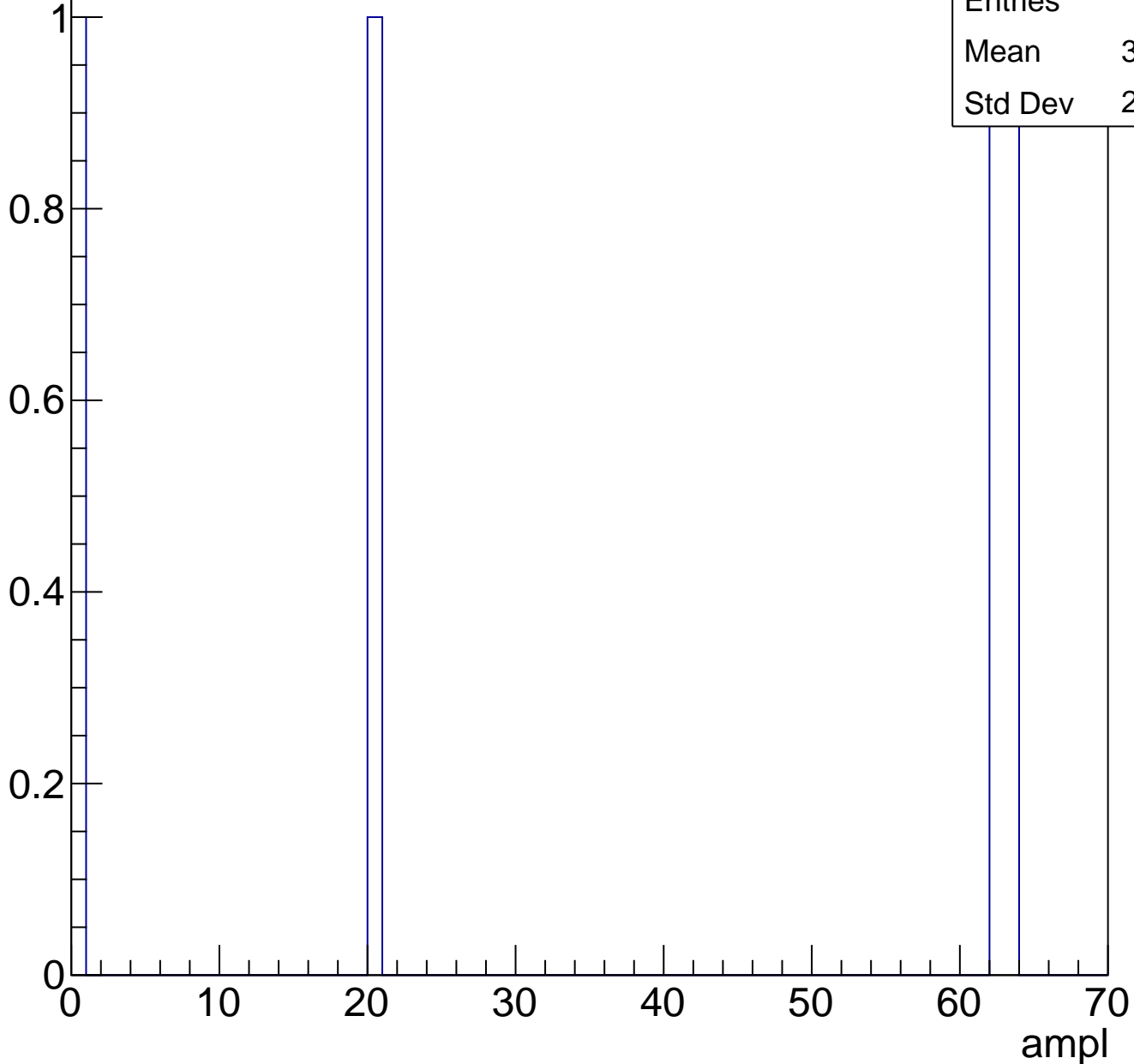
Entry



B0L101S, U22-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch115, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 151 |
| Mean | 29.72 |
| Std Dev | 5.523 |

Gaus mean : 30.7856

Gaus Width: 4.1139

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

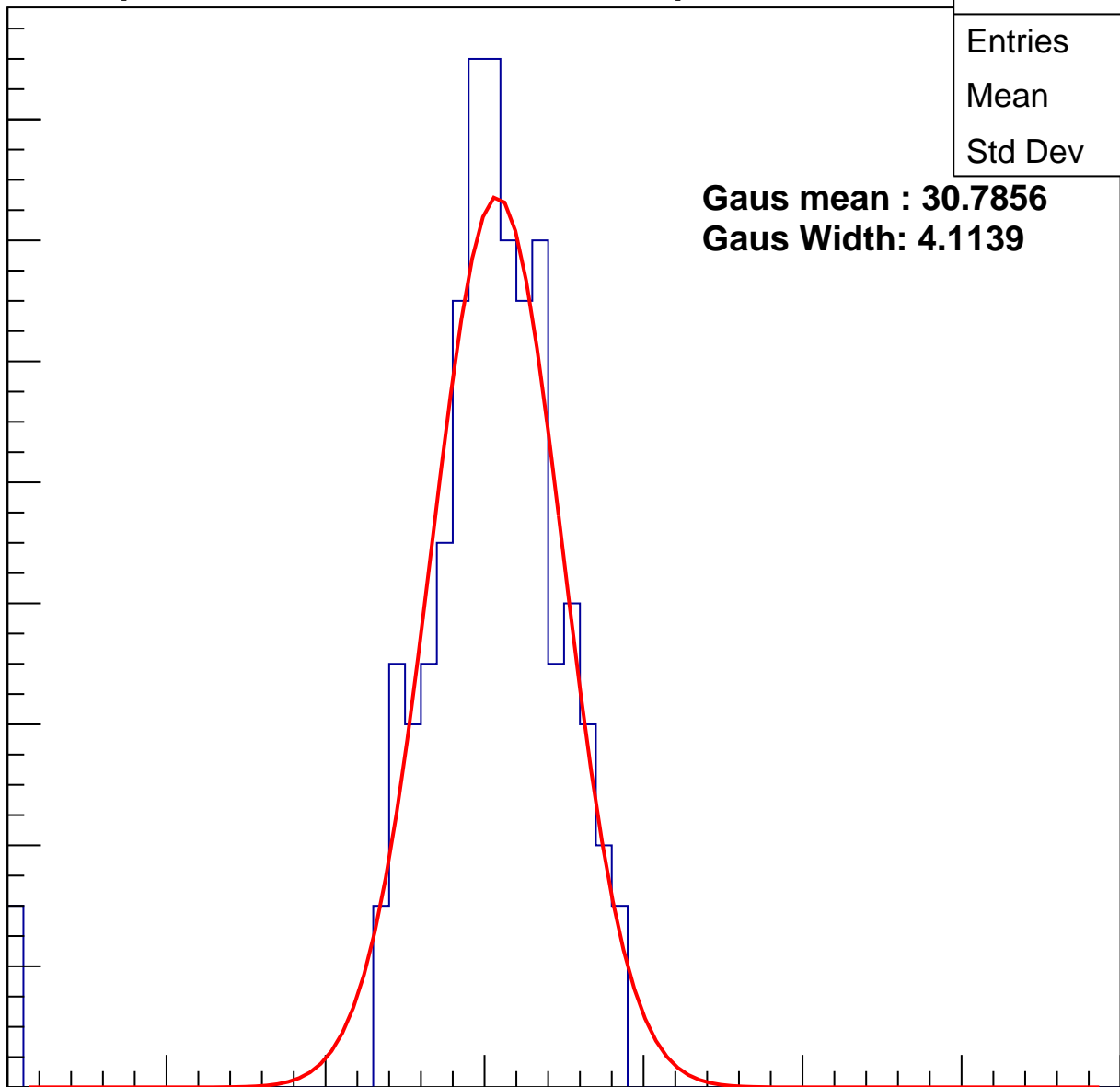
40

50

60

70

ampl



B0L101S, U22-ch115, adc1

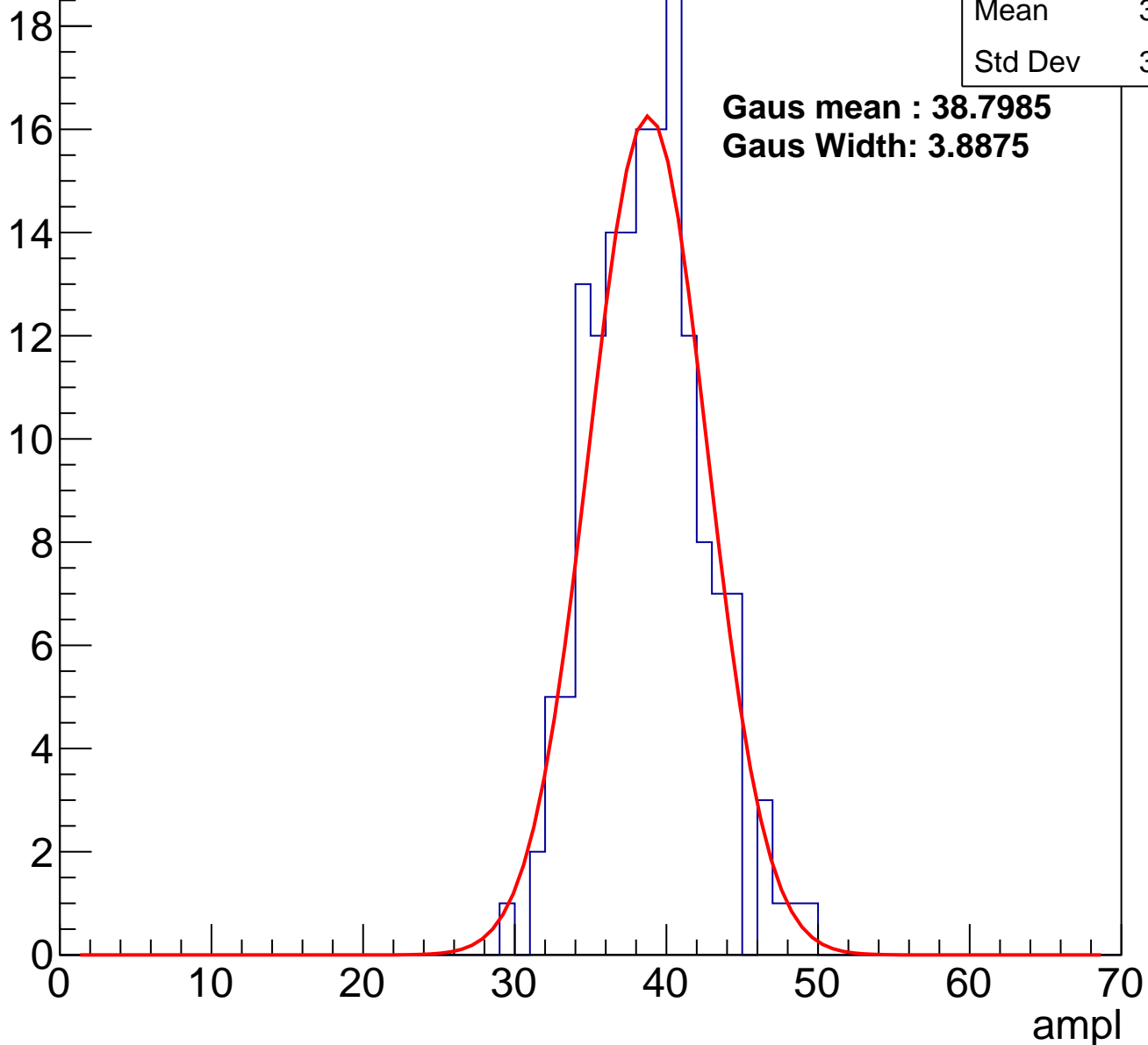
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 157 |
| Mean | 38.29 |
| Std Dev | 3.663 |

Gaus mean : 38.7985

Gaus Width: 3.8875

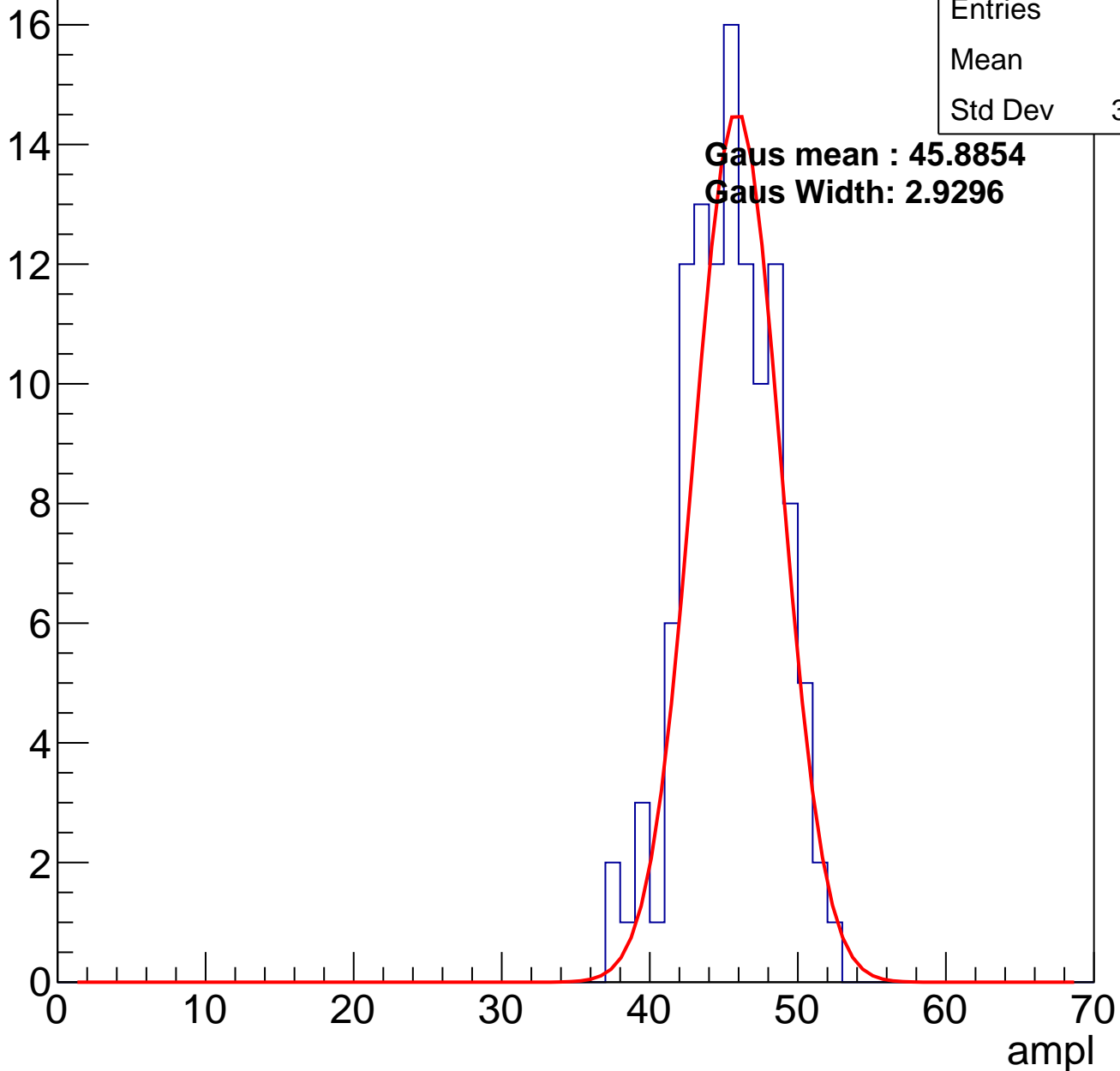
Entry



B0L101S, U22-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch115, adc3

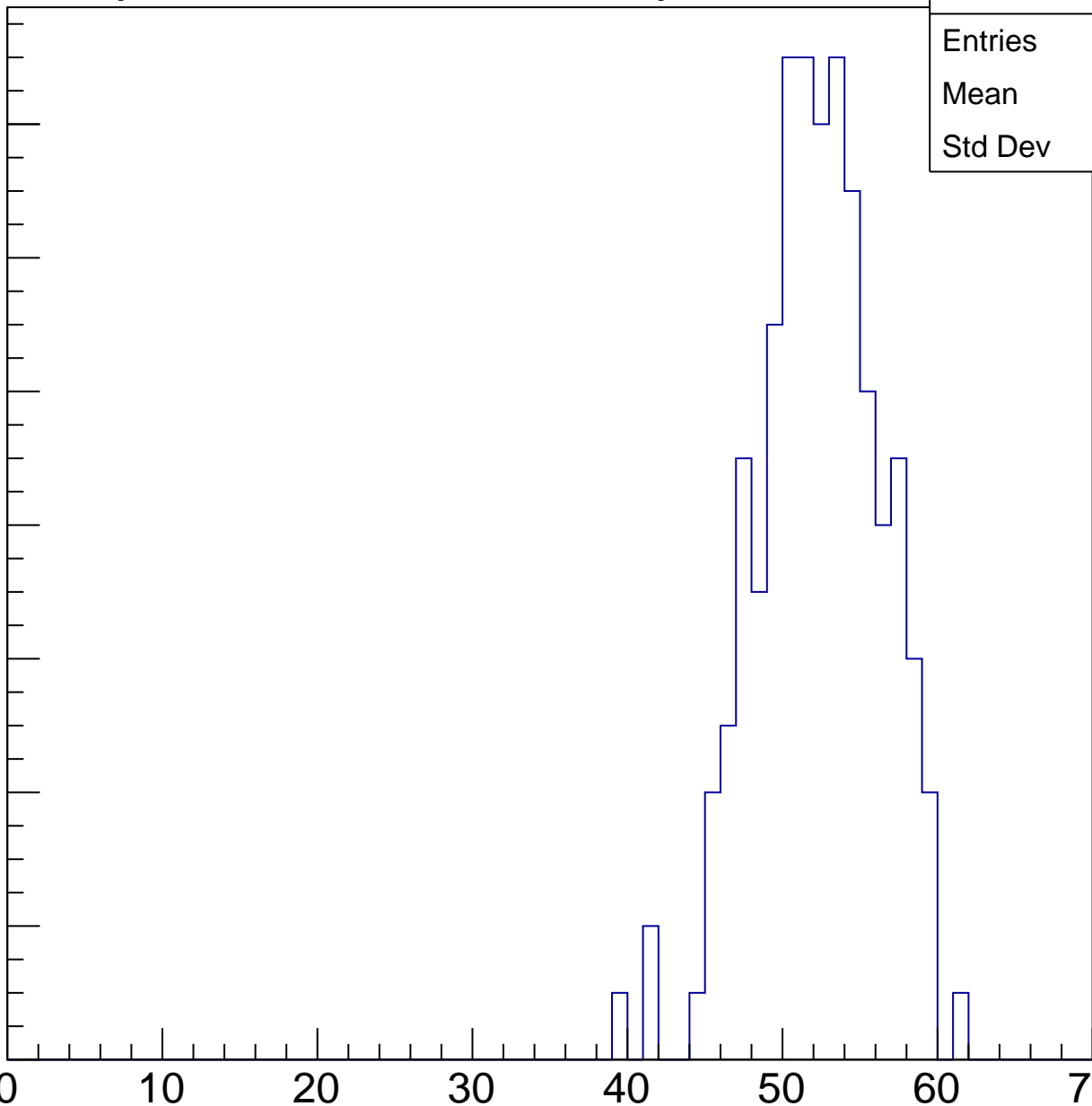
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 150 |
| Mean | 51.79 |
| Std Dev | 3.965 |

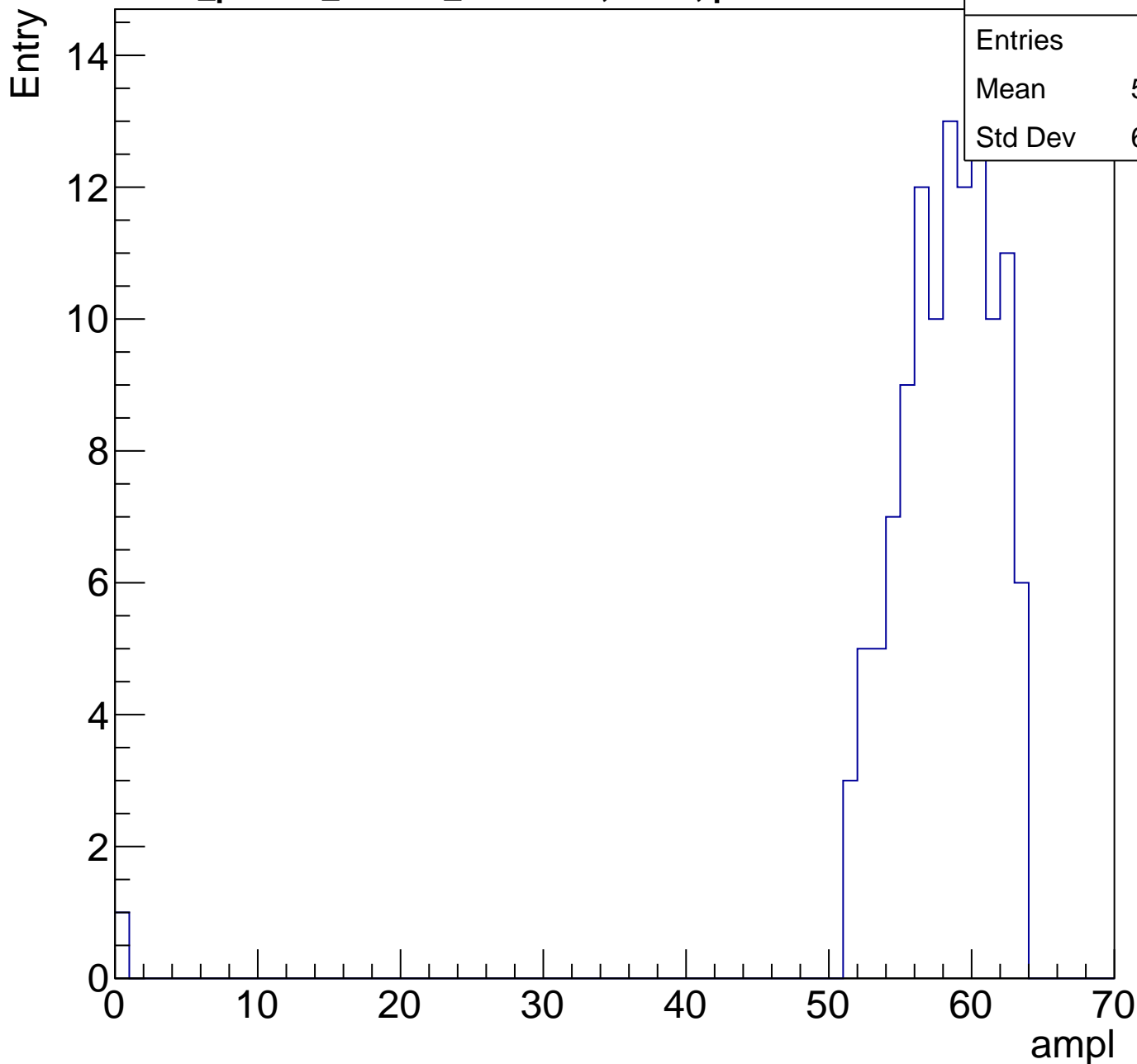
ampl



B0L101S, U22-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 118 |
| Mean | 57.33 |
| Std Dev | 6.174 |



B0L101S, U22-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

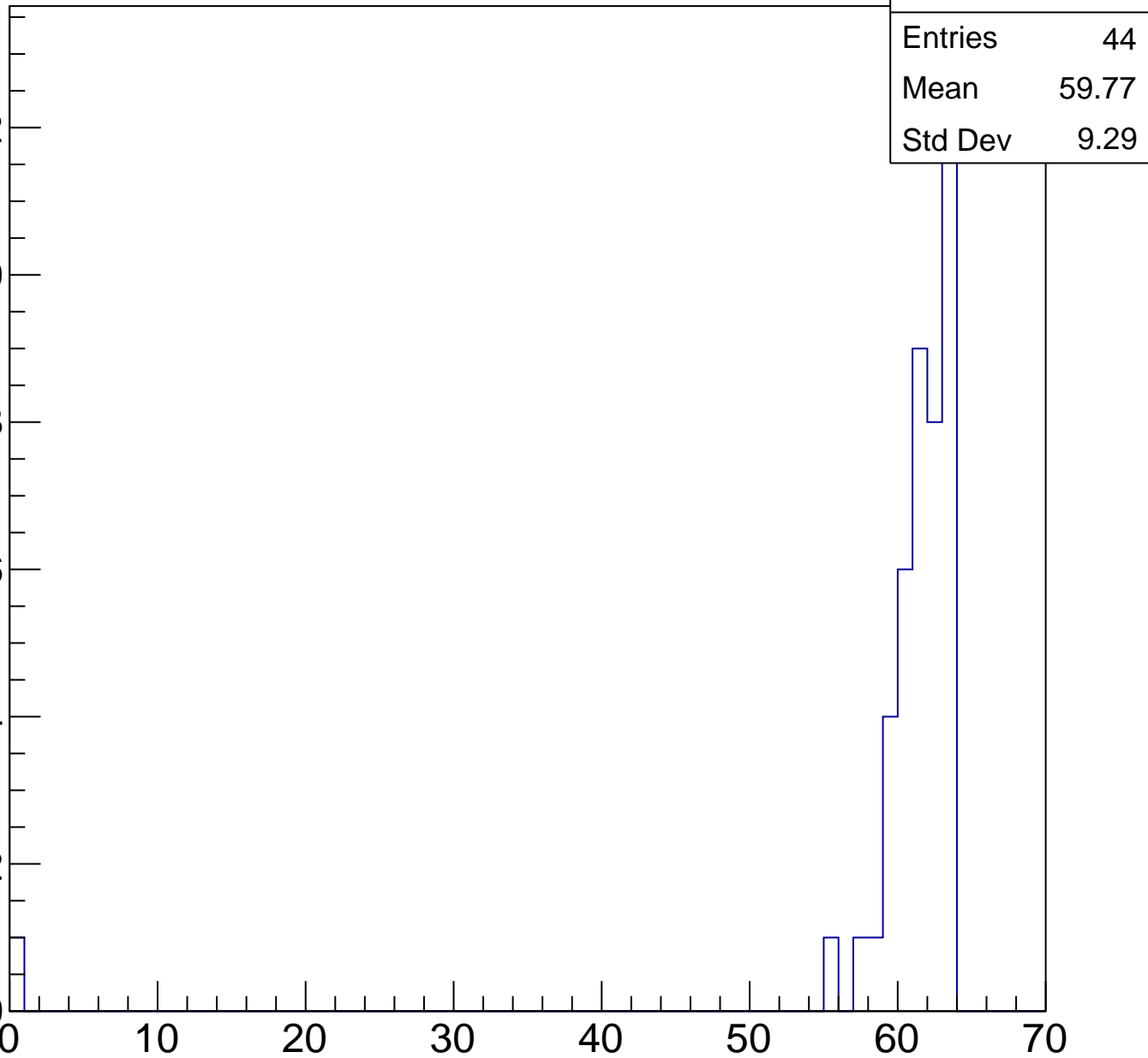
4

2

0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.77 |
| Std Dev | 9.29 |

ampl



B0L101S, U22-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

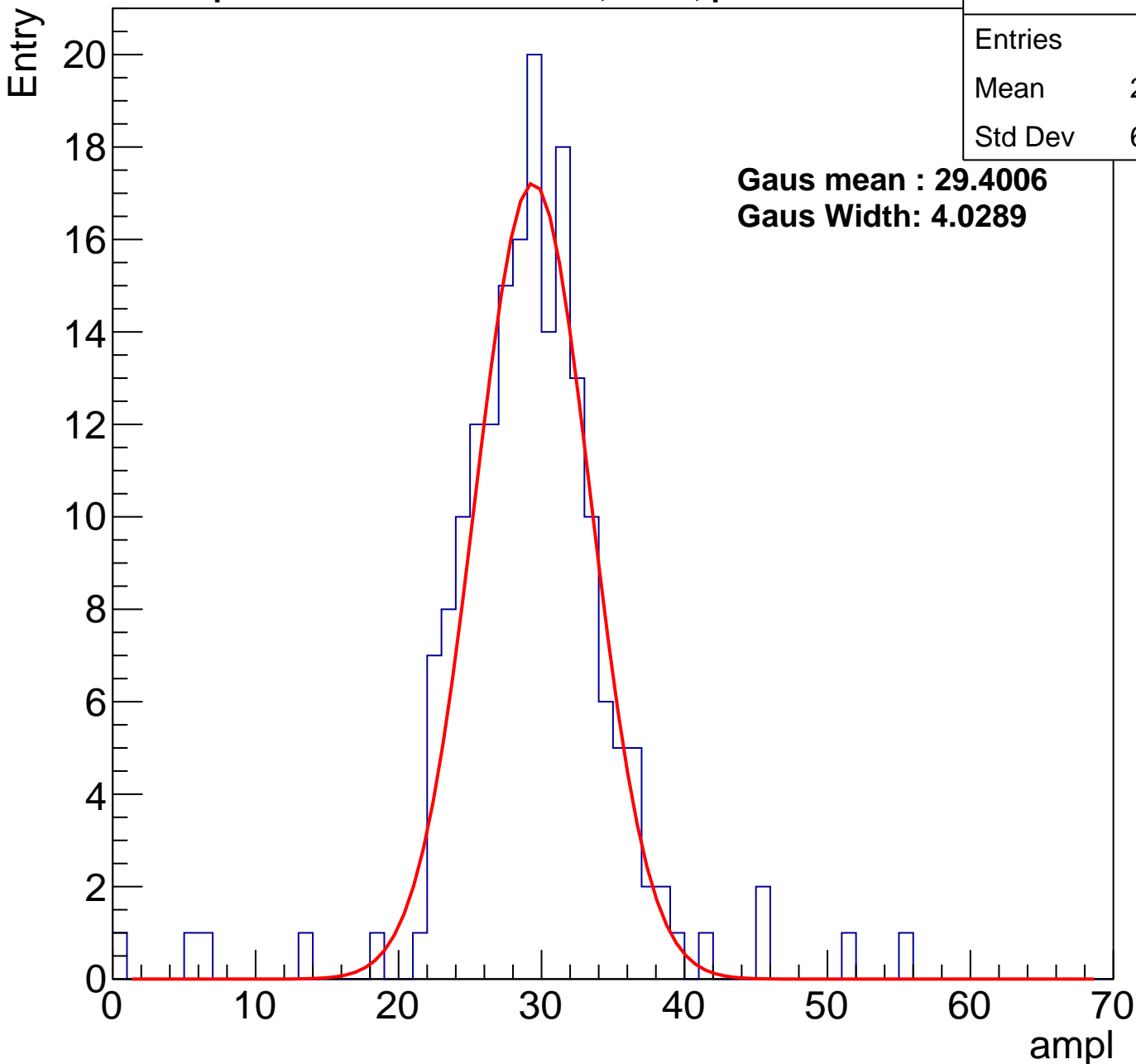
B0L101S, U22-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 187 |
| Mean | 28.85 |
| Std Dev | 6.023 |

Gaus mean : 29.4006

Gaus Width: 4.0289



B0L101S, U22-ch116, adc1

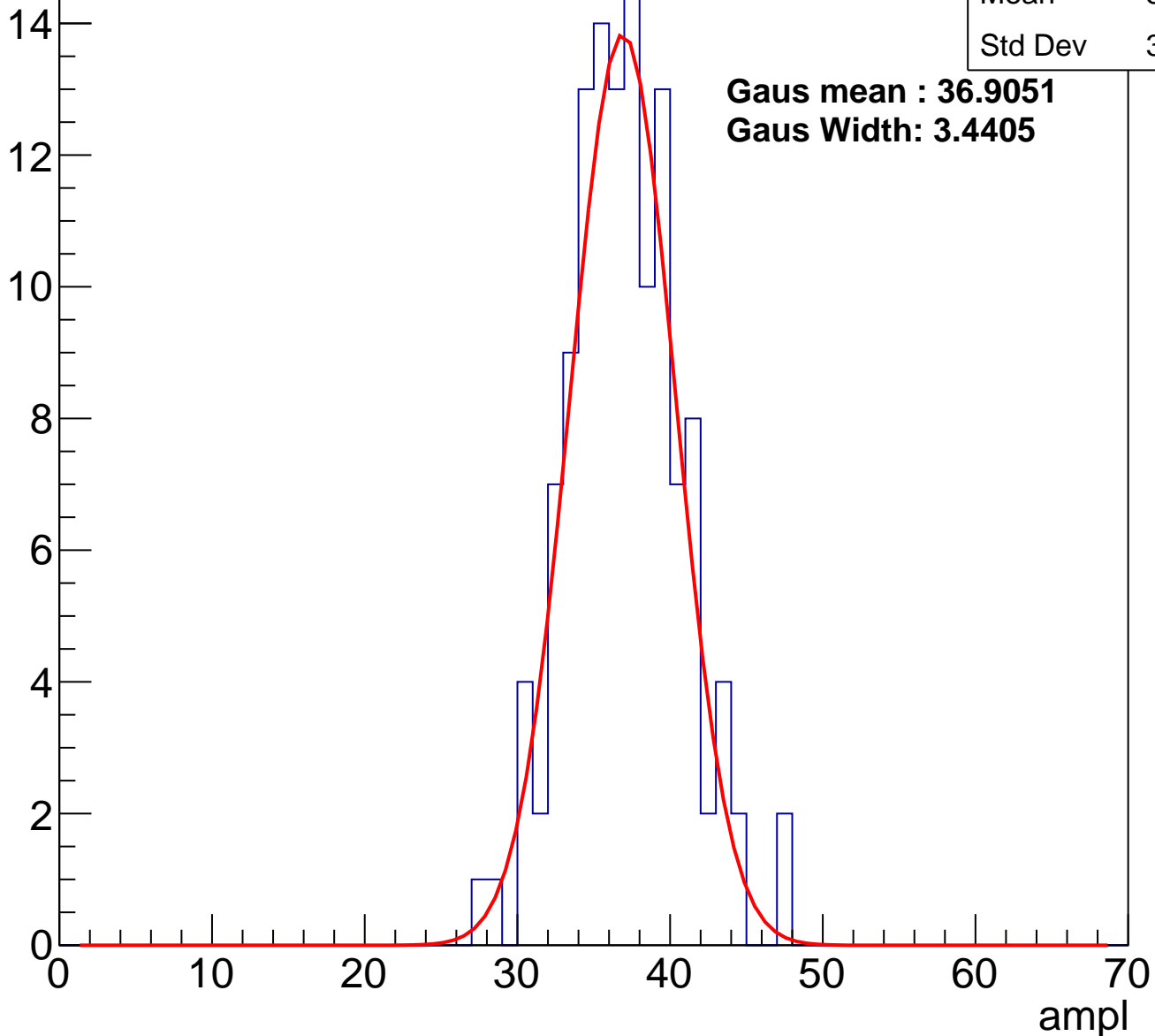
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 127 |
| Mean | 36.58 |
| Std Dev | 3.634 |

Gaus mean : 36.9051

Gaus Width: 3.4405

Entry



B0L101S, U22-ch116, adc2

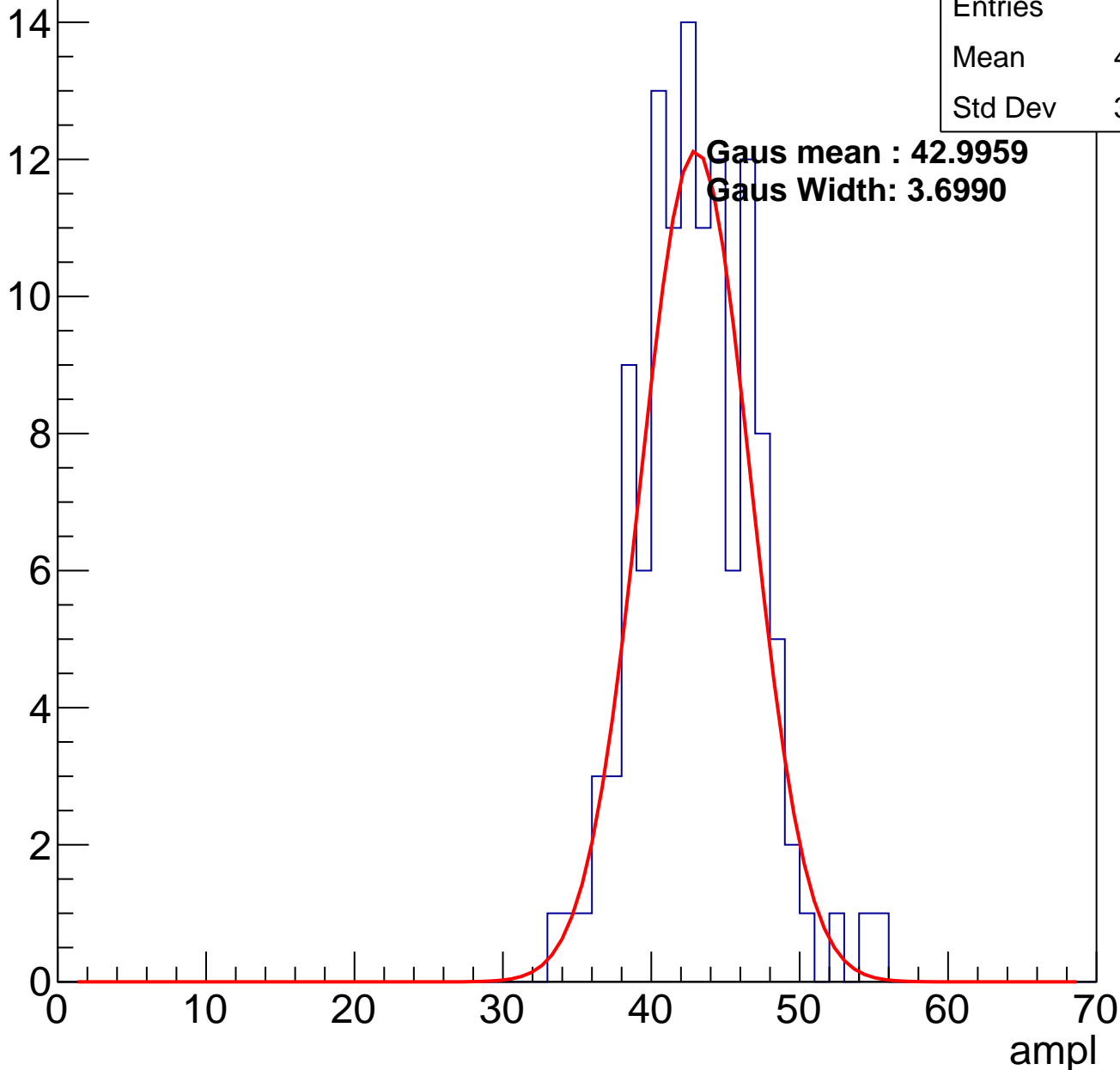
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 122 |
| Mean | 42.66 |
| Std Dev | 3.889 |

Gaus mean : 42.9959

Gaus Width: 3.6990

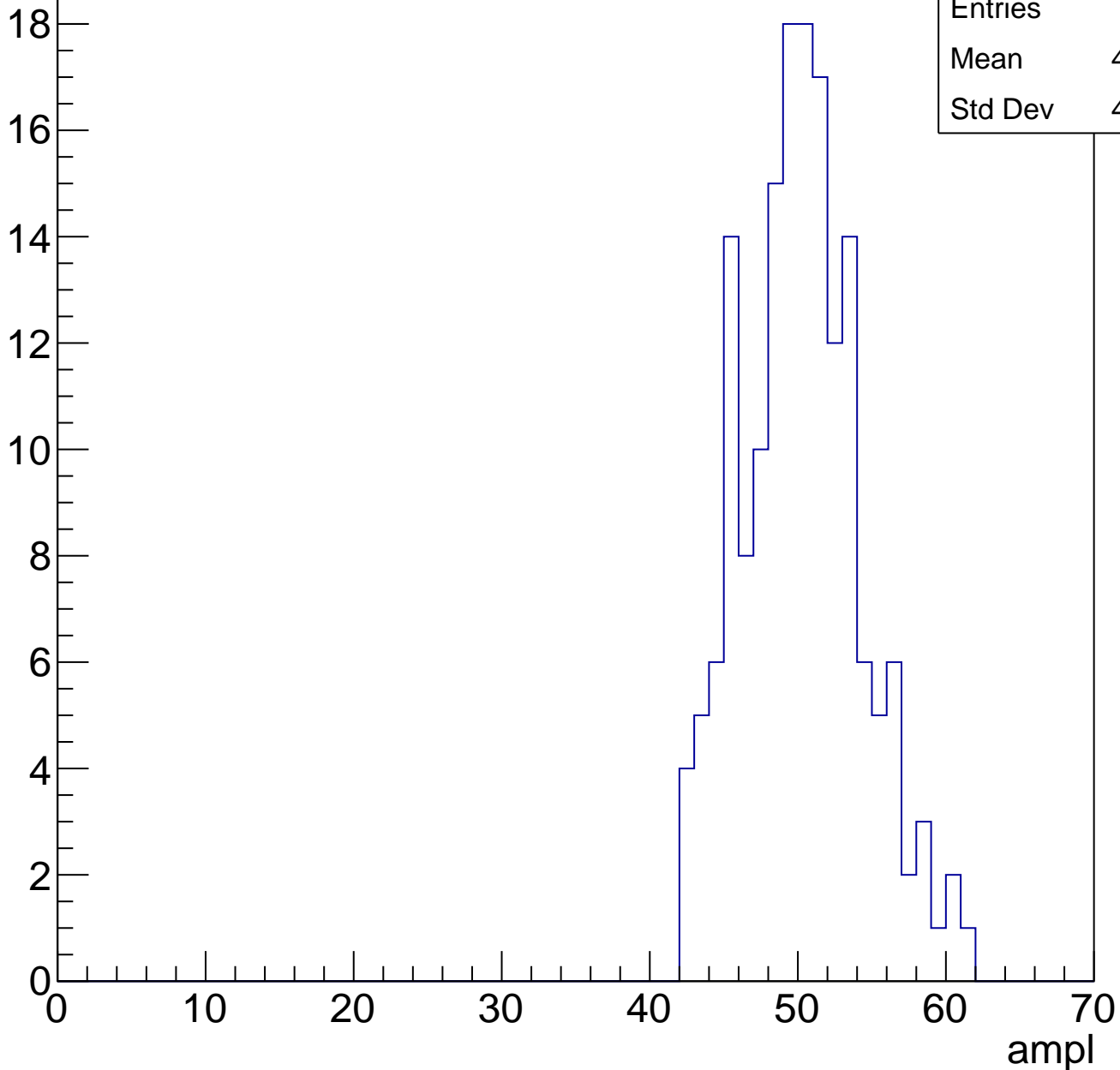


B0L101S, U22-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 167 |
| Mean | 49.78 |
| Std Dev | 4.002 |



B0L101S, U22-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

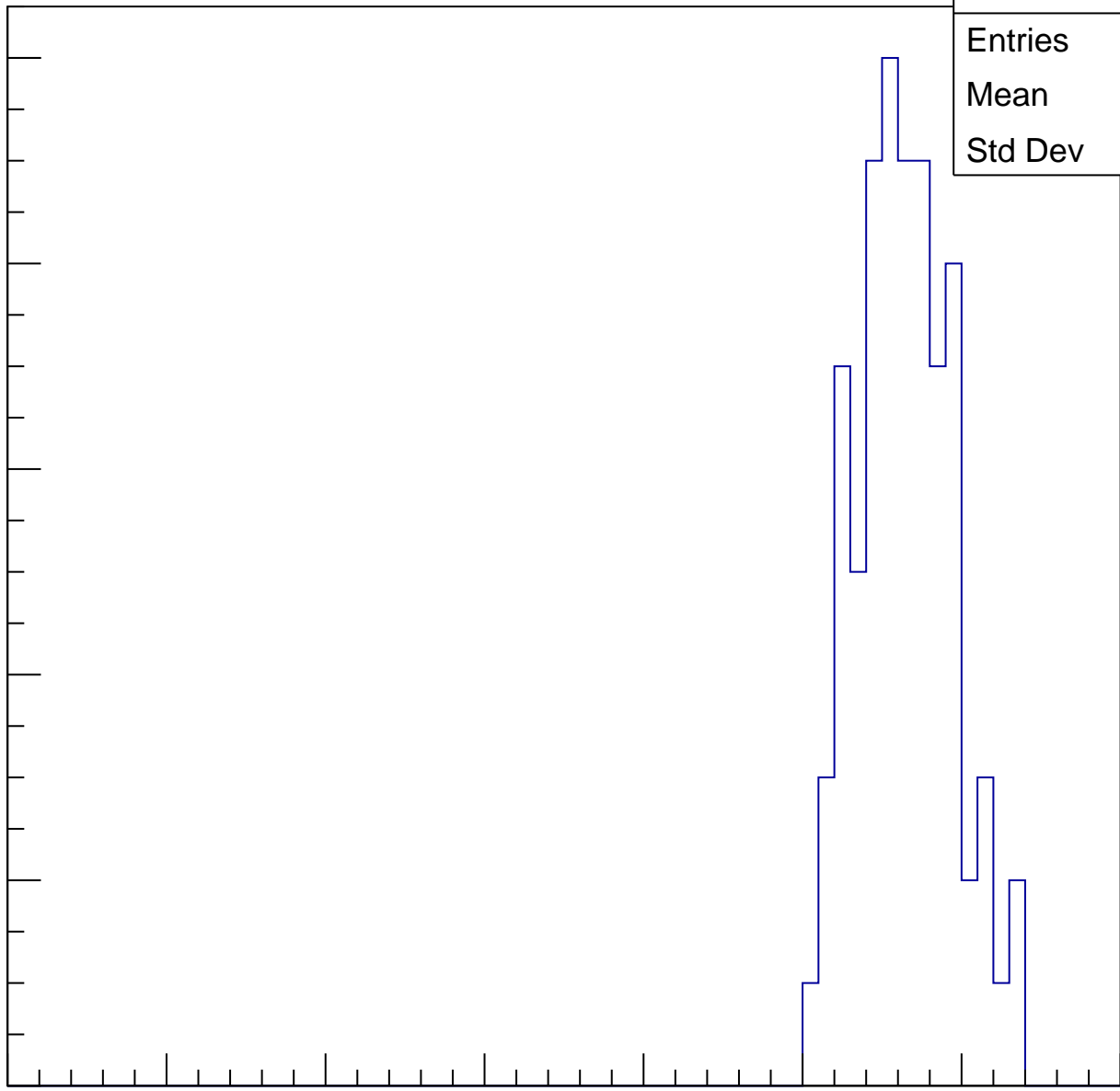
| | |
|---------|-------|
| Entries | 76 |
| Mean | 55.97 |
| Std Dev | 2.956 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch116, adc5

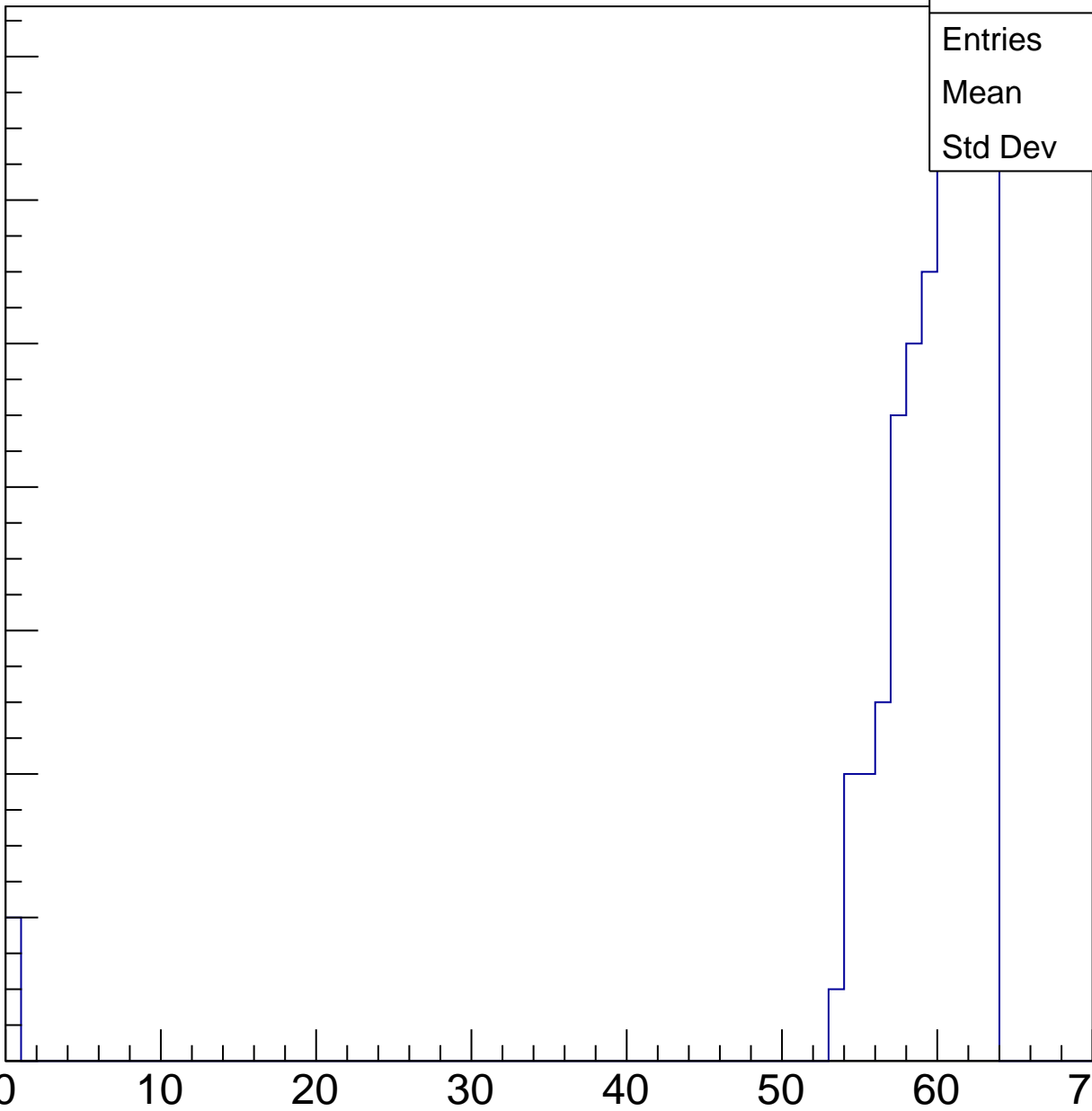
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 99 |
| Mean | 58.27 |
| Std Dev | 8.757 |

ampl

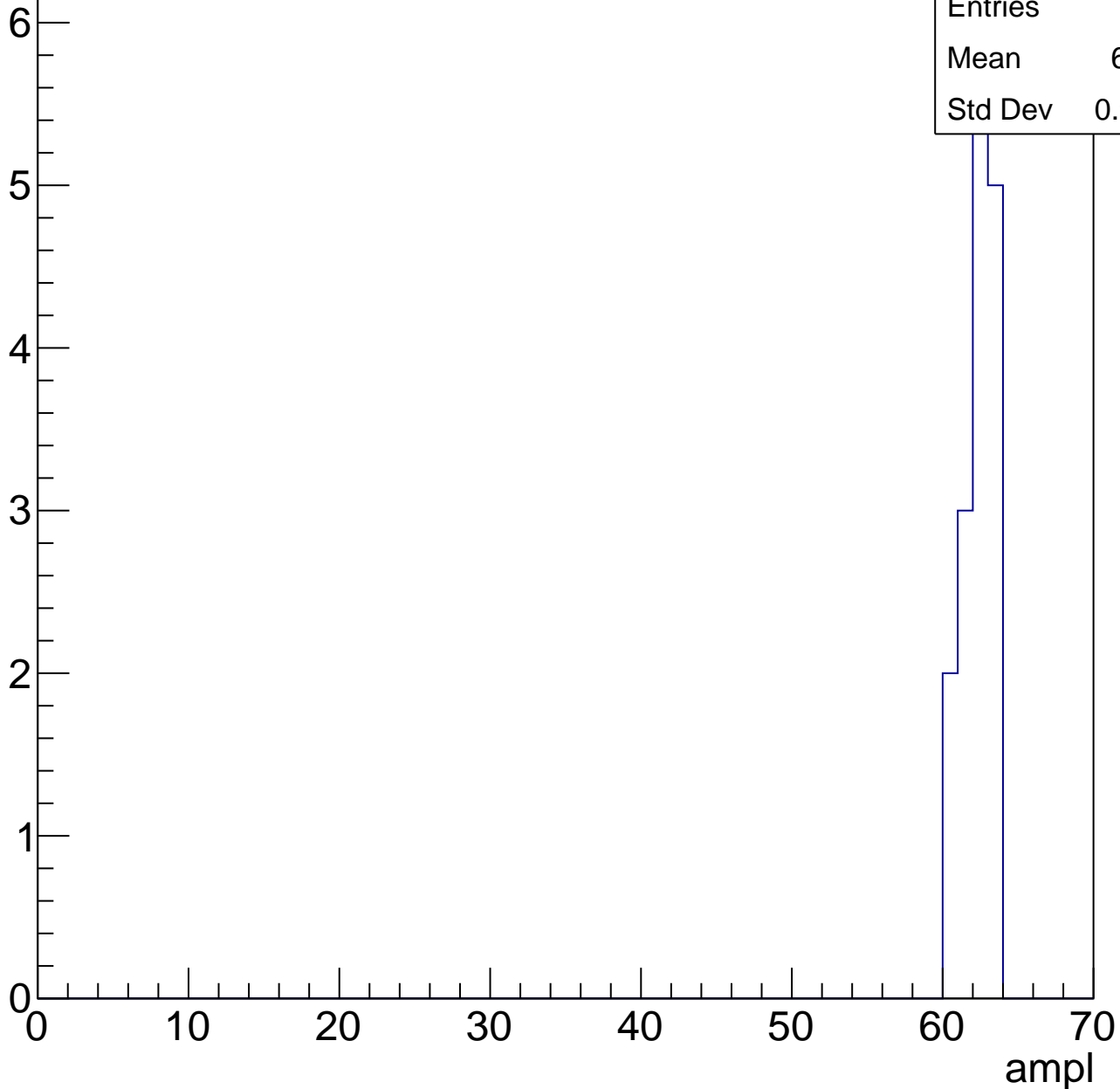


B0L101S, U22-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

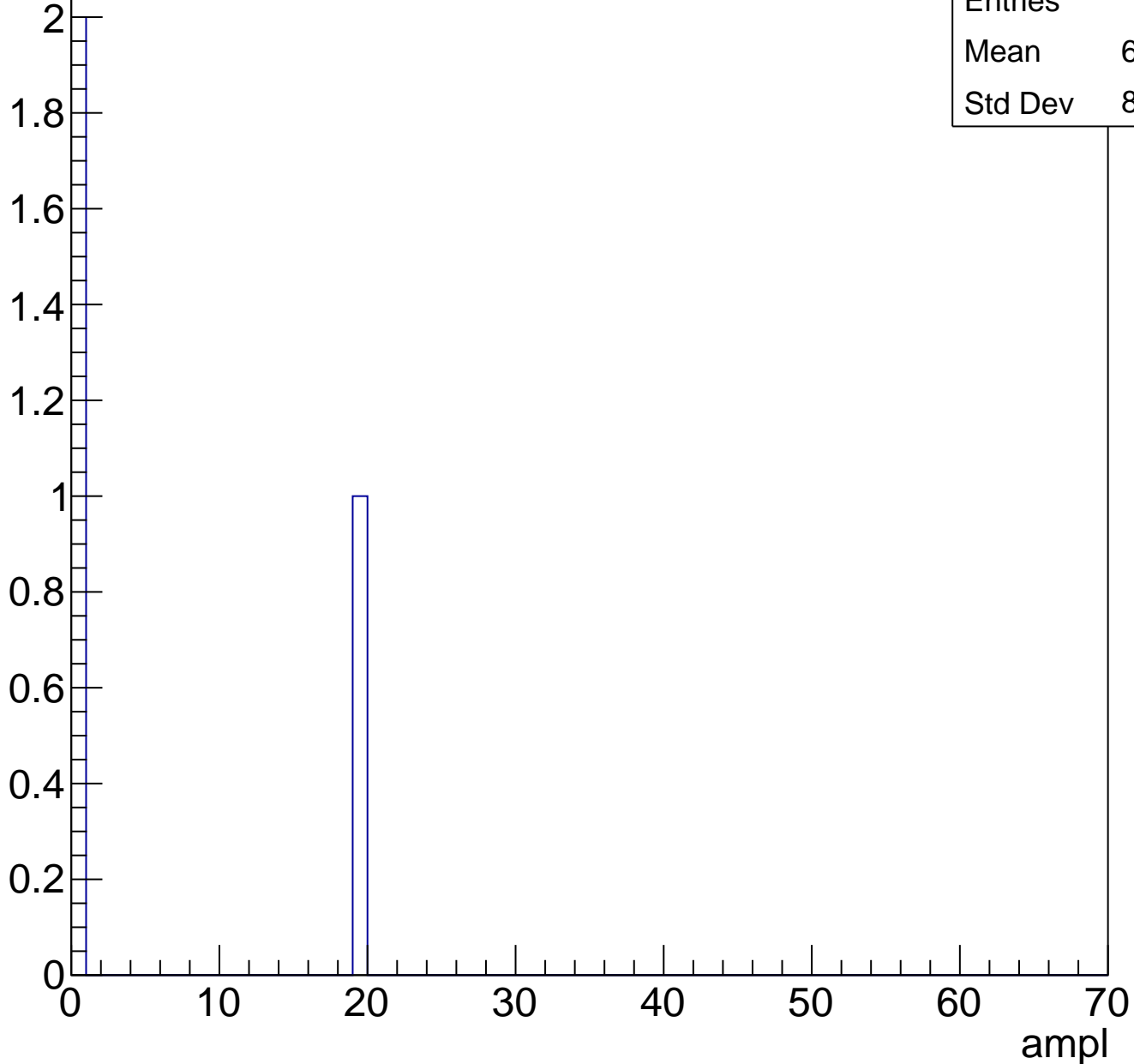
| | |
|---------|--------|
| Entries | 16 |
| Mean | 61.88 |
| Std Dev | 0.9922 |



B0L101S, U22-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

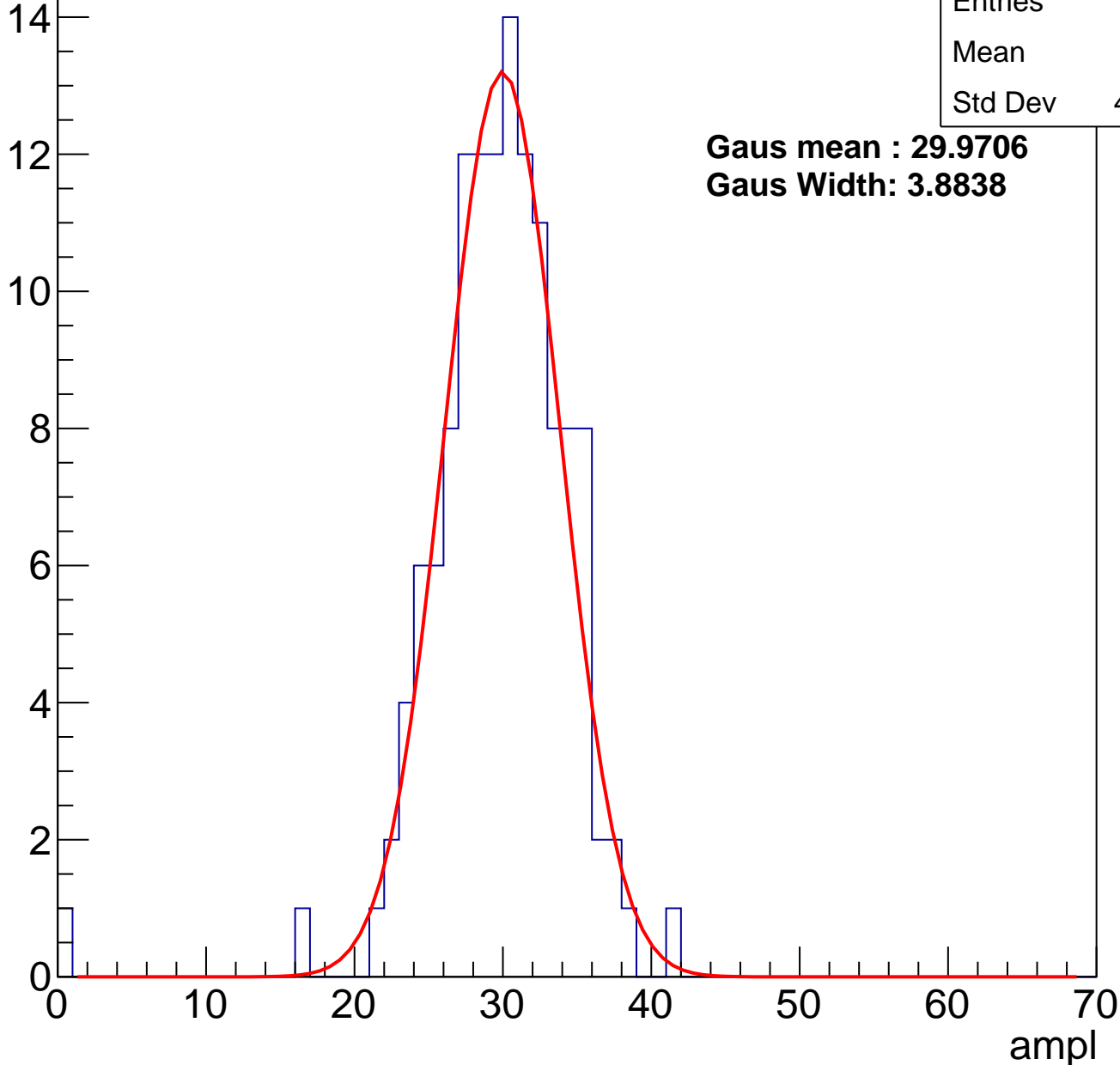
B0L101S, U22-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 132 |
| Mean | 29.3 |
| Std Dev | 4.695 |

Gaus mean : 29.9706
Gaus Width: 3.8838

Entry



B0L101S, U22-ch117, adc1

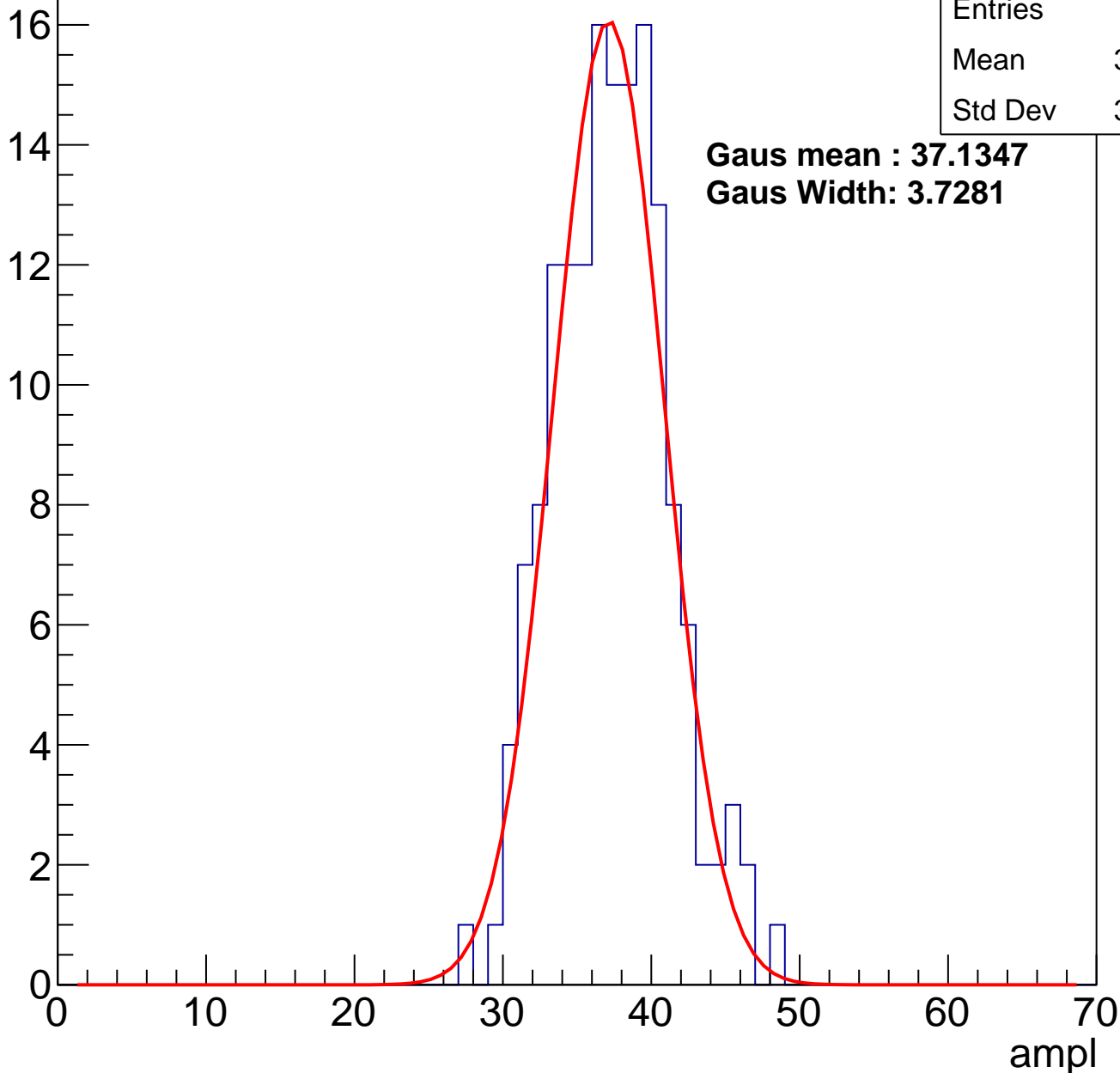
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 156 |
| Mean | 36.84 |
| Std Dev | 3.832 |

Gaus mean : 37.1347

Gaus Width: 3.7281

Entry



B0L101S, U22-ch117, adc2

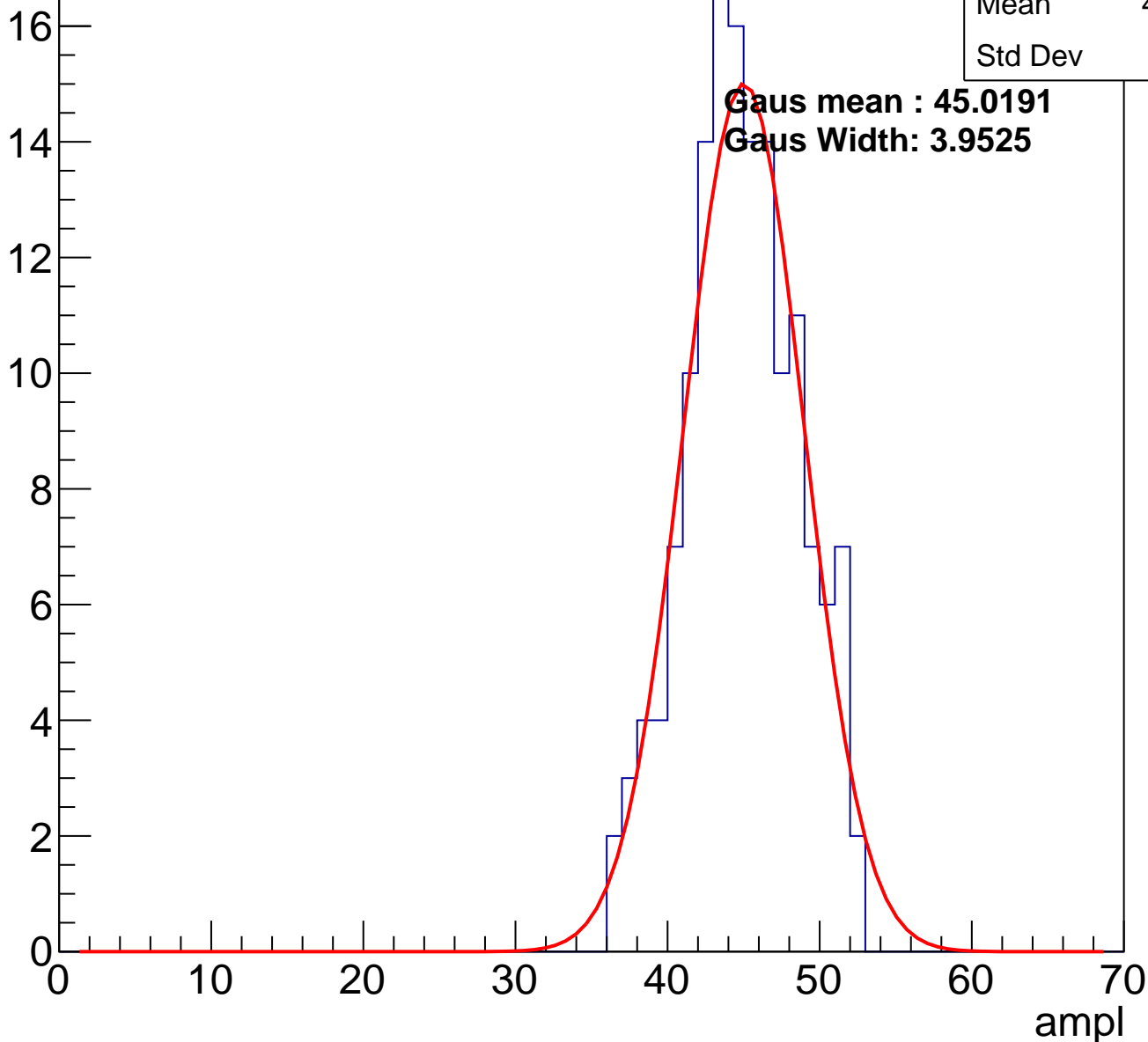
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 148 |
| Mean | 44.46 |
| Std Dev | 3.64 |

Gaus mean : 45.0191

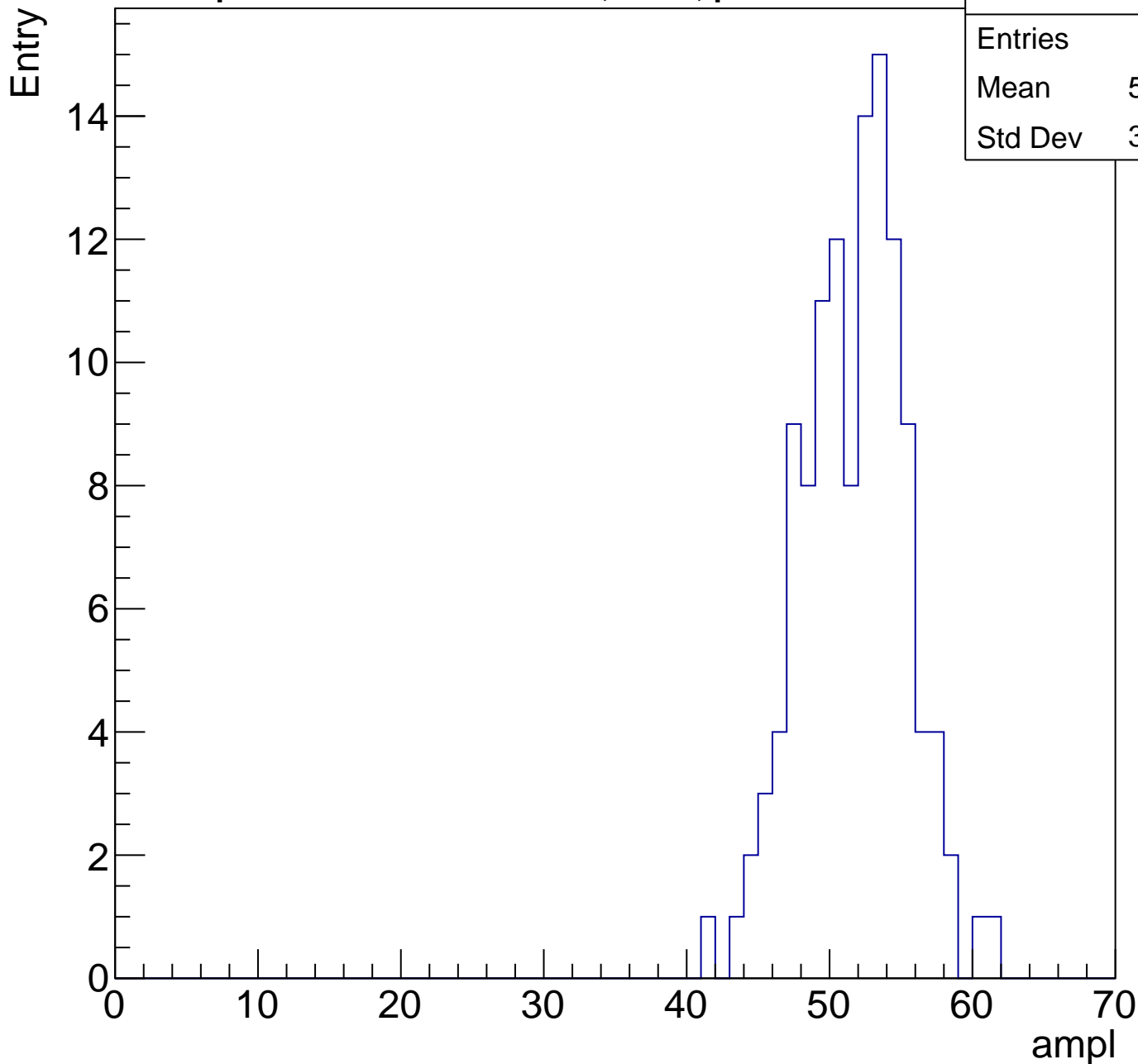
Gaus Width: 3.9525



B0L101S, U22-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

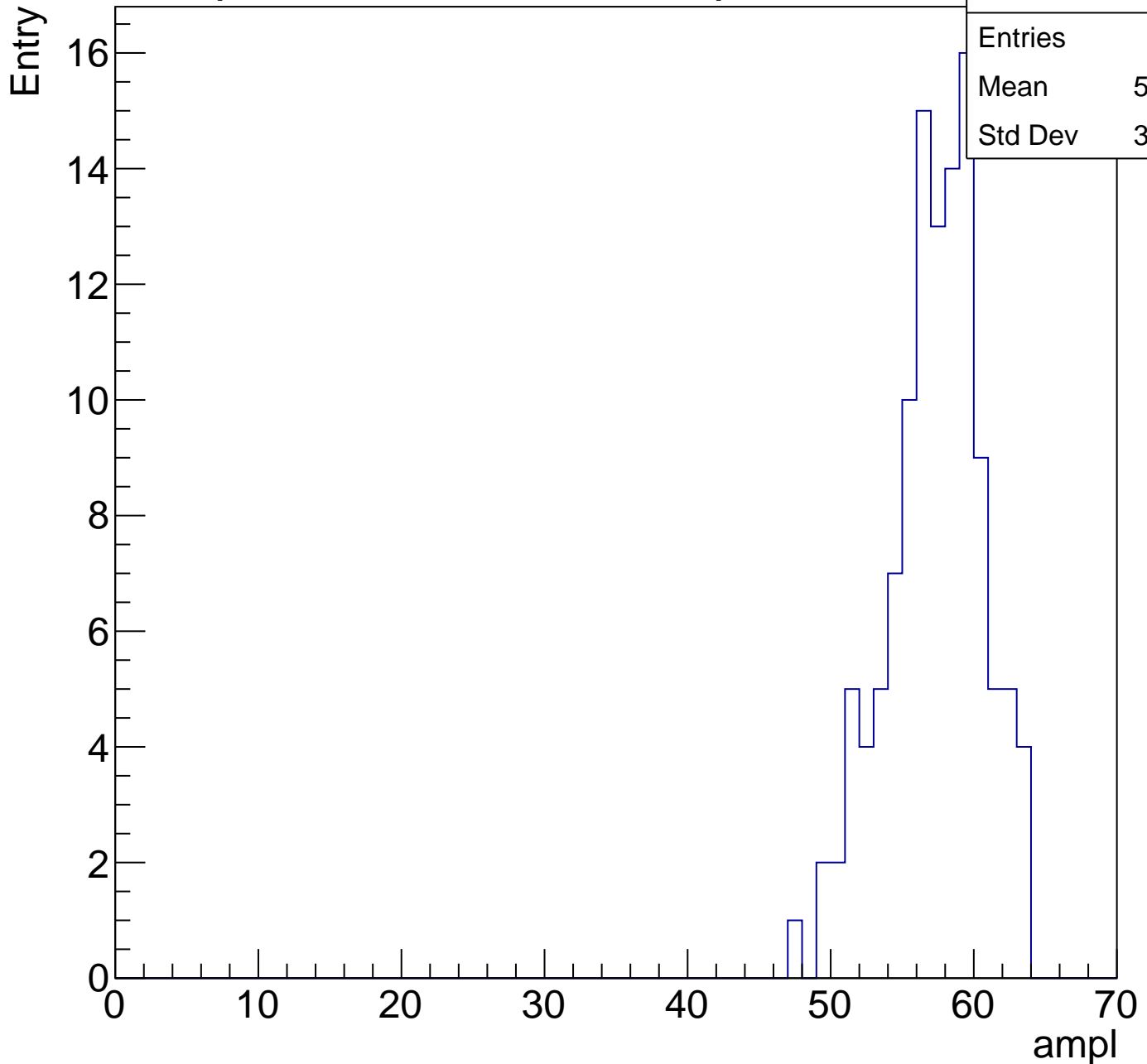
| | |
|---------|-------|
| Entries | 121 |
| Mean | 51.24 |
| Std Dev | 3.618 |



B0L101S, U22-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 117 |
| Mean | 56.79 |
| Std Dev | 3.342 |



B0L101S, U22-ch117, adc5

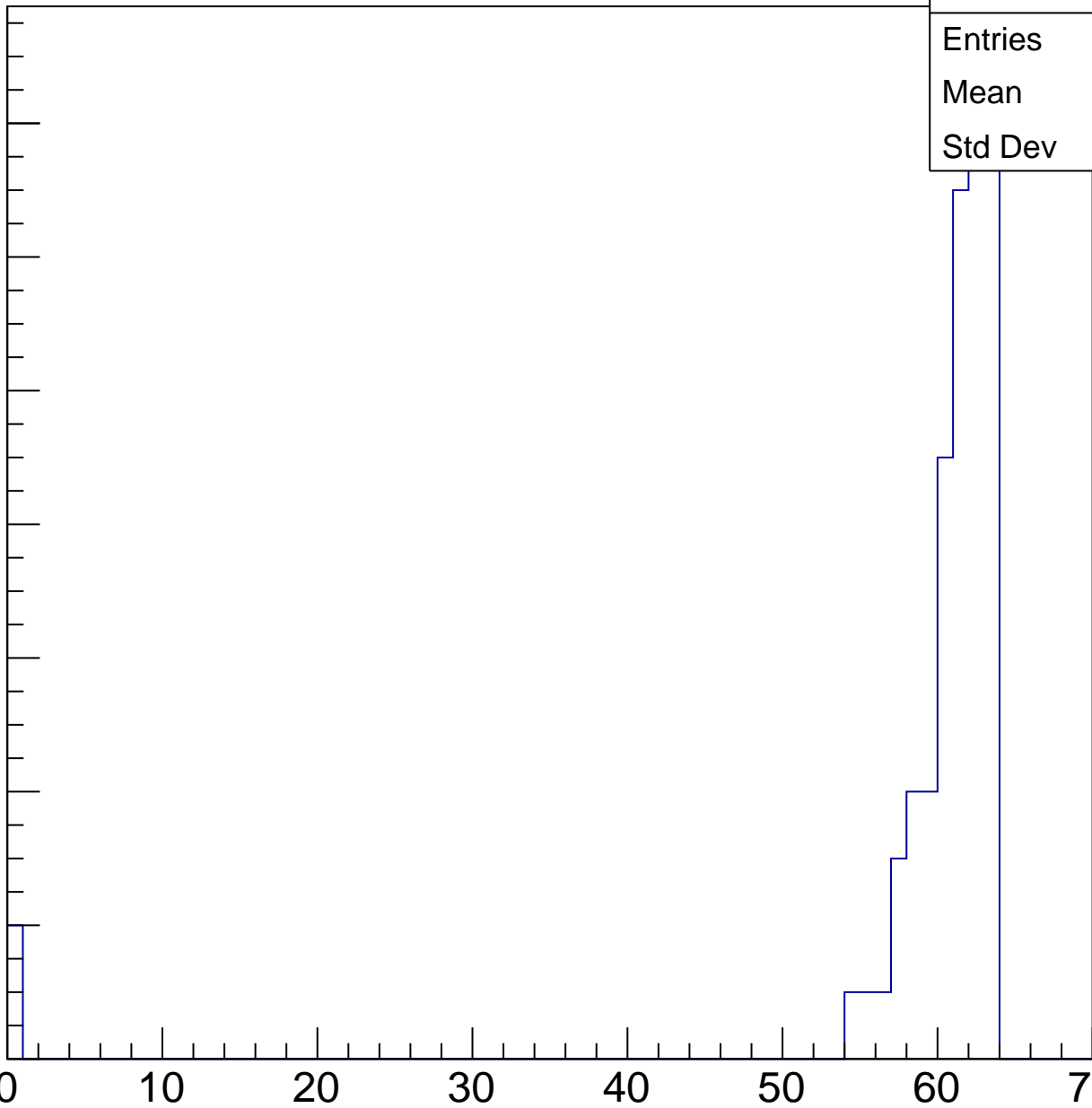
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 67 |
| Mean | 58.96 |
| Std Dev | 10.55 |

ampl



B0L101S, U22-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch118, adc0

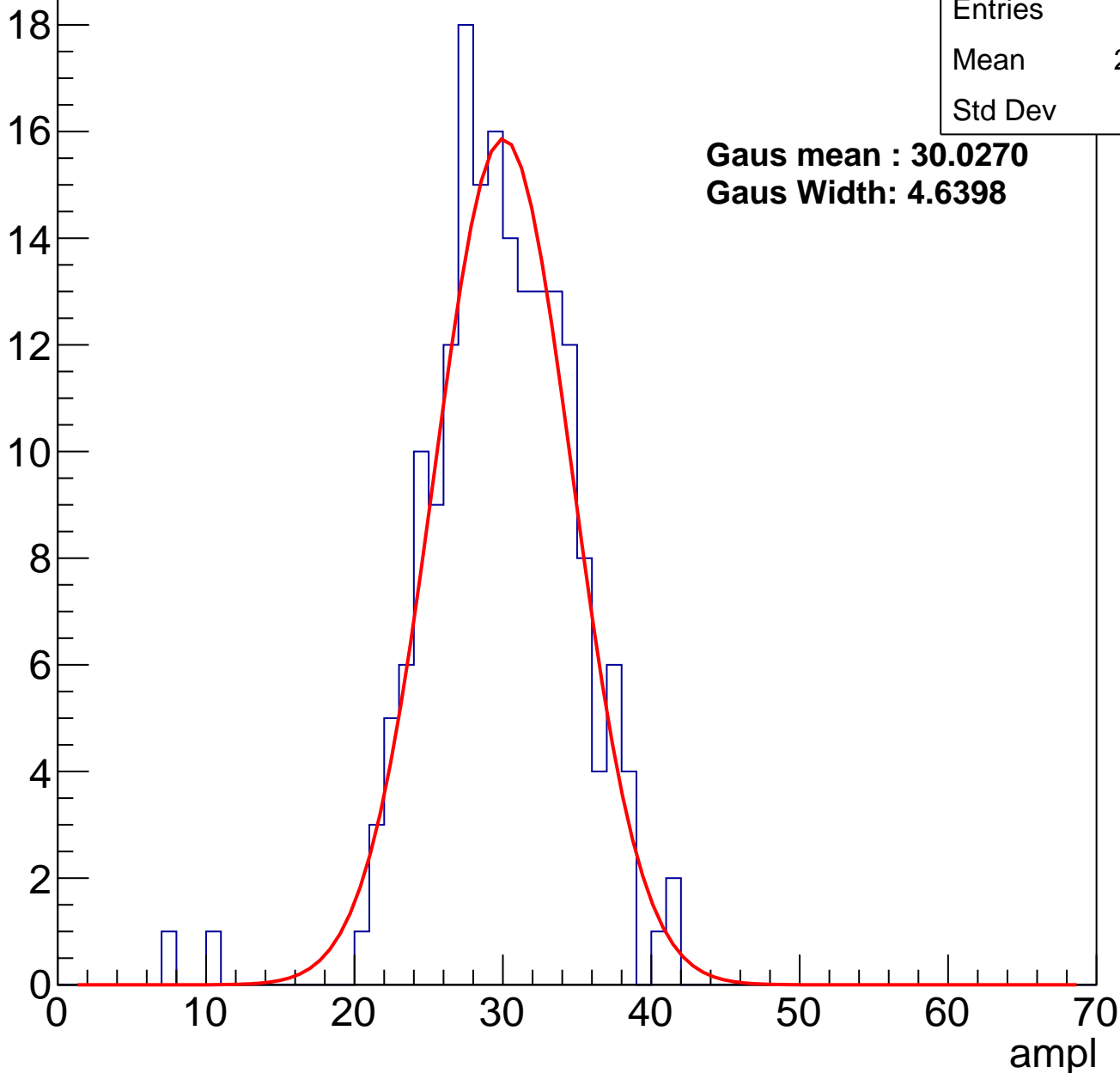
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 187 |
| Mean | 29.36 |
| Std Dev | 4.87 |

Gaus mean : 30.0270

Gaus Width: 4.6398

Entry



B0L101S, U22-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 110 |
| Mean | 36.29 |
| Std Dev | 4.041 |

Gaus mean : 37.1375

Gaus Width: 4.1425

Entry

12

10

8

6

4

2

0

0

10

20

30

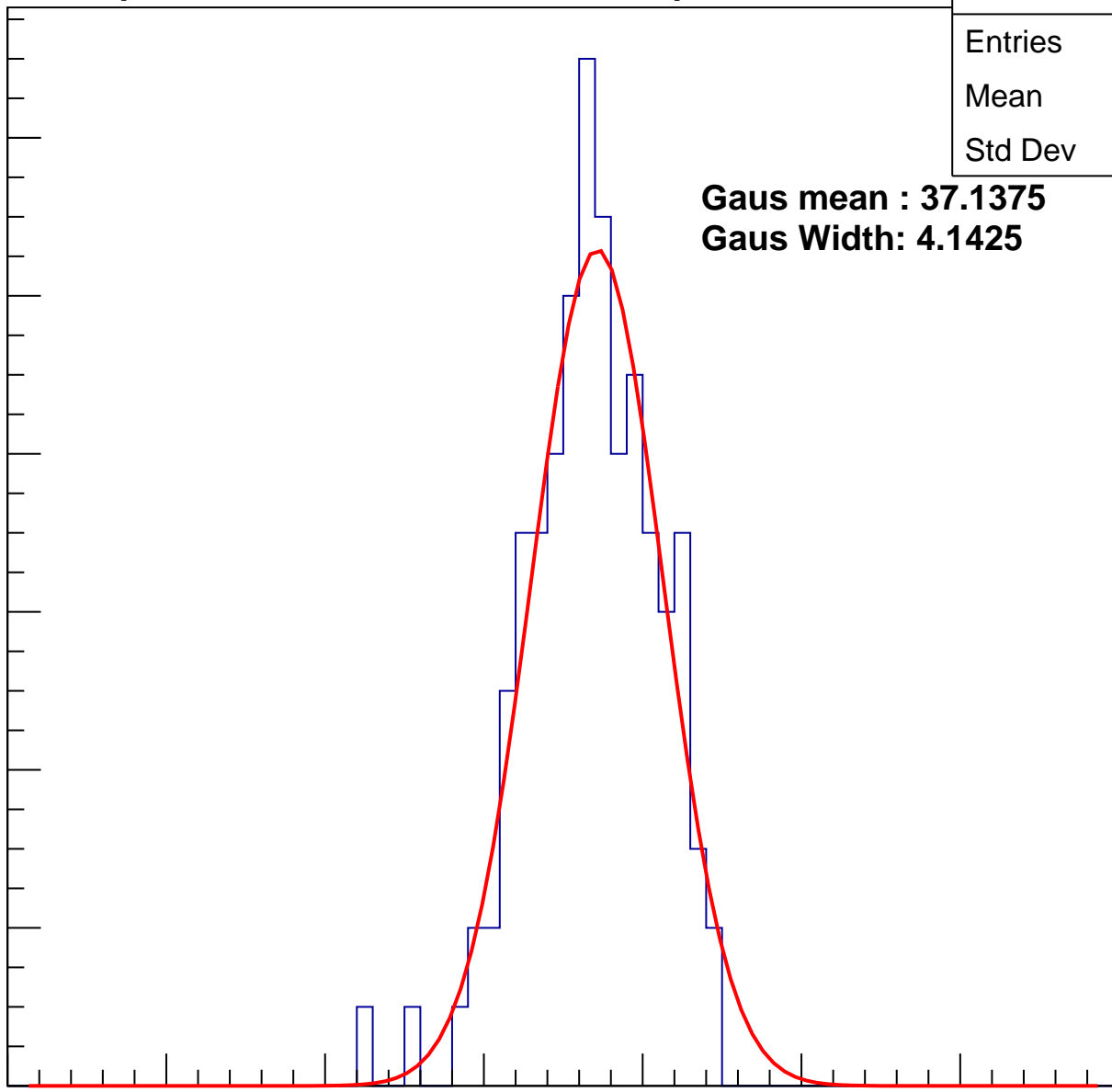
40

50

60

70

ampl



B0L101S, U22-ch118, adc2

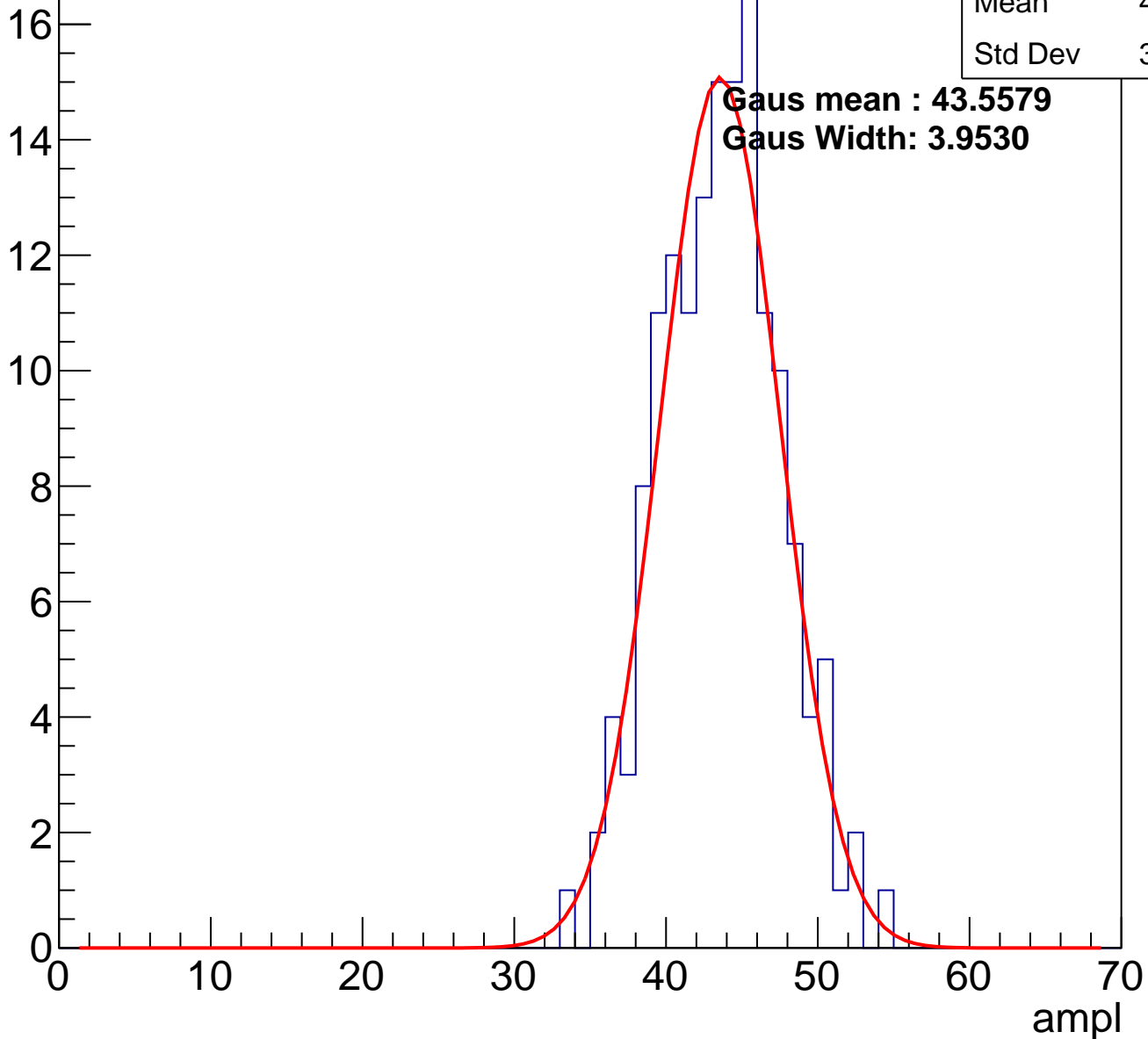
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 153 |
| Mean | 43.17 |
| Std Dev | 3.895 |

Gaus mean : 43.5579

Gaus Width: 3.9530



B0L101S, U22-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 126 |
| Mean | 50.4 |
| Std Dev | 3.55 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

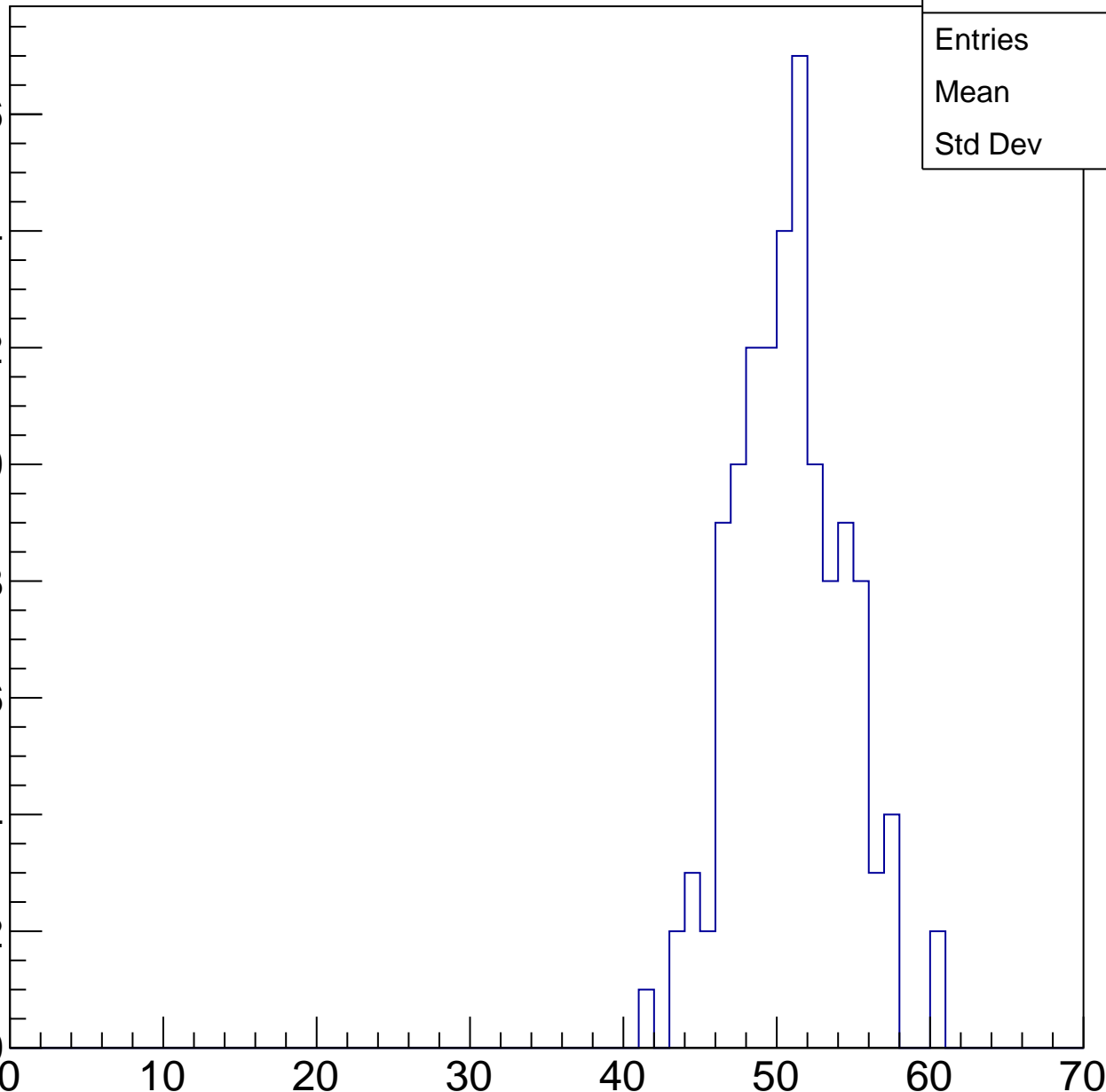
30

40

50

60

ampl



B0L101S, U22-ch118, adc4

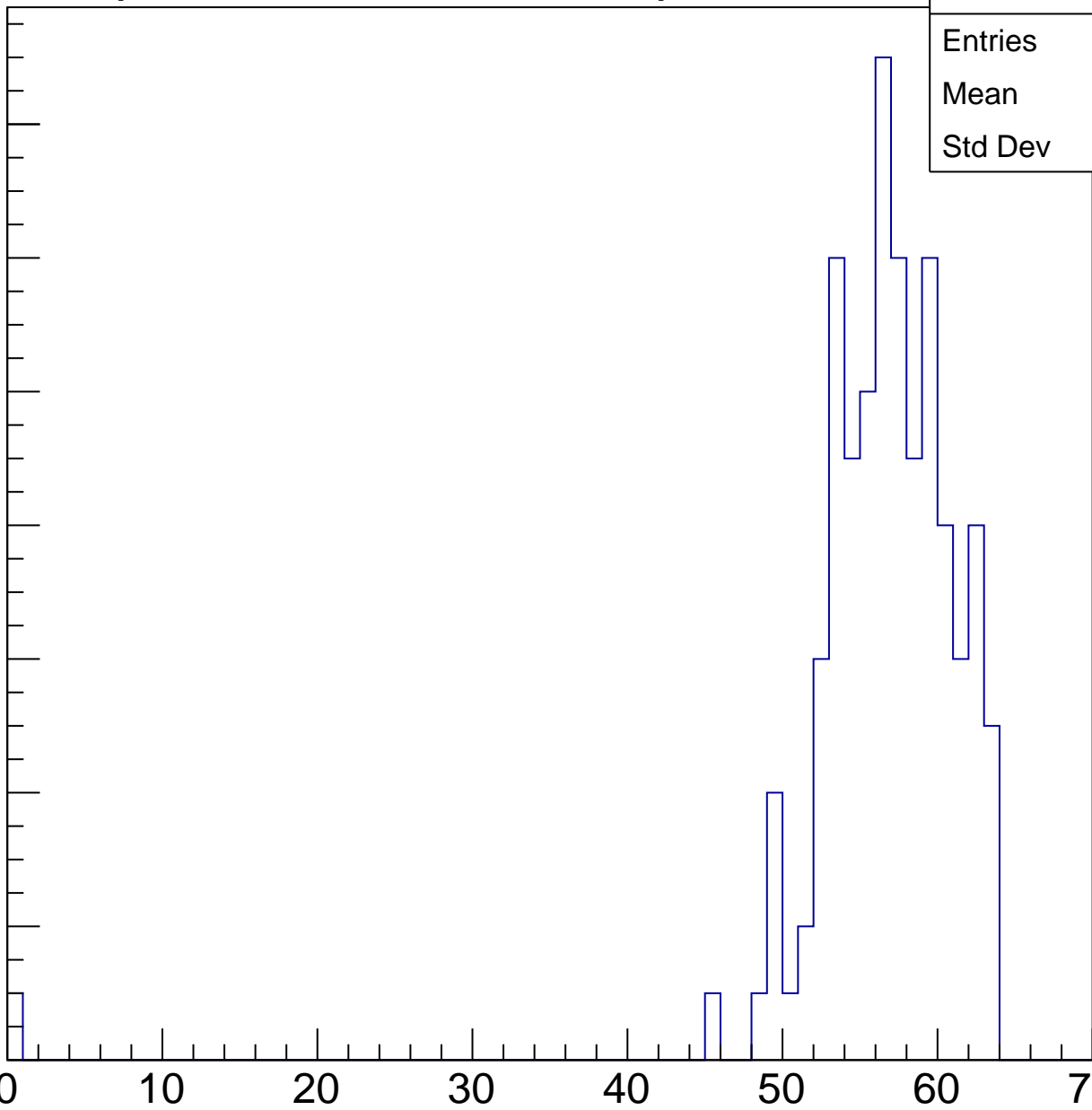
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 122 |
| Mean | 56.03 |
| Std Dev | 6.285 |

ampl



B0L101S, U22-ch118, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

73

Mean

59.86

Std Dev

2.582

ampl

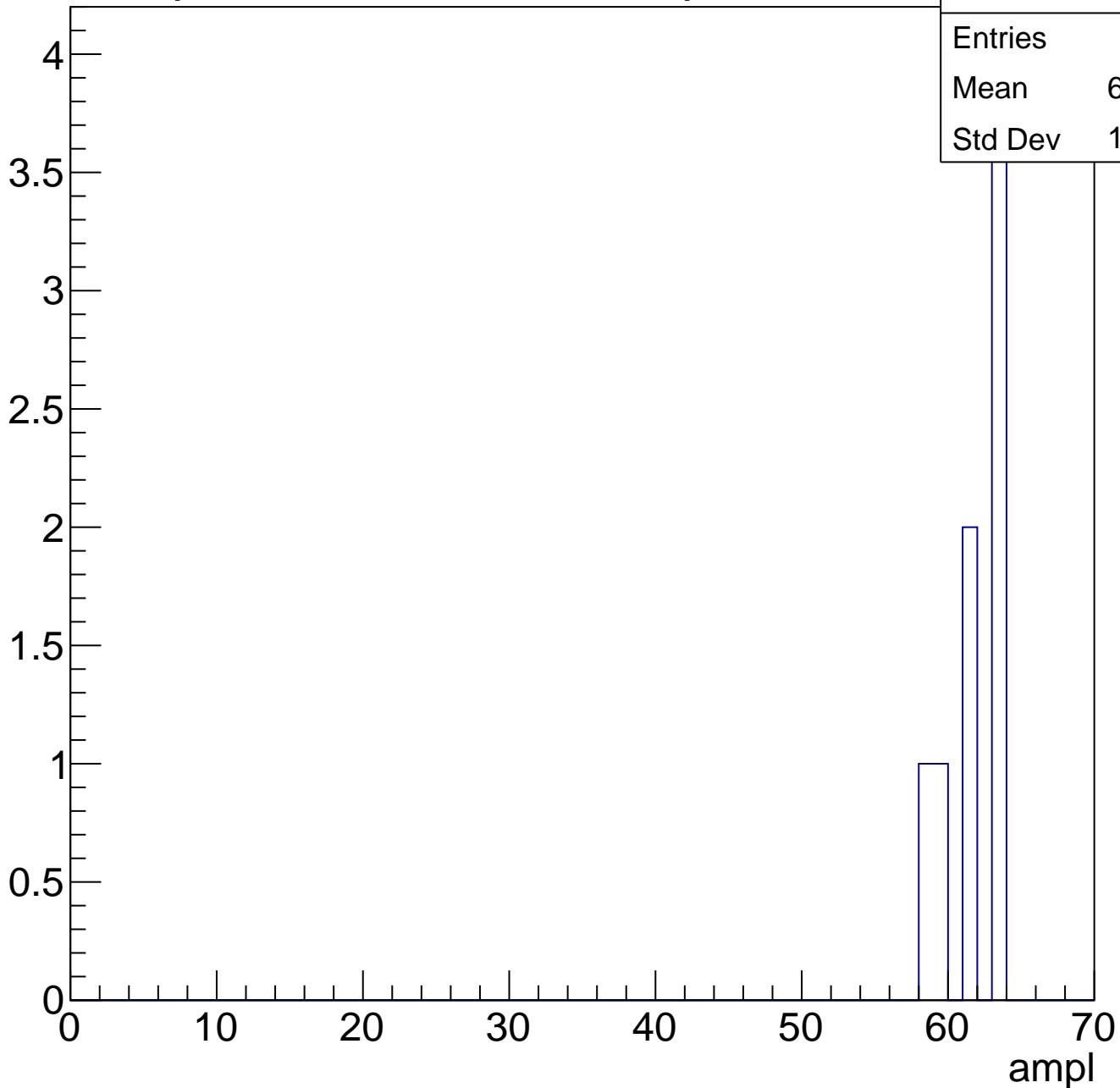
0 10 20 30 40 50 60 70

| | |
|---------|-------|
| Entries | 73 |
| Mean | 59.86 |
| Std Dev | 2.582 |

B0L101S, U22-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6 |
| Std Dev | 8.485 |

B0L101S, U22-ch119, adc0

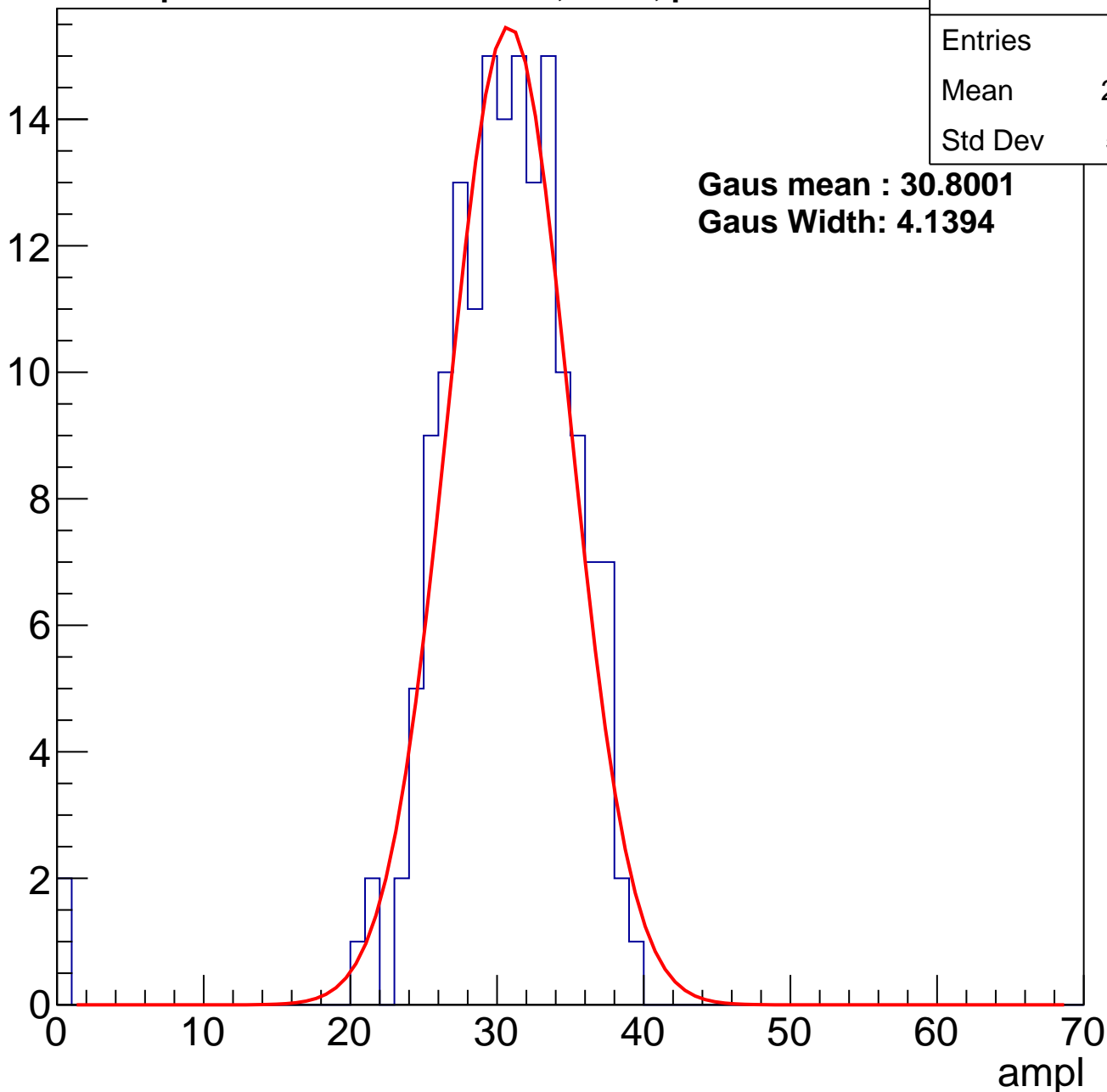
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 163 |
| Mean | 29.96 |
| Std Dev | 5.131 |

Gaus mean : 30.8001

Gaus Width: 4.1394

Entry



B0L101S, U22-ch119, adc1

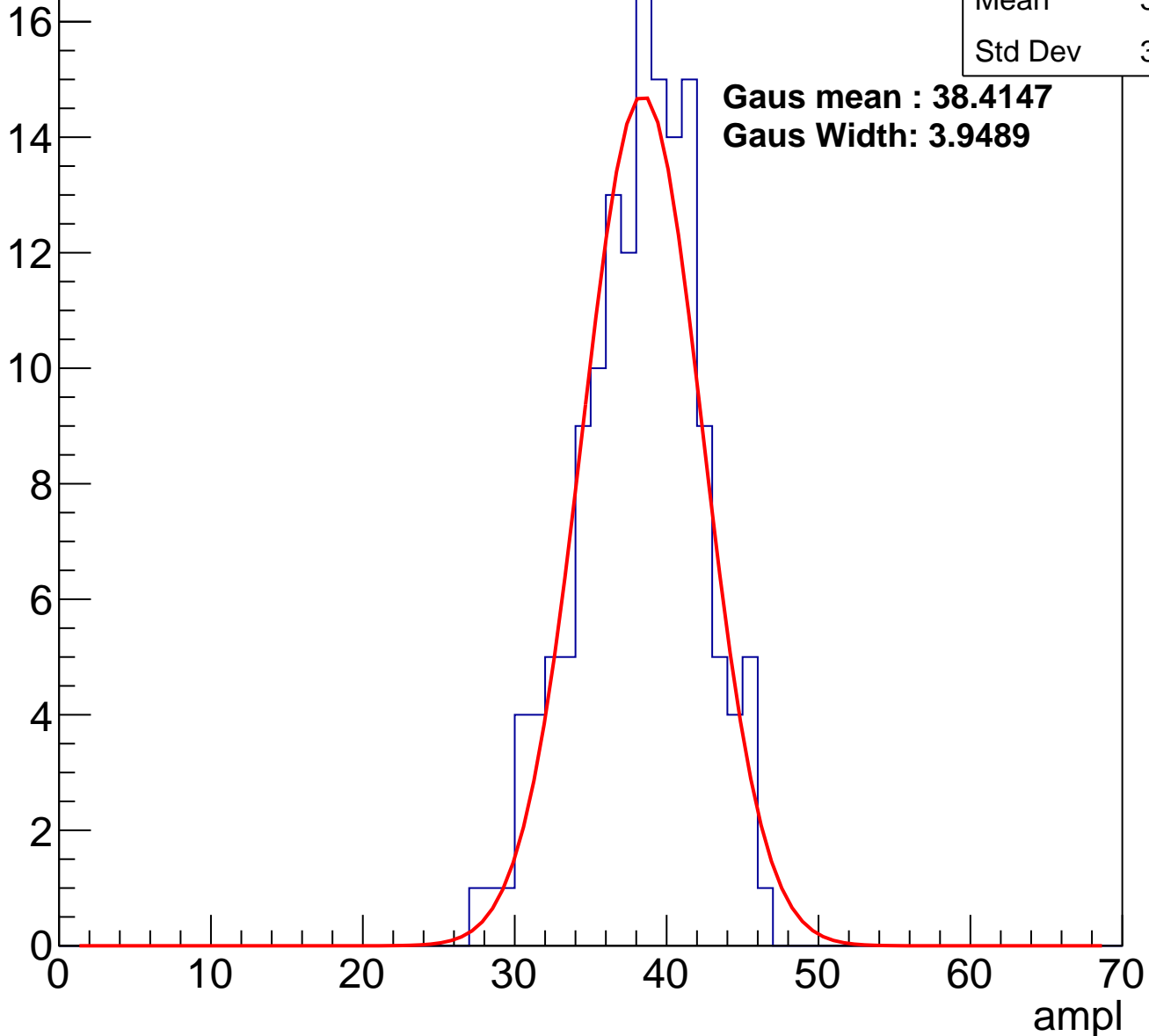
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 150 |
| Mean | 37.78 |
| Std Dev | 3.897 |

Gaus mean : 38.4147

Gaus Width: 3.9489

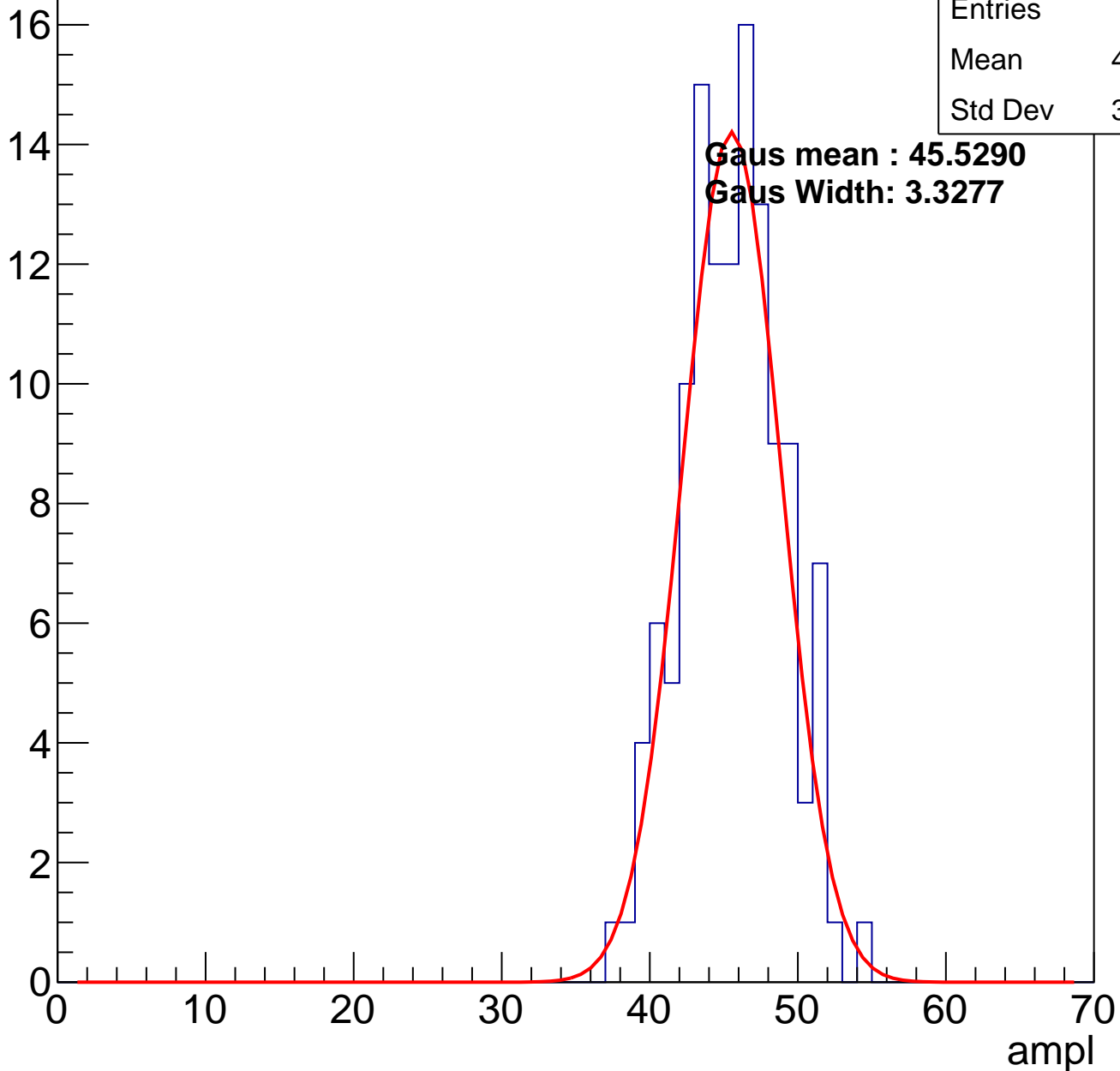
Entry



B0L101S, U22-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 139 |
| Mean | 51.7 |
| Std Dev | 3.87 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

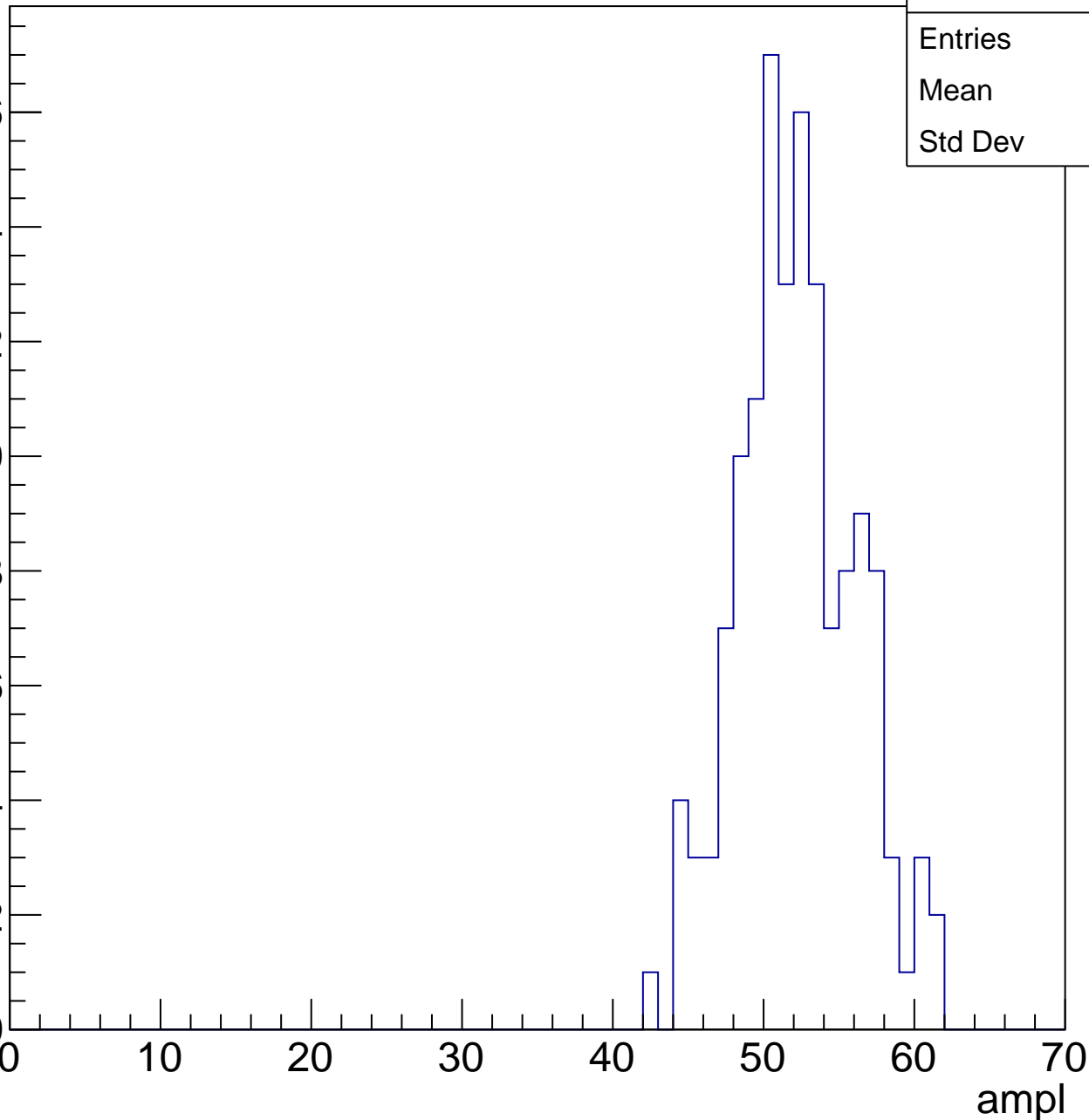
30

40

50

60

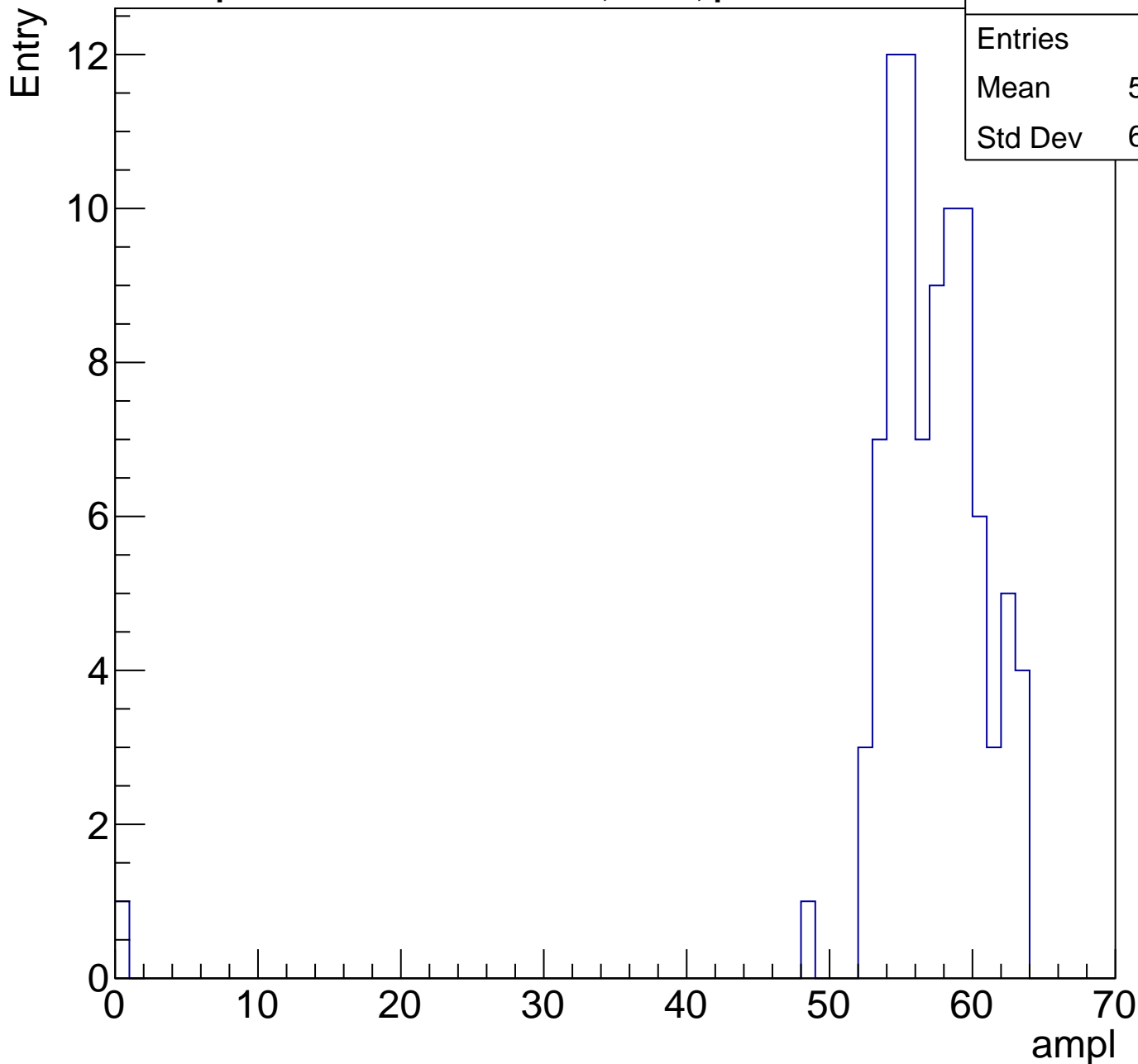
ampl



B0L101S, U22-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 90 |
| Mean | 56.26 |
| Std Dev | 6.709 |



B0L101S, U22-ch119, adc5

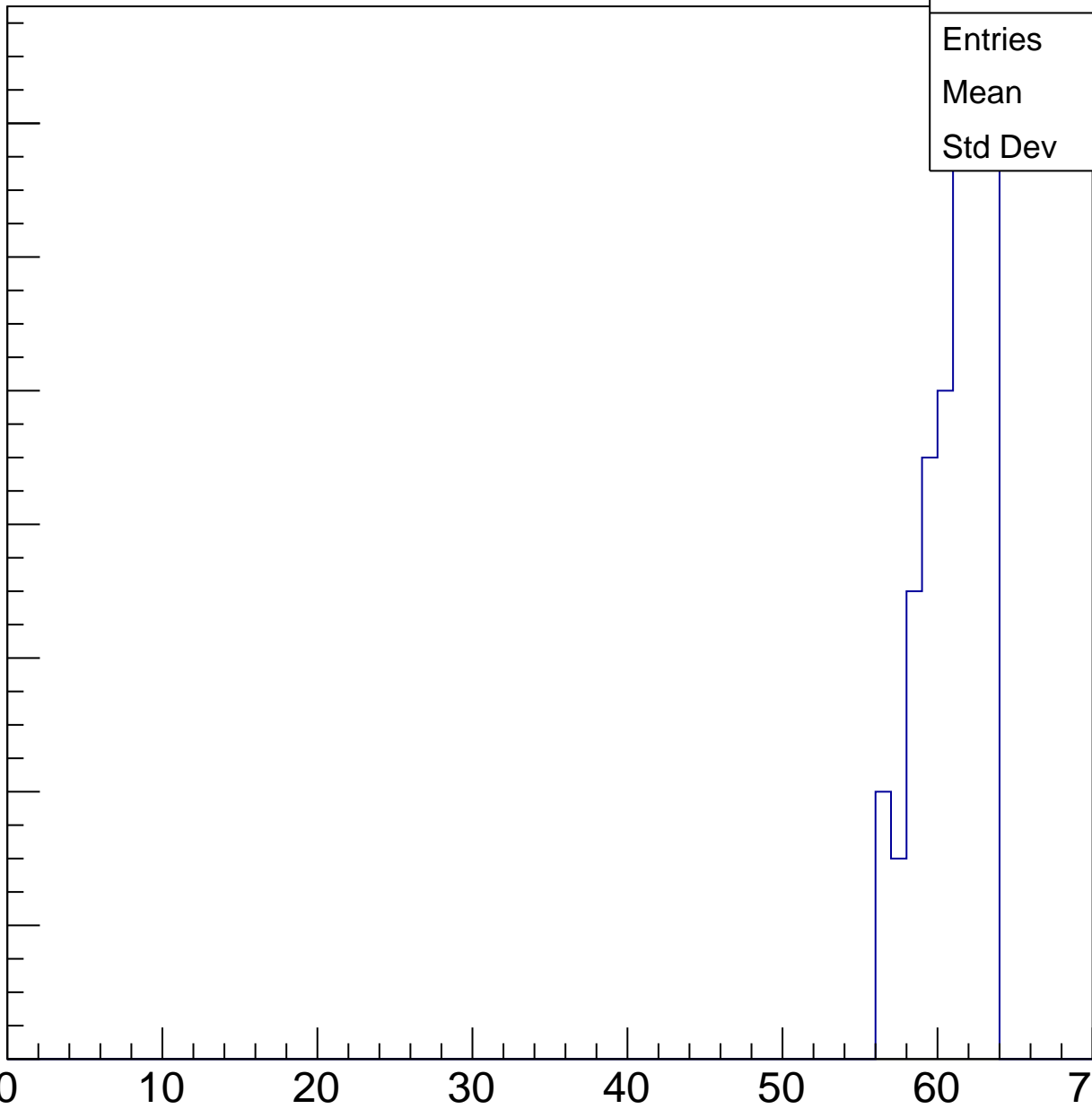
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 60.49 |
| Std Dev | 2.01 |

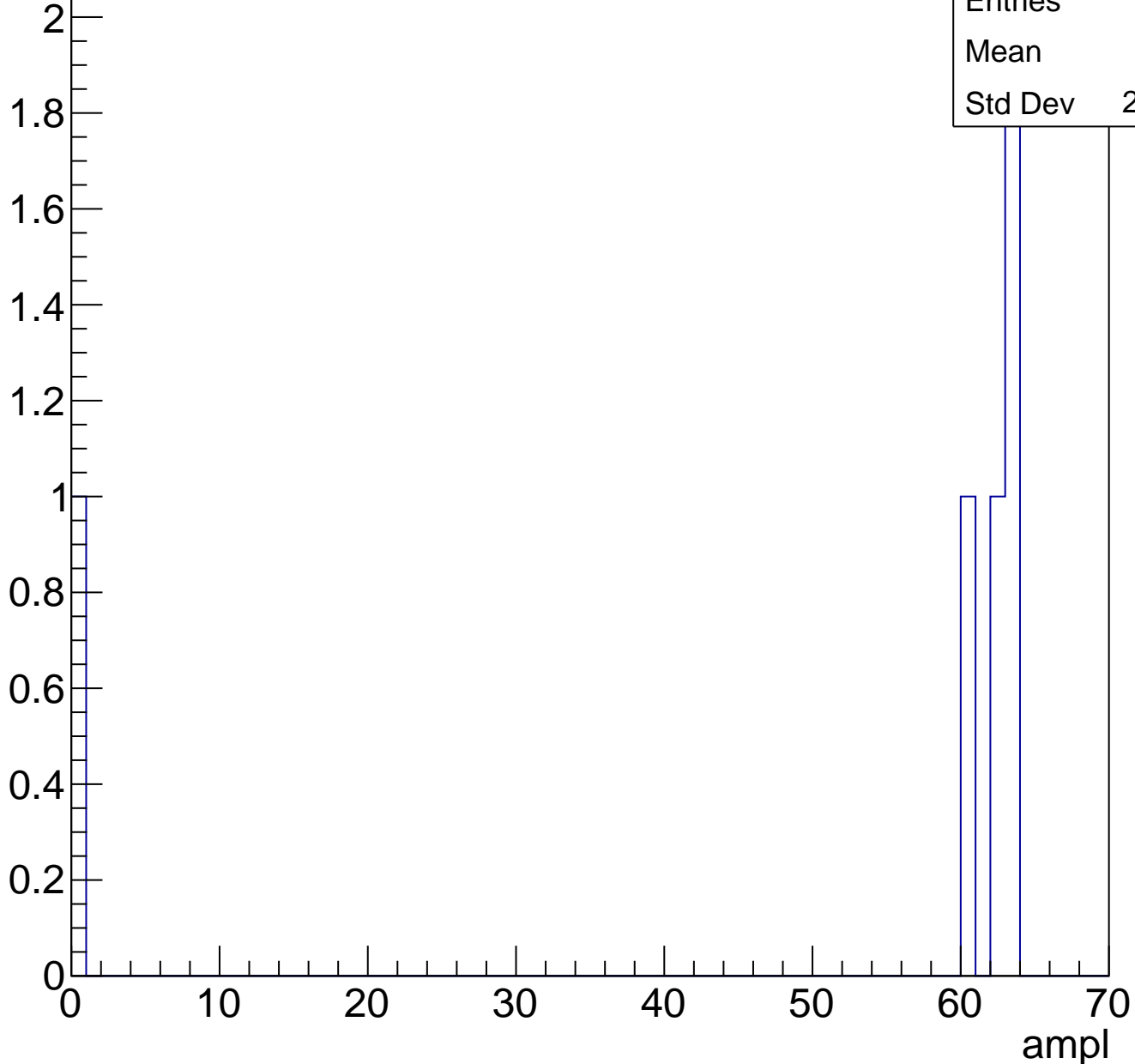
ampl



B0L101S, U22-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 5 |
| Mean | 49.6 |
| Std Dev | 24.82 |

B0L101S, U22-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch120, adc0

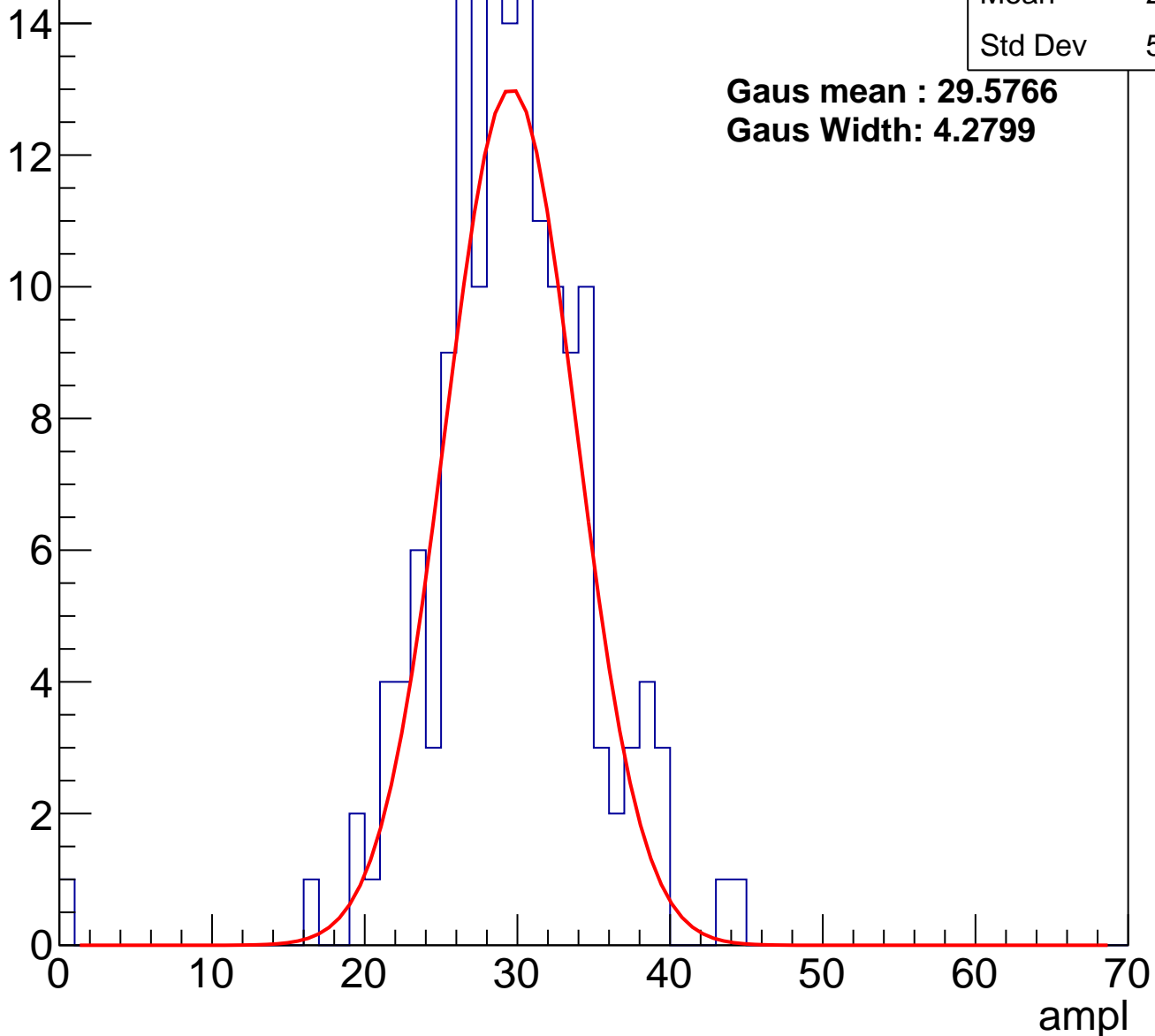
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 157 |
| Mean | 29.04 |
| Std Dev | 5.266 |

Gaus mean : 29.5766

Gaus Width: 4.2799

Entry



B0L101S, U22-ch120, adc1

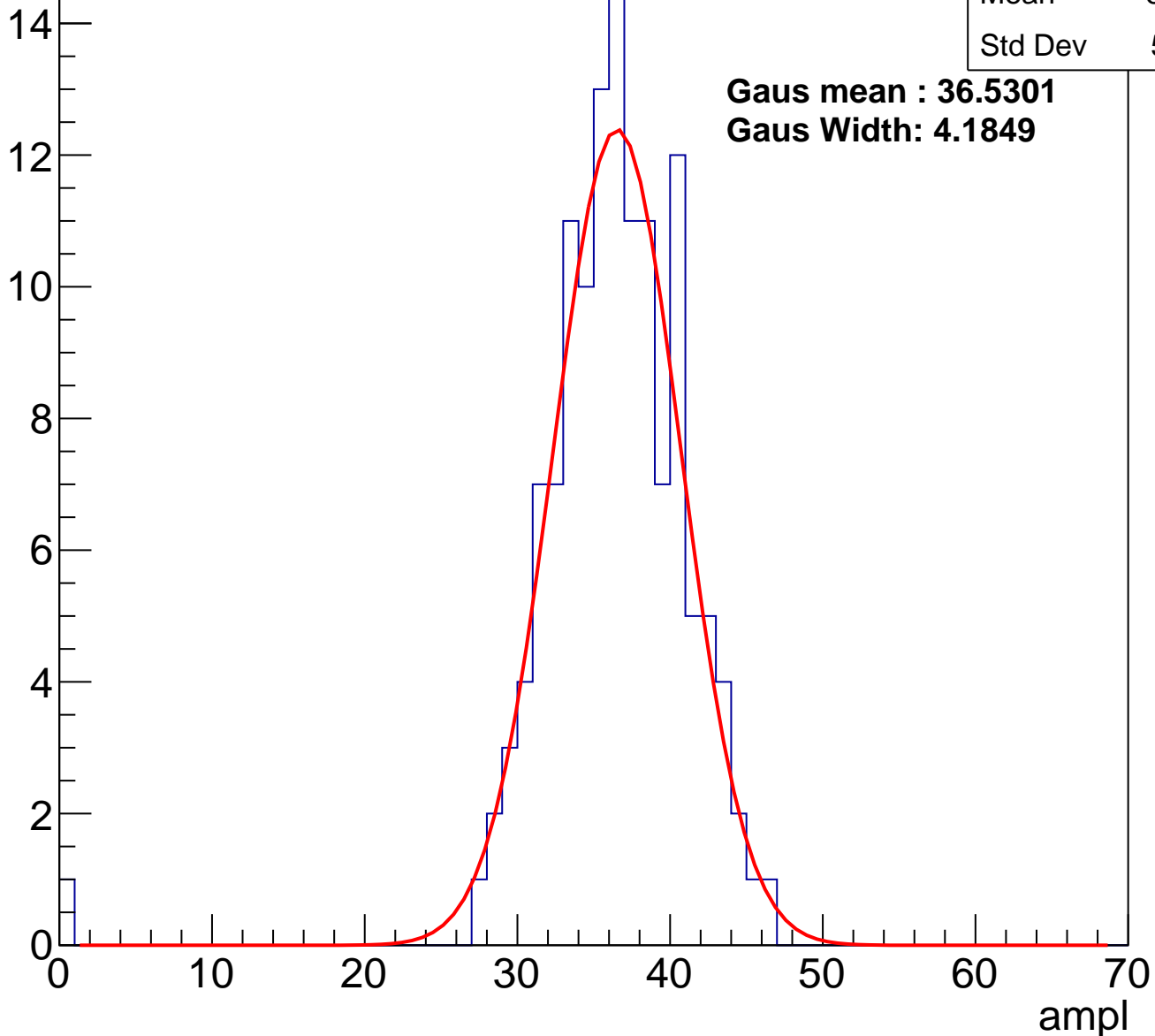
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 133 |
| Mean | 35.89 |
| Std Dev | 5.021 |

Gaus mean : 36.5301

Gaus Width: 4.1849

Entry



B0L101S, U22-ch120, adc2

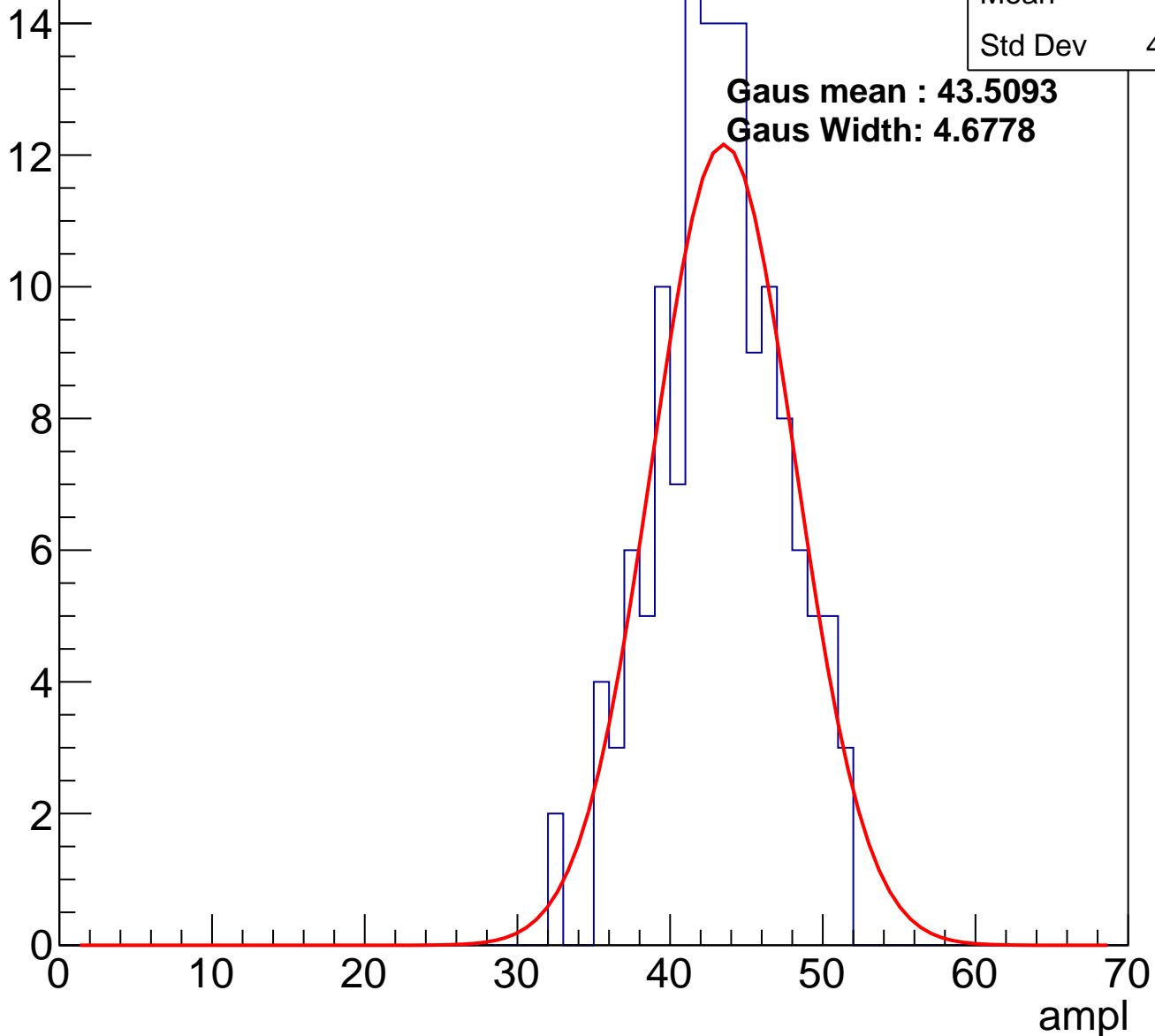
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 140 |
| Mean | 42.8 |
| Std Dev | 4.064 |

Gaus mean : 43.5093

Gaus Width: 4.6778

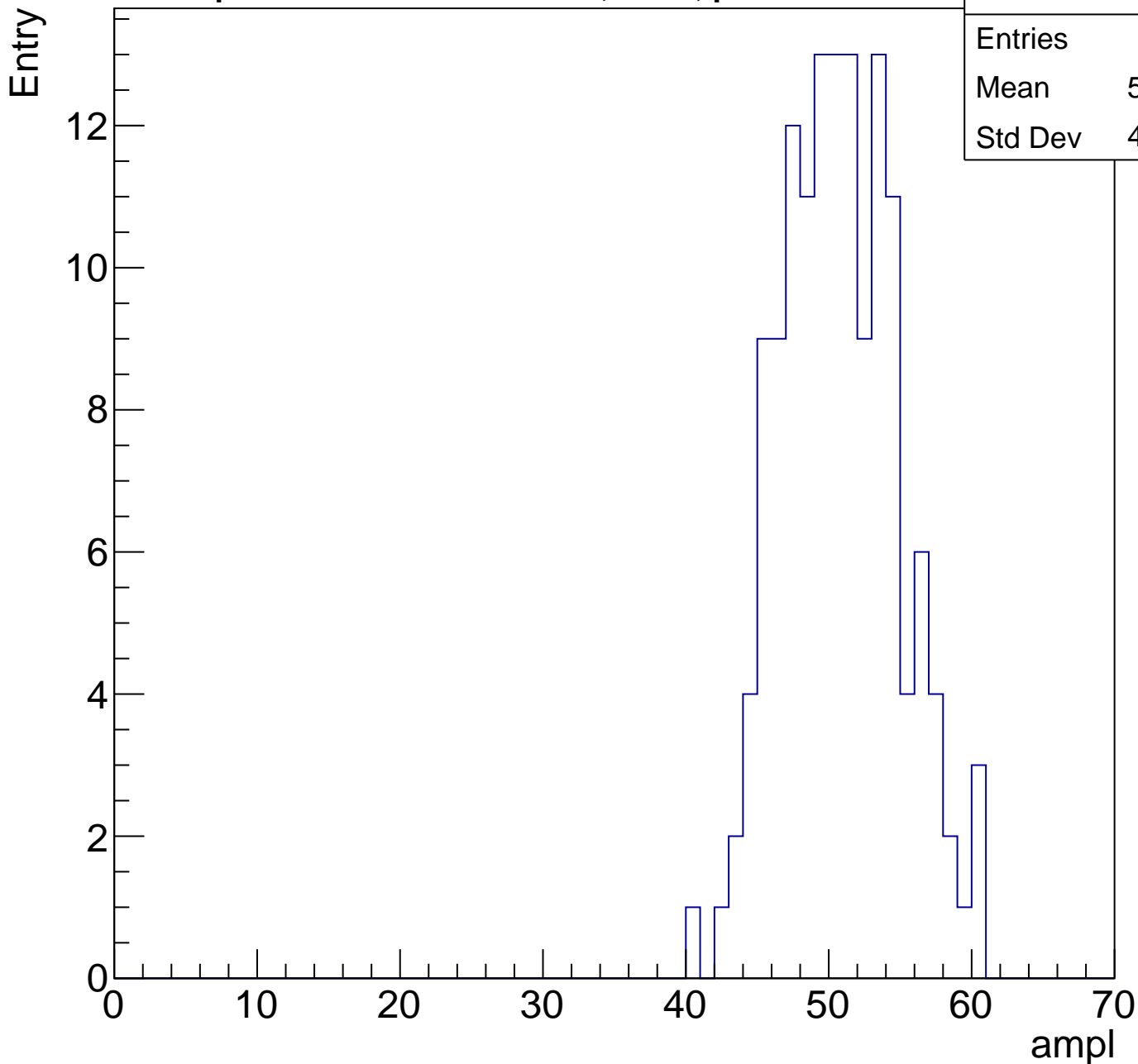
Entry



B0L101S, U22-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 141 |
| Mean | 50.32 |
| Std Dev | 4.029 |



B0L101S, U22-ch120, adc4

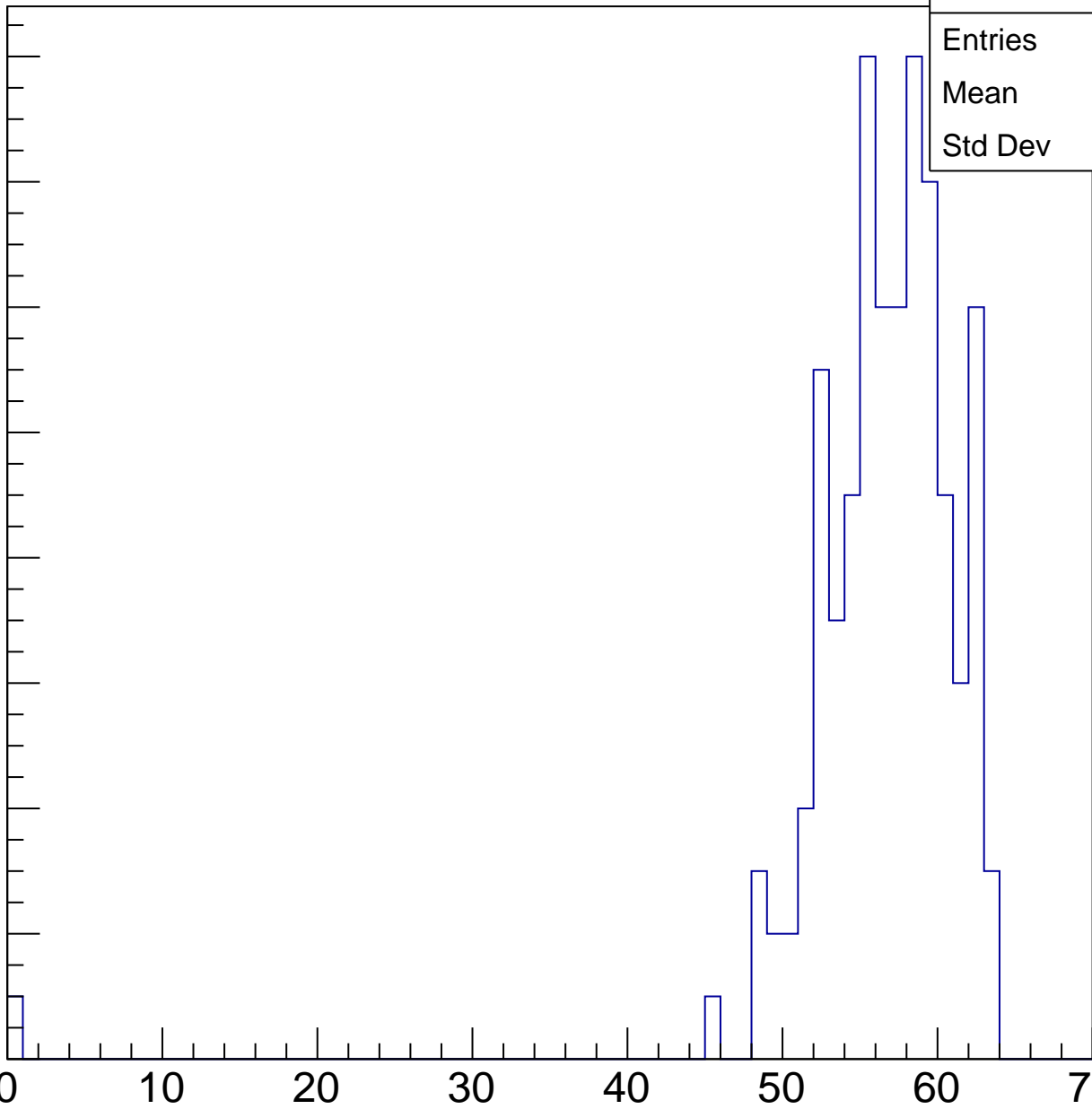
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 140 |
| Mean | 56.06 |
| Std Dev | 6.026 |

ampl



B0L101S, U22-ch120, adc5

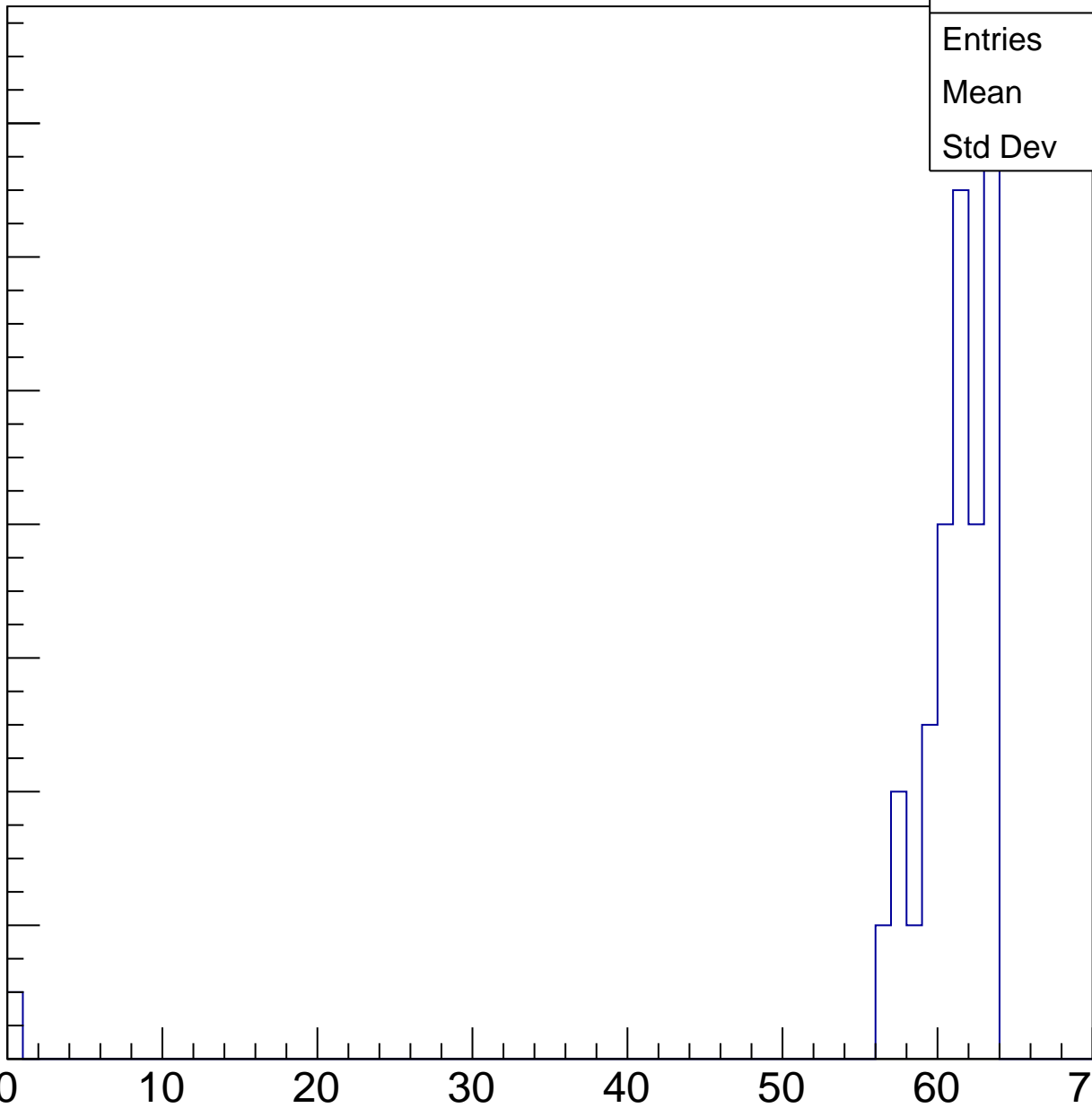
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 59.74 |
| Std Dev | 8.155 |

ampl



B0L101S, U22-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.33 |
| Std Dev | 0.9428 |

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch121, adc0

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 98 |
| Mean | 29.39 |
| Std Dev | 4.117 |

Gaus mean : 30.0370

Gaus Width: 2.8412

Entry

12

10

8

6

4

2

0

0

10

20

30

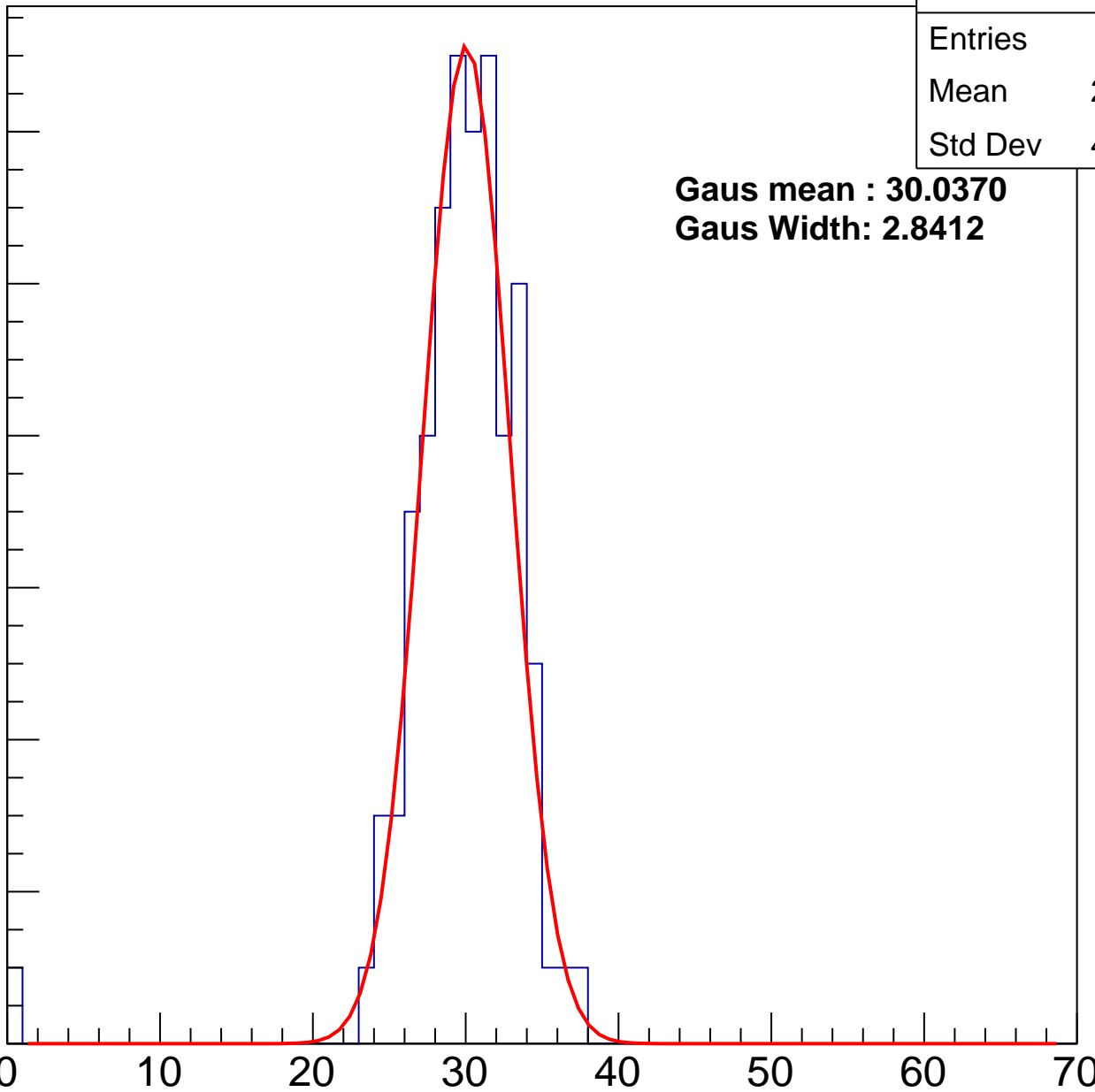
40

50

60

70

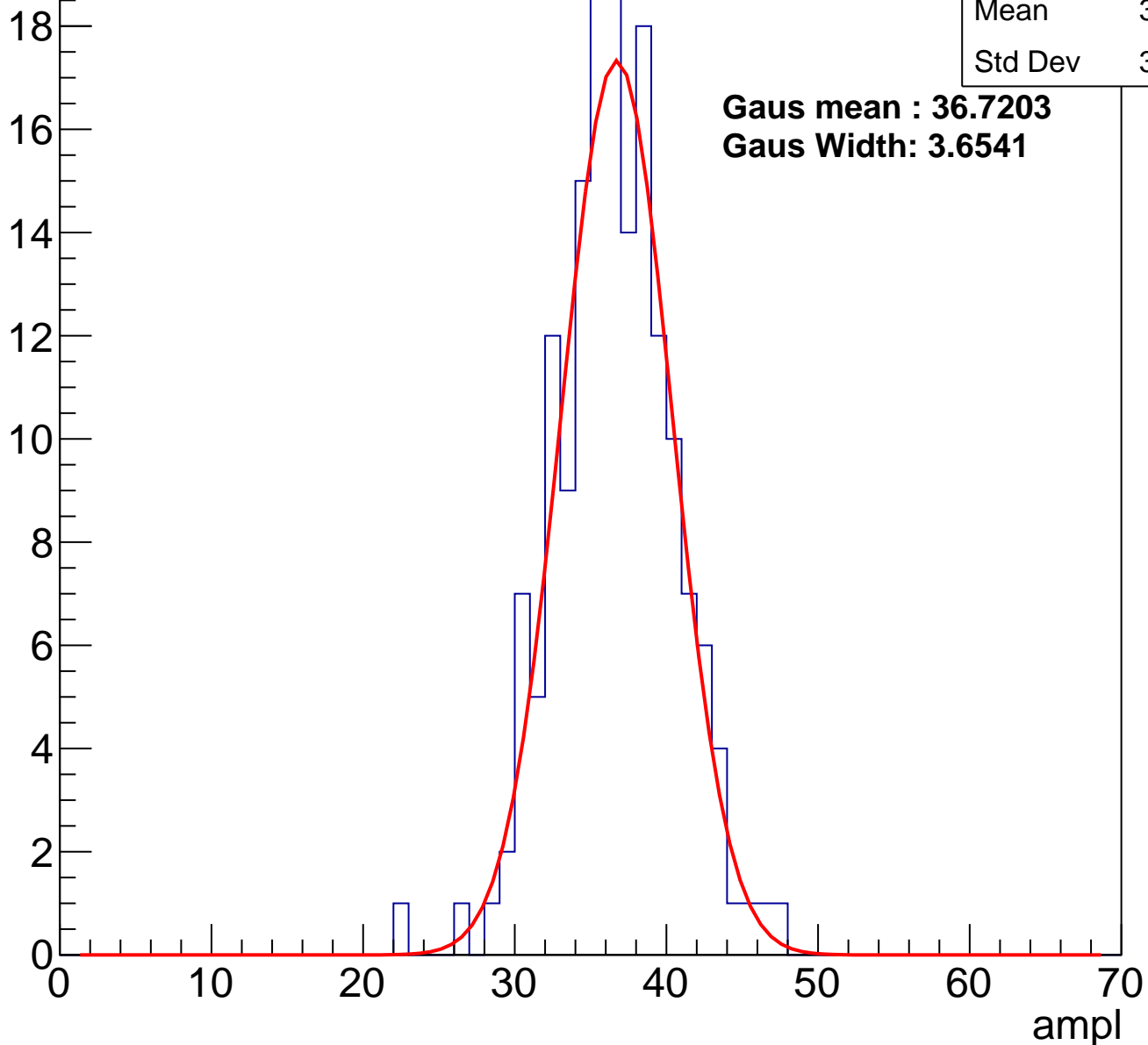
ampl



B0L101S, U22-ch121, adc1

calib_packv5_042523_0143.root, FC#1, port C1

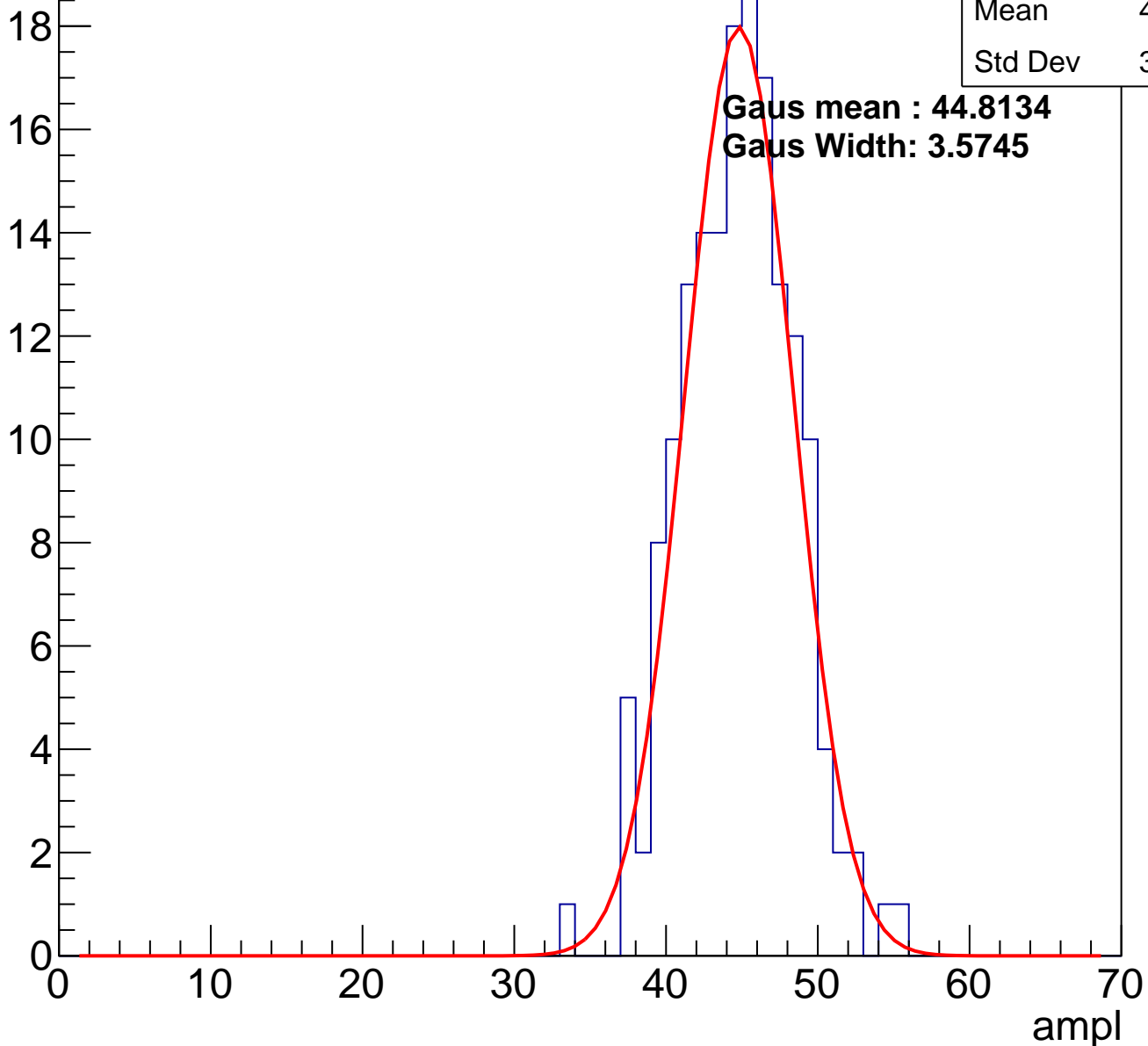
Entry



B0L101S, U22-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch121, adc3

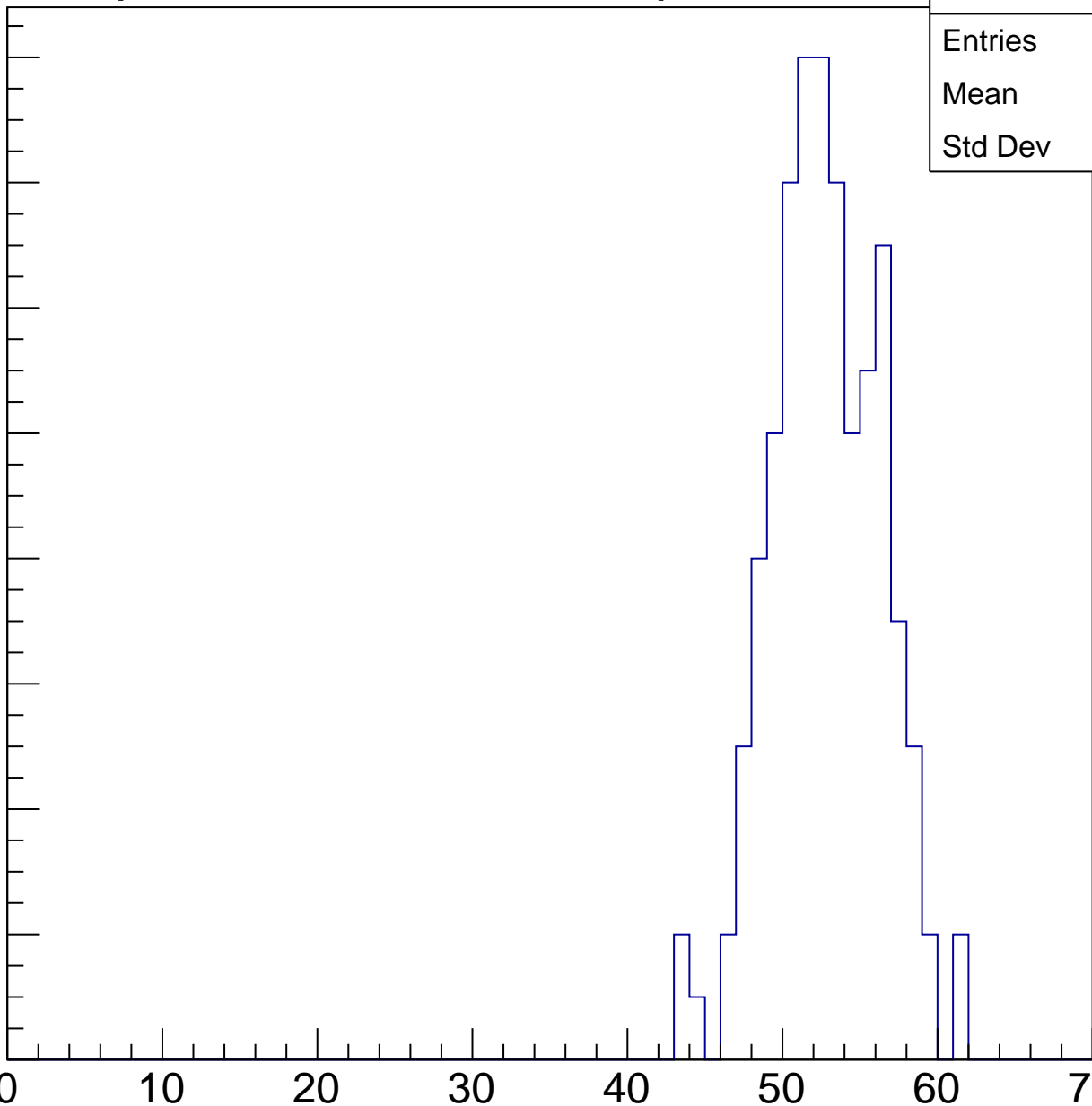
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 138 |
| Mean | 52.34 |
| Std Dev | 3.482 |

ampl



B0L101S, U22-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

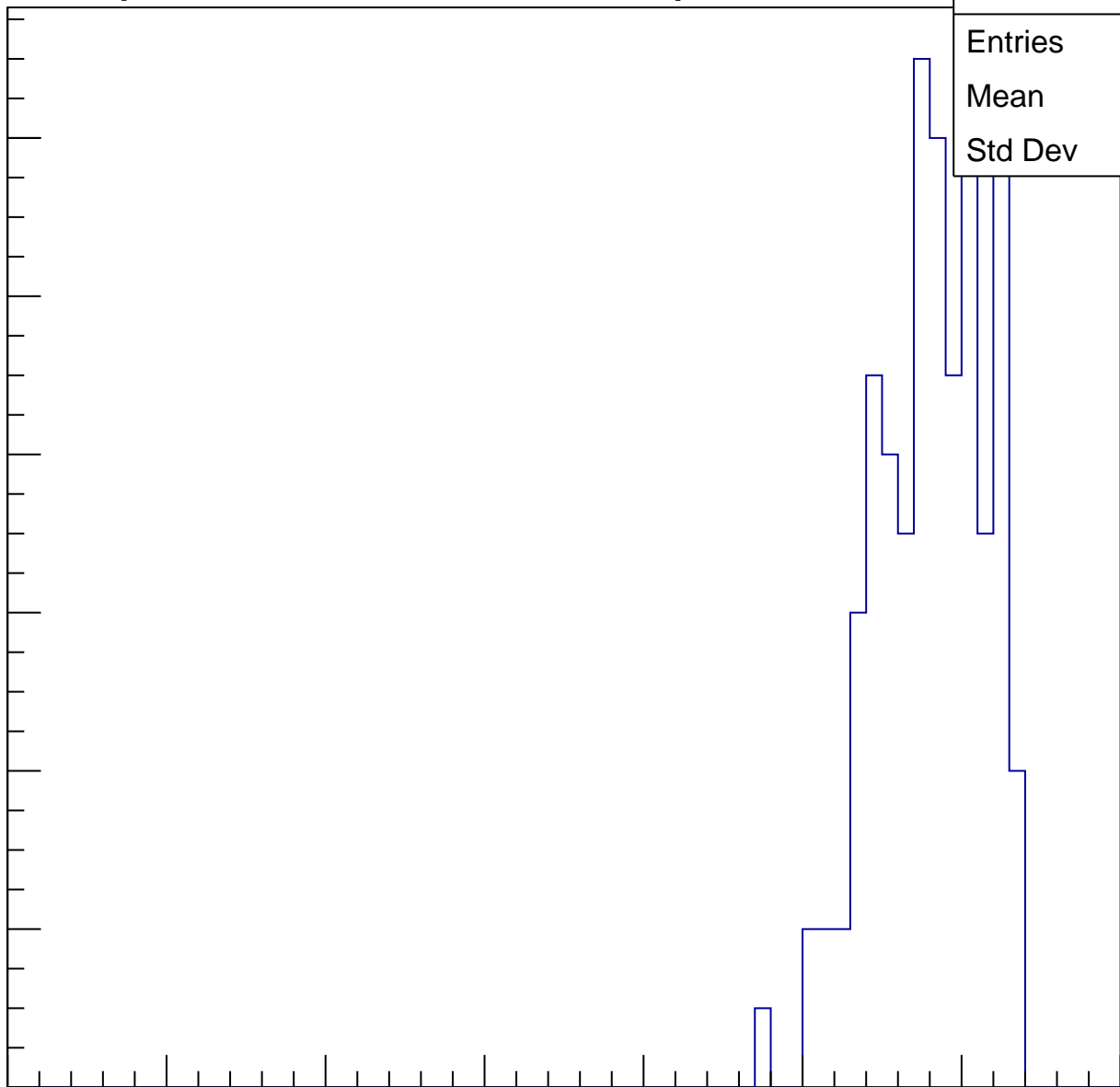
2

0

0 10 20 30 40 50 60 70

ampl

| | |
|---------|-------|
| Entries | 108 |
| Mean | 57.61 |
| Std Dev | 3.391 |



B0L101S, U22-ch121, adc5

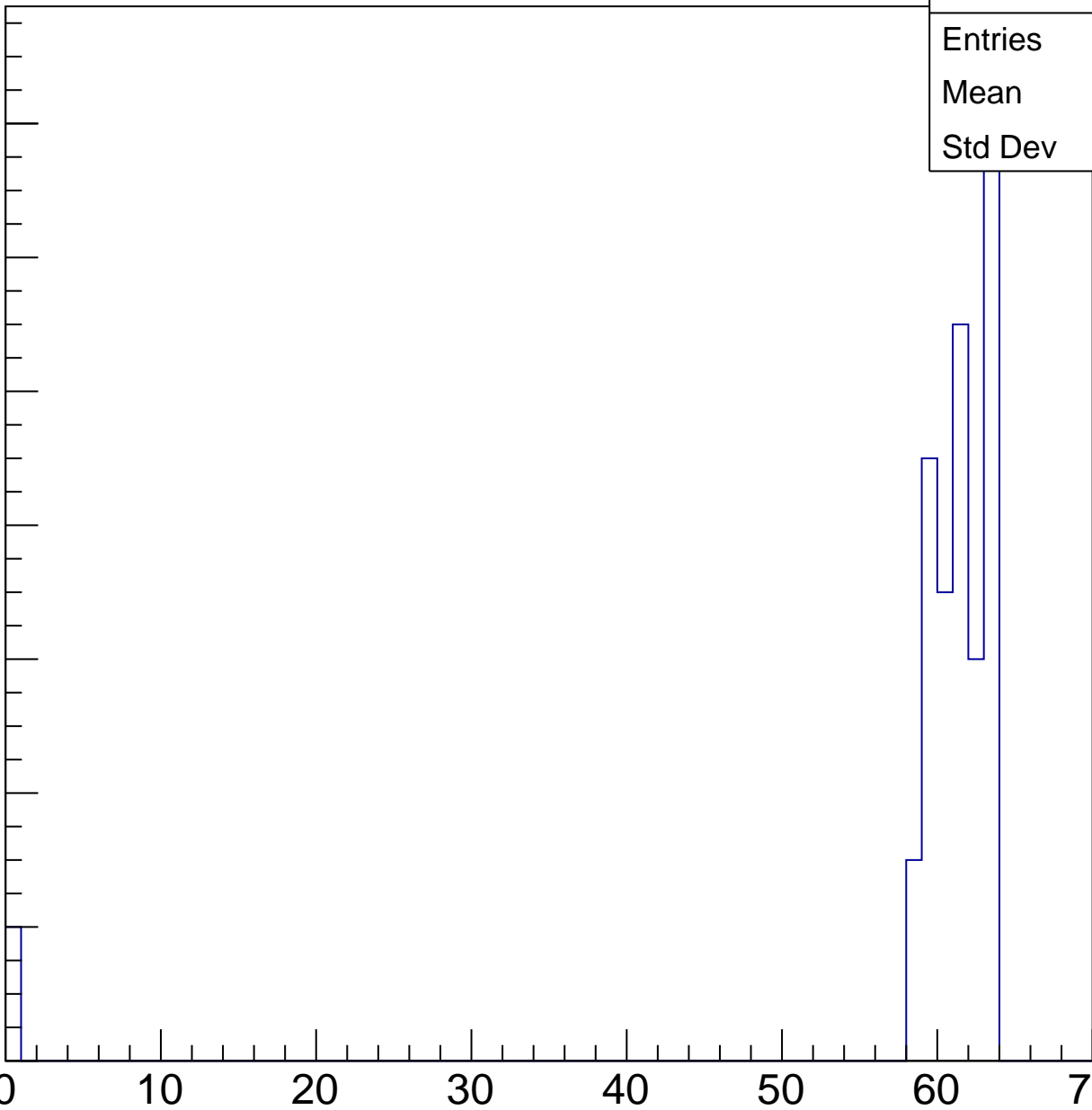
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.74 |
| Std Dev | 11.74 |

ampl



B0L101S, U22-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

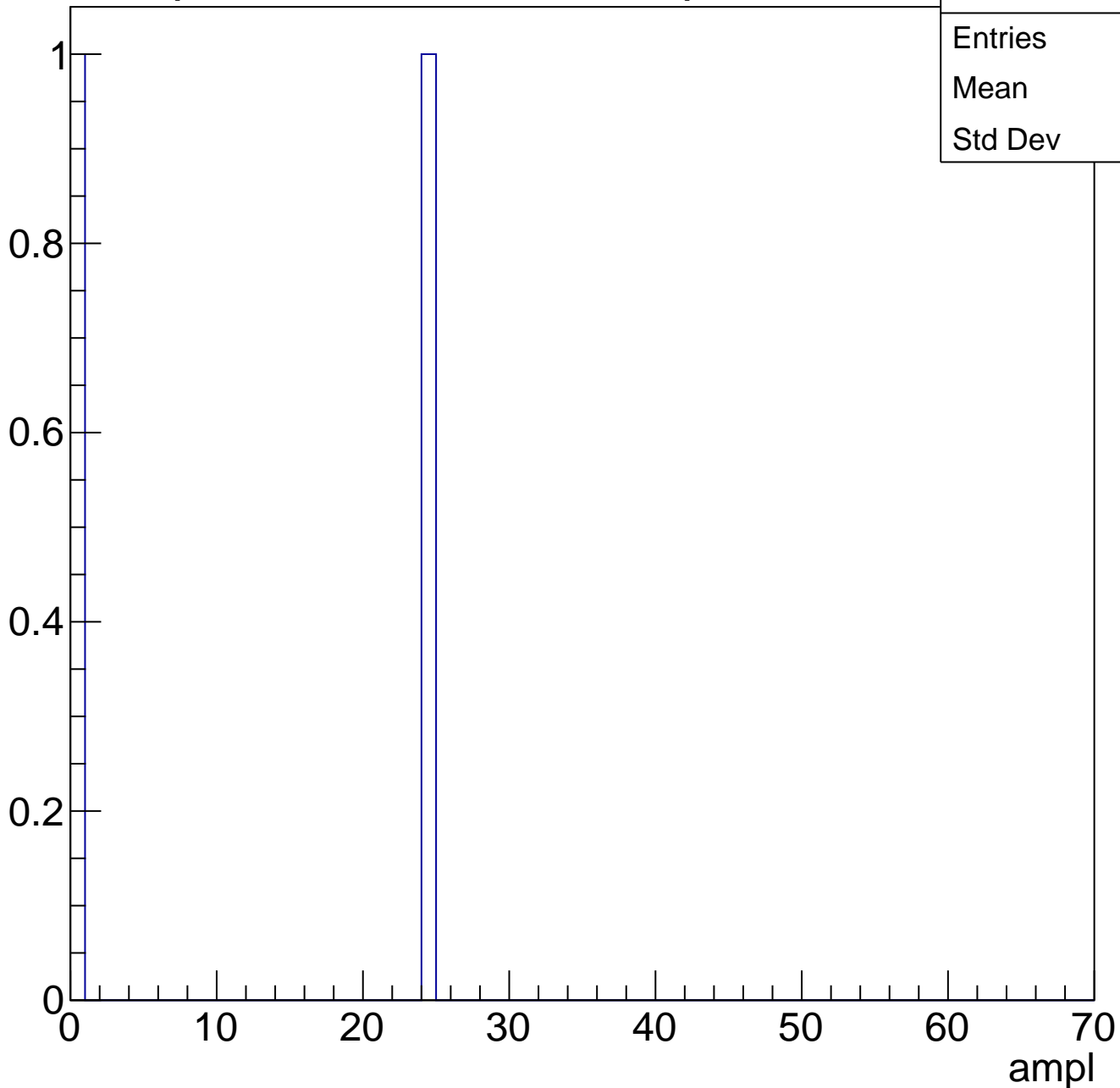
Entry



B0L101S, U22-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch122, adc0

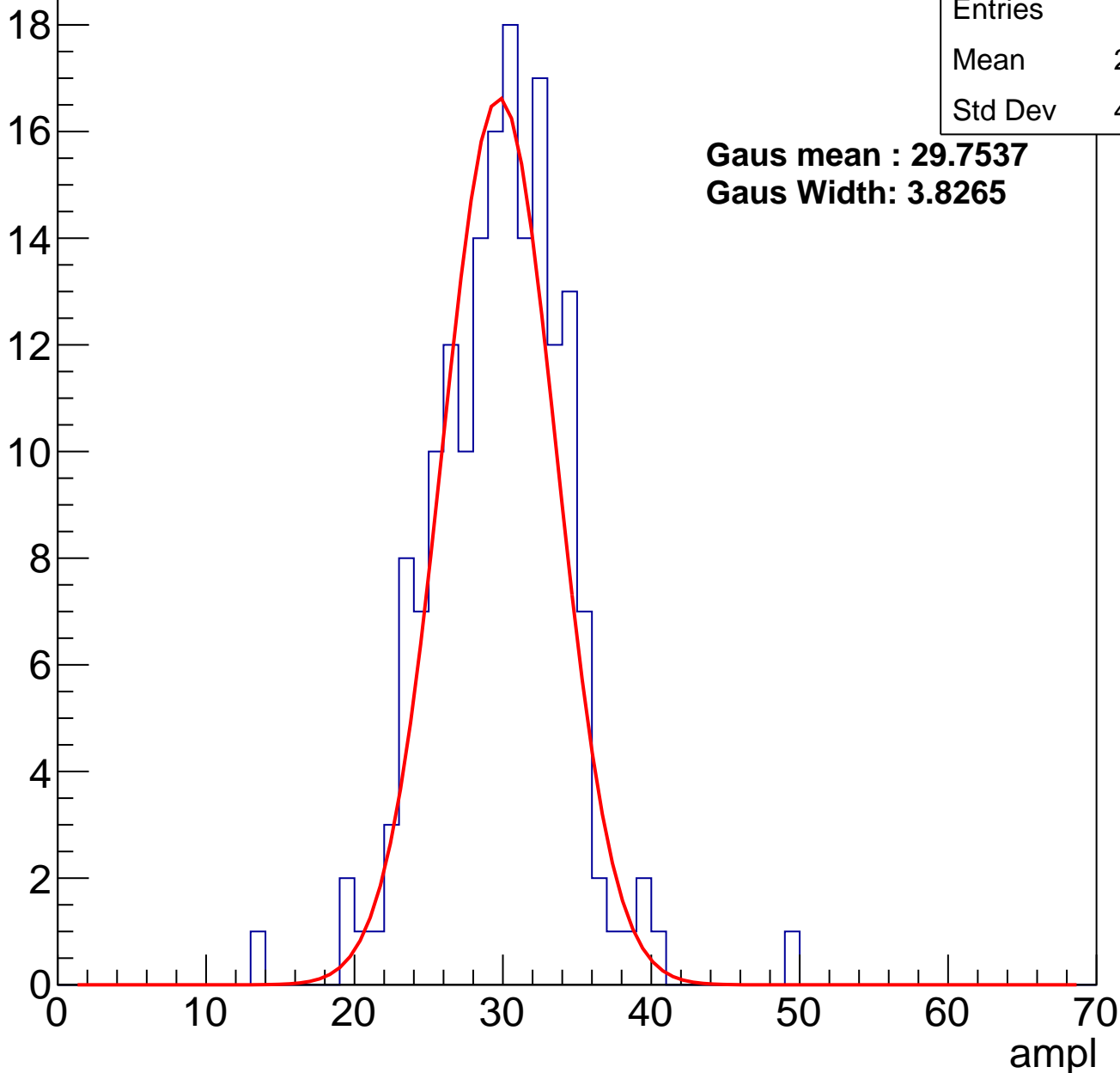
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 174 |
| Mean | 29.39 |
| Std Dev | 4.464 |

Gaus mean : 29.7537

Gaus Width: 3.8265

Entry



B0L101S, U22-ch122, adc1

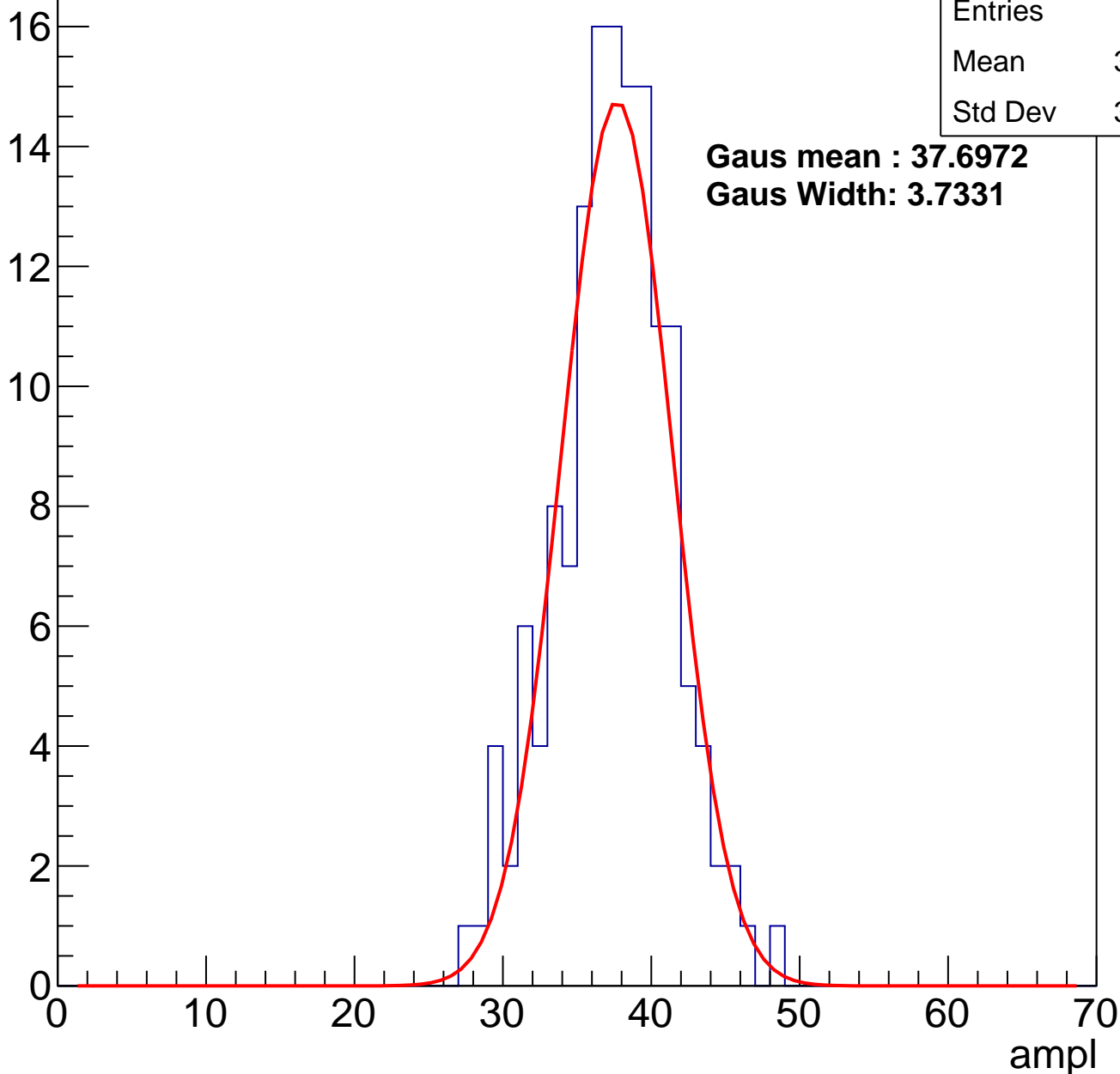
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 37.03 |
| Std Dev | 3.857 |

Gaus mean : 37.6972

Gaus Width: 3.7331

Entry



B0L101S, U22-ch122, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

110

Mean

43.95

Std Dev

3.667

Gaus mean : 44.3862

Gaus Width: 3.8095

0

10

20

30

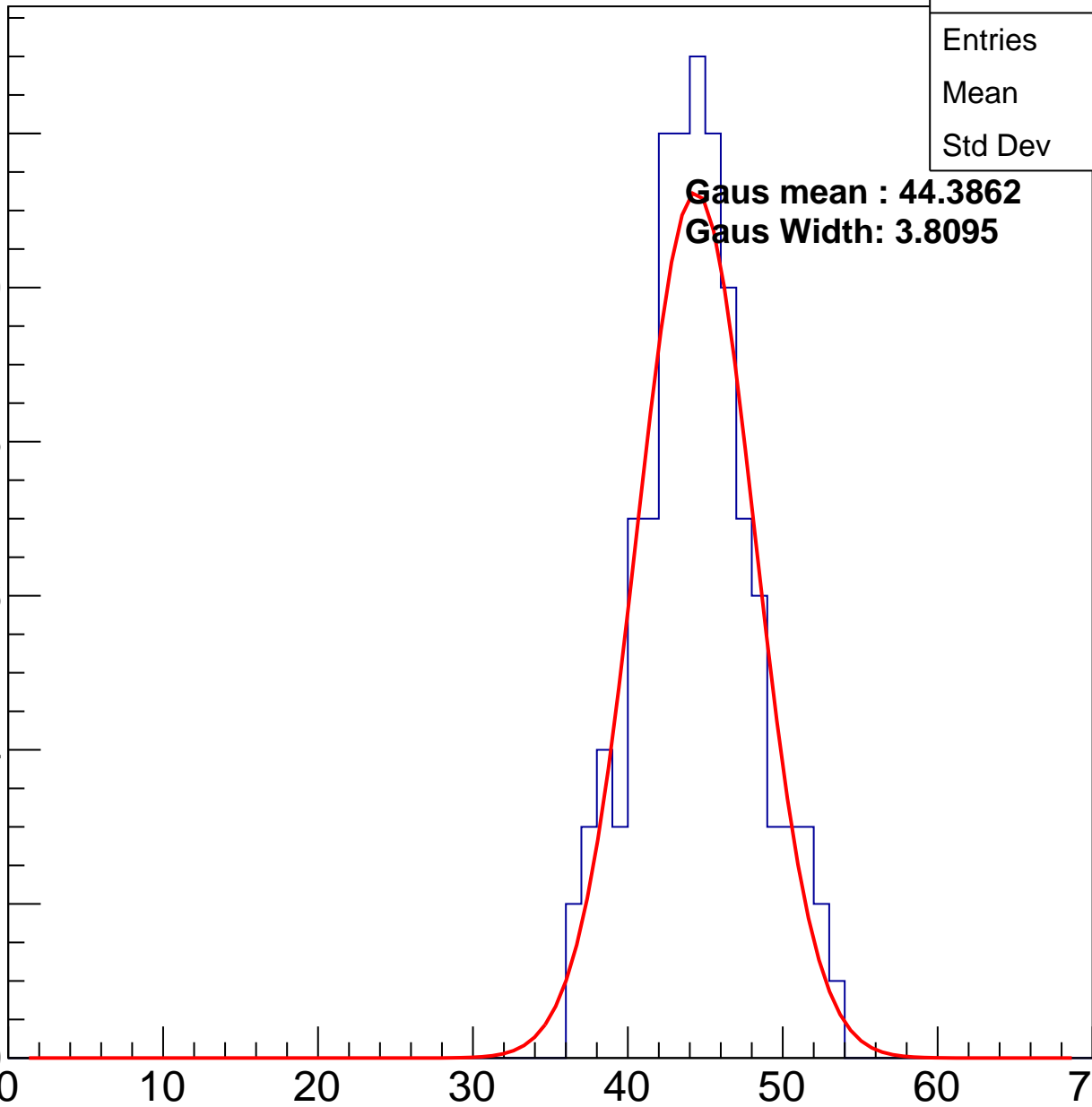
40

50

60

70

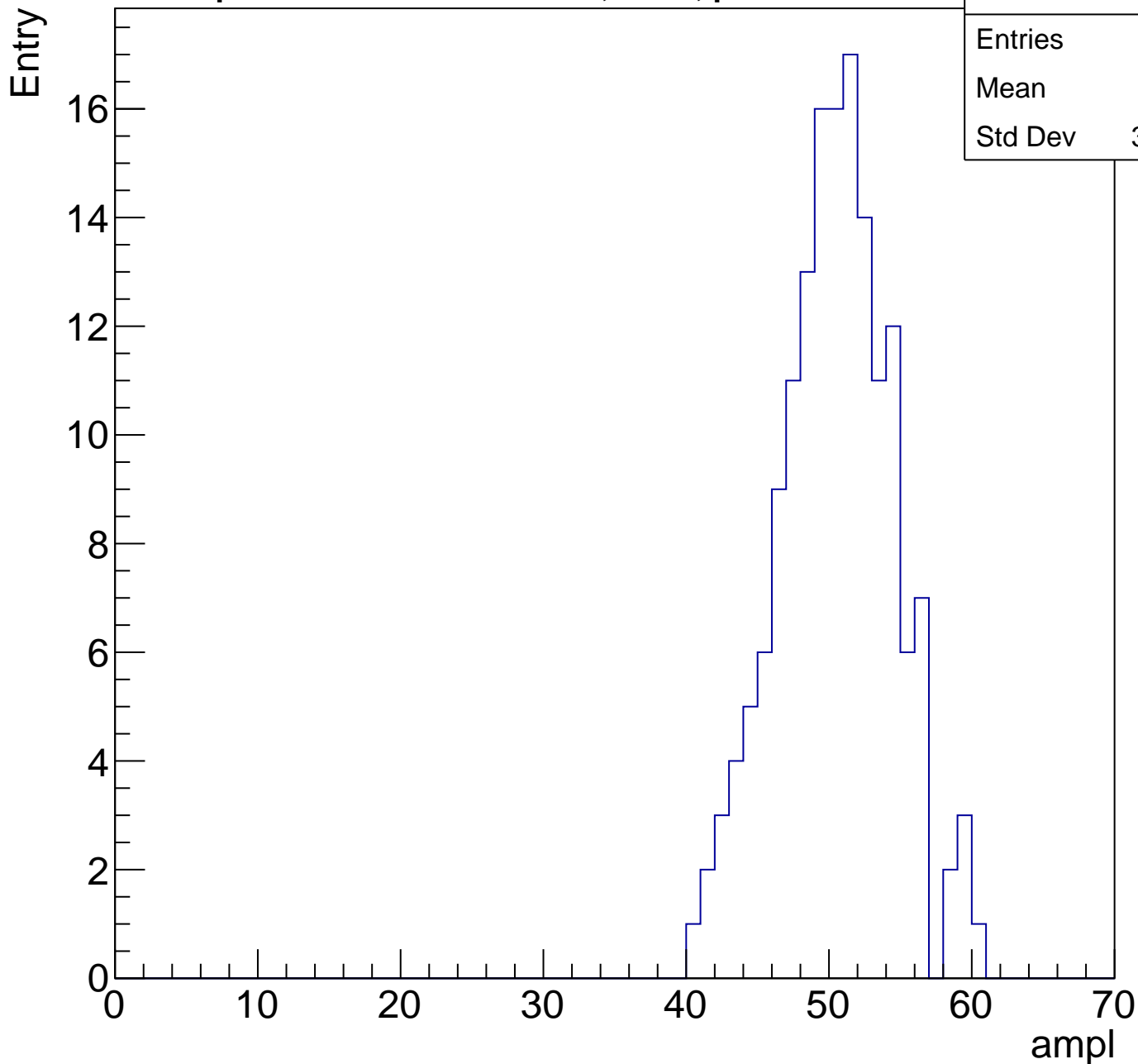
ampl



B0L101S, U22-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 159 |
| Mean | 50 |
| Std Dev | 3.987 |



B0L101S, U22-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

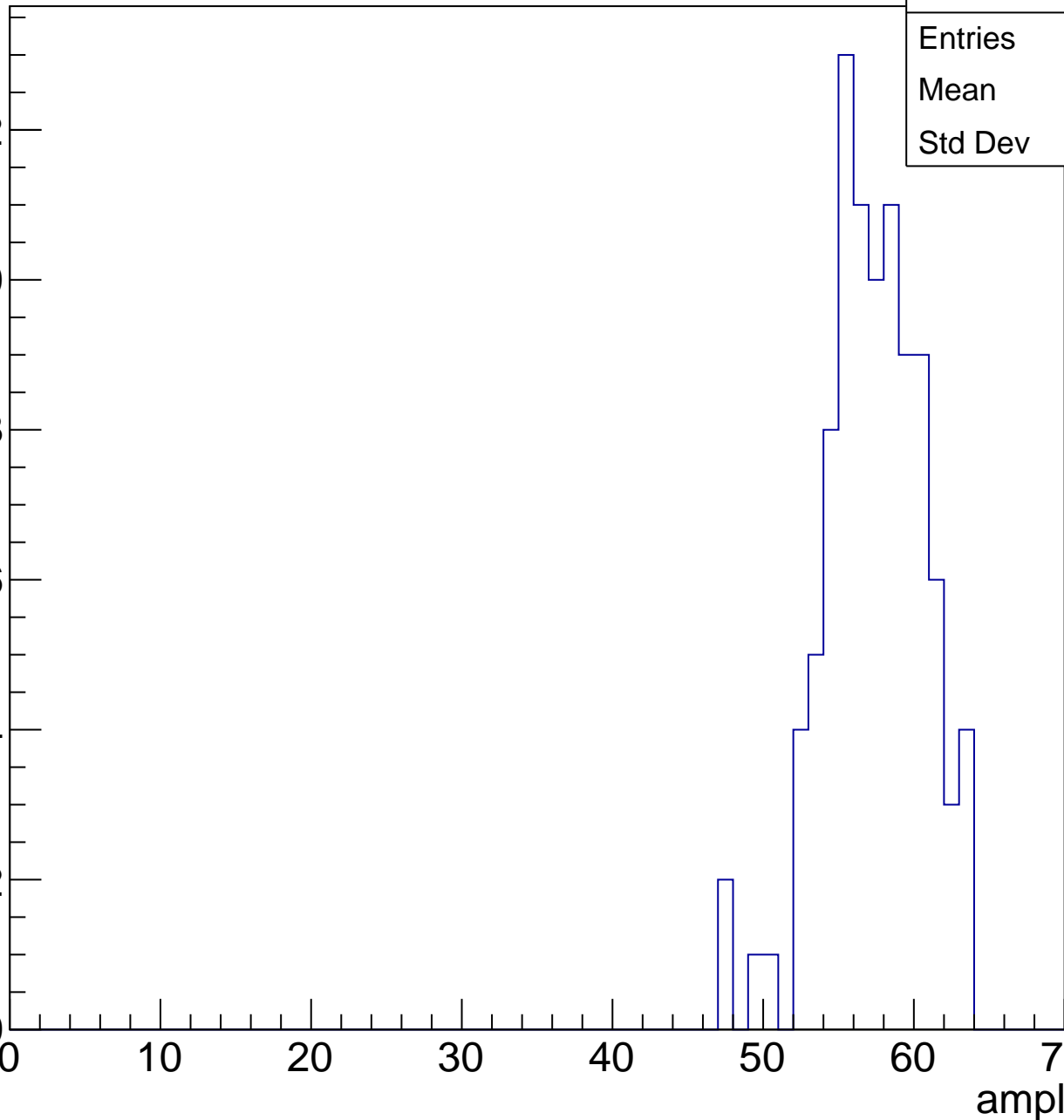
50

60

70

ampl

| | |
|---------|-------|
| Entries | 97 |
| Mean | 56.82 |
| Std Dev | 3.318 |



B0L101S, U22-ch122, adc5

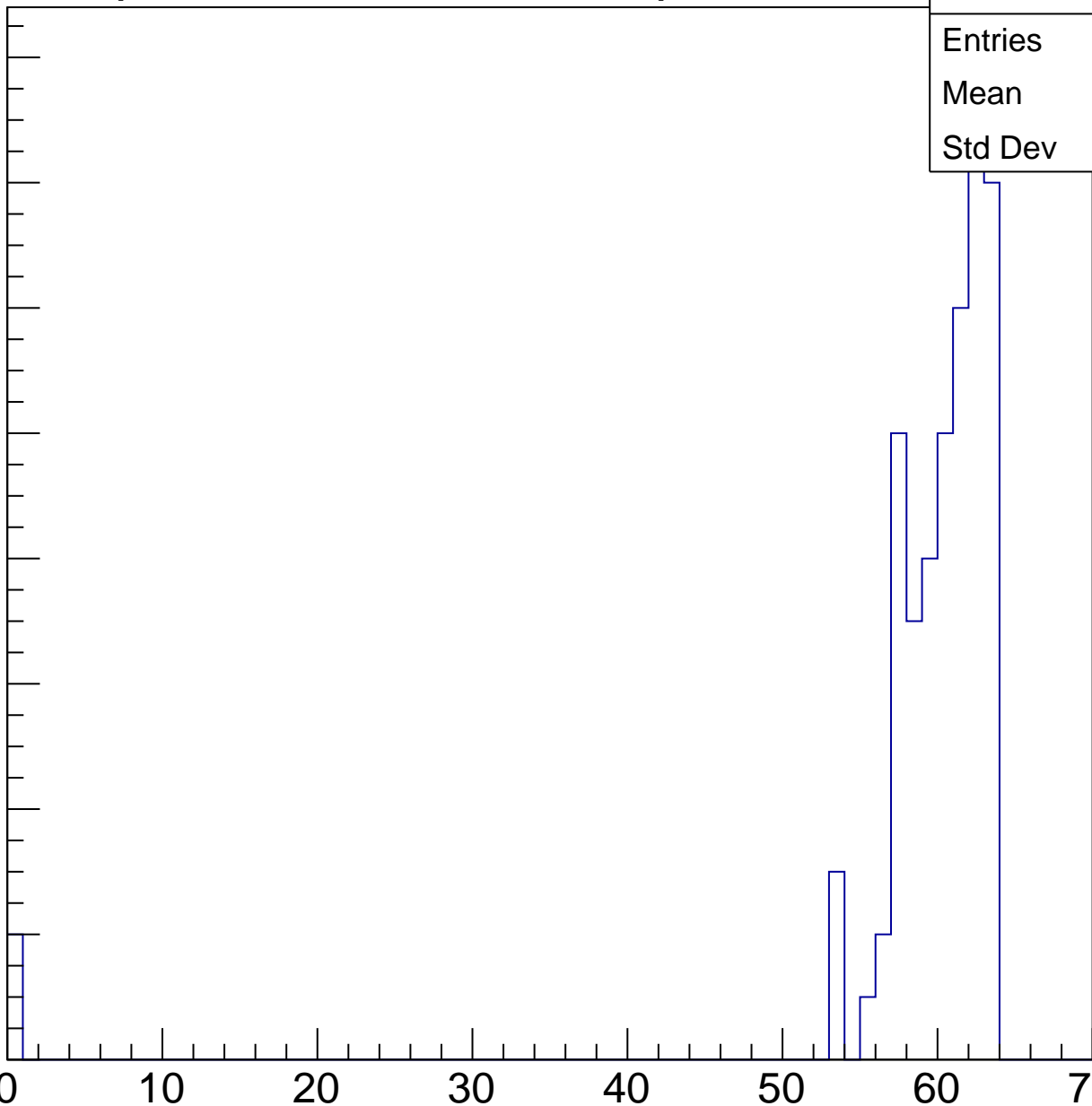
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 58.59 |
| Std Dev | 9.432 |

ampl



B0L101S, U22-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

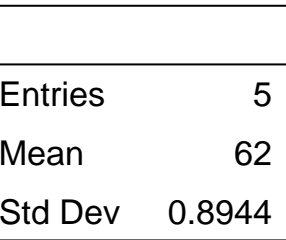
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62 |
| Std Dev | 0.8944 |

0 10 20 30 40 50 60 70

ampl



B0L101S, U22-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch123, adc0

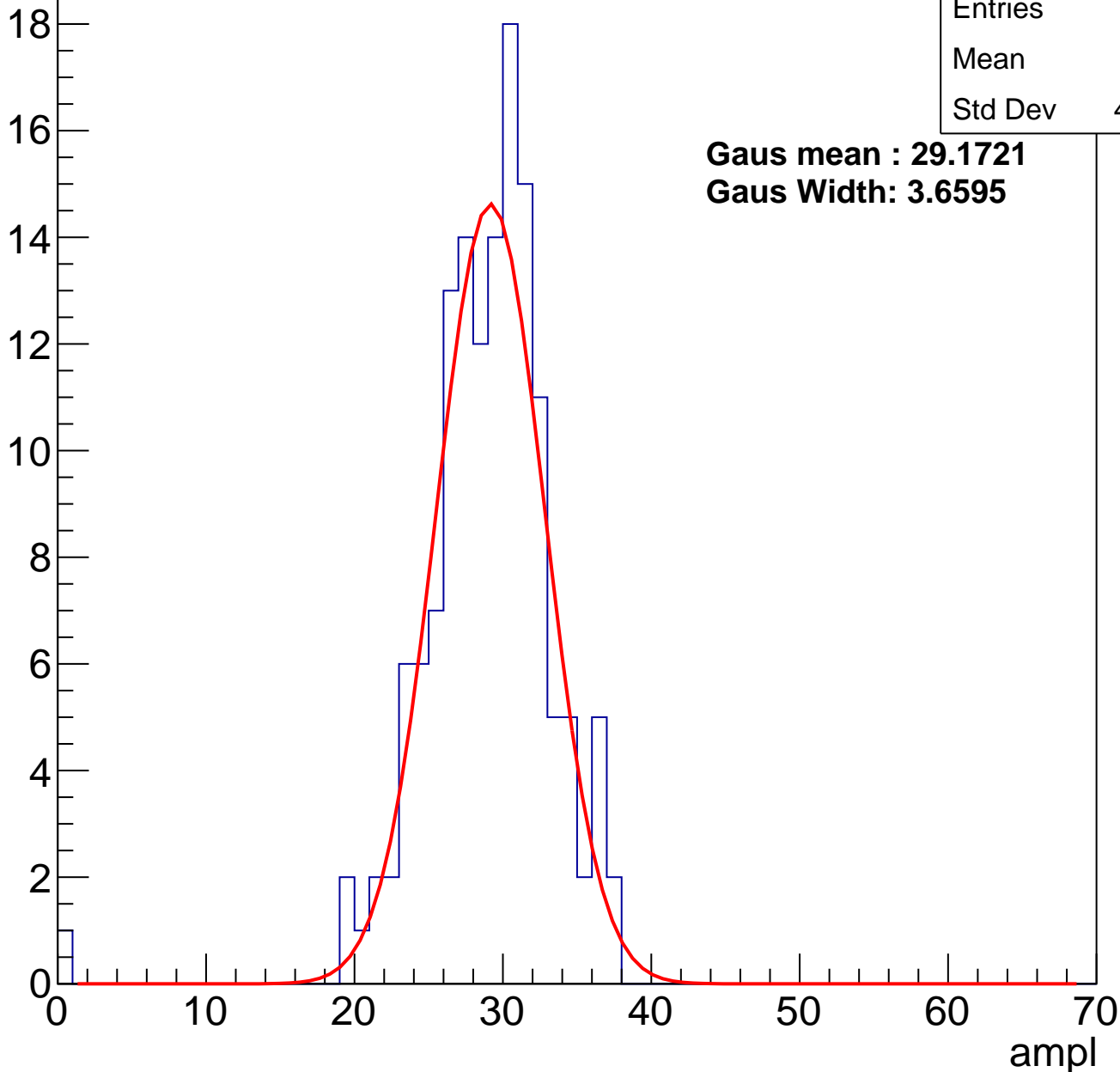
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 143 |
| Mean | 28.5 |
| Std Dev | 4.426 |

Gaus mean : 29.1721

Gaus Width: 3.6595

Entry



B0L101S, U22-ch123, adc1

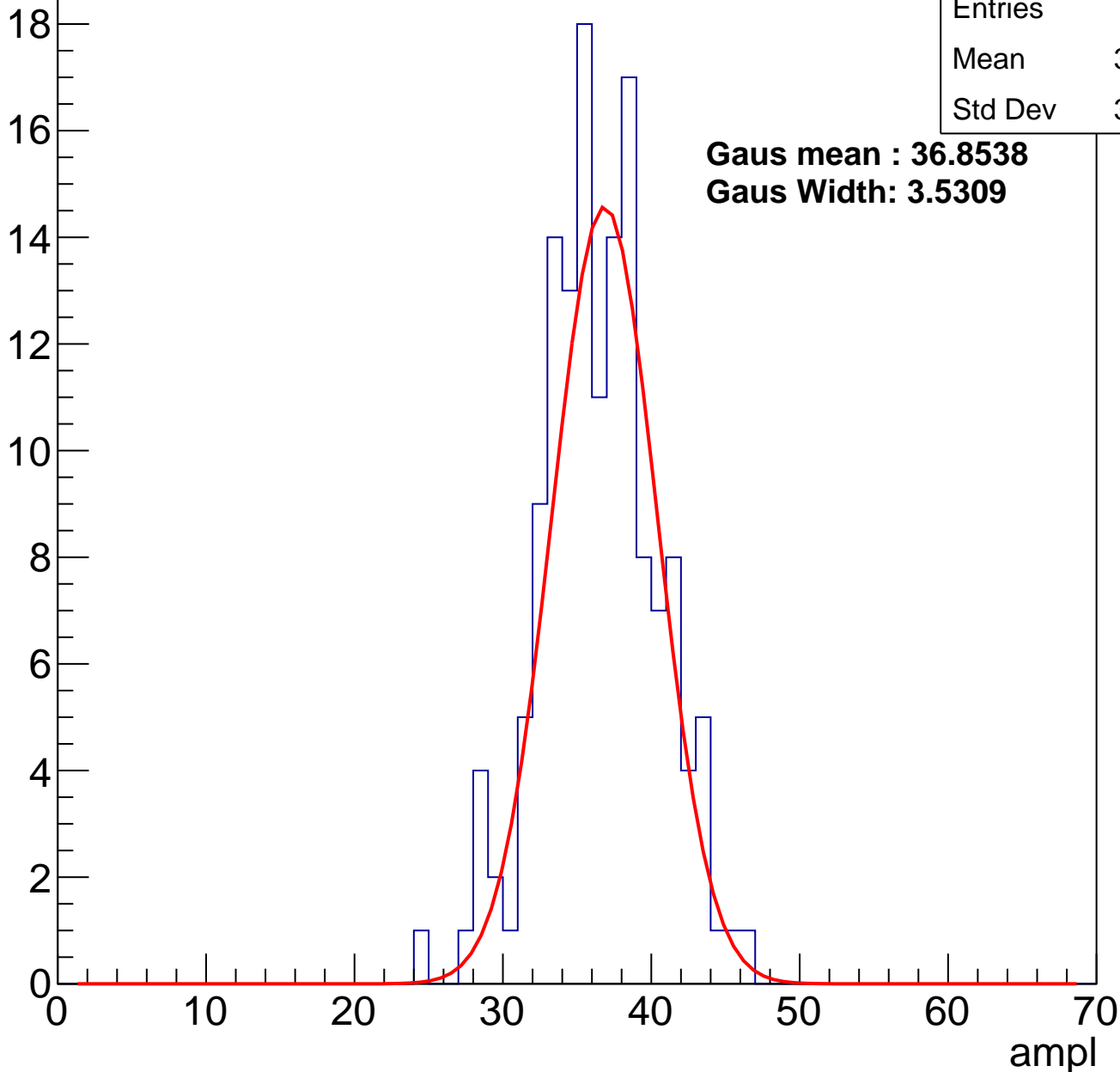
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 145 |
| Mean | 36.04 |
| Std Dev | 3.877 |

Gaus mean : 36.8538

Gaus Width: 3.5309

Entry



B0L101S, U22-ch123, adc2

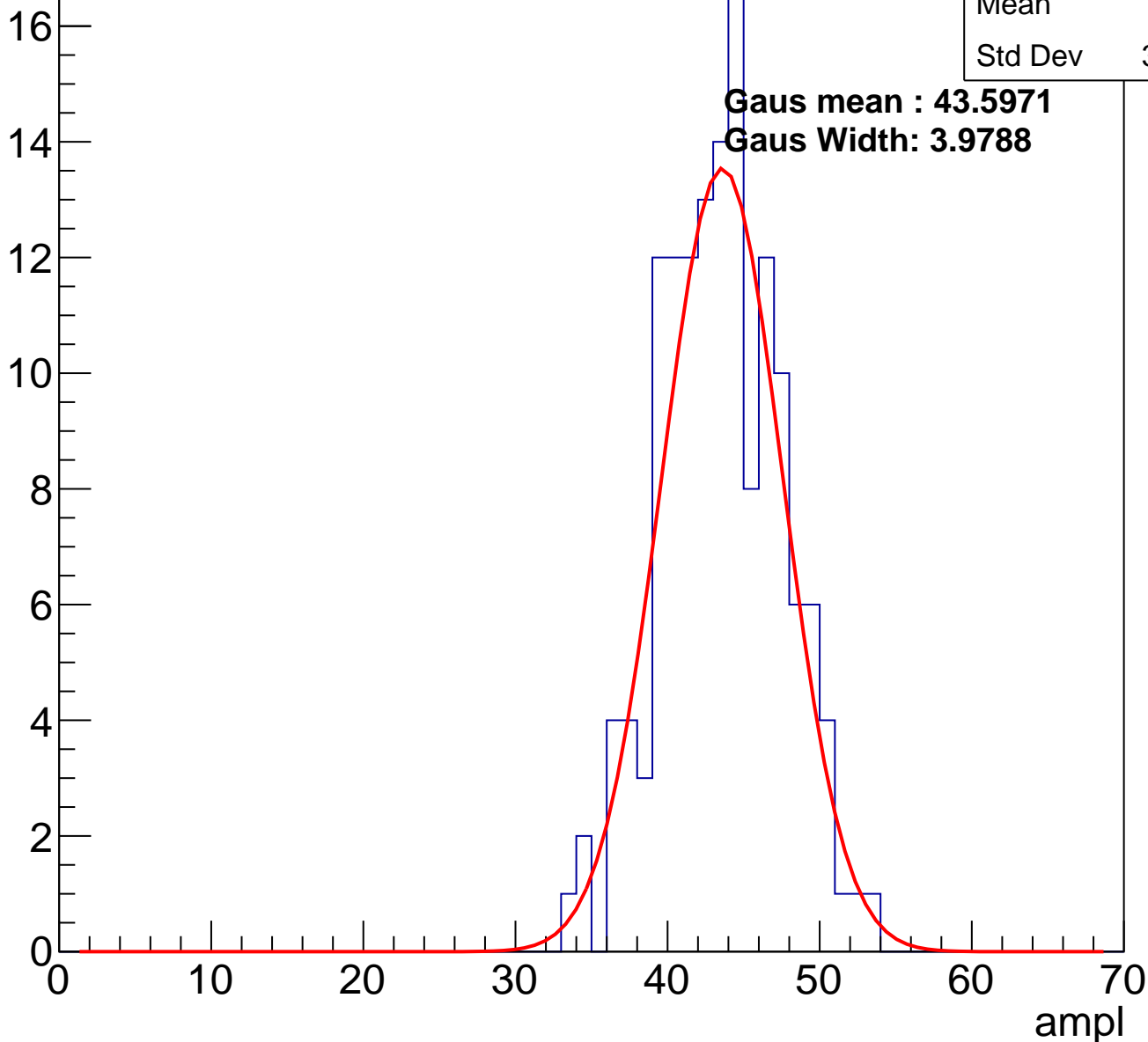
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 143 |
| Mean | 43.1 |
| Std Dev | 3.887 |

Gaus mean : 43.5971

Gaus Width: 3.9788



B0L101S, U22-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

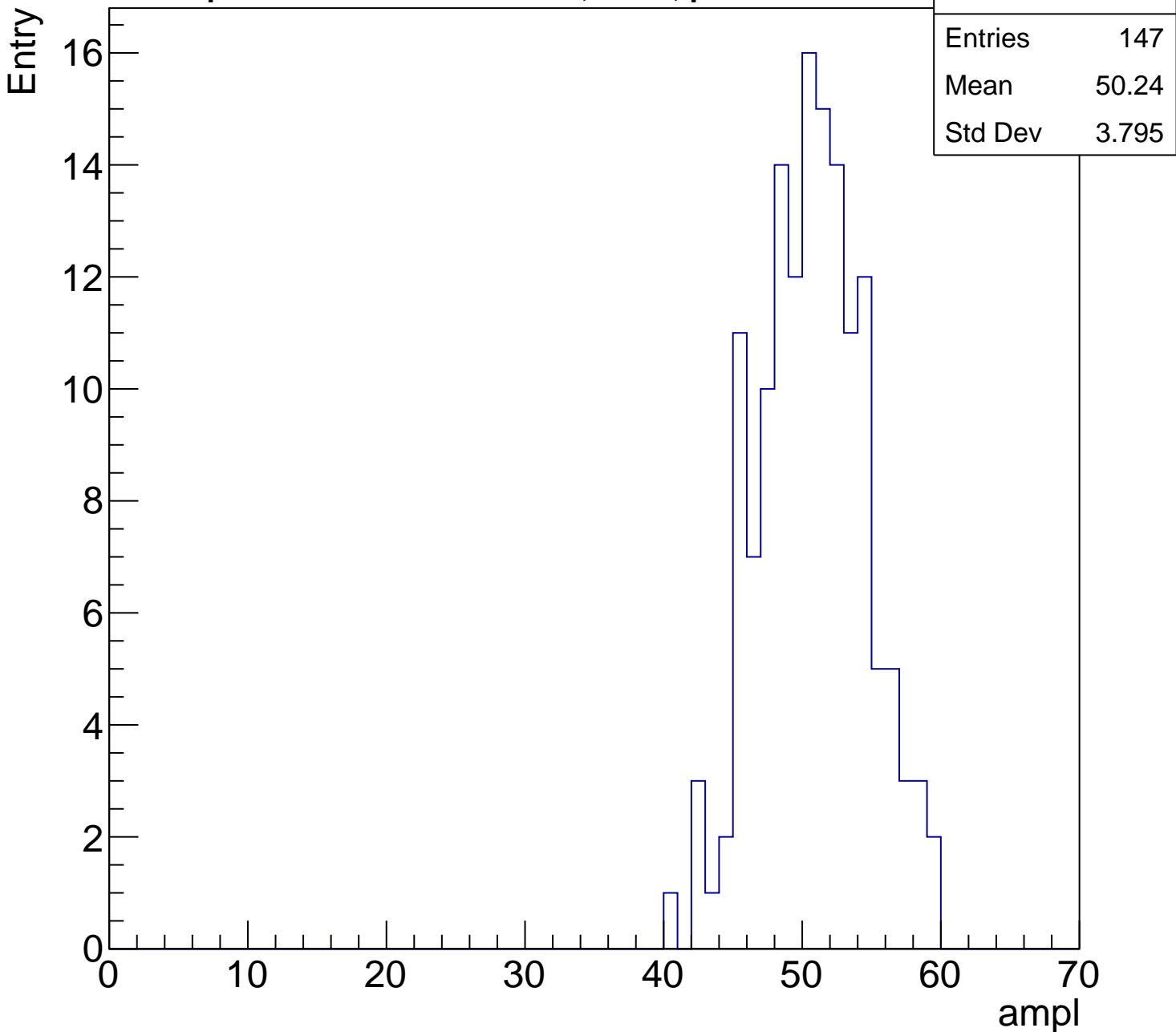
| | |
|---------|-------|
| Entries | 147 |
| Mean | 50.24 |
| Std Dev | 3.795 |

Entry

16
14
12
10
8
6
4
2
0

ampl

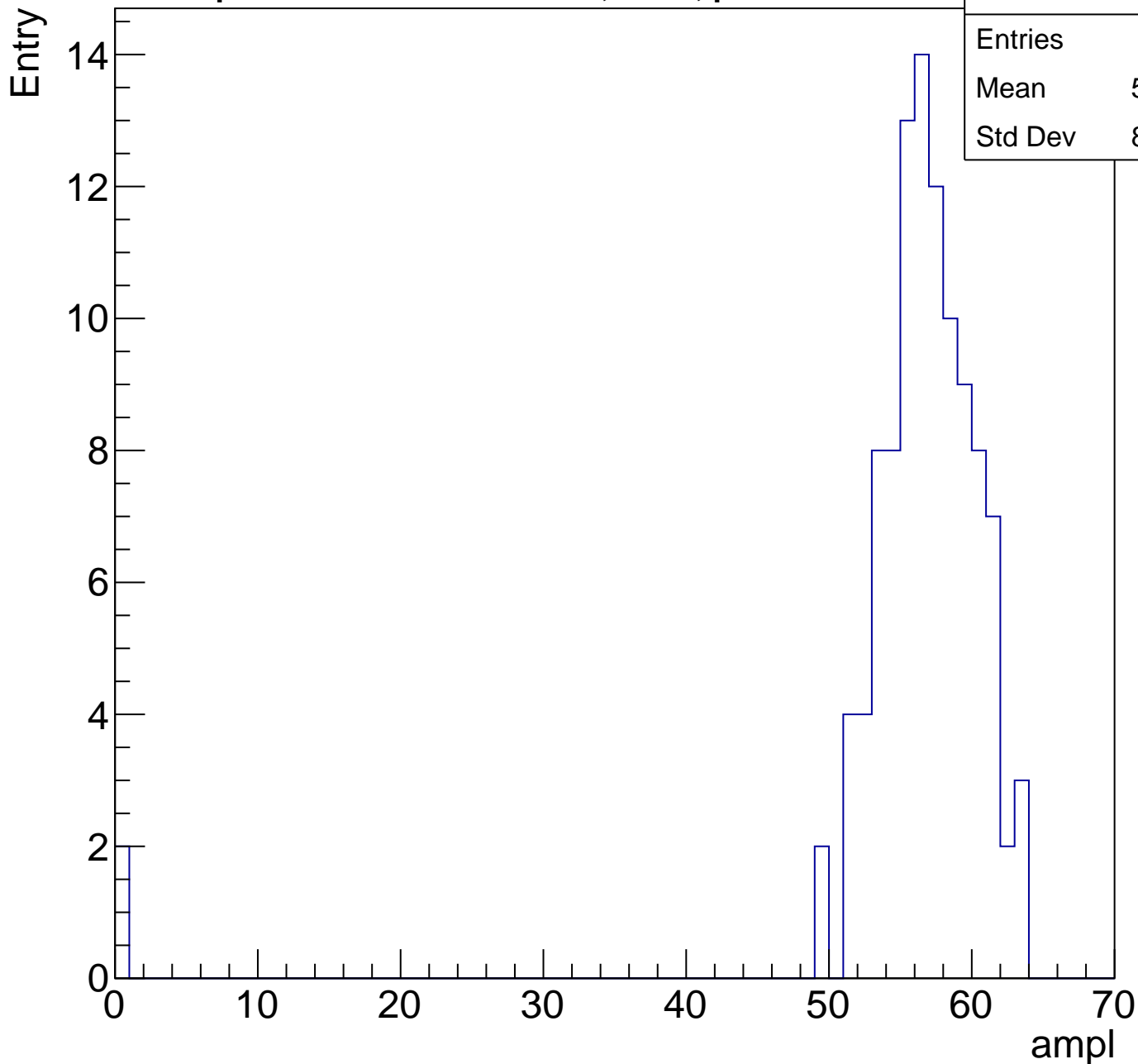
0 10 20 30 40 50 60 70



B0L101S, U22-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

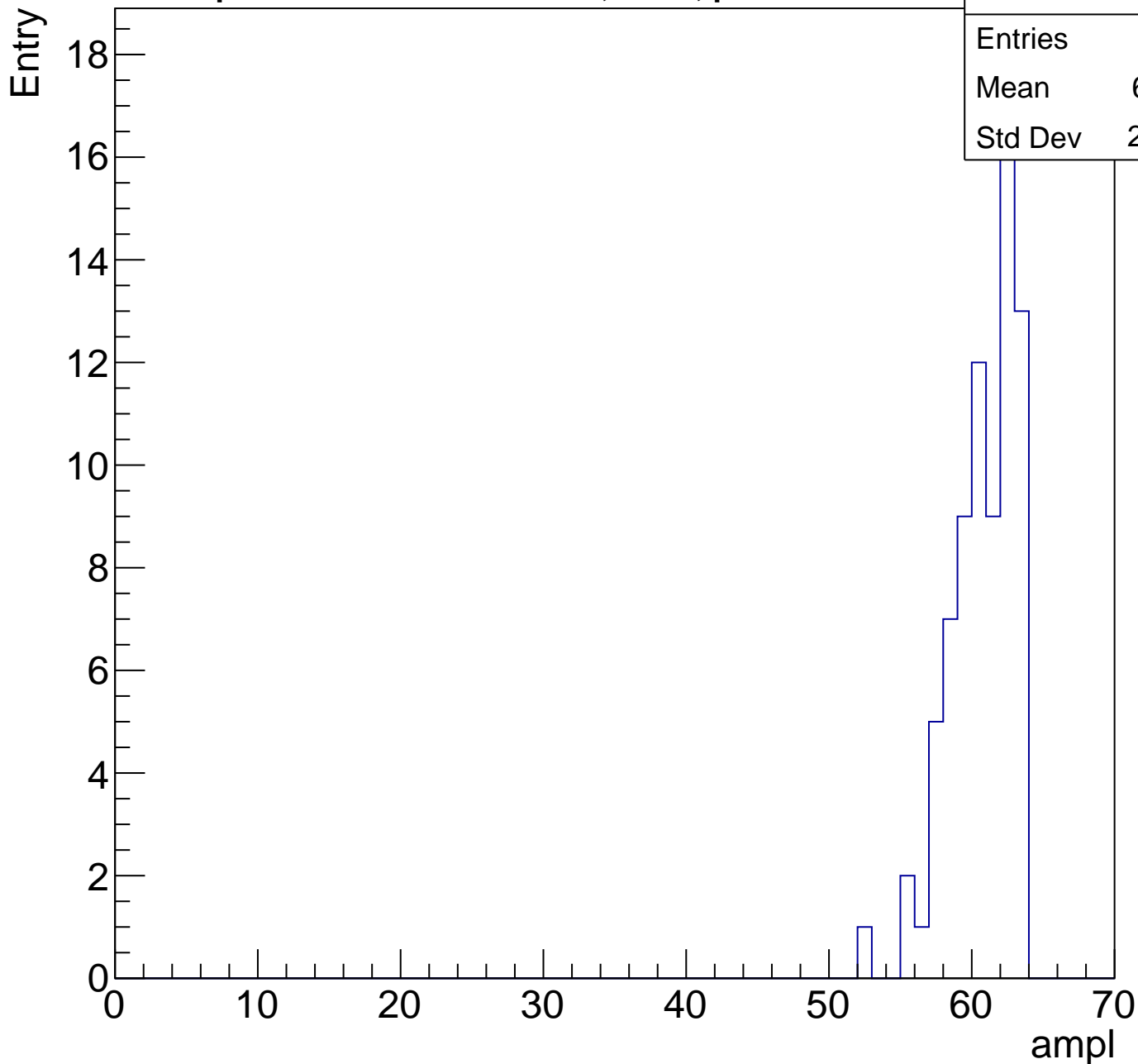
| | |
|---------|-------|
| Entries | 106 |
| Mean | 55.47 |
| Std Dev | 8.285 |



B0L101S, U22-ch123, adc5

calib_packv5_042523_0143.root, FC#1, port C1

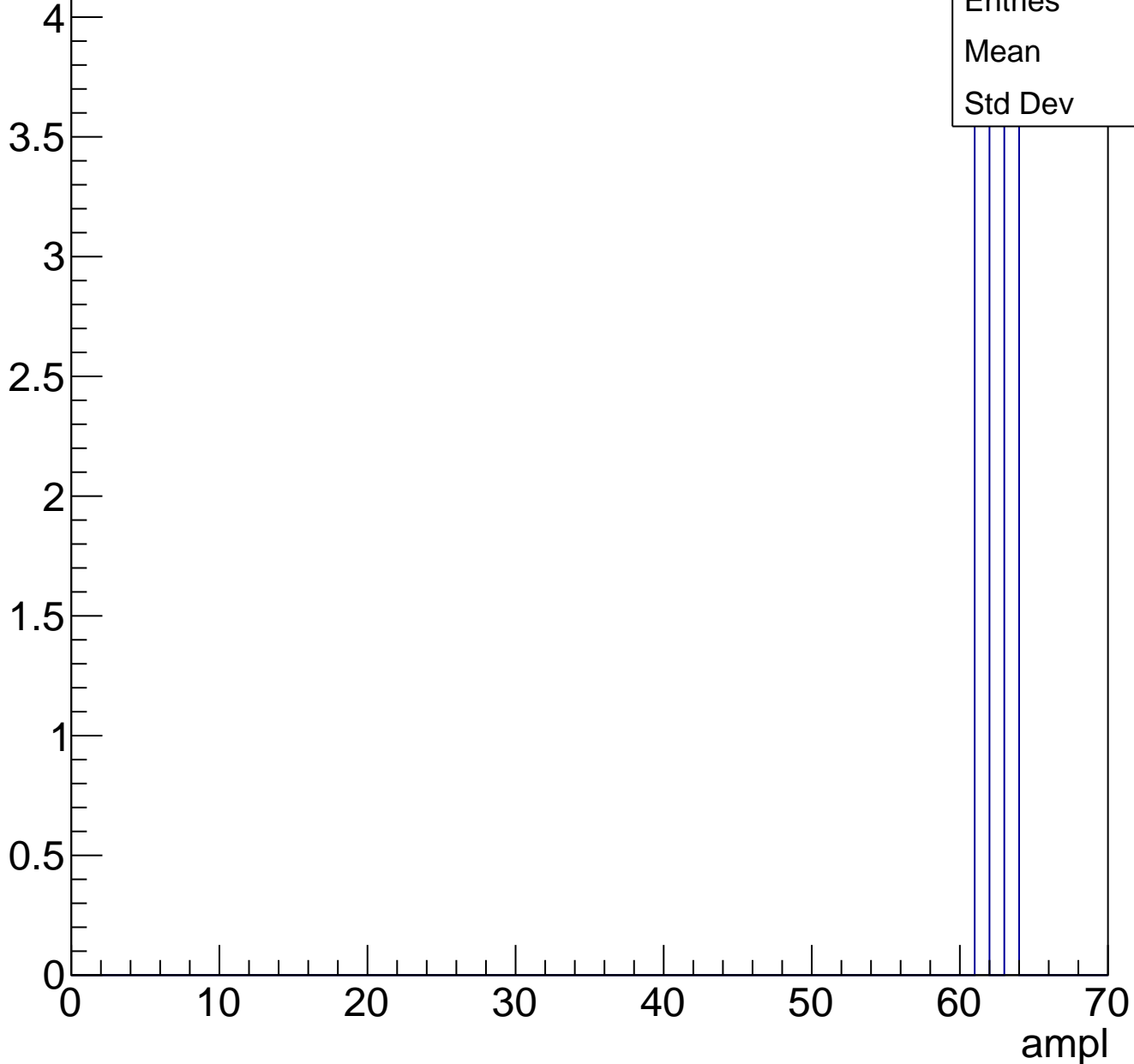
| | |
|---------|-------|
| Entries | 77 |
| Mean | 60.31 |
| Std Dev | 2.286 |



B0L101S, U22-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch124, adc0

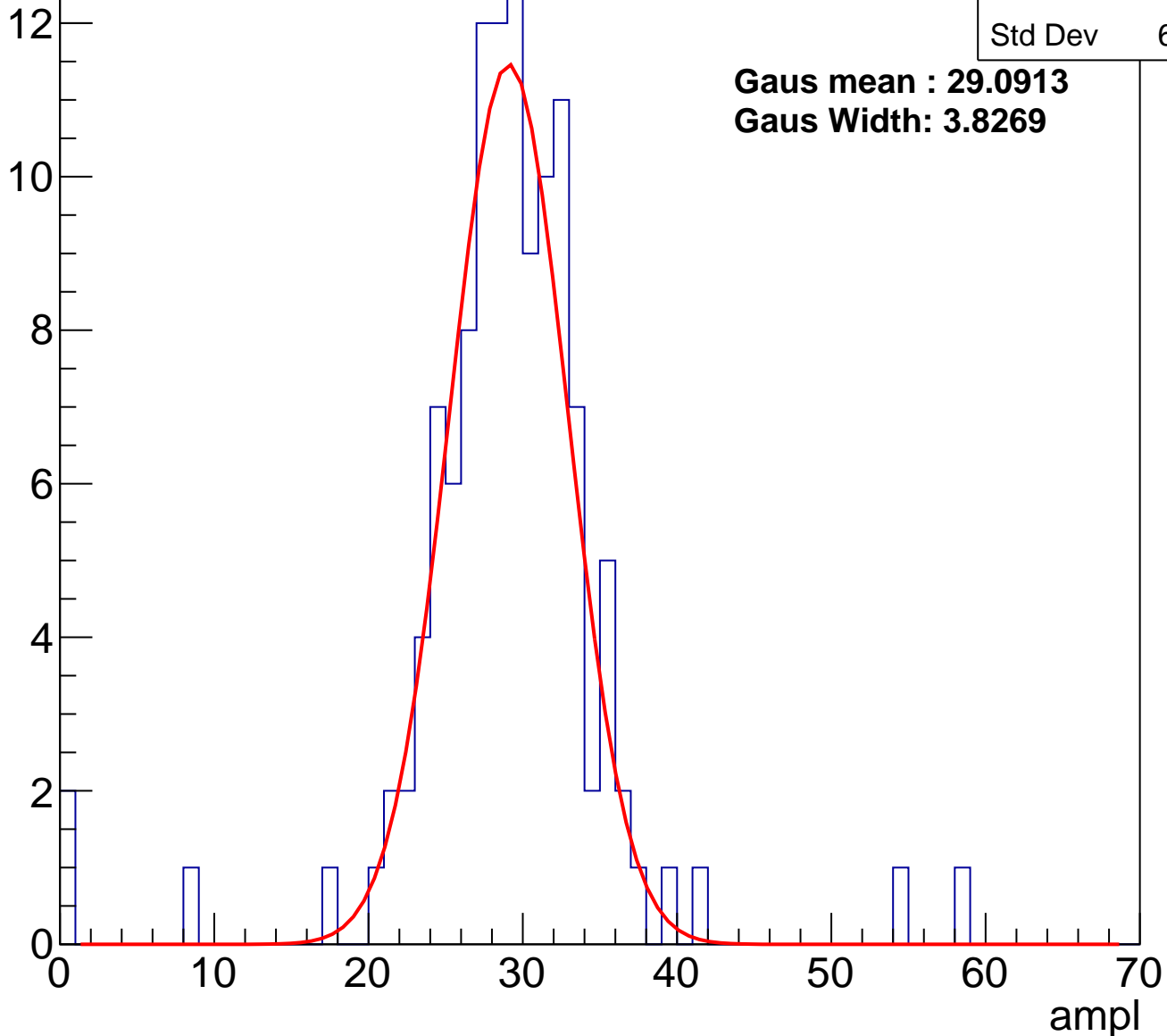
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 122 |
| Mean | 28.63 |
| Std Dev | 6.694 |

Gaus mean : 29.0913

Gaus Width: 3.8269

Entry



B0L101S, U22-ch124, adc1

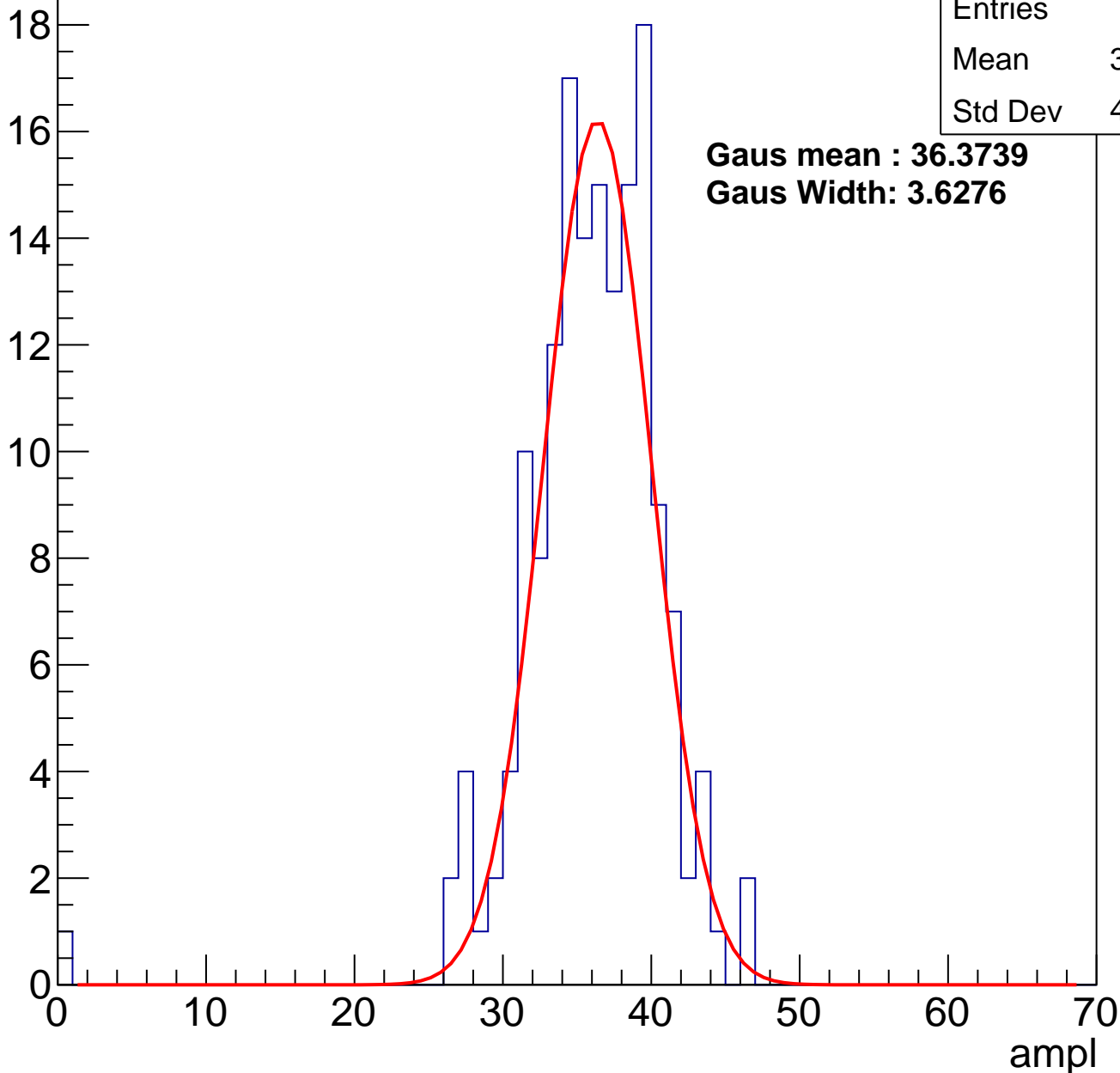
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 35.58 |
| Std Dev | 4.827 |

Gaus mean : 36.3739

Gaus Width: 3.6276

Entry



B0L101S, U22-ch124, adc2

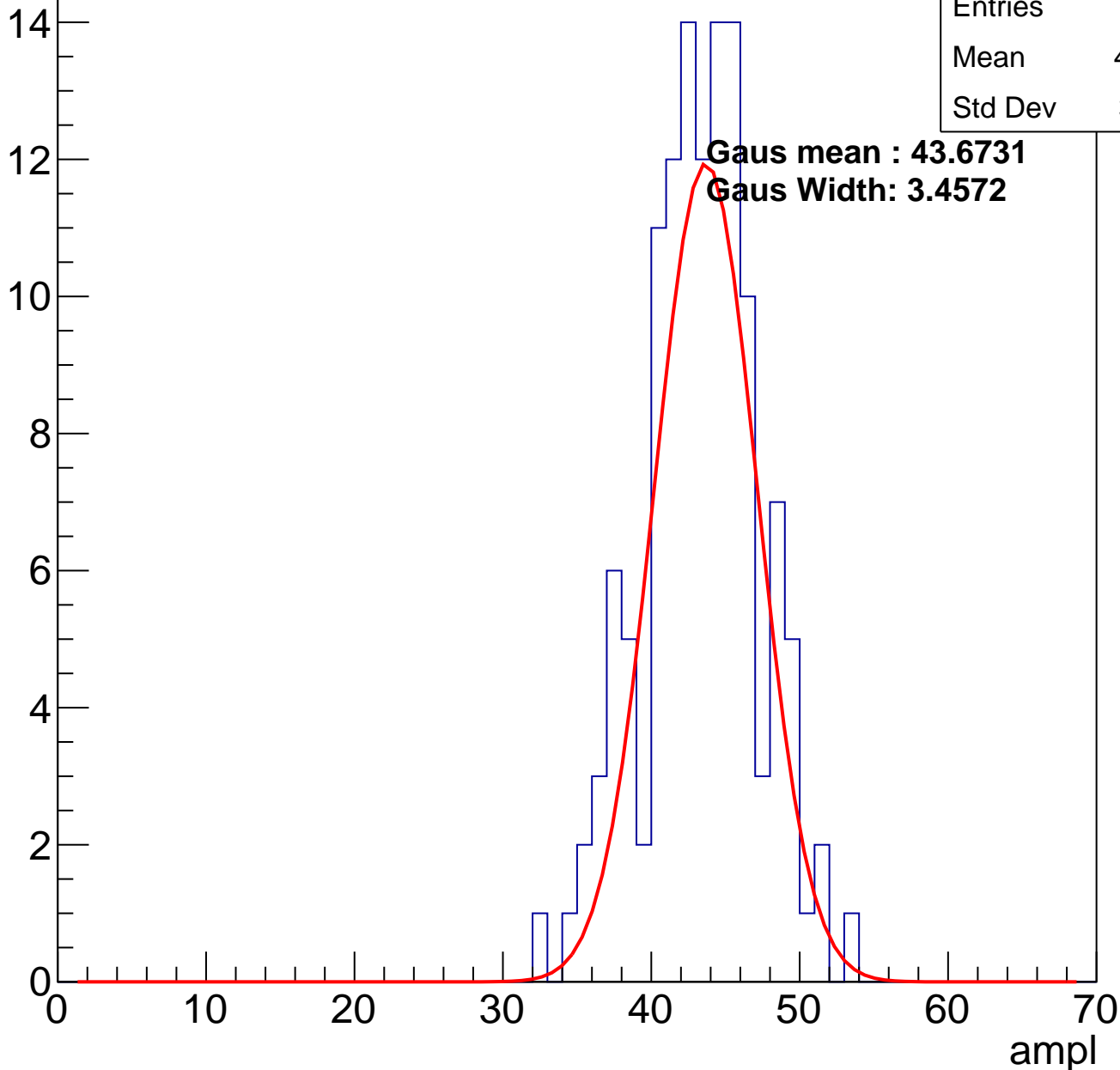
calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 126 |
| Mean | 42.88 |
| Std Dev | 3.831 |

Gaus mean : 43.6731

Gaus Width: 3.4572

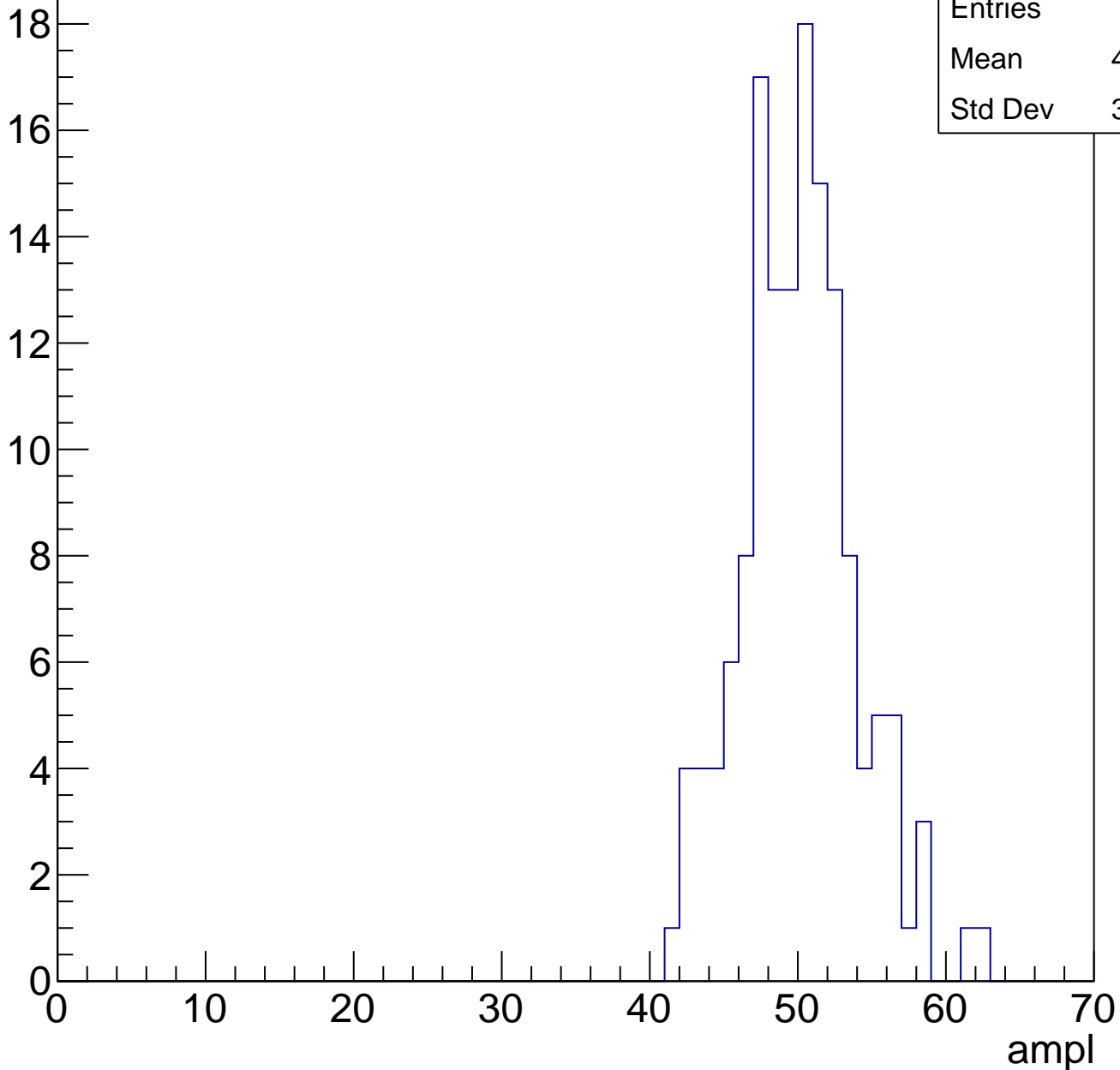


B0L101S, U22-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

| | |
|---------|-------|
| Entries | 144 |
| Mean | 49.62 |
| Std Dev | 3.878 |



B0L101S, U22-ch124, adc4

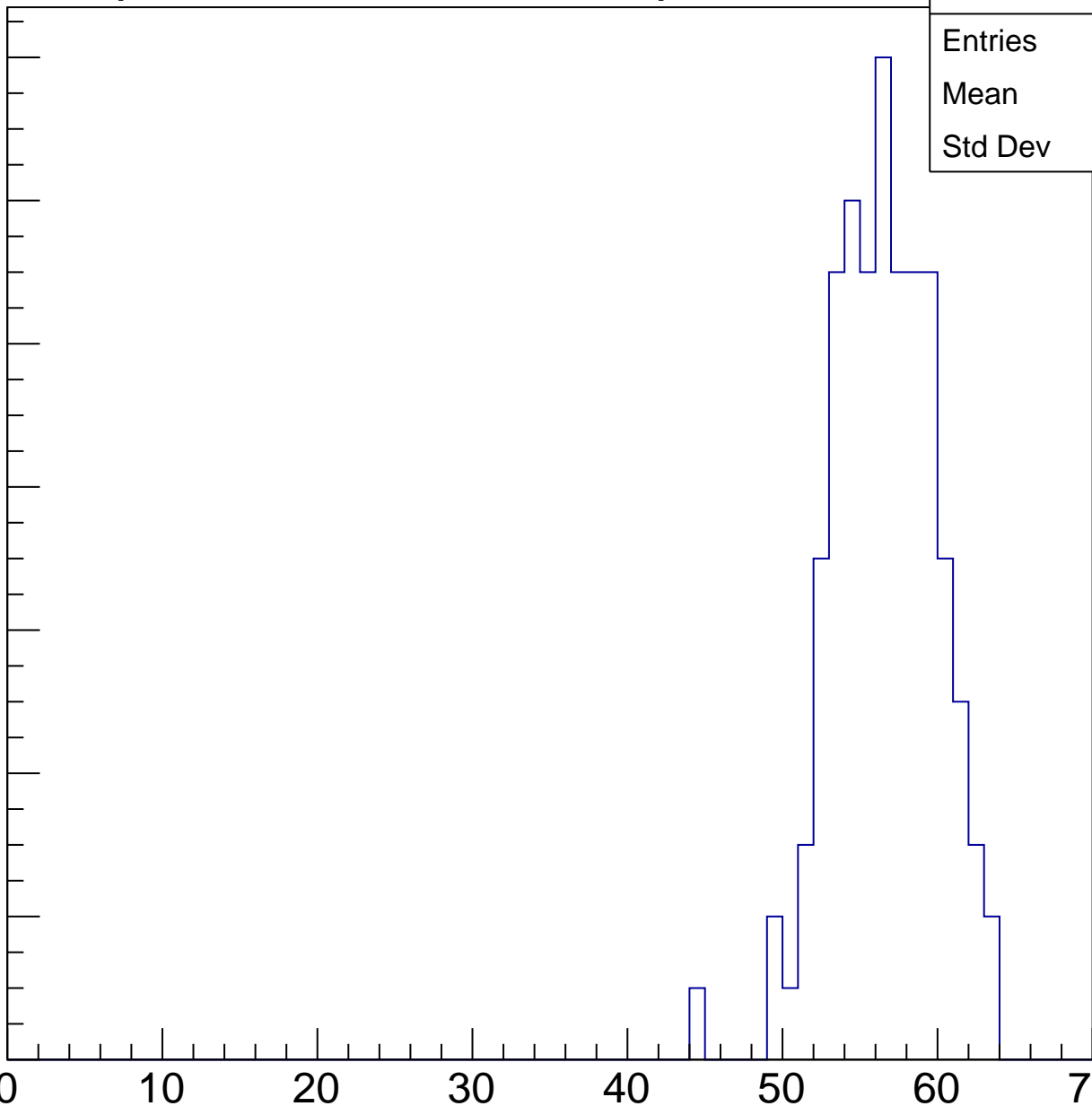
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 112 |
| Mean | 56.07 |
| Std Dev | 3.3 |

ampl



B0L101S, U22-ch124, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

72

Mean

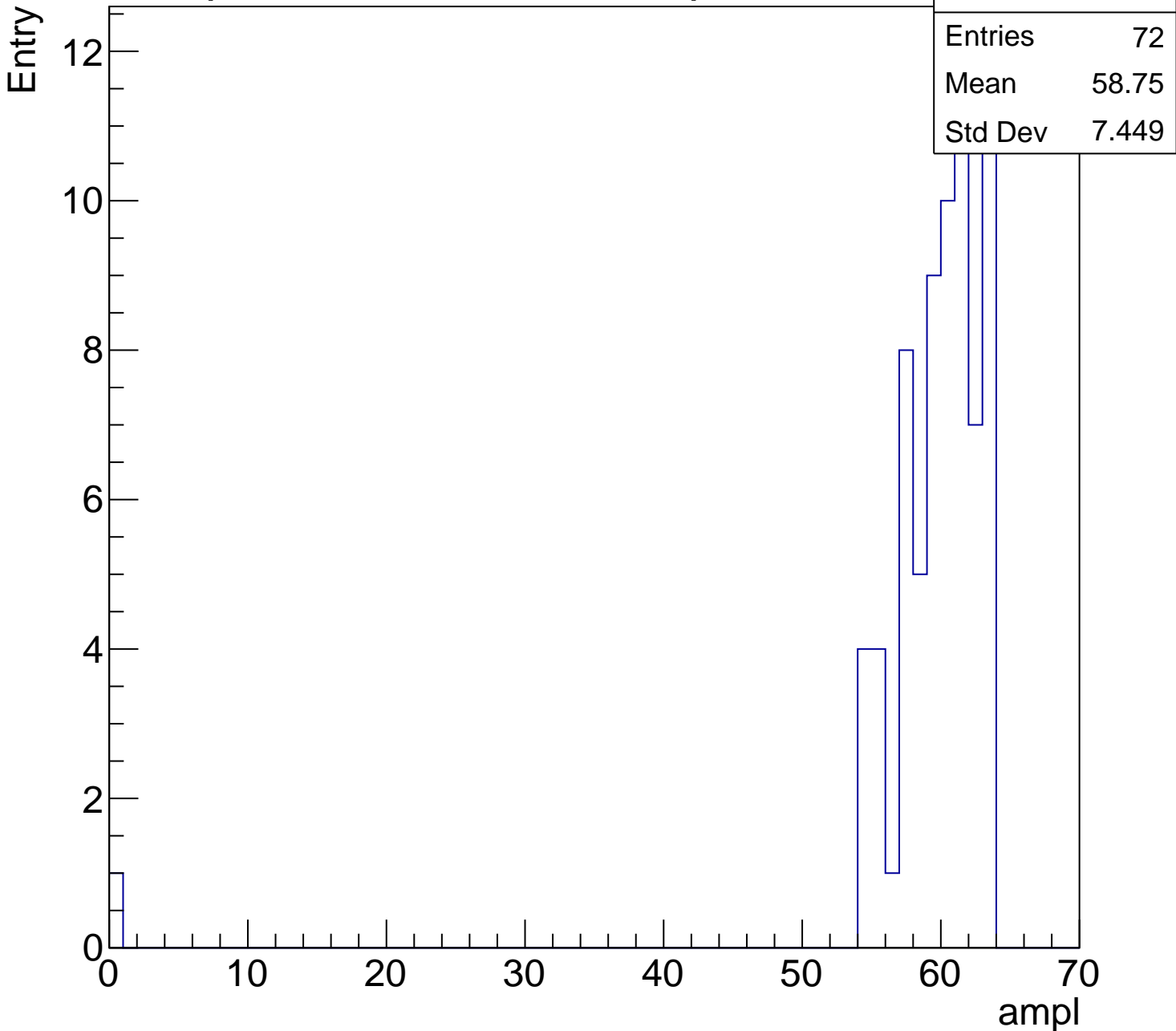
58.75

Std Dev

7.449

ampl

0 10 20 30 40 50 60 70

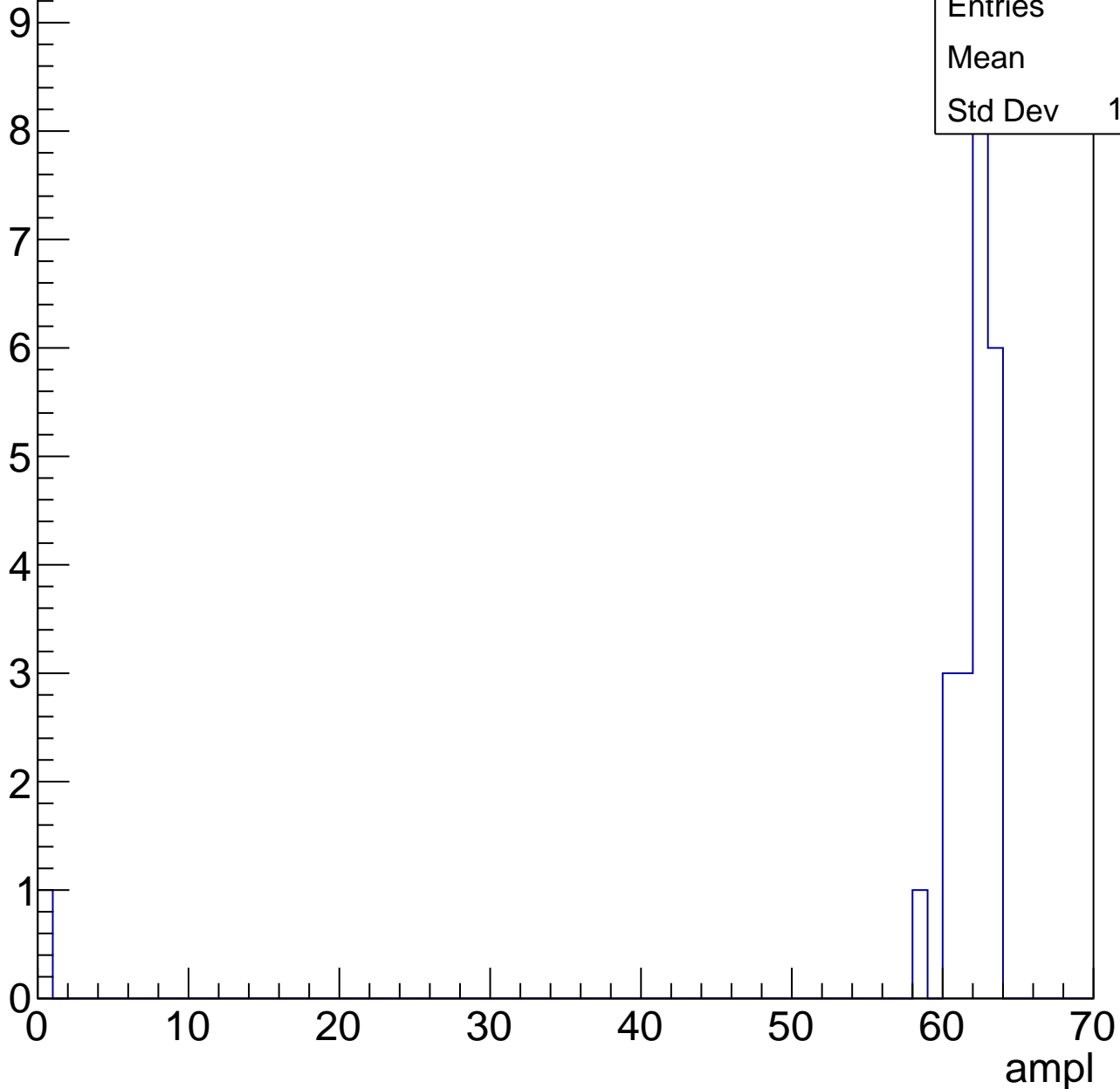


B0L101S, U22-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

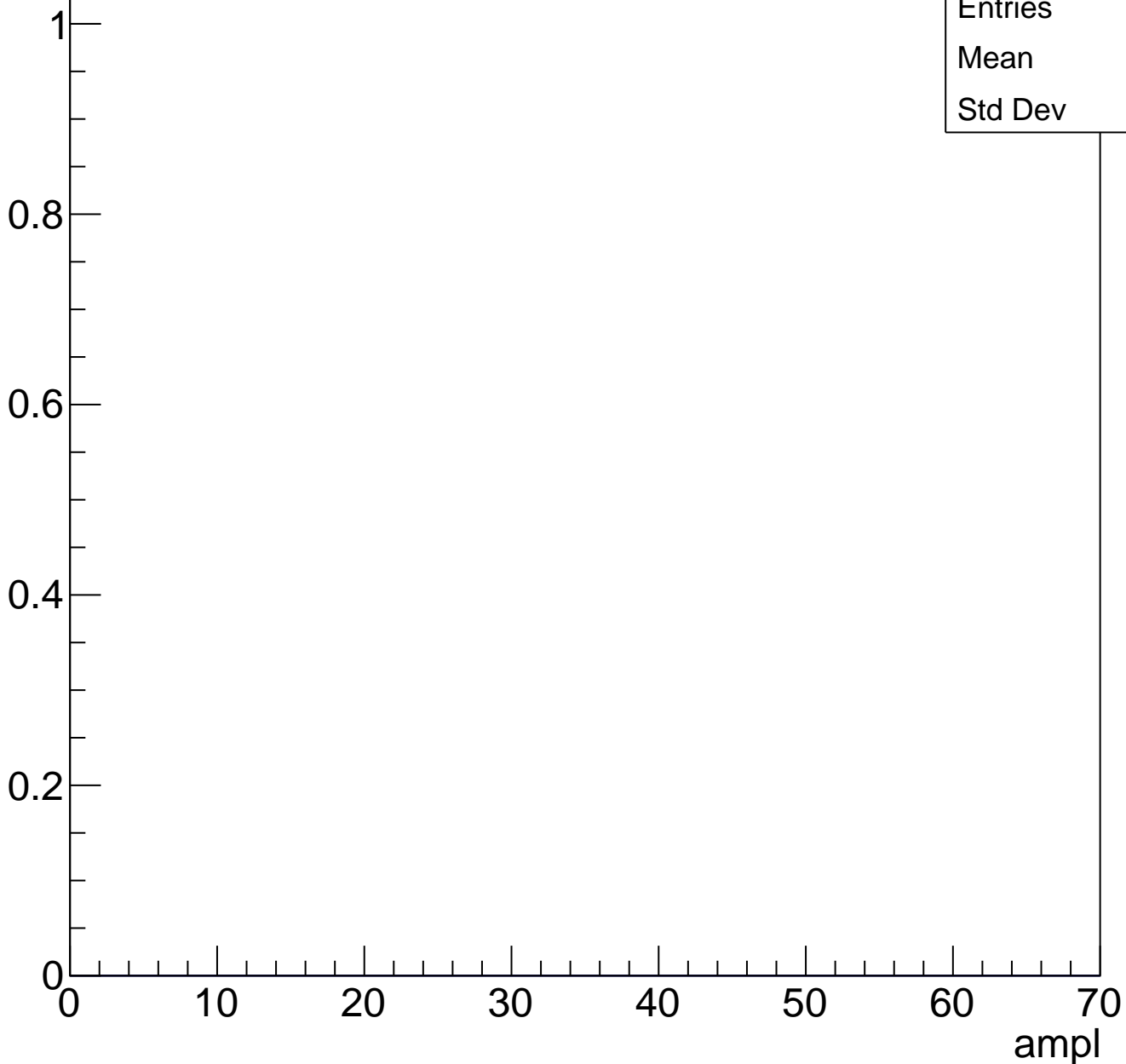
| | |
|---------|-------|
| Entries | 23 |
| Mean | 59 |
| Std Dev | 12.64 |



B0L101S, U22-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch125, adc0

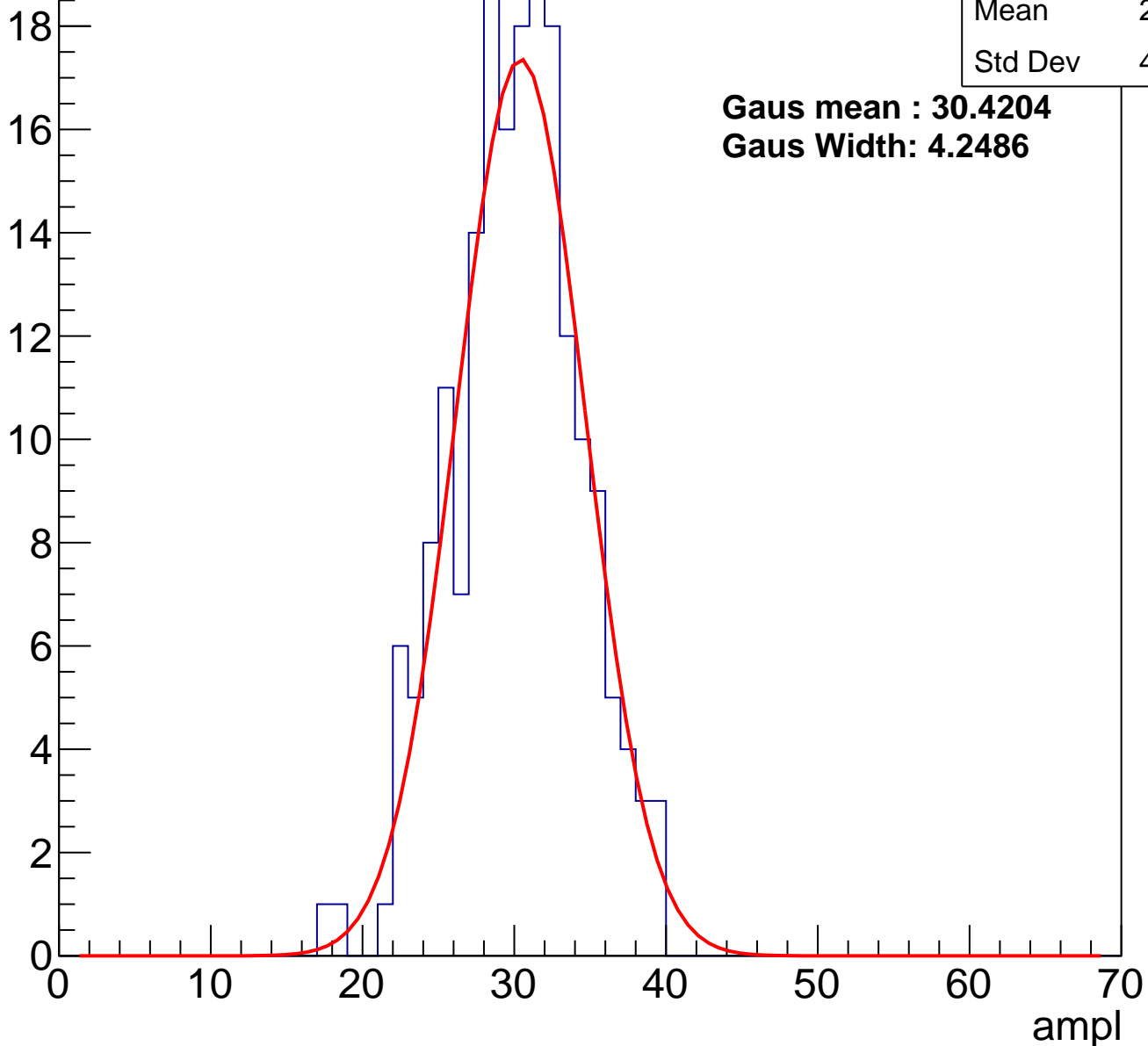
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 190 |
| Mean | 29.69 |
| Std Dev | 4.163 |

Gaus mean : 30.4204

Gaus Width: 4.2486

Entry



B0L101S, U22-ch125, adc1

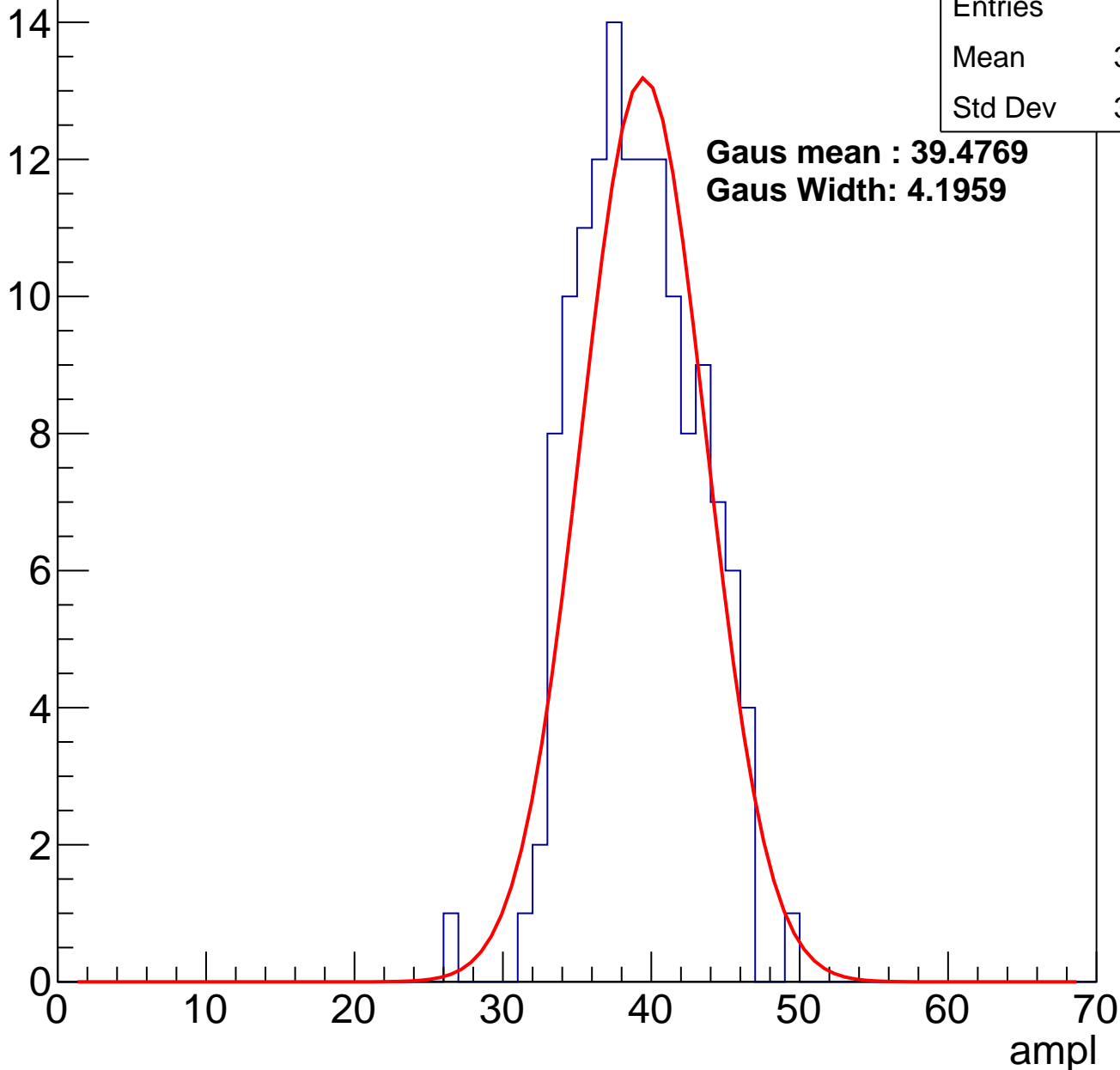
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 140 |
| Mean | 38.63 |
| Std Dev | 3.925 |

Gaus mean : 39.4769

Gaus Width: 4.1959

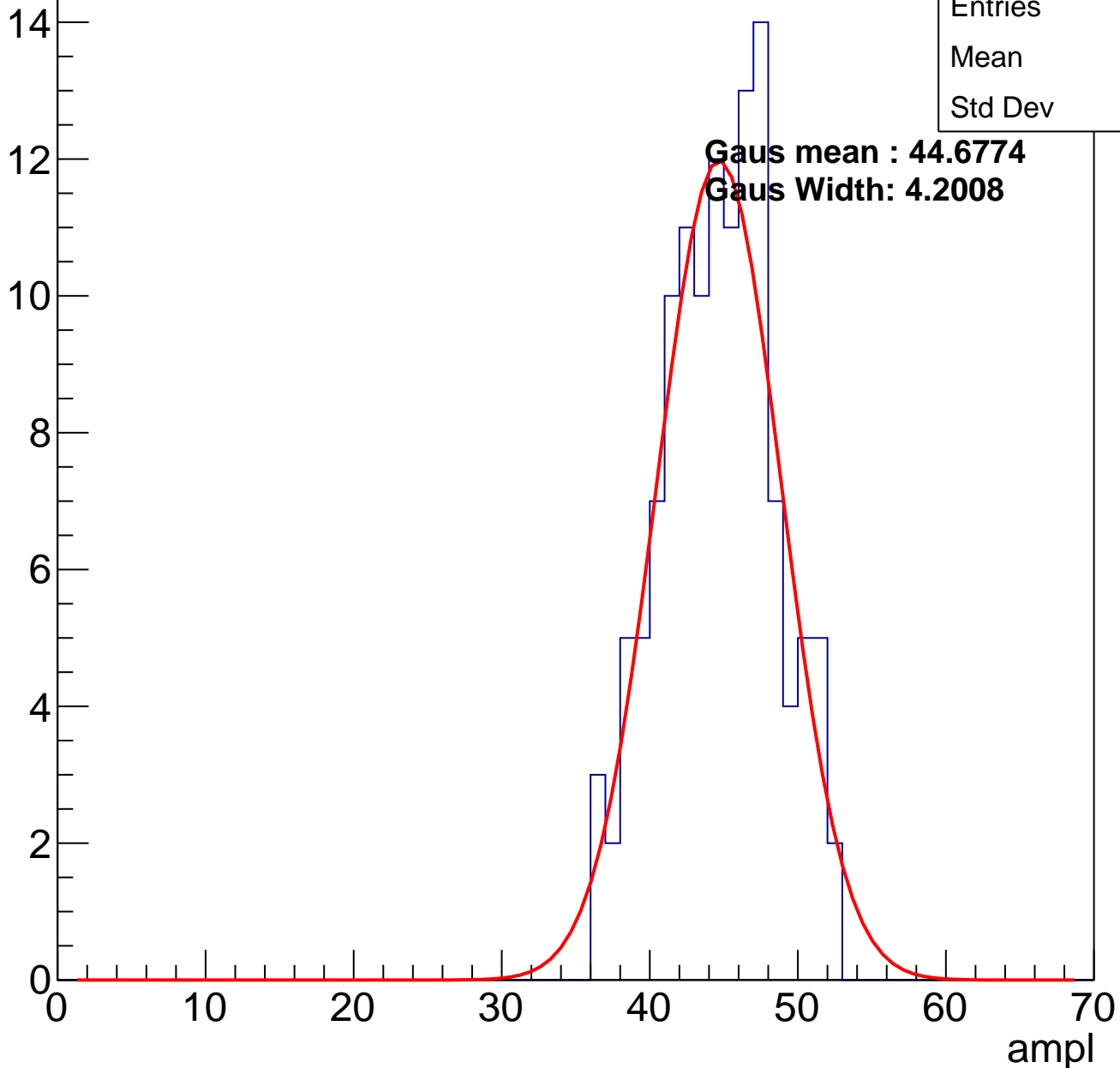
Entry



B0L101S, U22-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 106 |
| Mean | 50.66 |
| Std Dev | 3.464 |

Entry

12

10

8

6

4

2

0

0

10

20

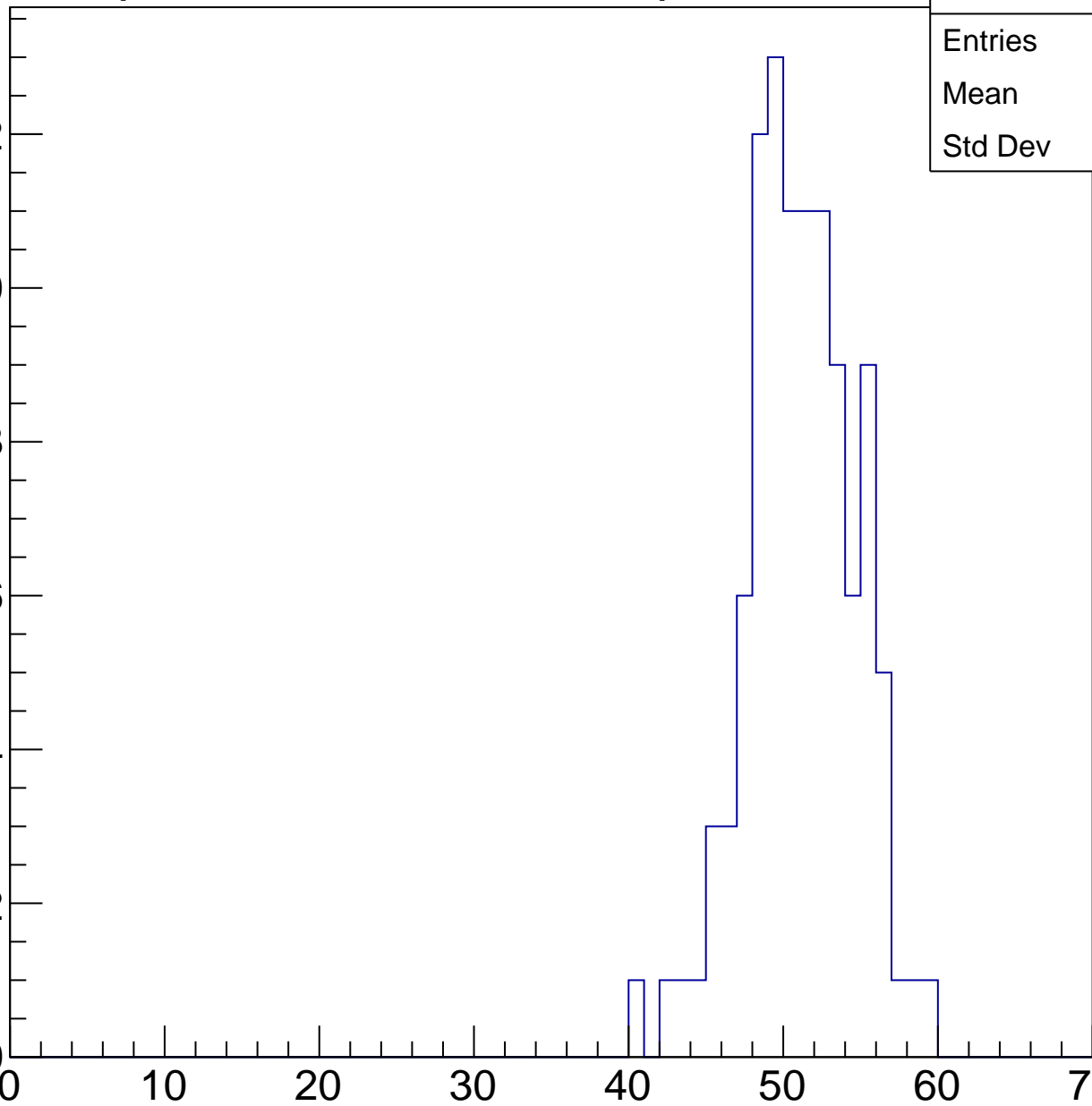
30

40

50

60

ampl



B0L101S, U22-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 137 |
| Mean | 56.18 |
| Std Dev | 5.914 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

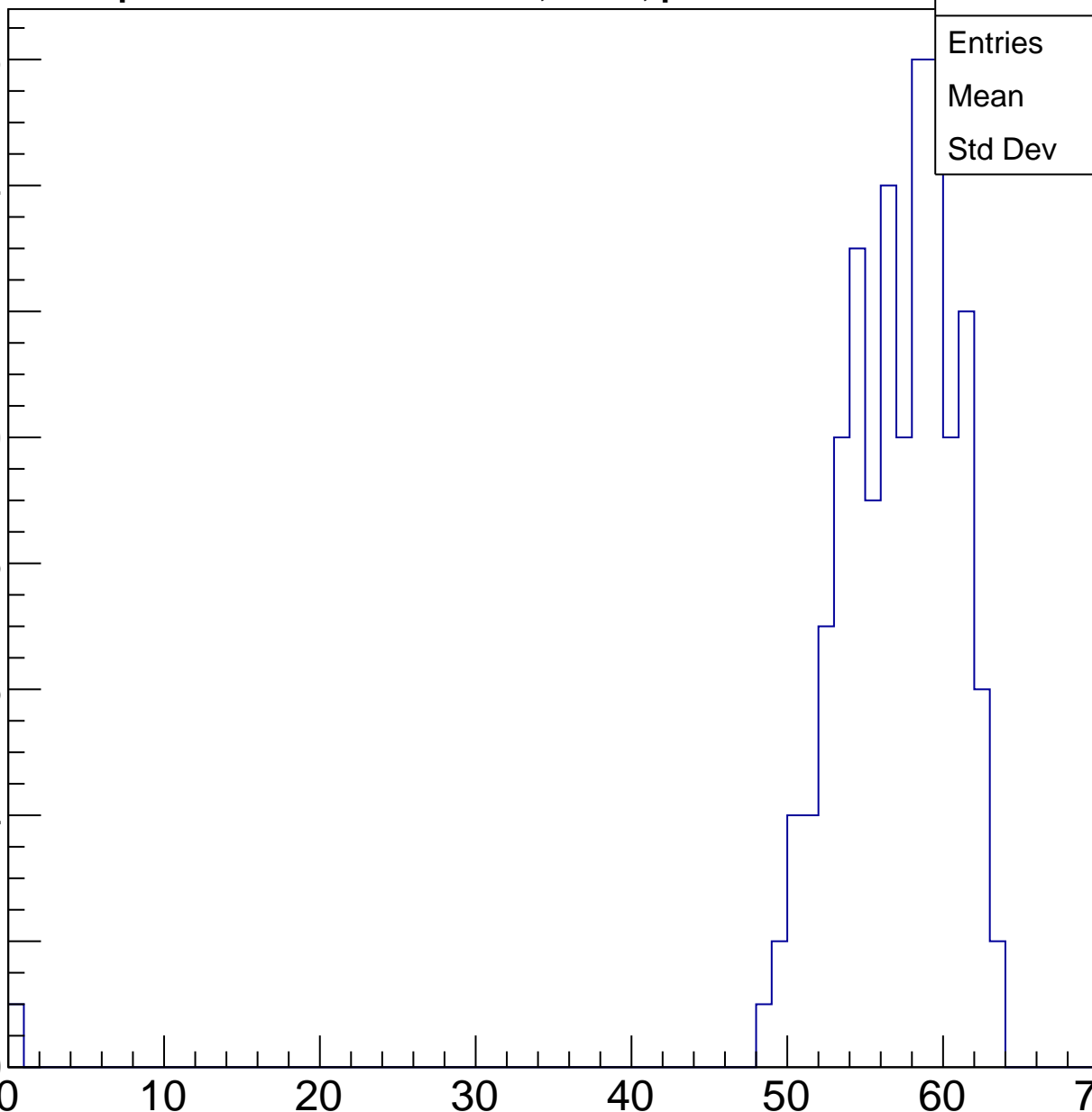
40

50

60

70

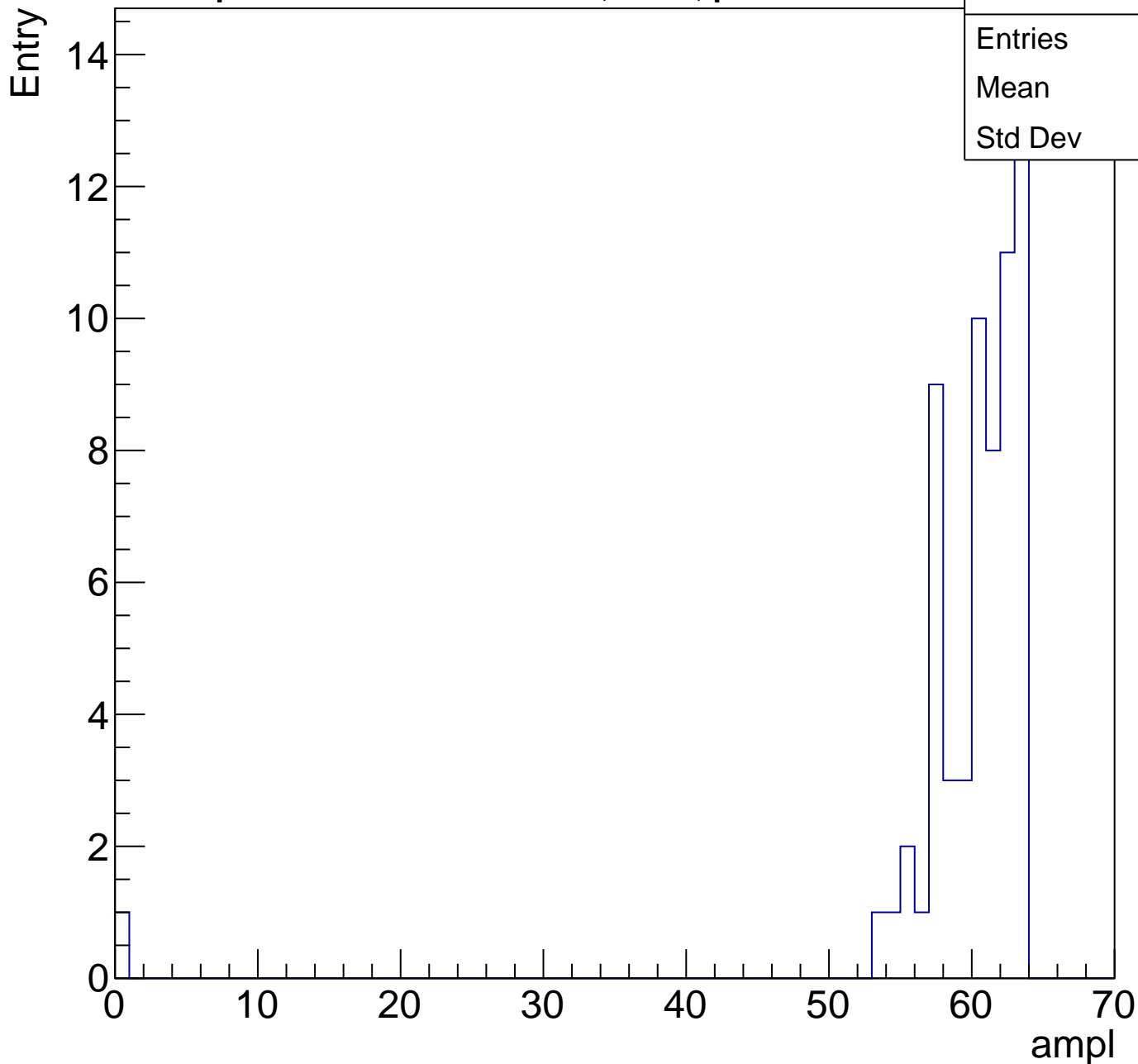
ampl



B0L101S, U22-ch125, adc5

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 64 |
| Mean | 59.2 |
| Std Dev | 7.89 |



B0L101S, U22-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U22-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U22-ch126, adc0

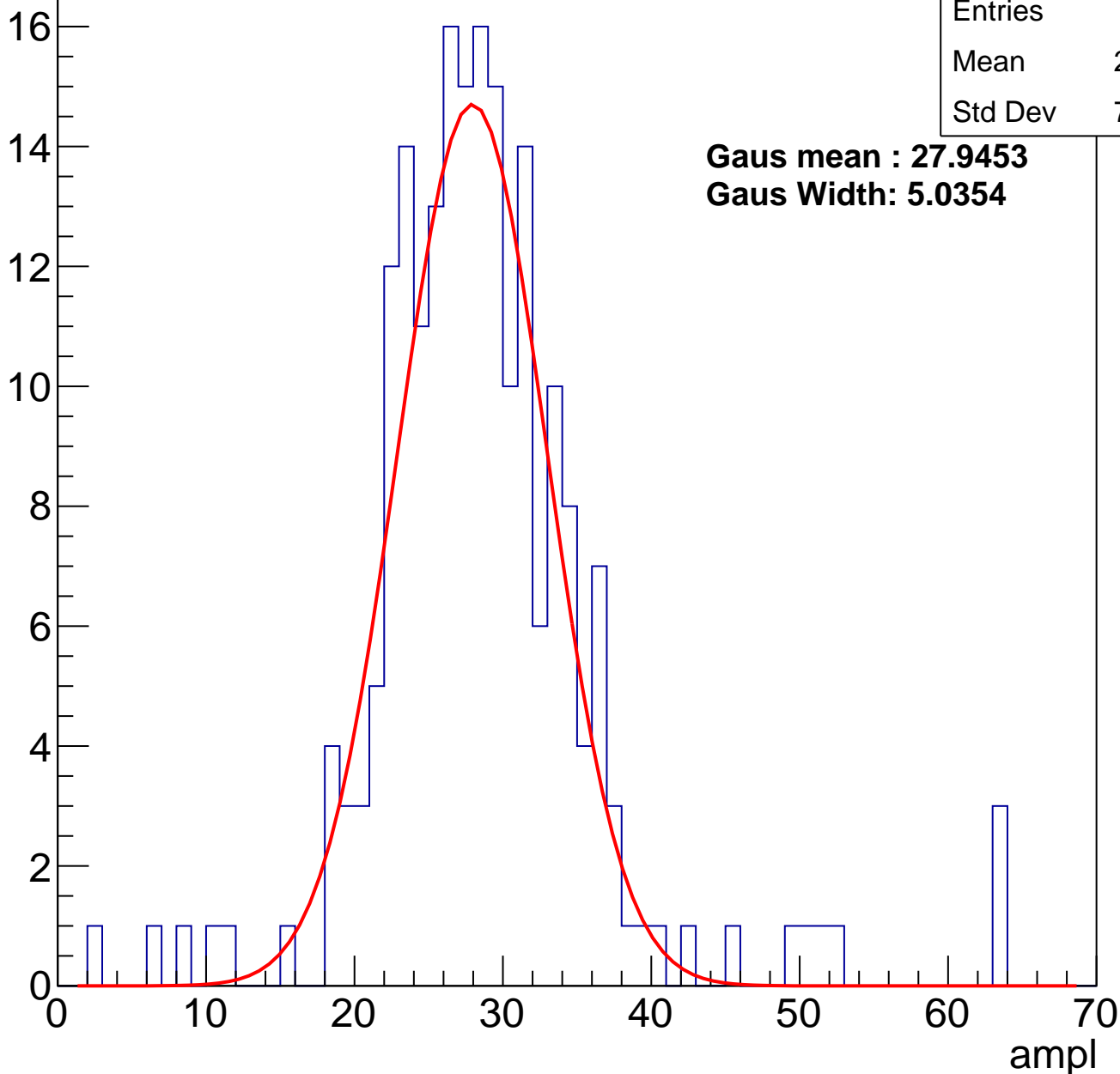
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 207 |
| Mean | 28.27 |
| Std Dev | 7.869 |

Gaus mean : 27.9453

Gaus Width: 5.0354

Entry



B0L101S, U22-ch126, adc1

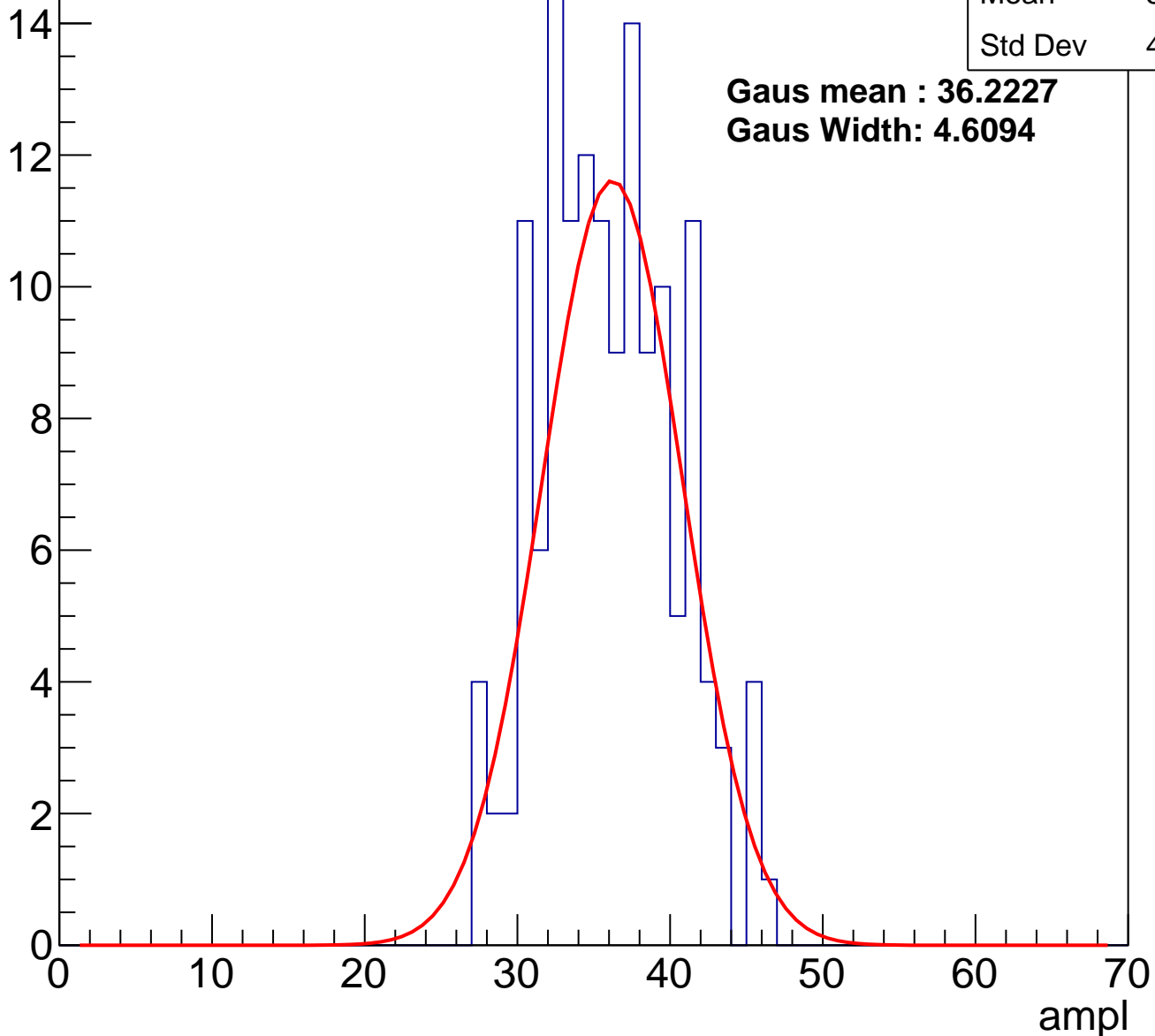
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 144 |
| Mean | 35.57 |
| Std Dev | 4.312 |

Gaus mean : 36.2227

Gaus Width: 4.6094

Entry



B0L101S, U22-ch126, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 119 |
| Mean | 41.92 |
| Std Dev | 3.778 |

Gaus mean : 42.1020

Gaus Width: 4.1531

0

2

4

6

8

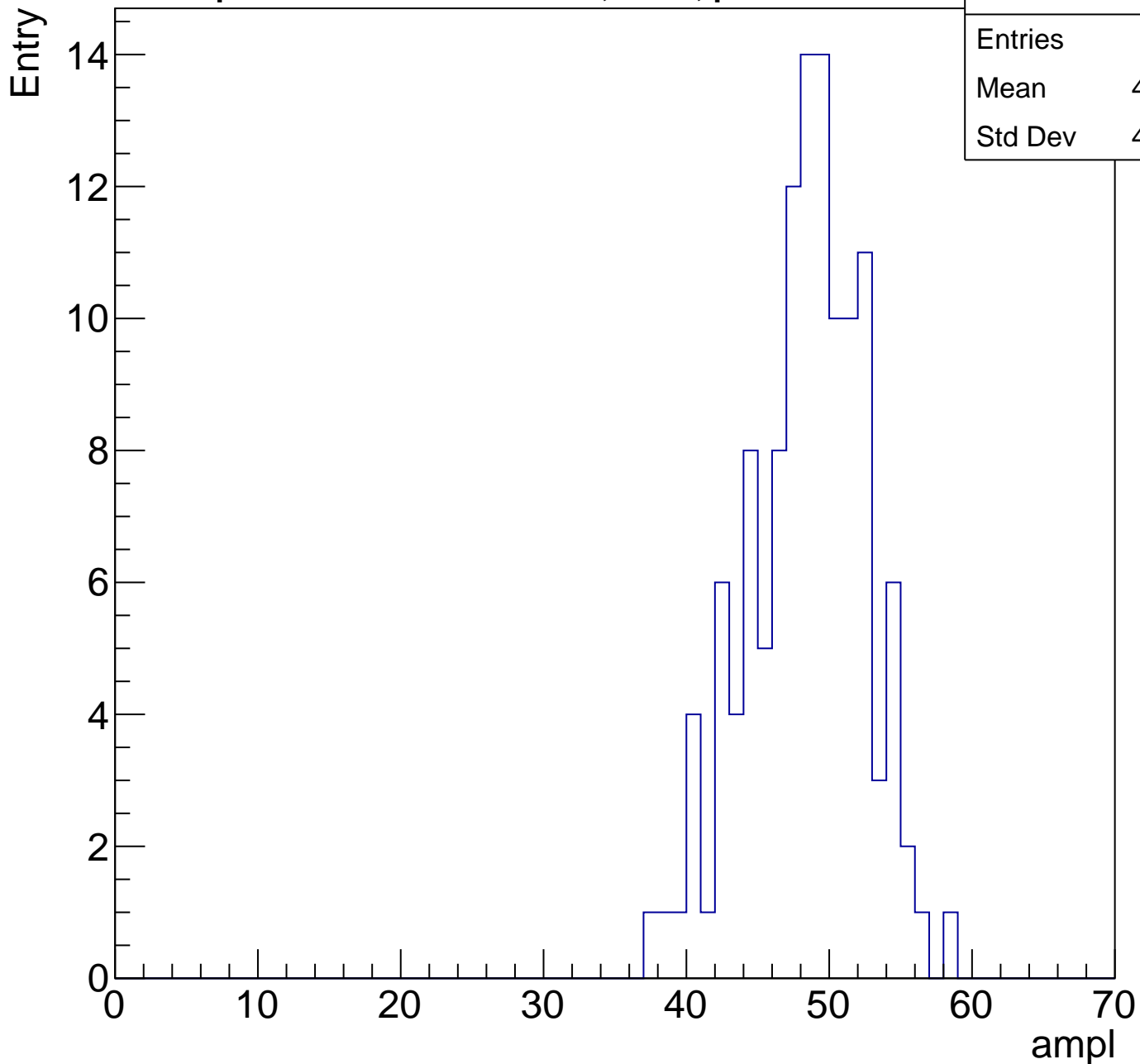
10

12

B0L101S, U22-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 123 |
| Mean | 47.93 |
| Std Dev | 4.045 |

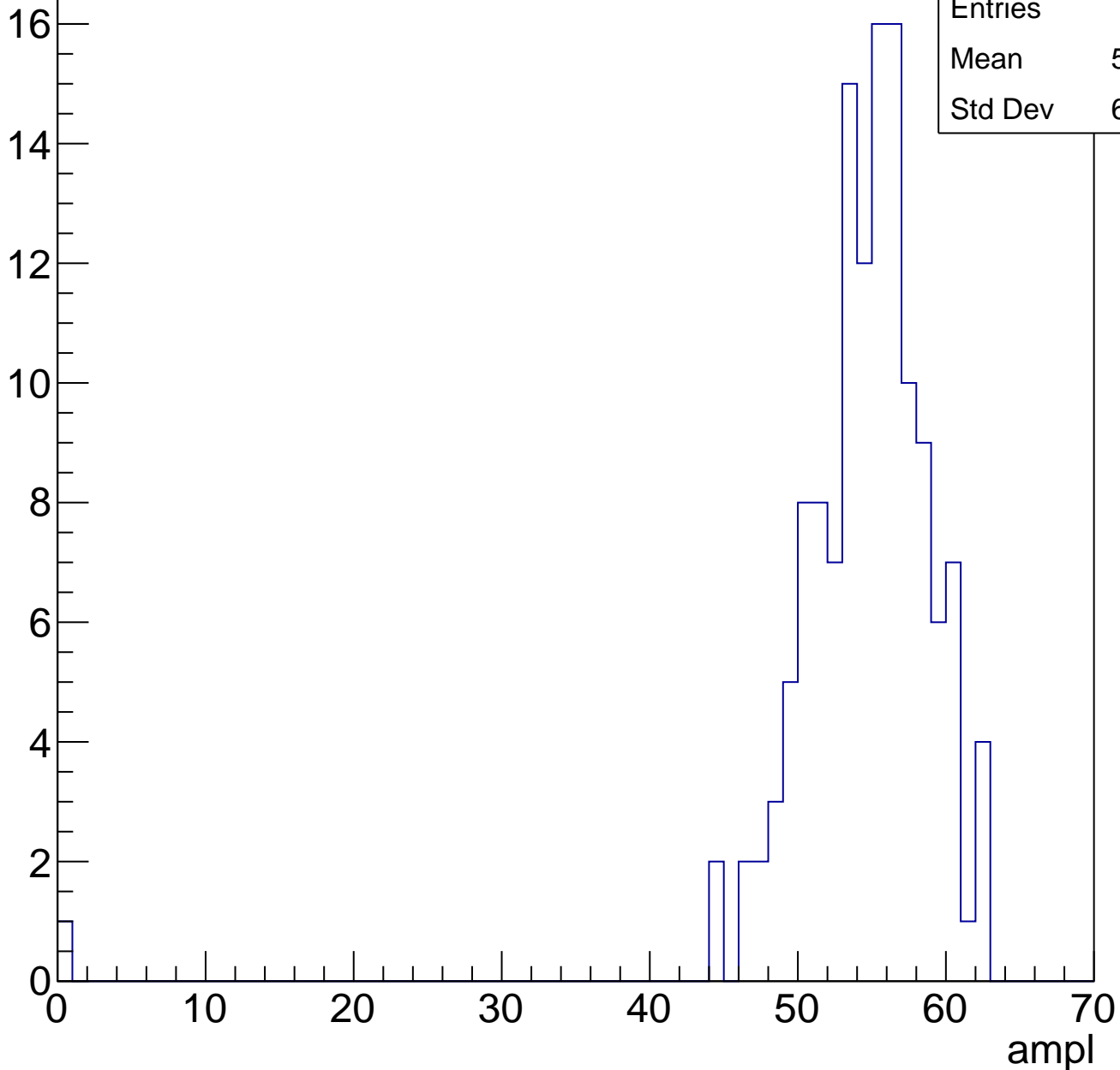


B0L101S, U22-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 134 |
| Mean | 53.95 |
| Std Dev | 6.009 |

Entry



B0L101S, U22-ch126, adc5

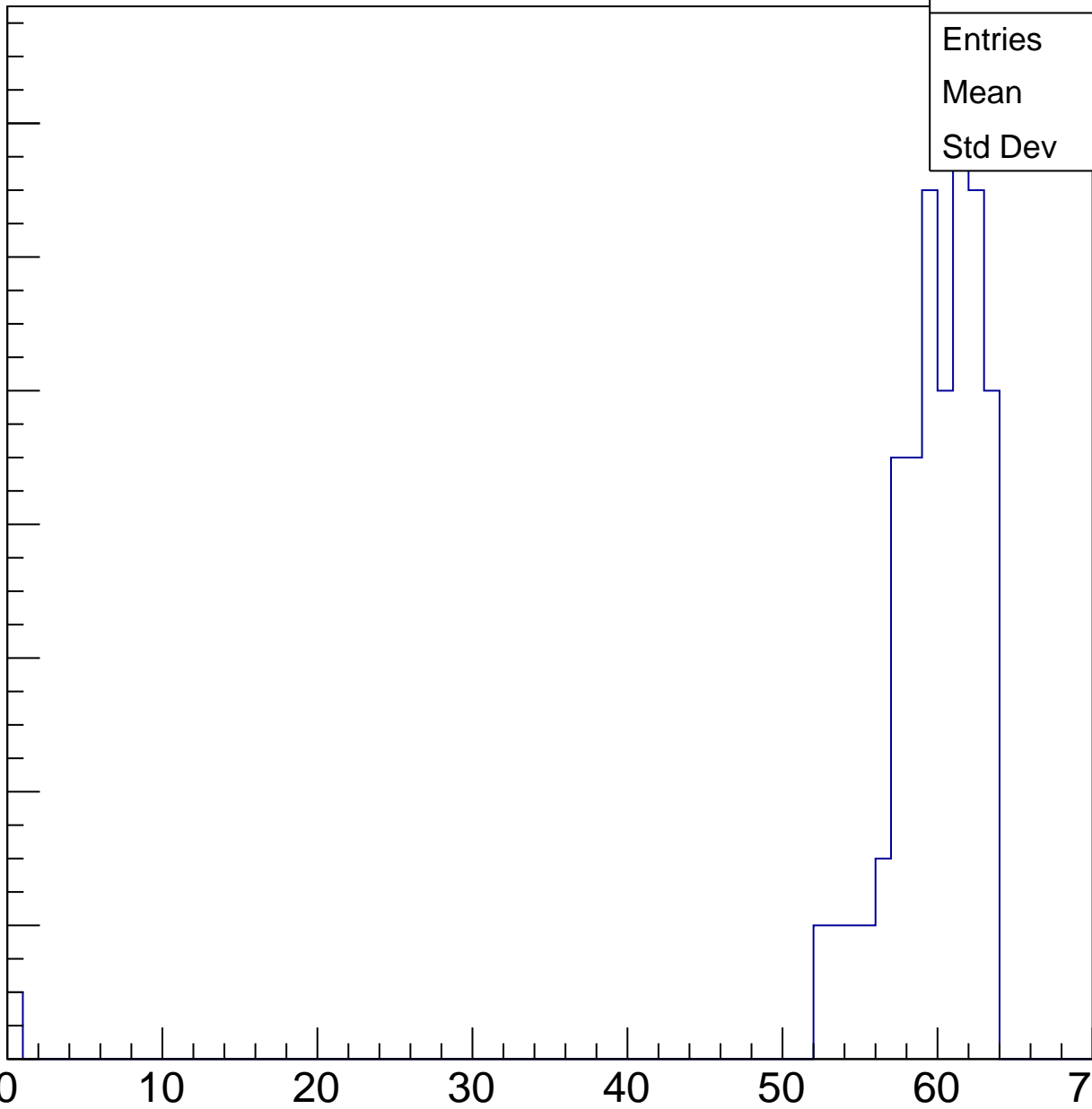
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 58.78 |
| Std Dev | 6.751 |

ampl

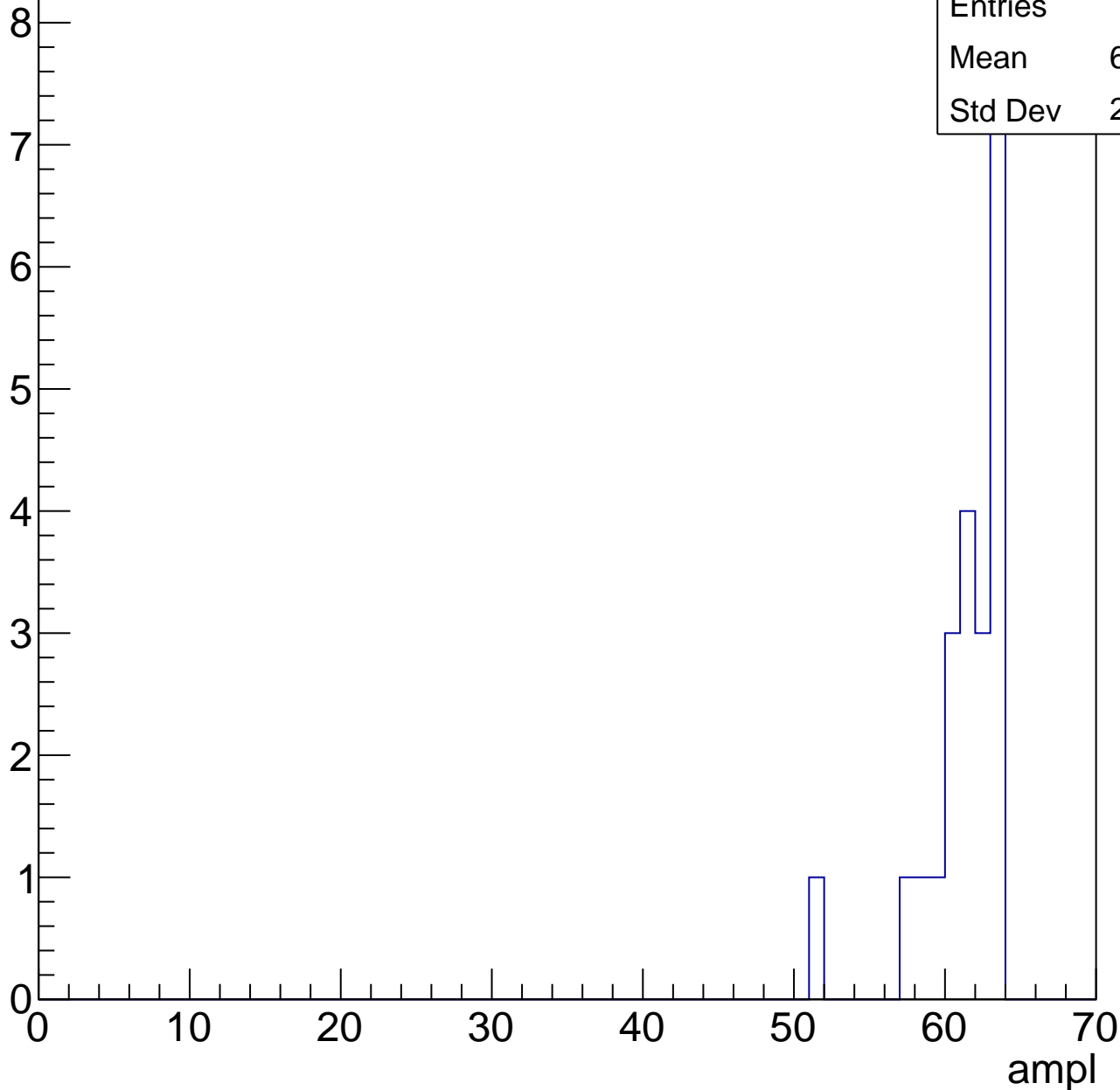


B0L101S, U22-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

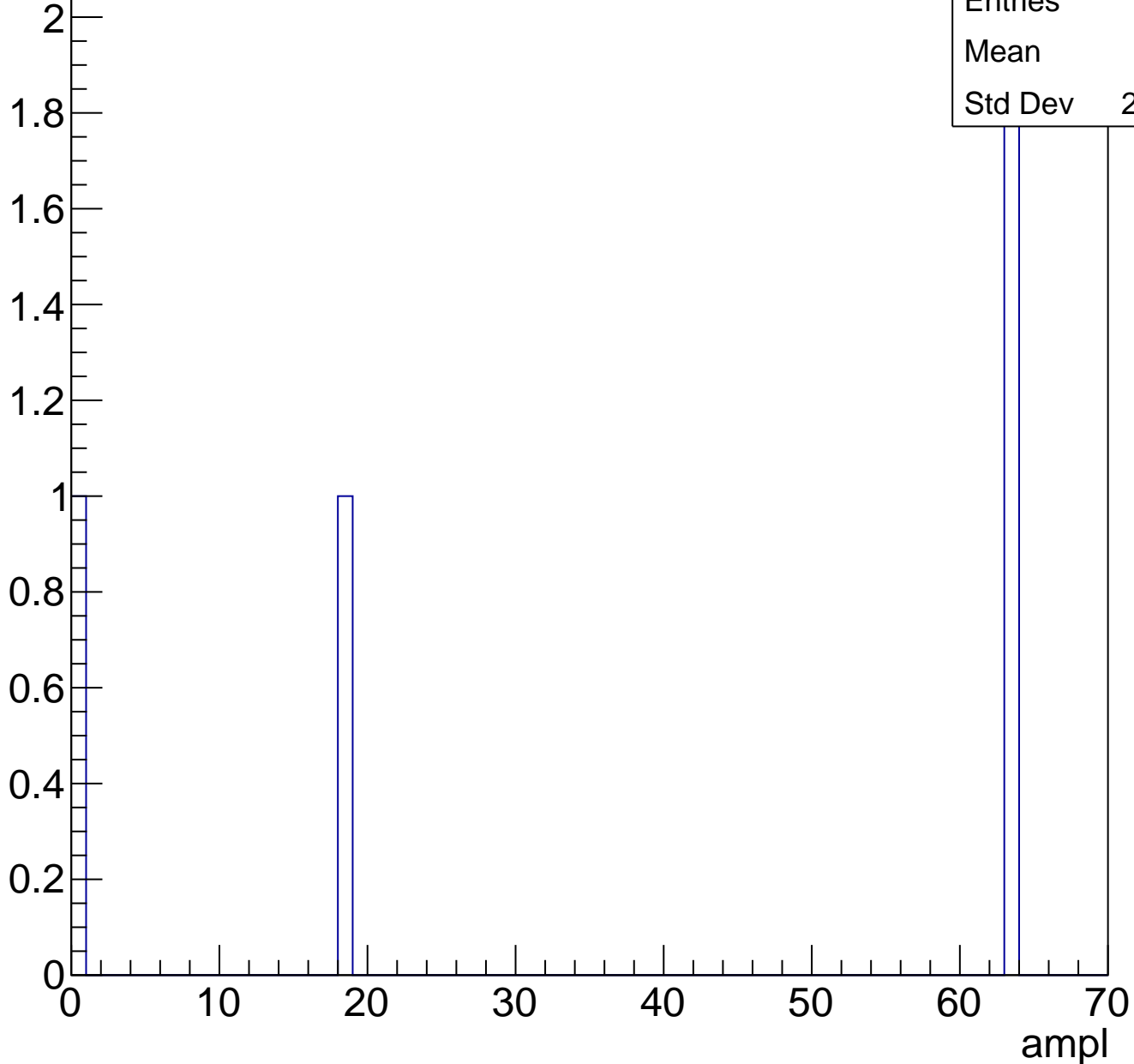
| | |
|---------|-------|
| Entries | 22 |
| Mean | 60.86 |
| Std Dev | 2.752 |



B0L101S, U22-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 36 |
| Std Dev | 27.74 |

B0L101S, U22-ch127, adc0

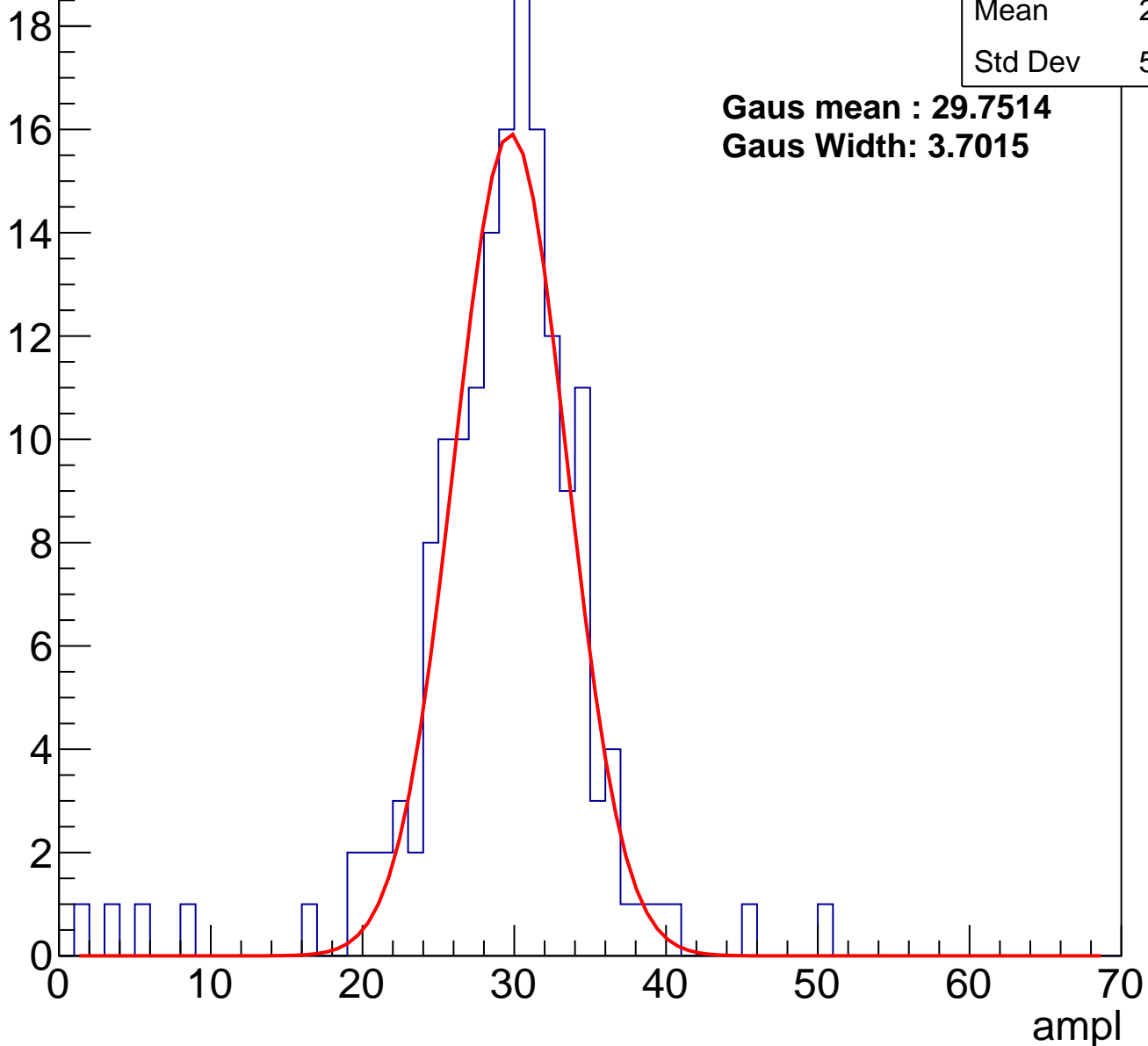
calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 165 |
| Mean | 28.72 |
| Std Dev | 5.943 |

Gaus mean : 29.7514

Gaus Width: 3.7015

Entry



B0L101S, U22-ch127, adc1

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|------|
| Entries | 138 |
| Mean | 36.9 |
| Std Dev | 3.67 |

Gaus mean : 37.2095

Gaus Width: 3.3883

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

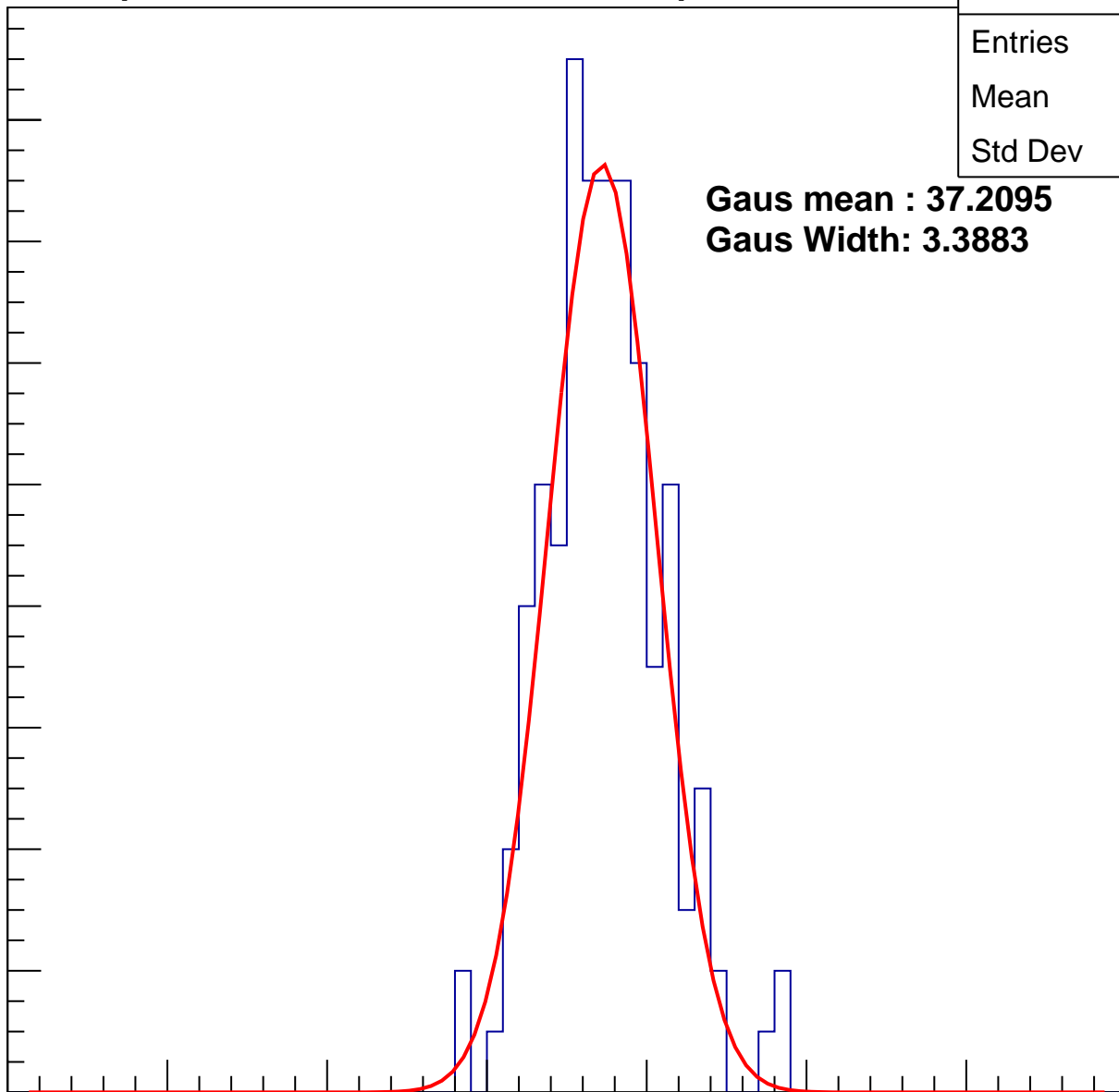
40

50

60

70

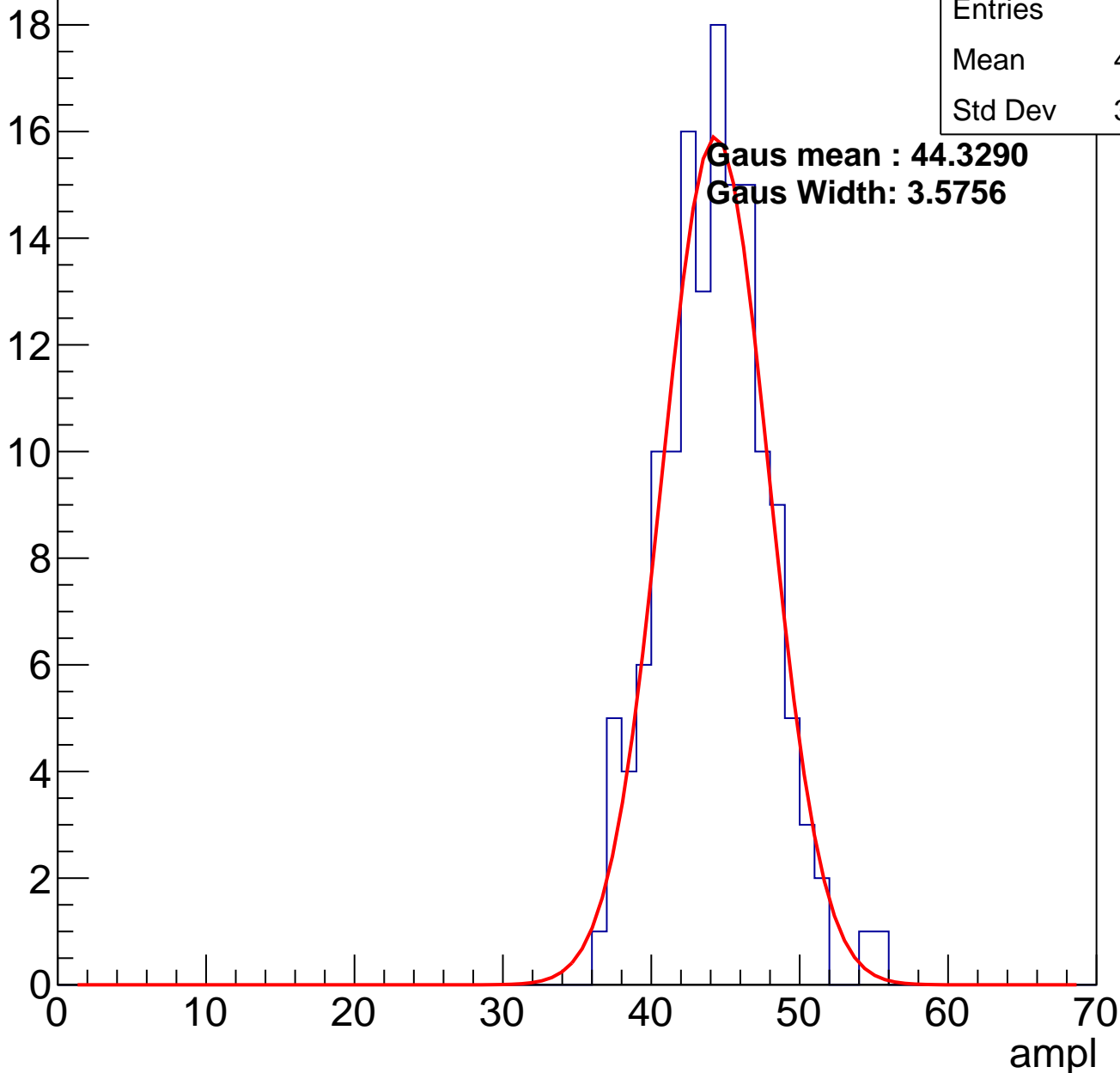
ampl



B0L101S, U22-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

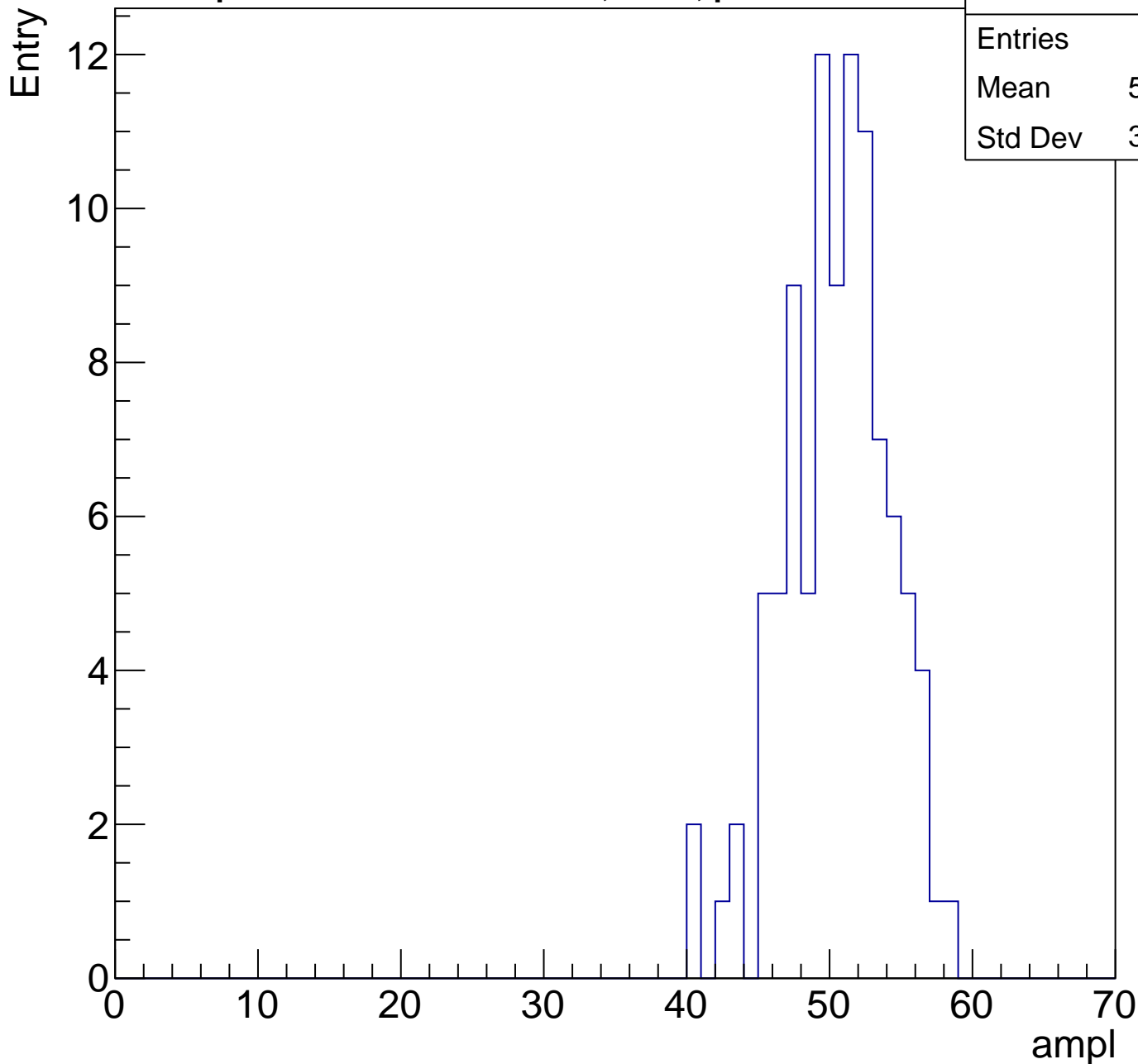
Entry



B0L101S, U22-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

| | |
|---------|-------|
| Entries | 97 |
| Mean | 50.07 |
| Std Dev | 3.628 |



B0L101S, U22-ch127, adc4

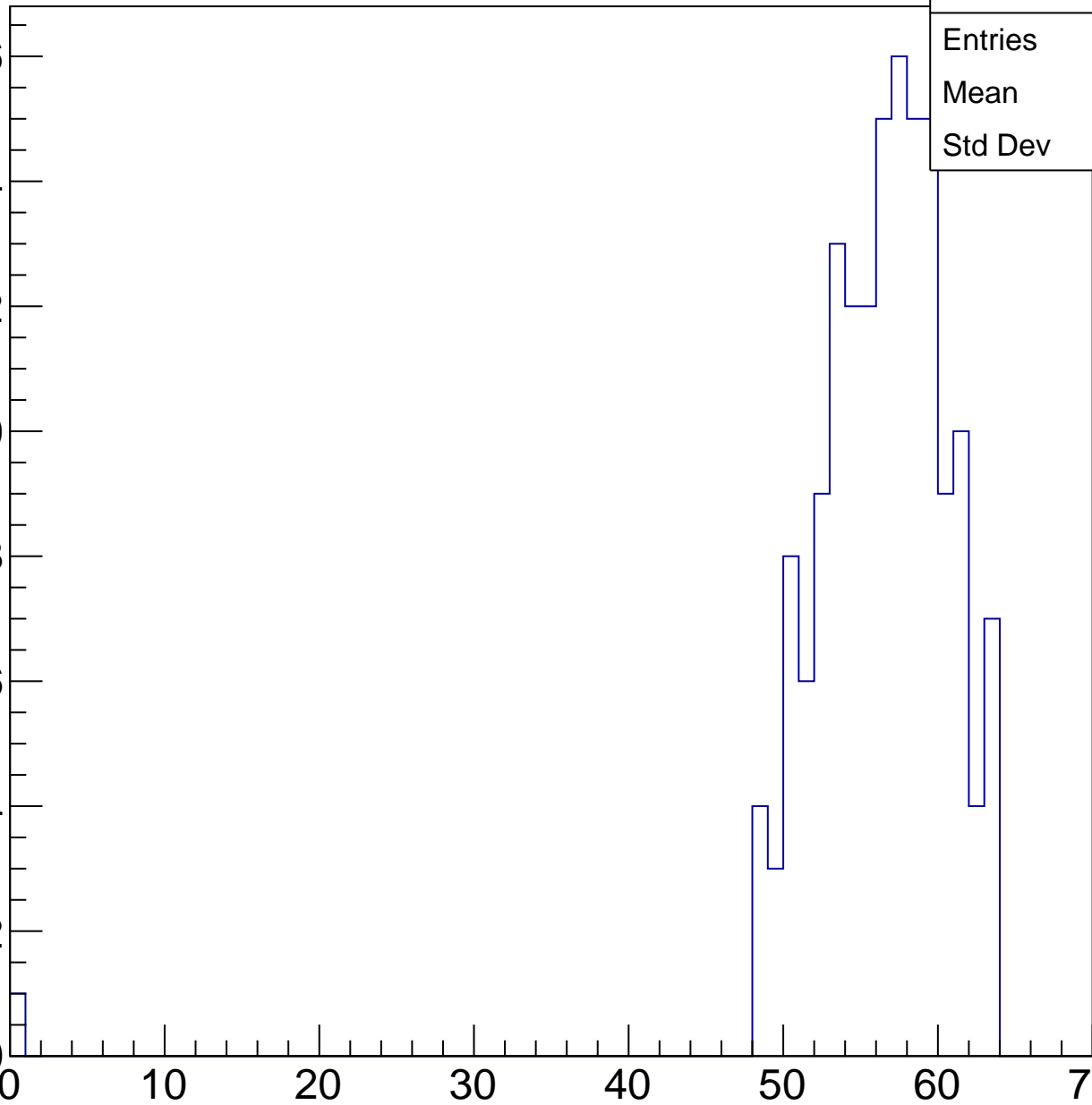
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 159 |
| Mean | 55.7 |
| Std Dev | 5.814 |

ampl



B0L101S, U22-ch127, adc5

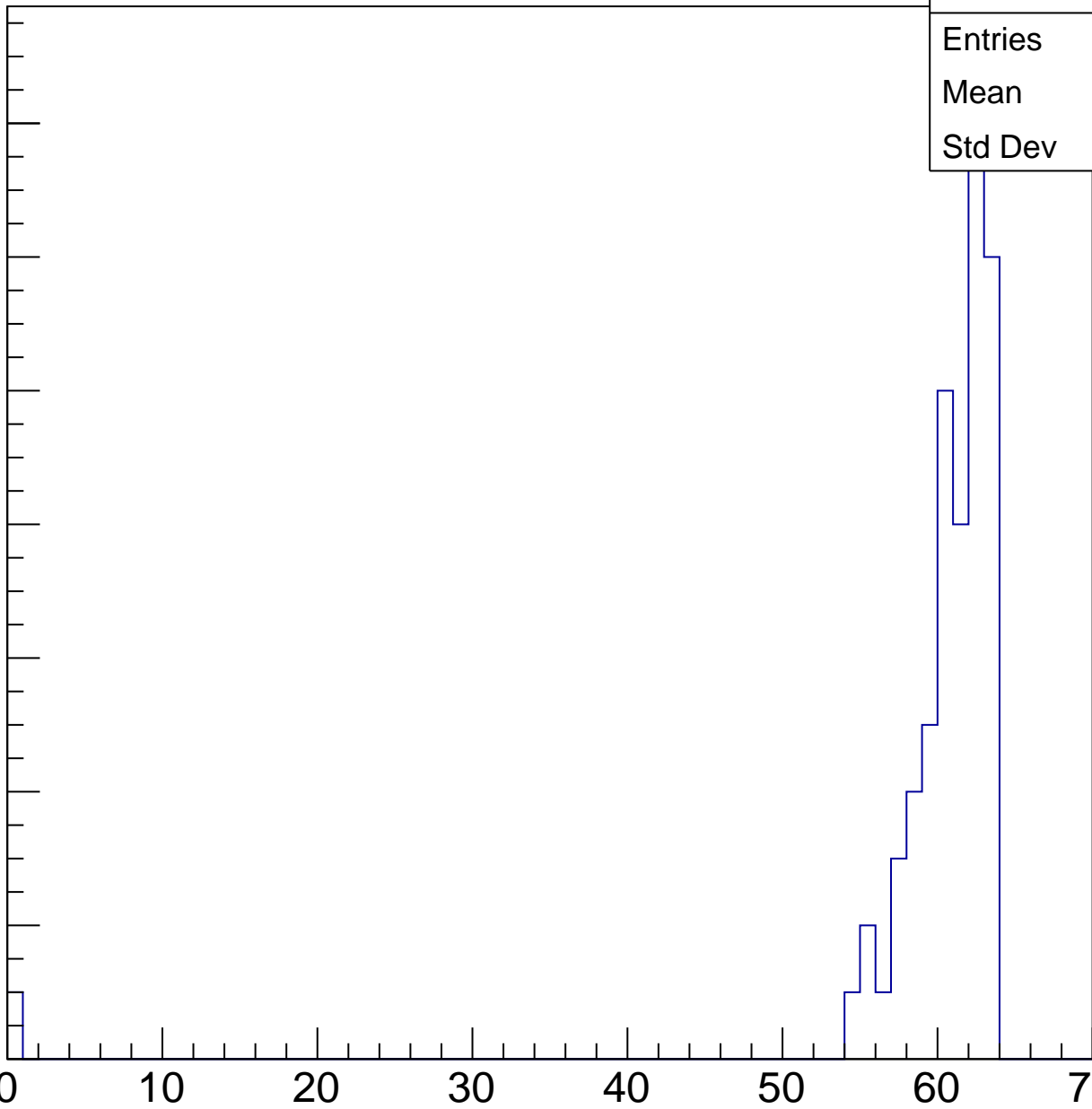
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.55 |
| Std Dev | 7.945 |

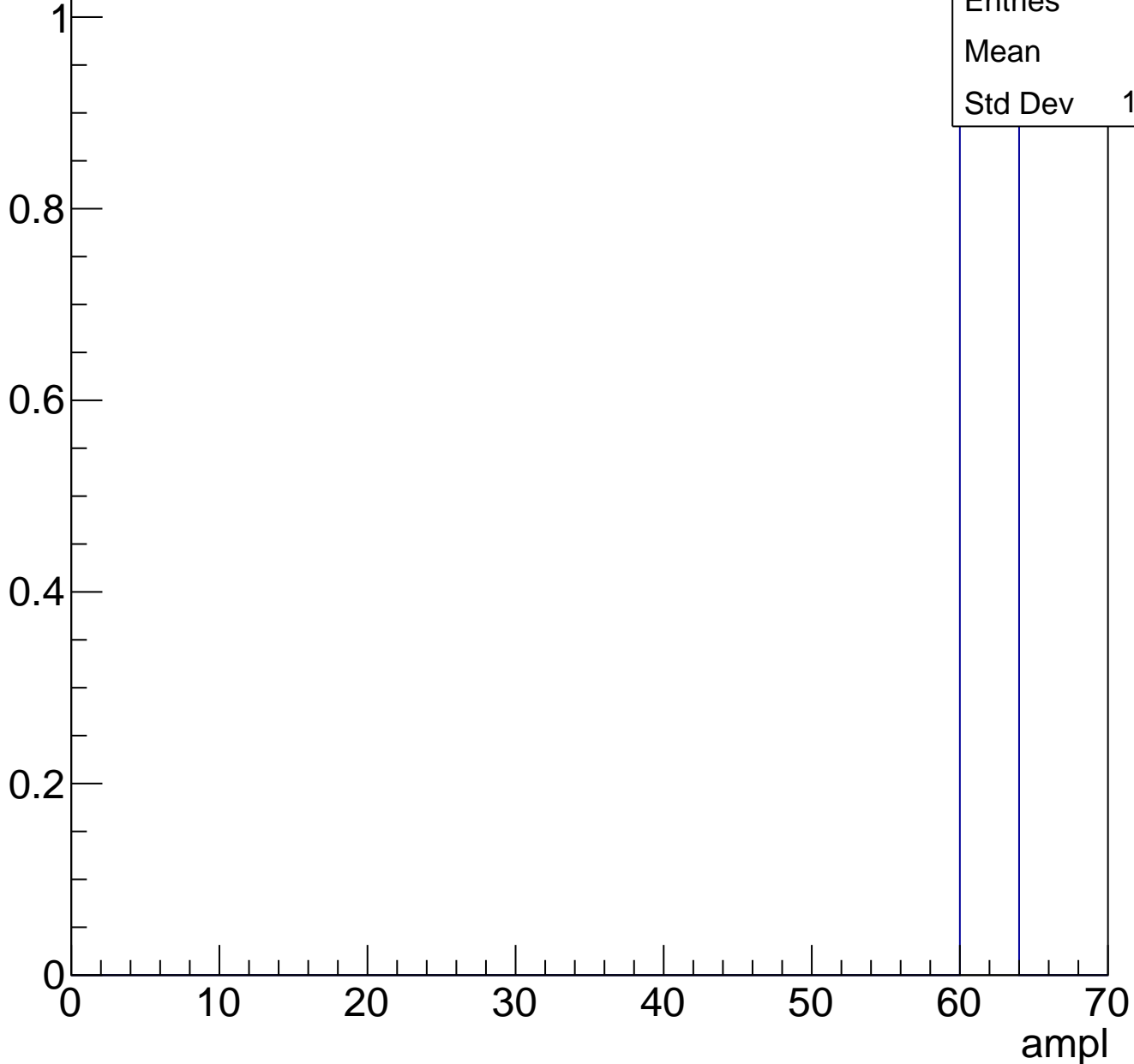
ampl



B0L101S, U22-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

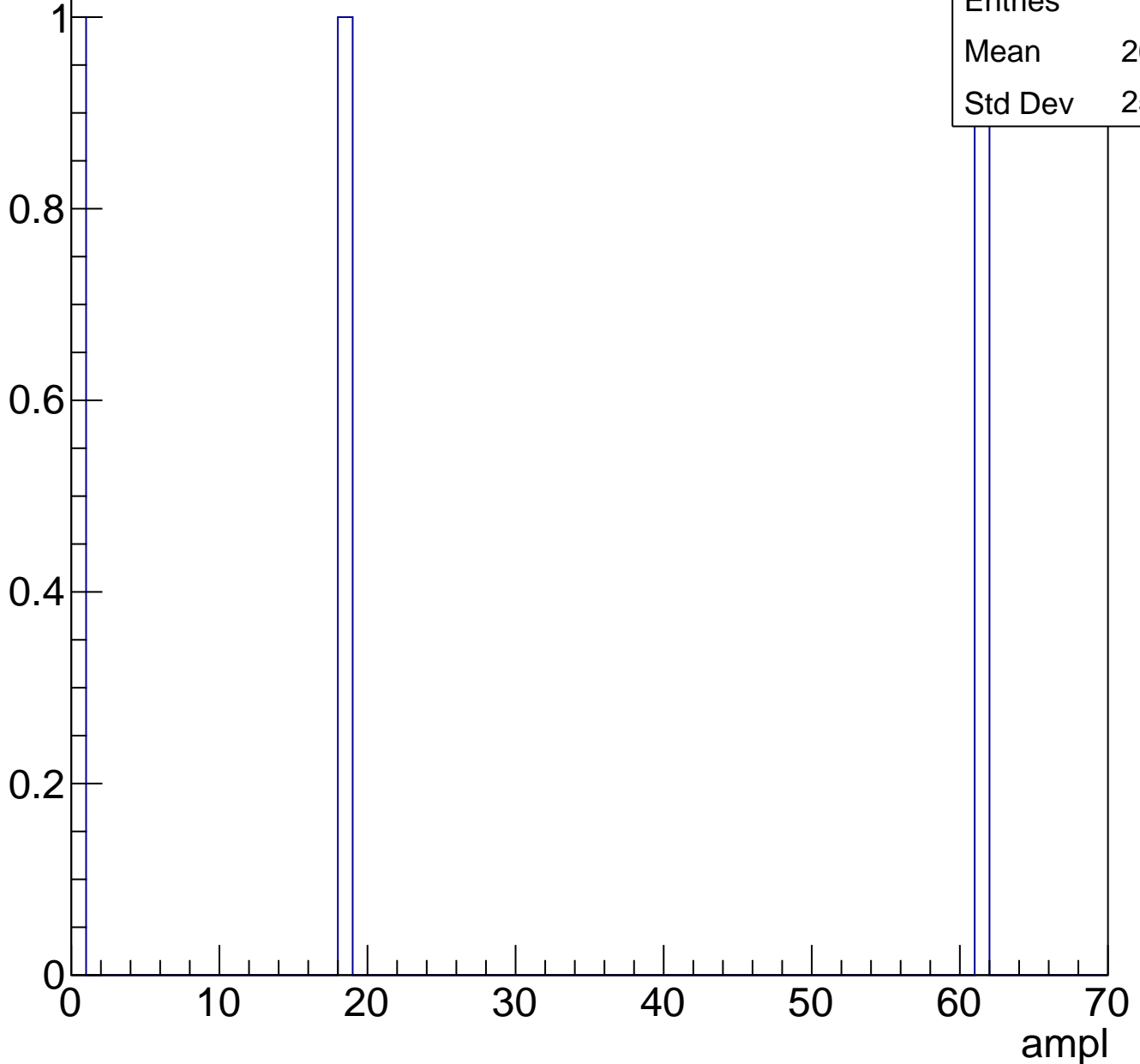
Entry



B0L101S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

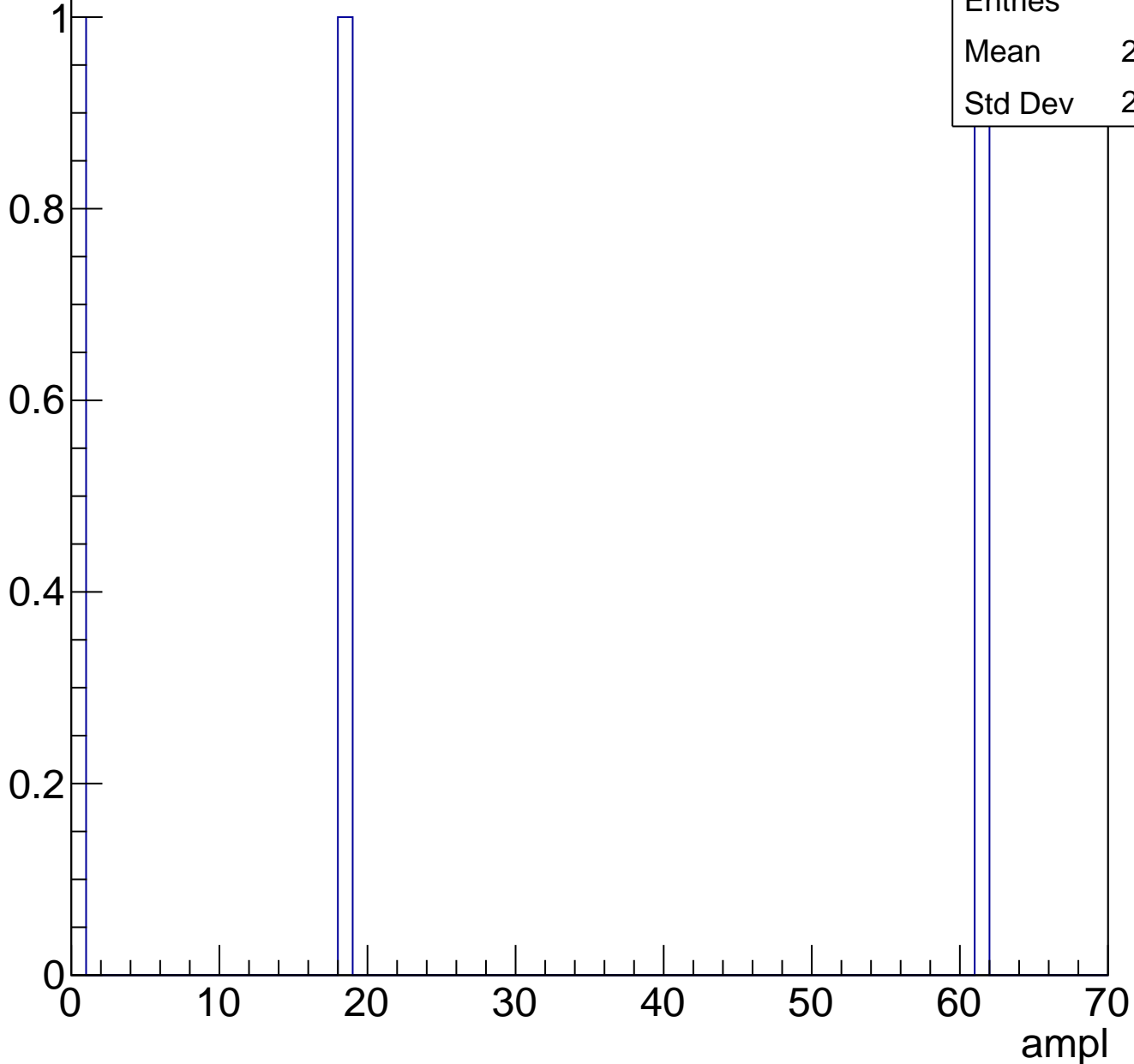


| | |
|---------|-------|
| Entries | 3 |
| Mean | 26.33 |
| Std Dev | 25.59 |

B0L101S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 26.33 |
| Std Dev | 25.59 |