

B1L100S, U10-ch0, adc0

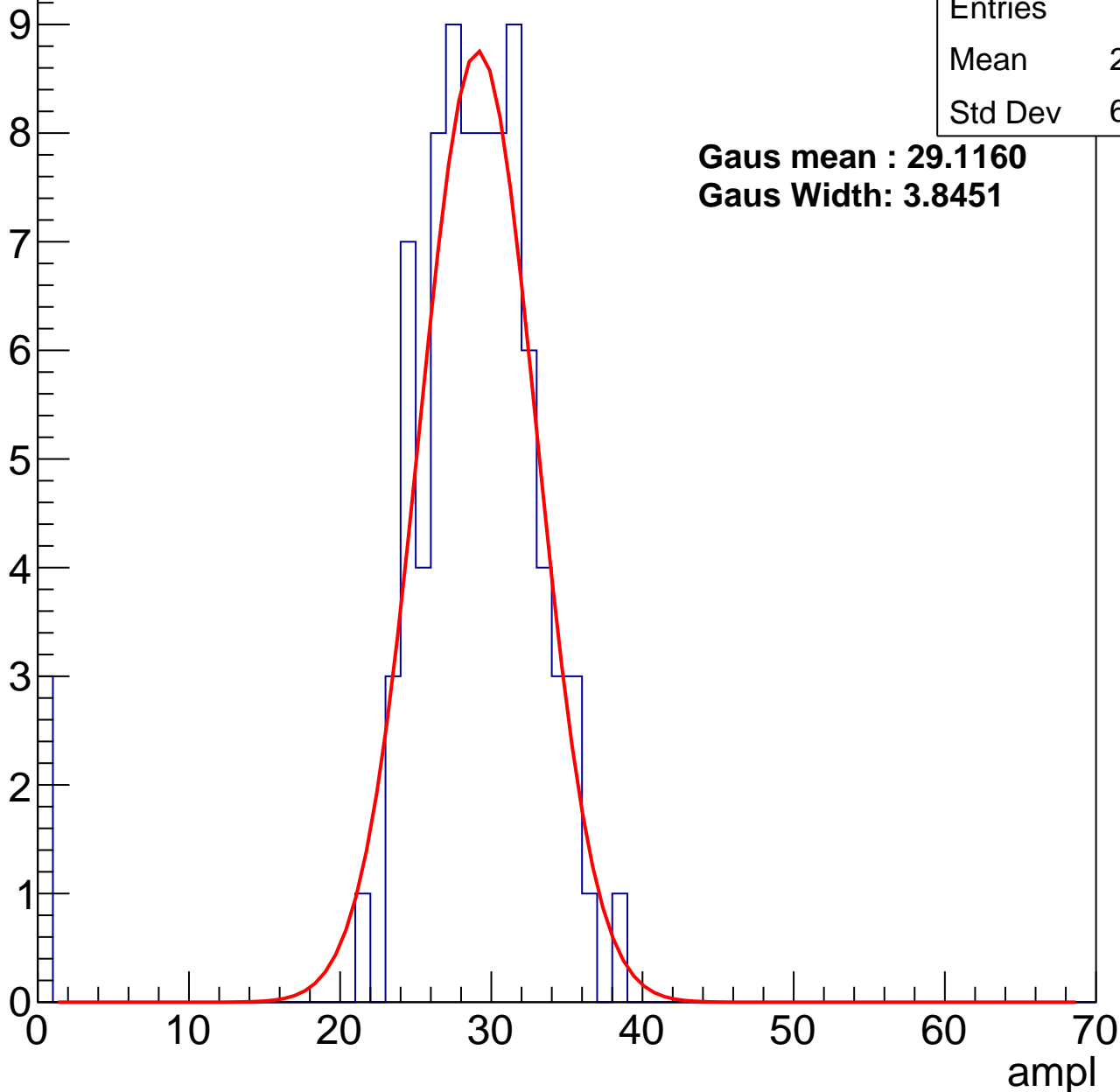
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	27.78
Std Dev	6.285

Gaus mean : 29.160

Gaus Width: 3.8451



B1L100S, U10-ch0, adc1

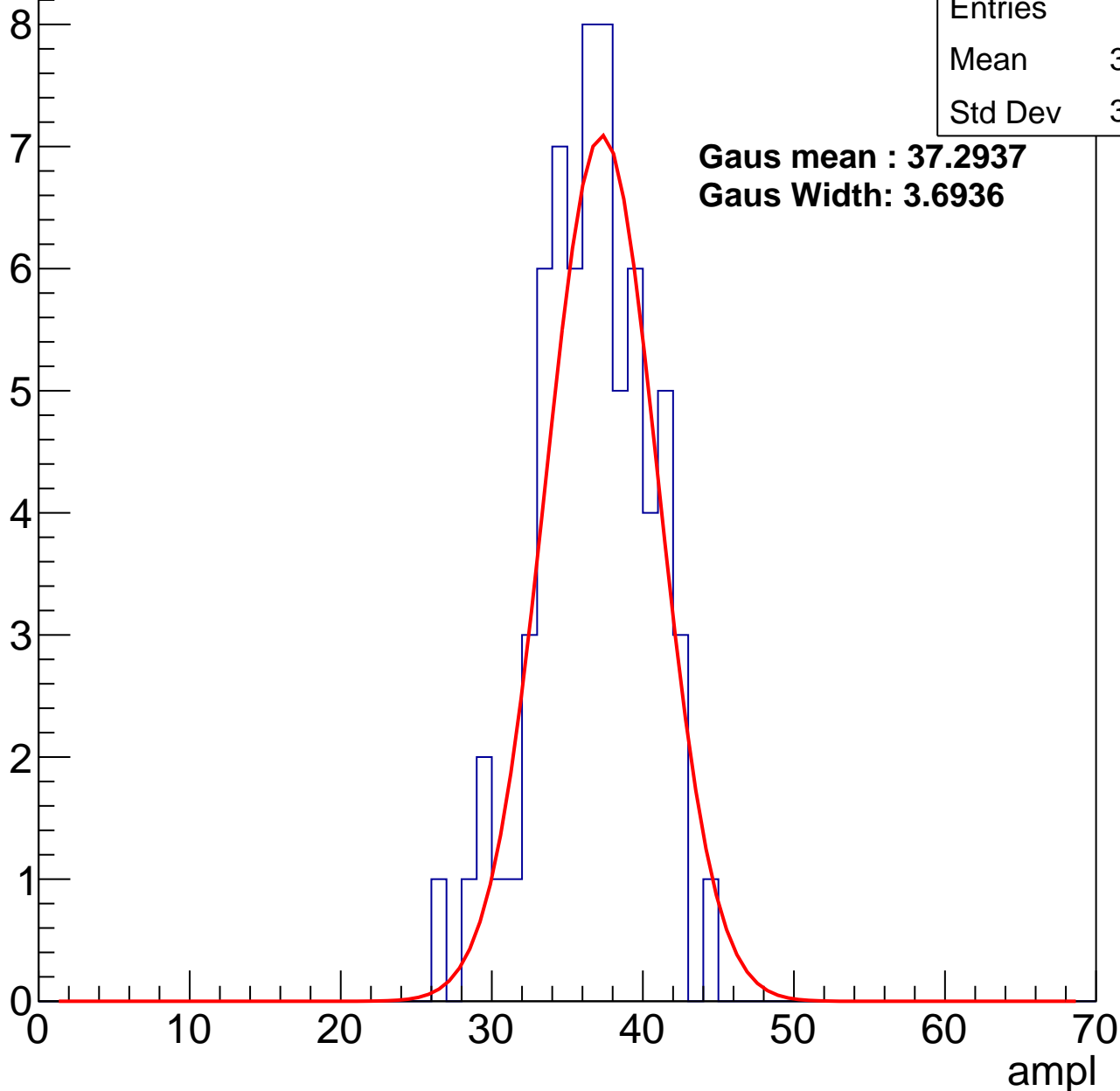
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.15
Std Dev	3.639

Gaus mean : 37.2937

Gaus Width: 3.6936

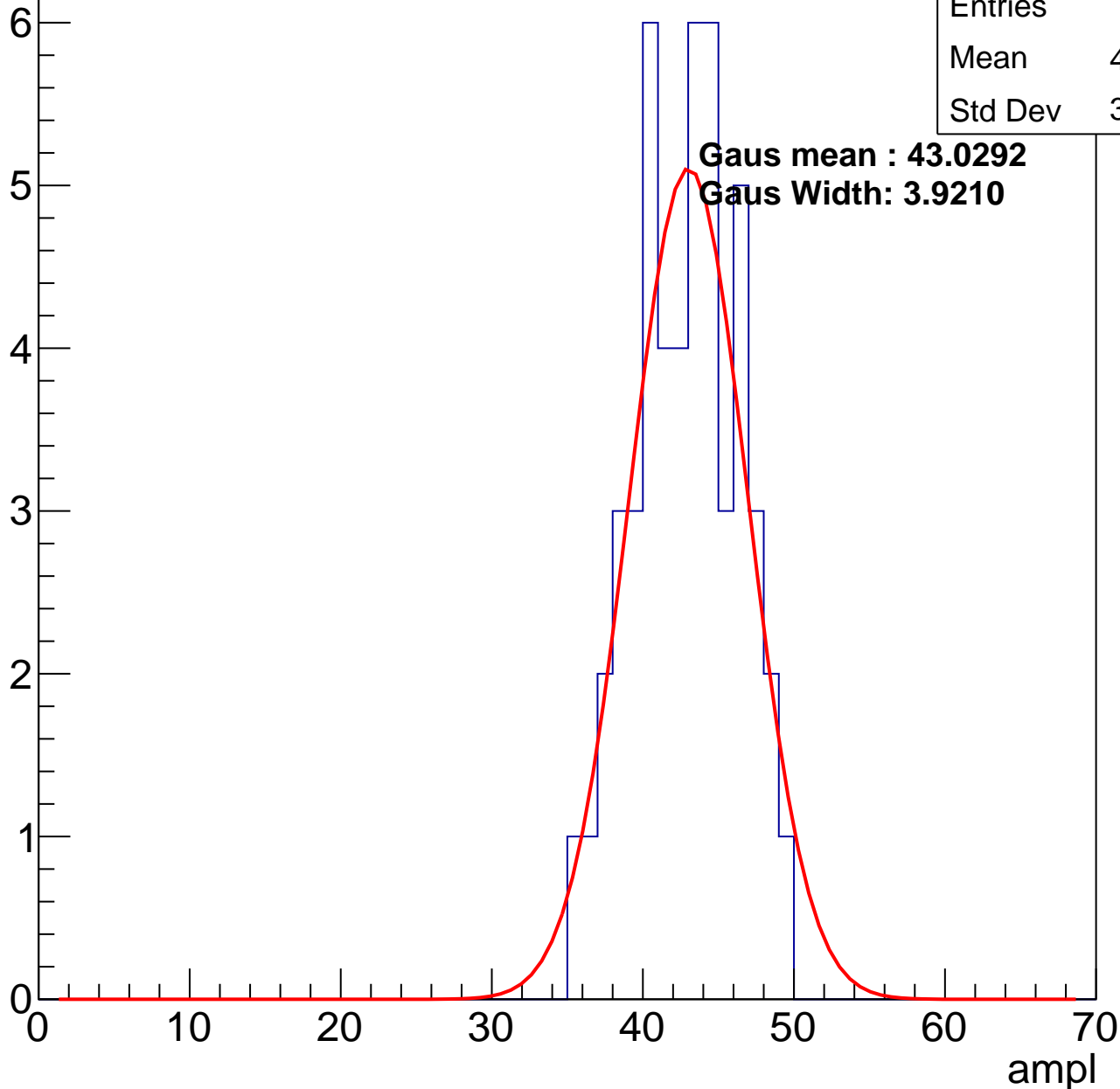


B1L100S, U10-ch0, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	42.42
Std Dev	3.353



B1L100S, U10-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

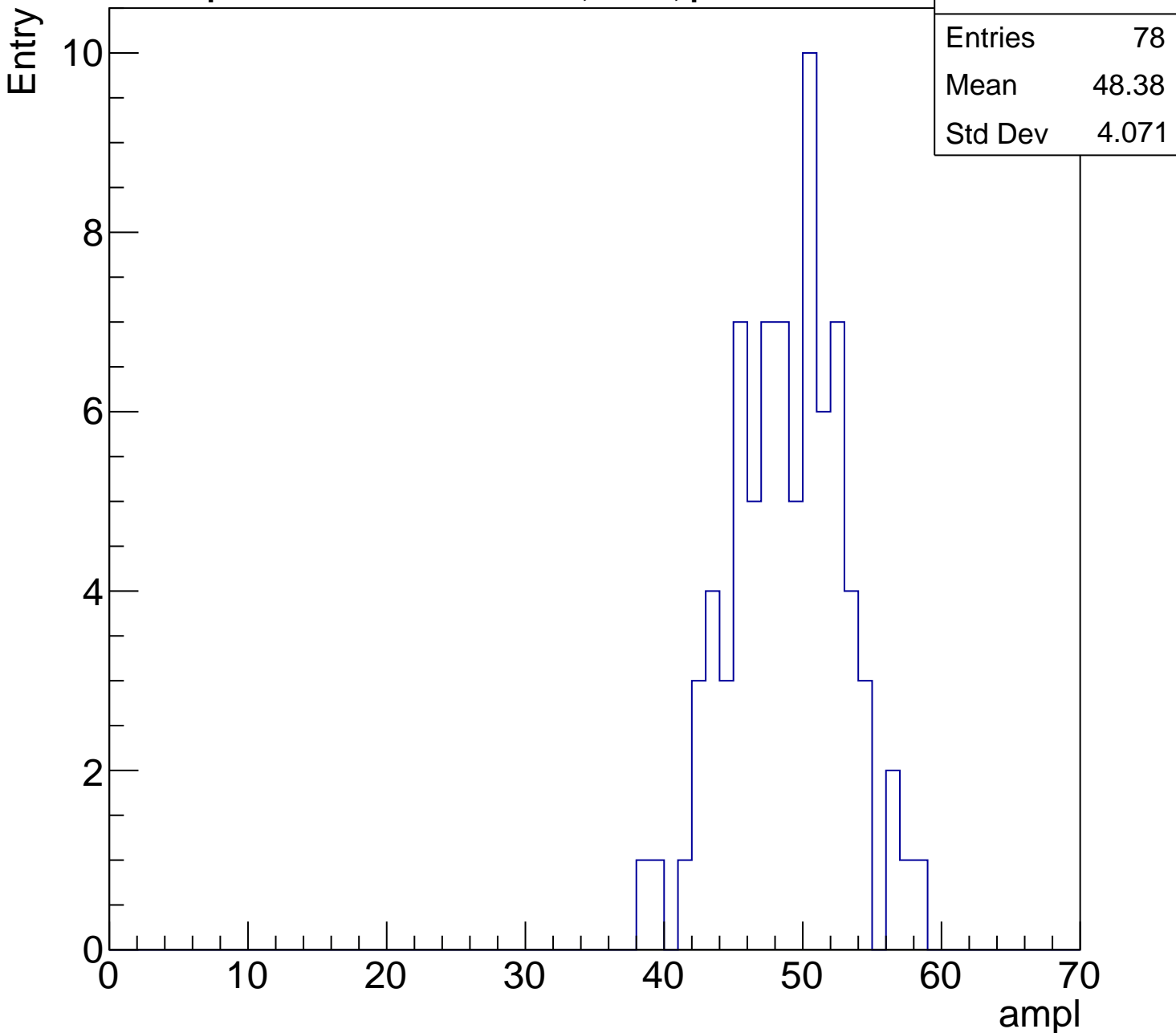
Entries	78
Mean	48.38
Std Dev	4.071

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

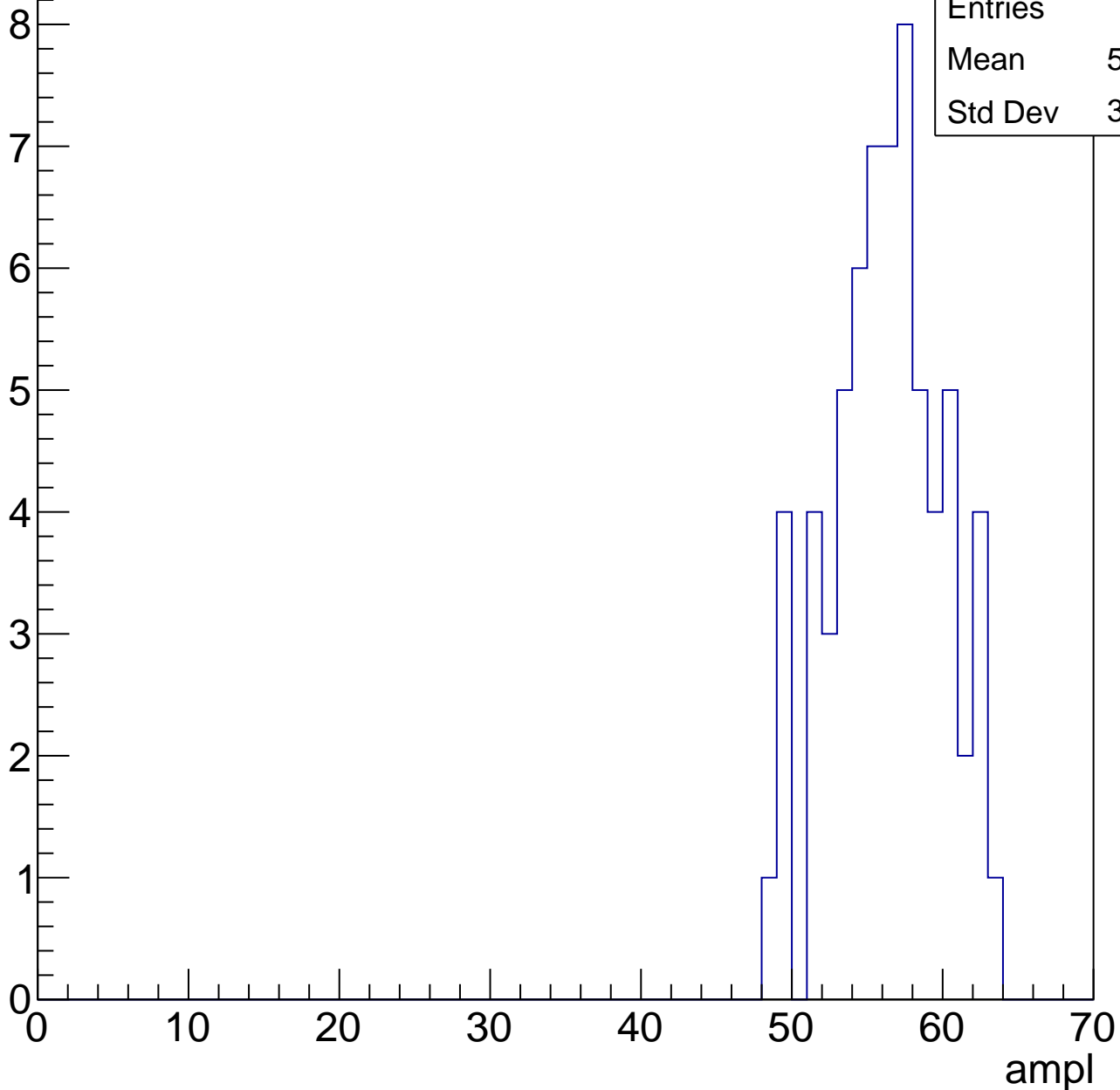


B1L100S, U10-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

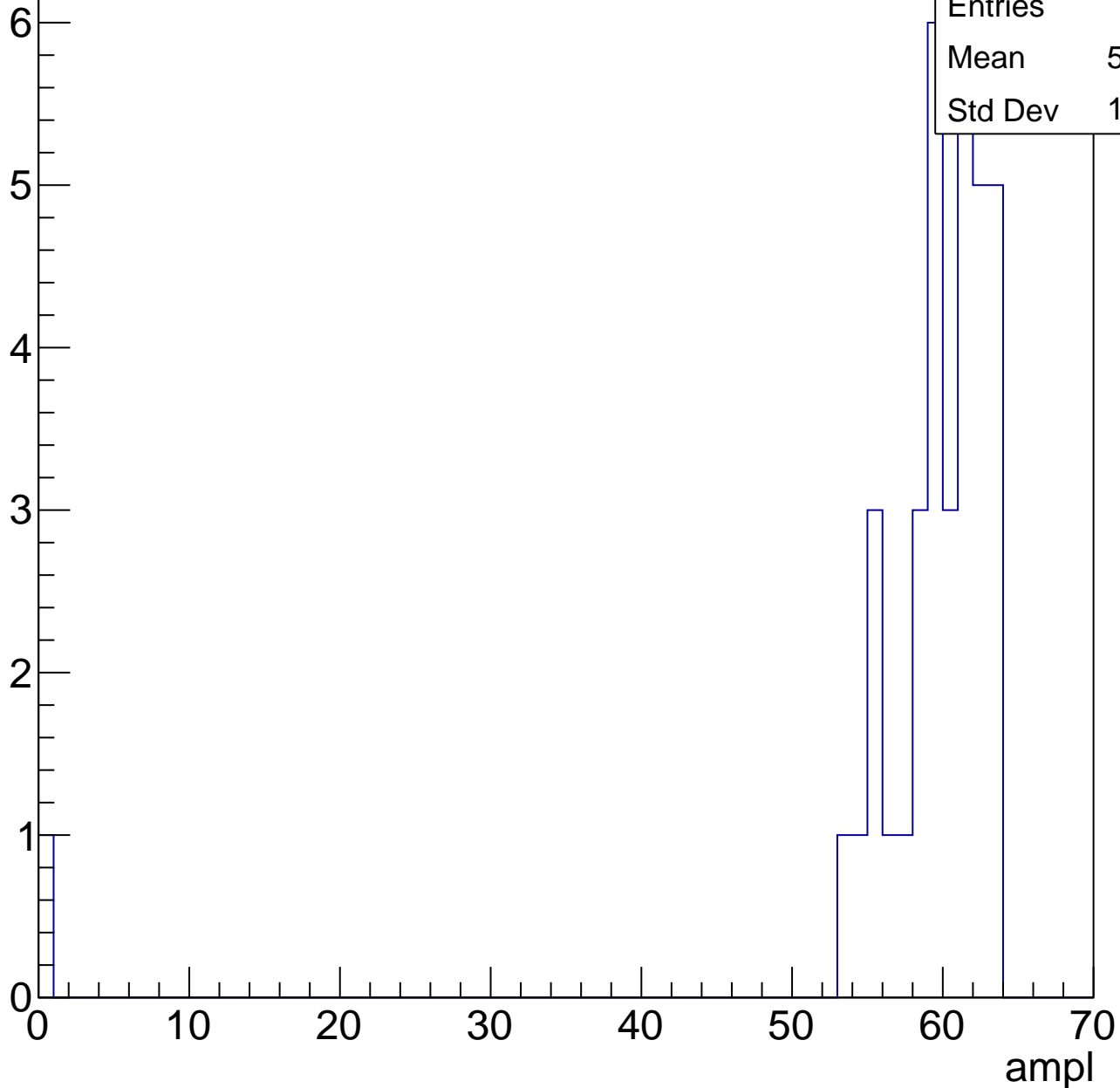
Entries	66
Mean	55.83
Std Dev	3.629



B1L100S, U10-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

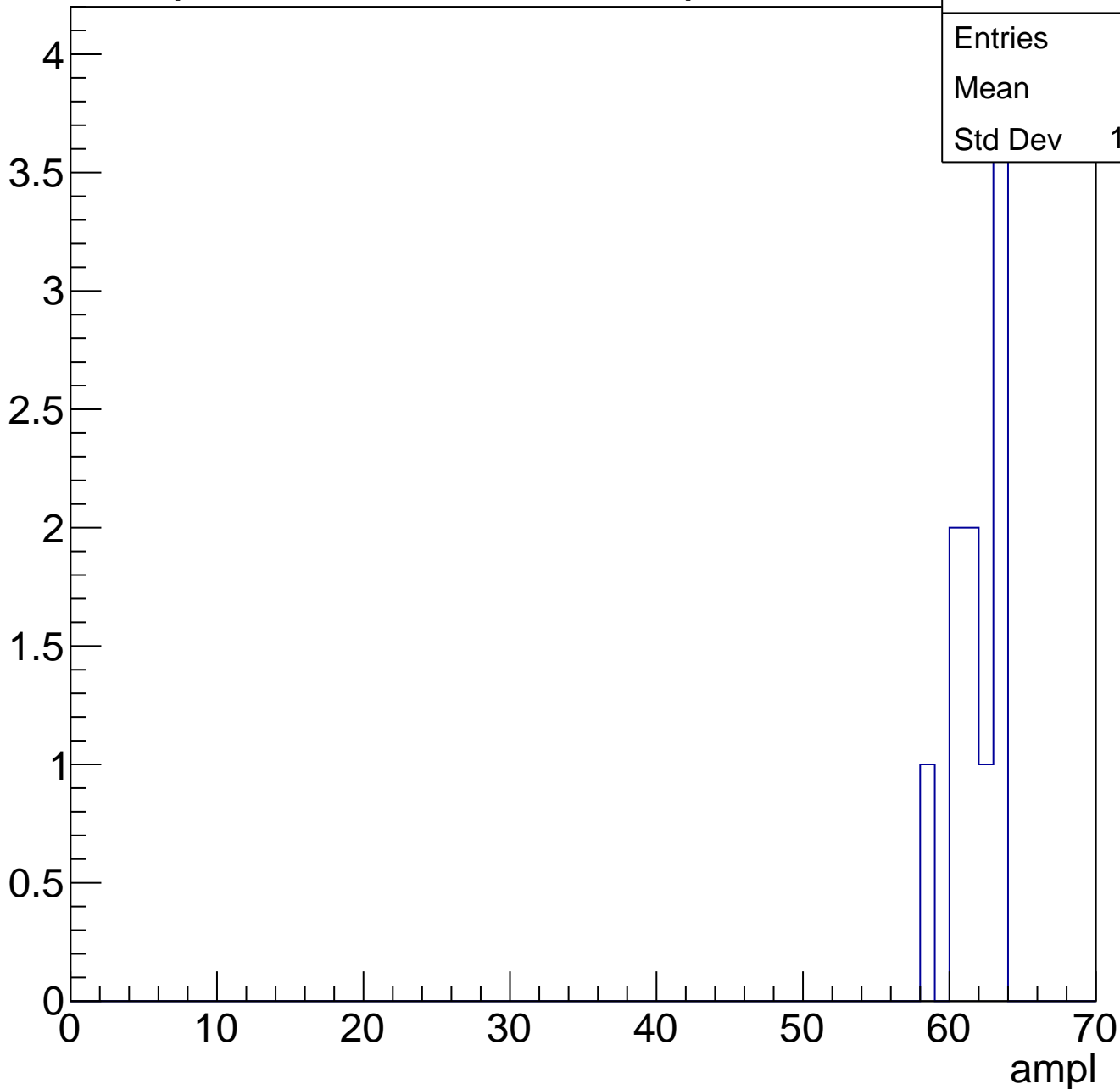
Entry



B1L100S, U10-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch1, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	95
Mean	30.15
Std Dev	3.719

Gaus mean : 30.5475

Gaus Width: 3.9470

10

8

6

4

2

0

0

10

20

30

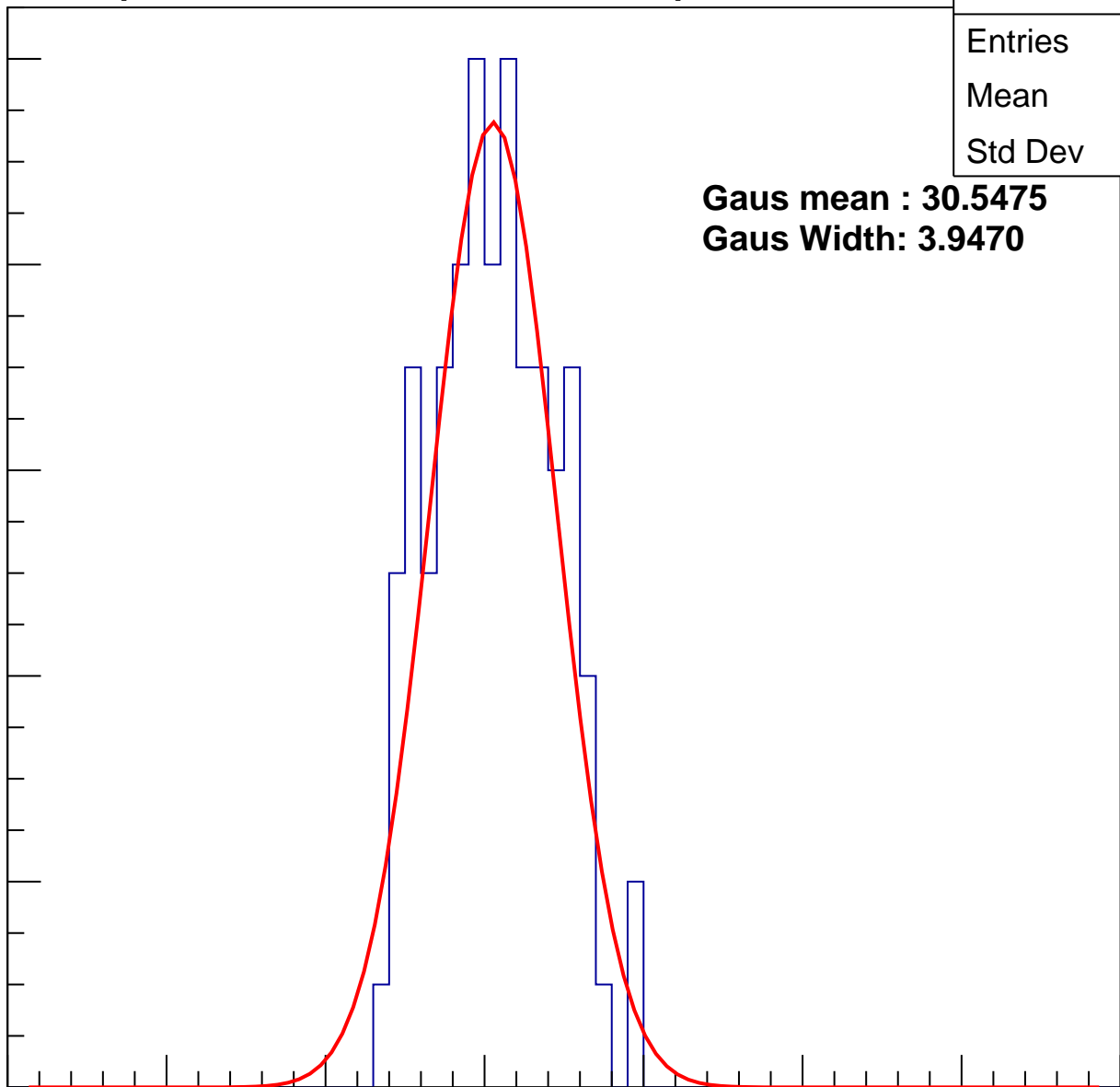
40

50

60

70

ampl



B1L100S, U10-ch1, adc1

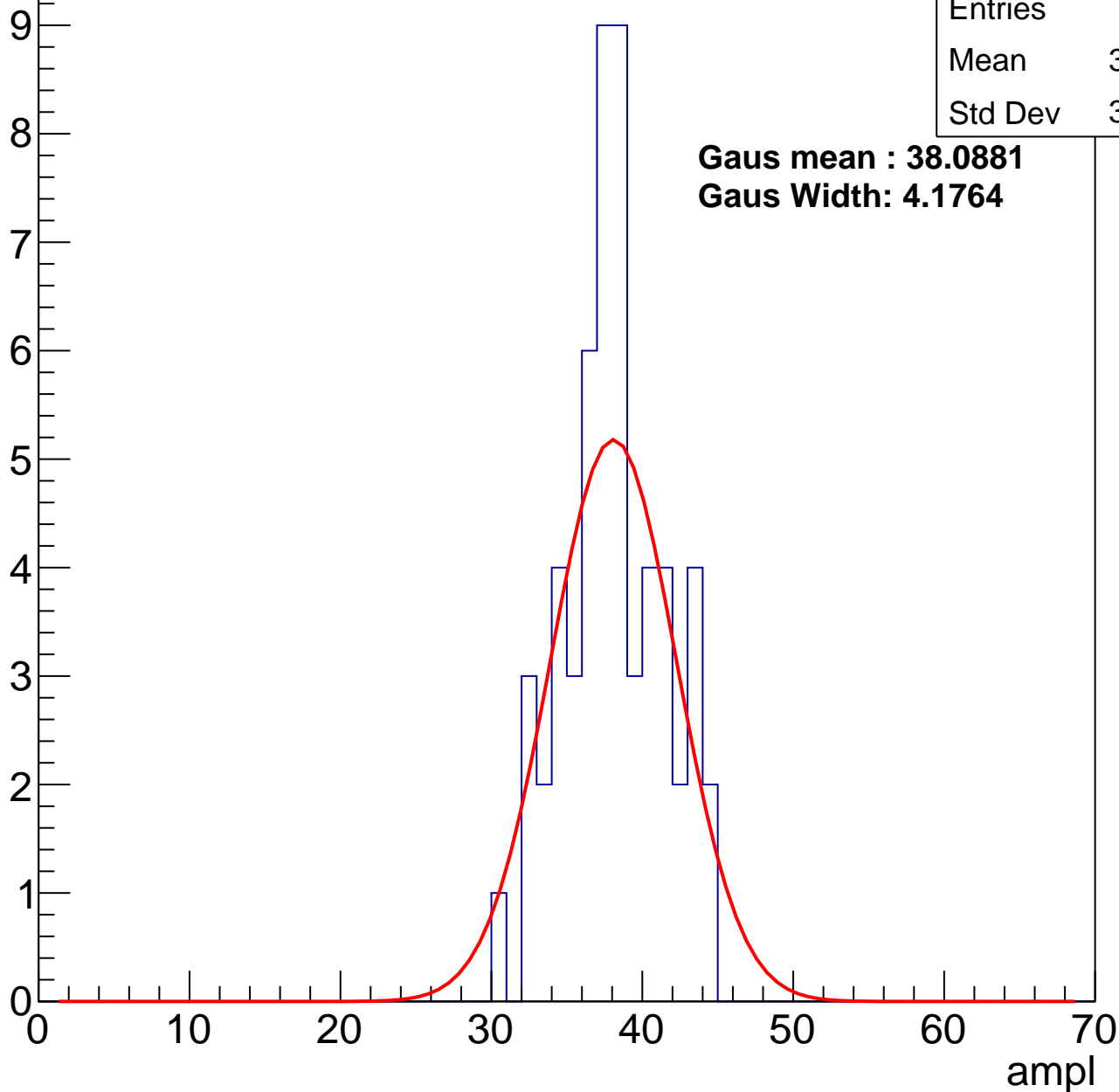
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.66
Std Dev	3.269

Gaus mean : 38.0881

Gaus Width: 4.1764



B1L100S, U10-ch1, adc2

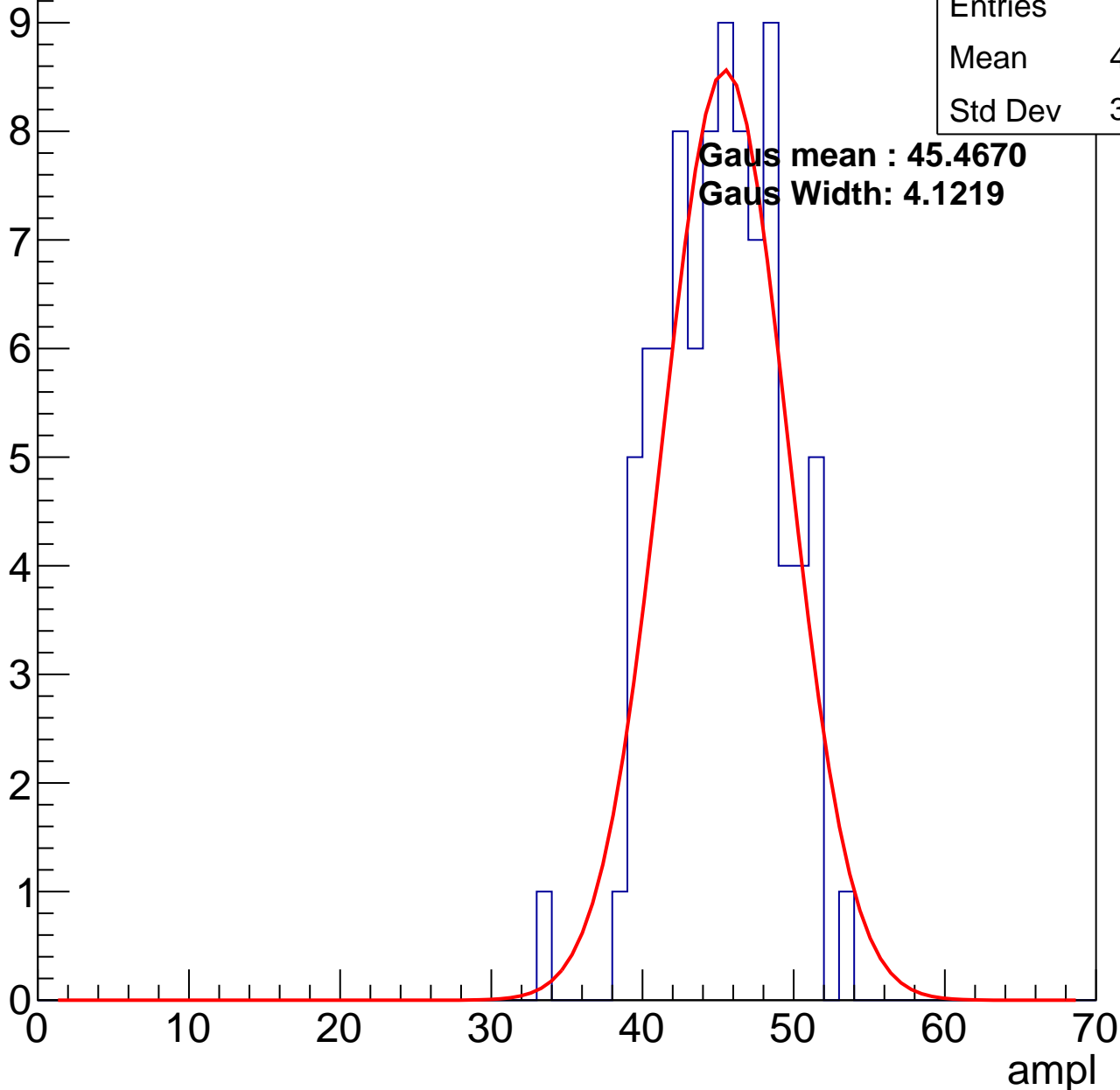
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	44.73
Std Dev	3.756

Gaus mean : 45.4670

Gaus Width: 4.1219

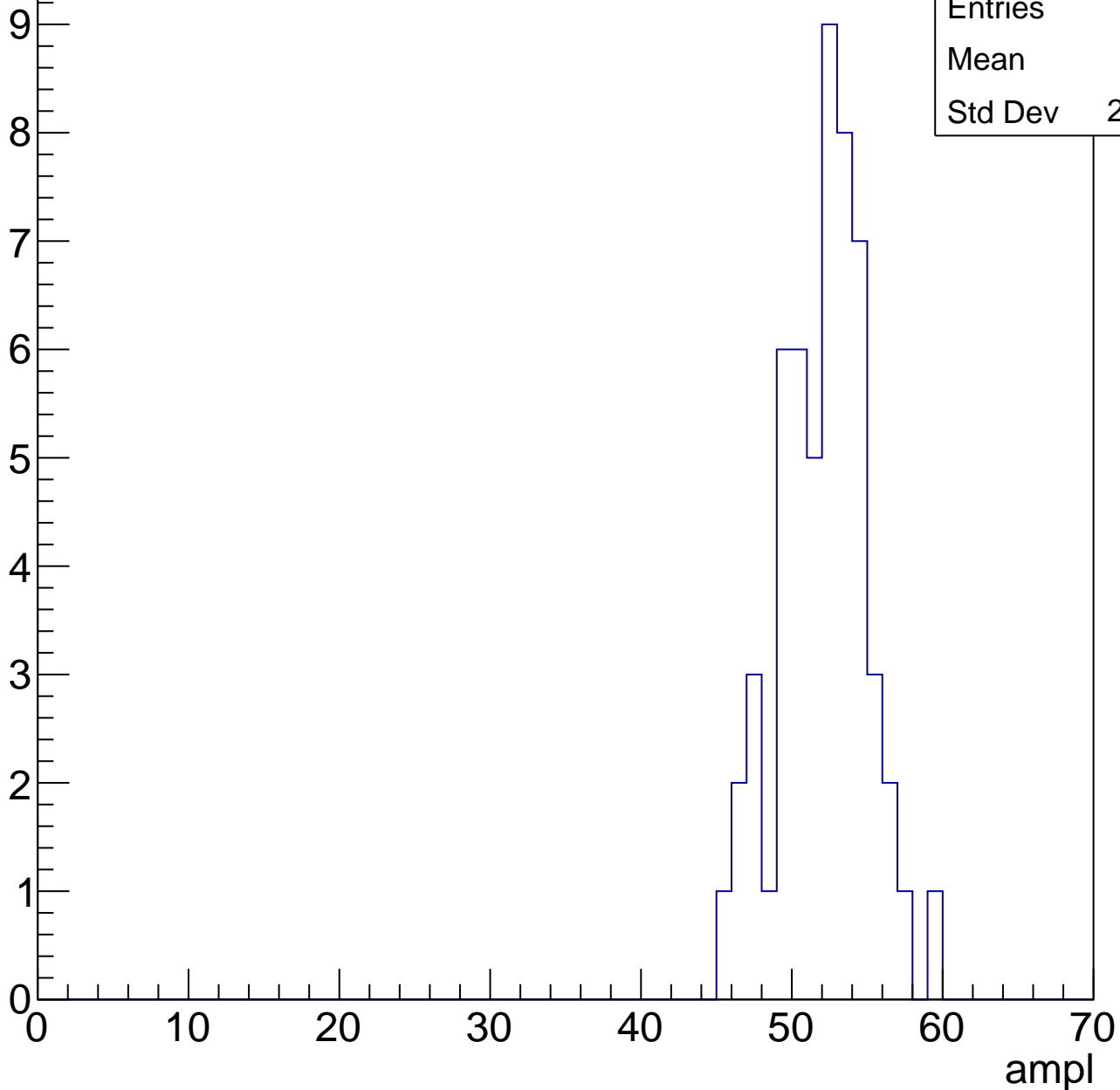


B1L100S, U10-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	51.6
Std Dev	2.877

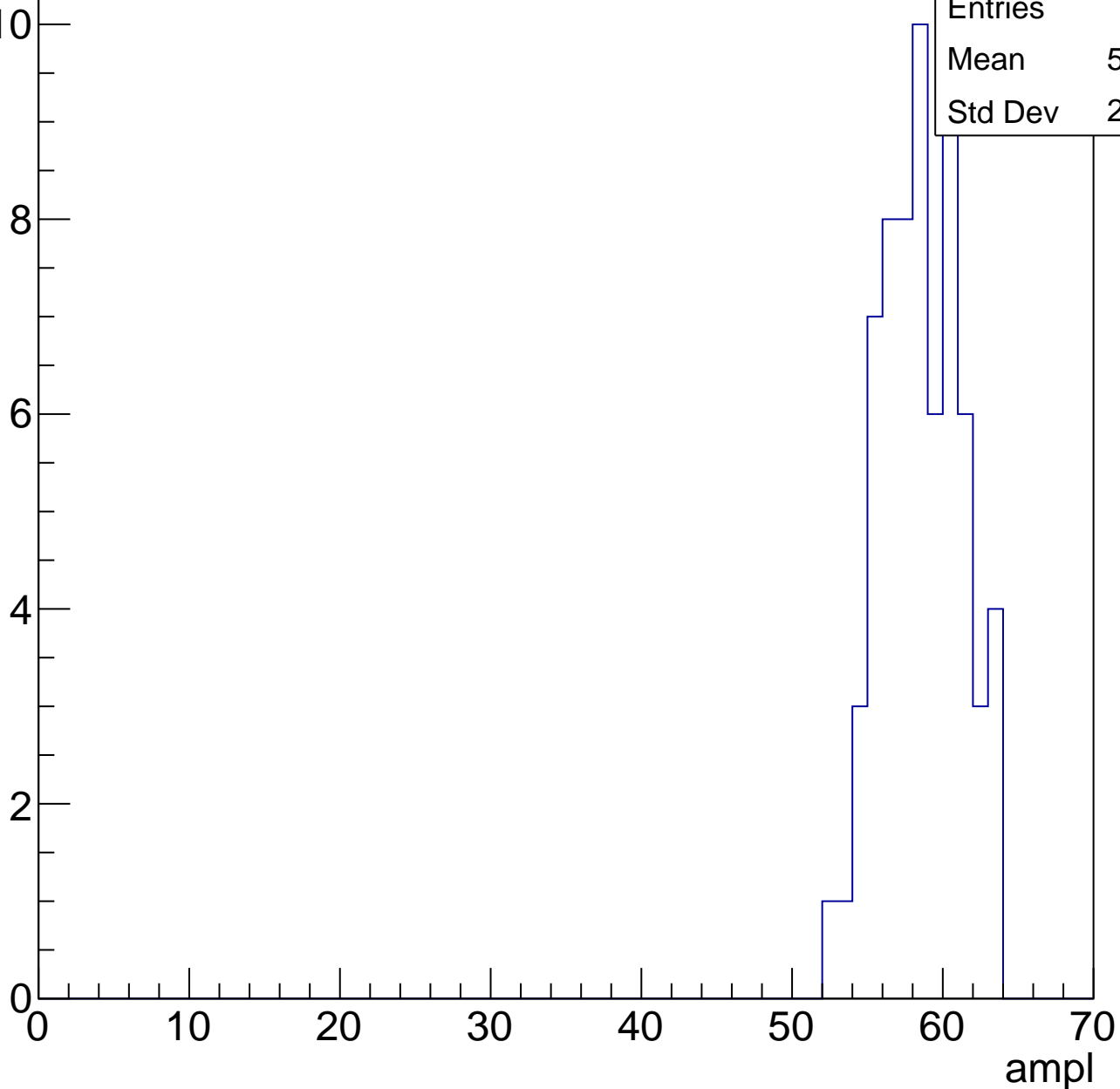


B1L100S, U10-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

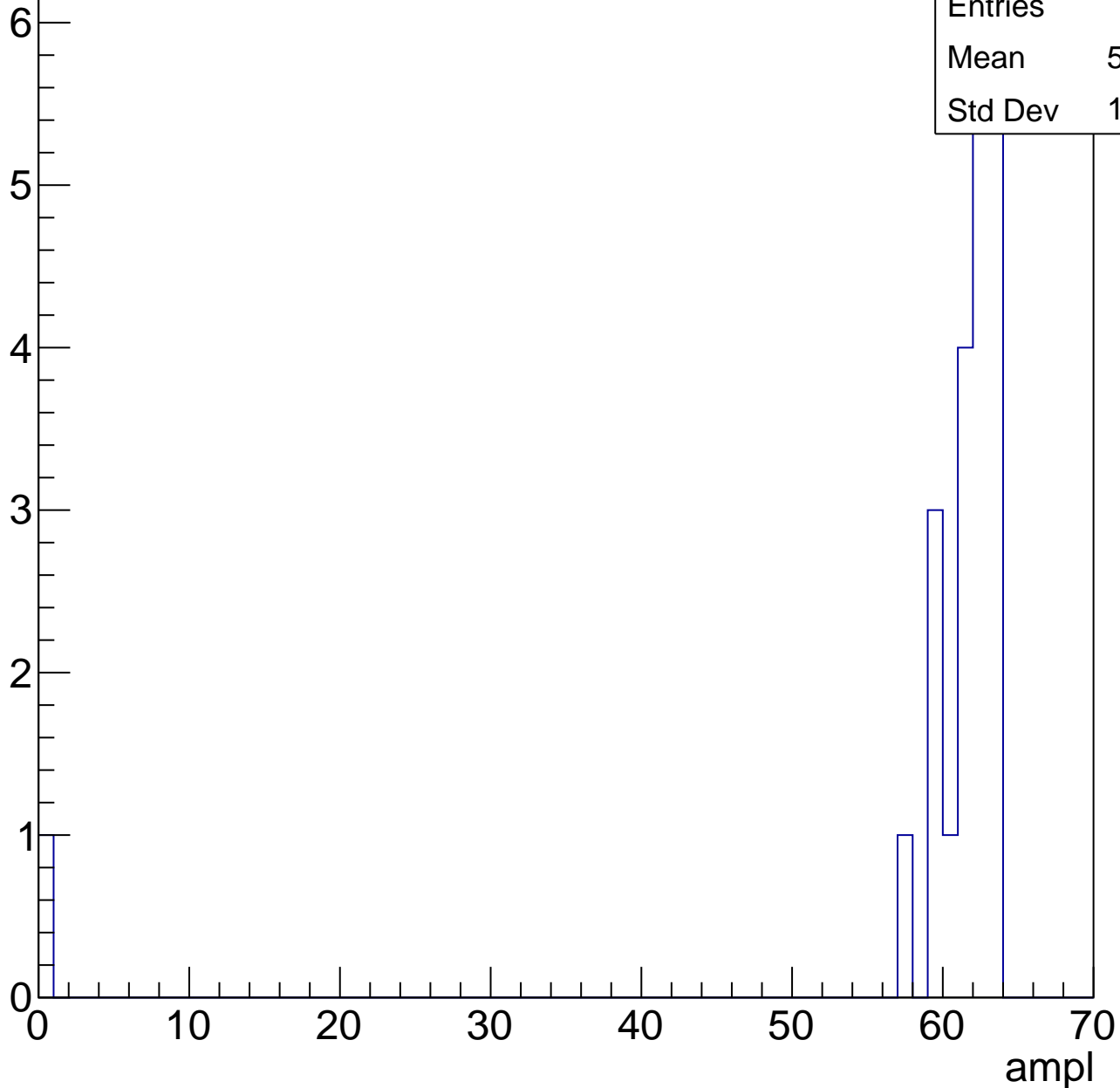
Entries	66
Mean	58.09
Std Dev	2.627



B1L100S, U10-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

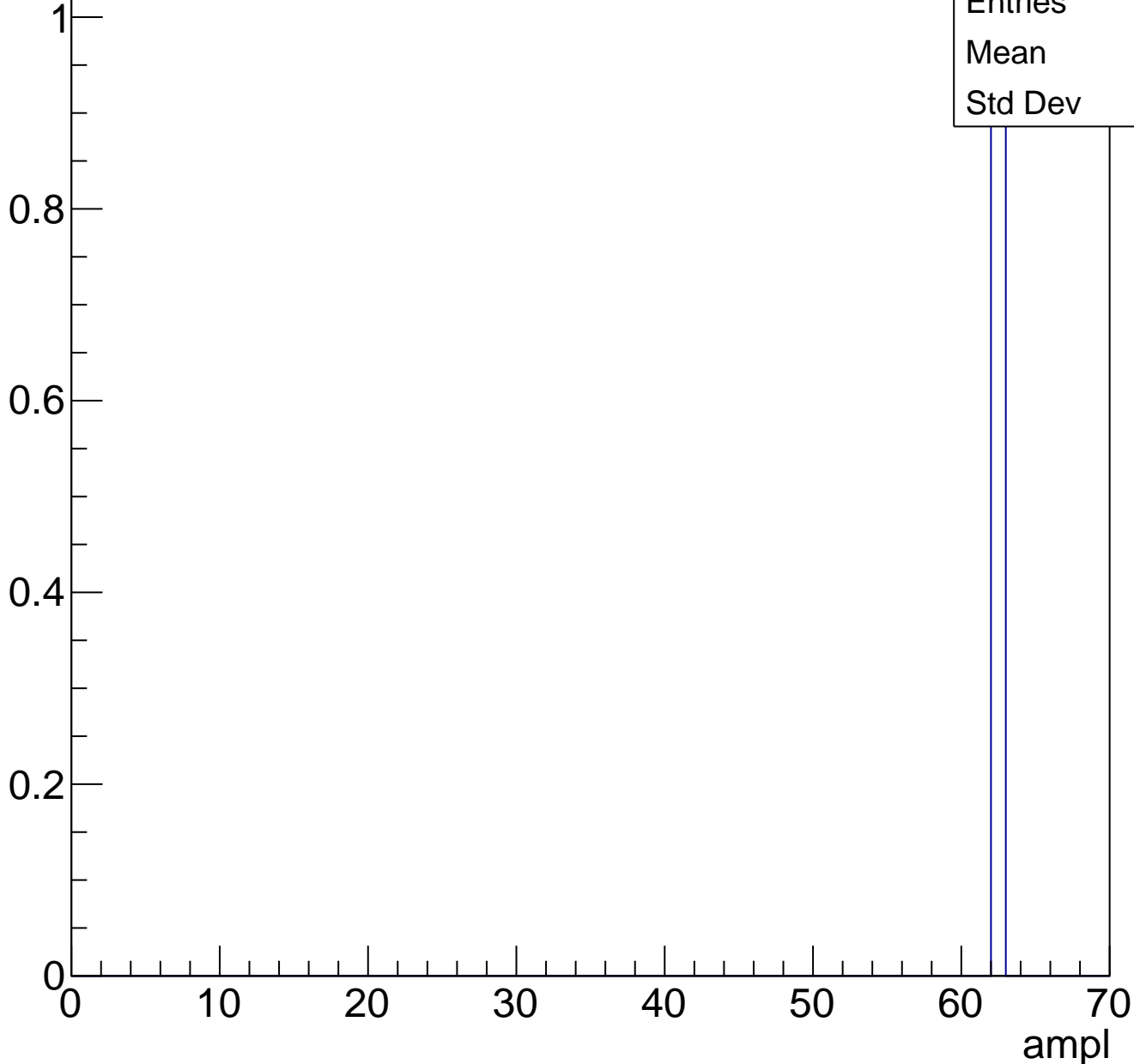
Entry



B1L100S, U10-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

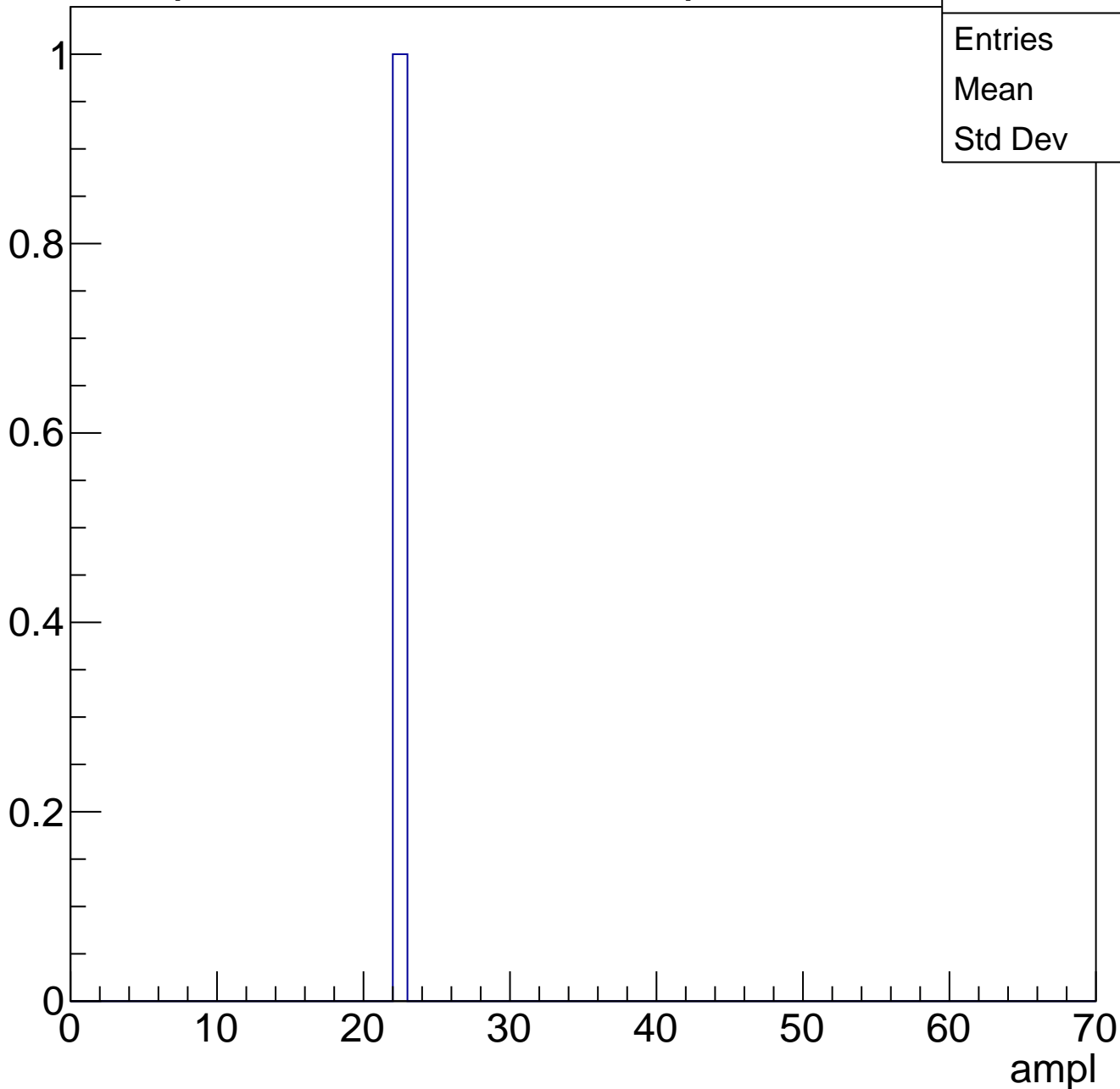
Entry



B1L100S, U10-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U10-ch2, adc0

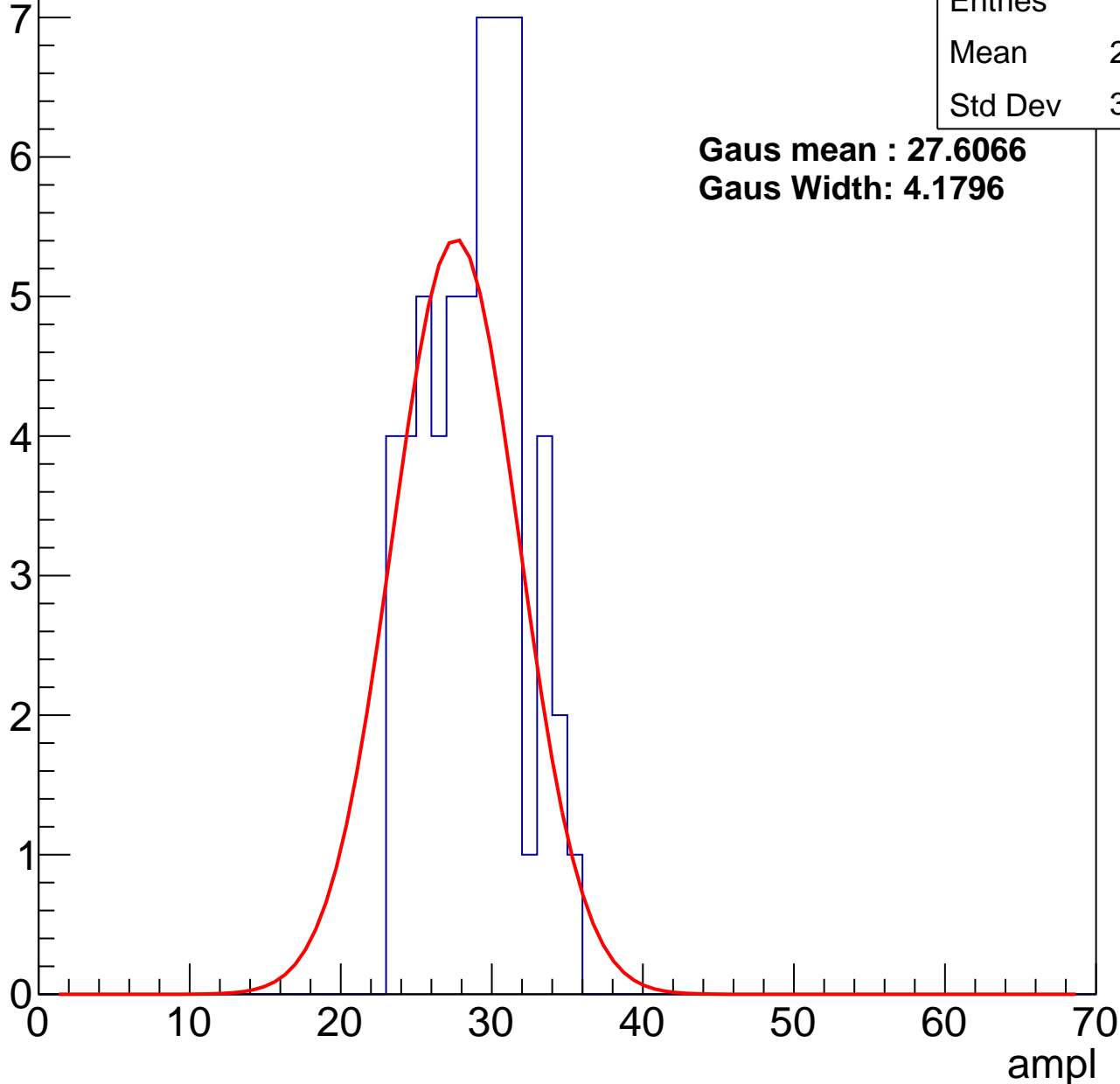
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	28.38
Std Dev	3.154

Gaus mean : 27.6066

Gaus Width: 4.1796



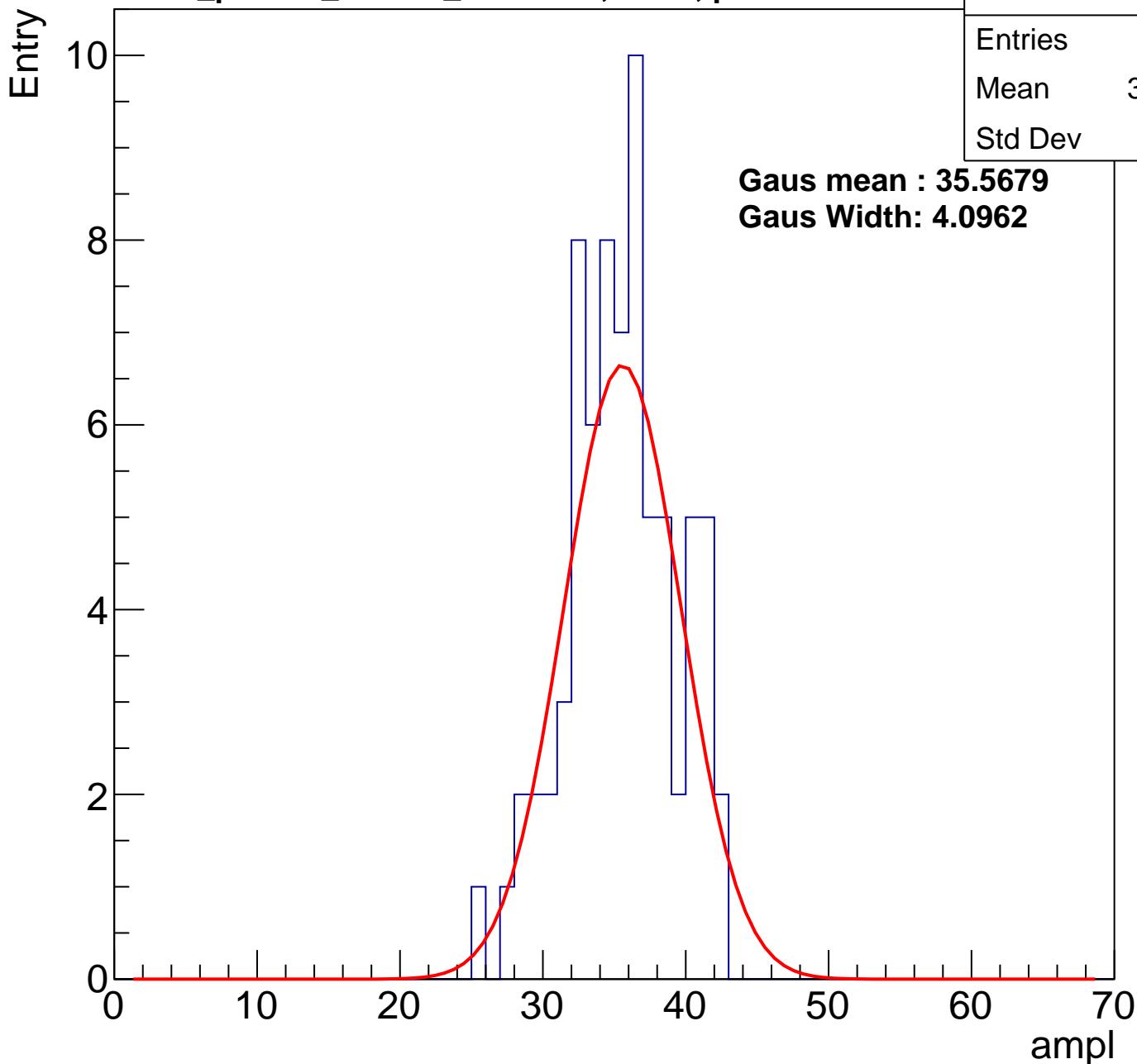
B1L100S, U10-ch2, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	35.03
Std Dev	3.76

Gaus mean : 35.5679

Gaus Width: 4.0962



B1L100S, U10-ch2, adc2

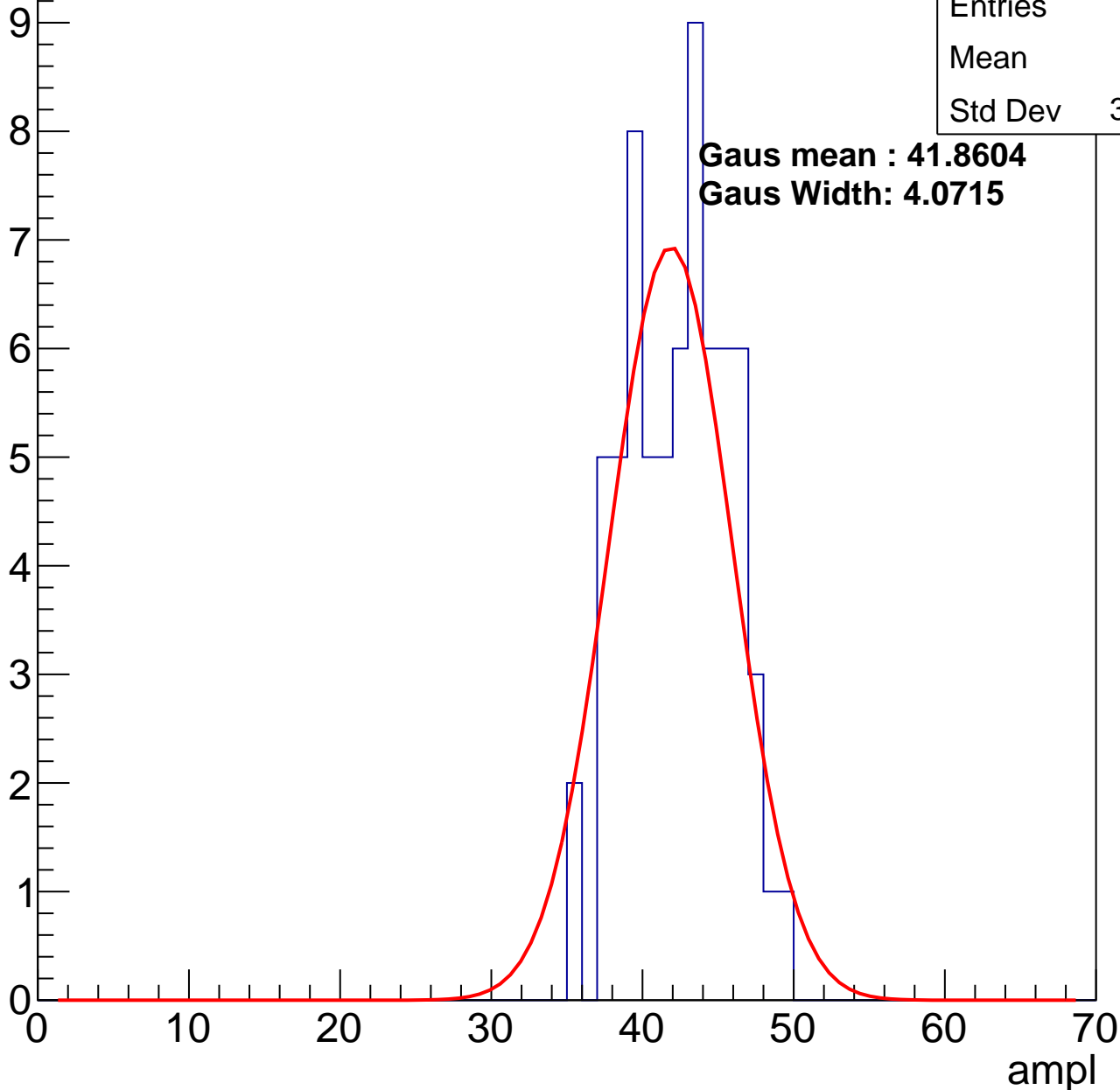
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	41.9
Std Dev	3.304

Gaus mean : 41.8604

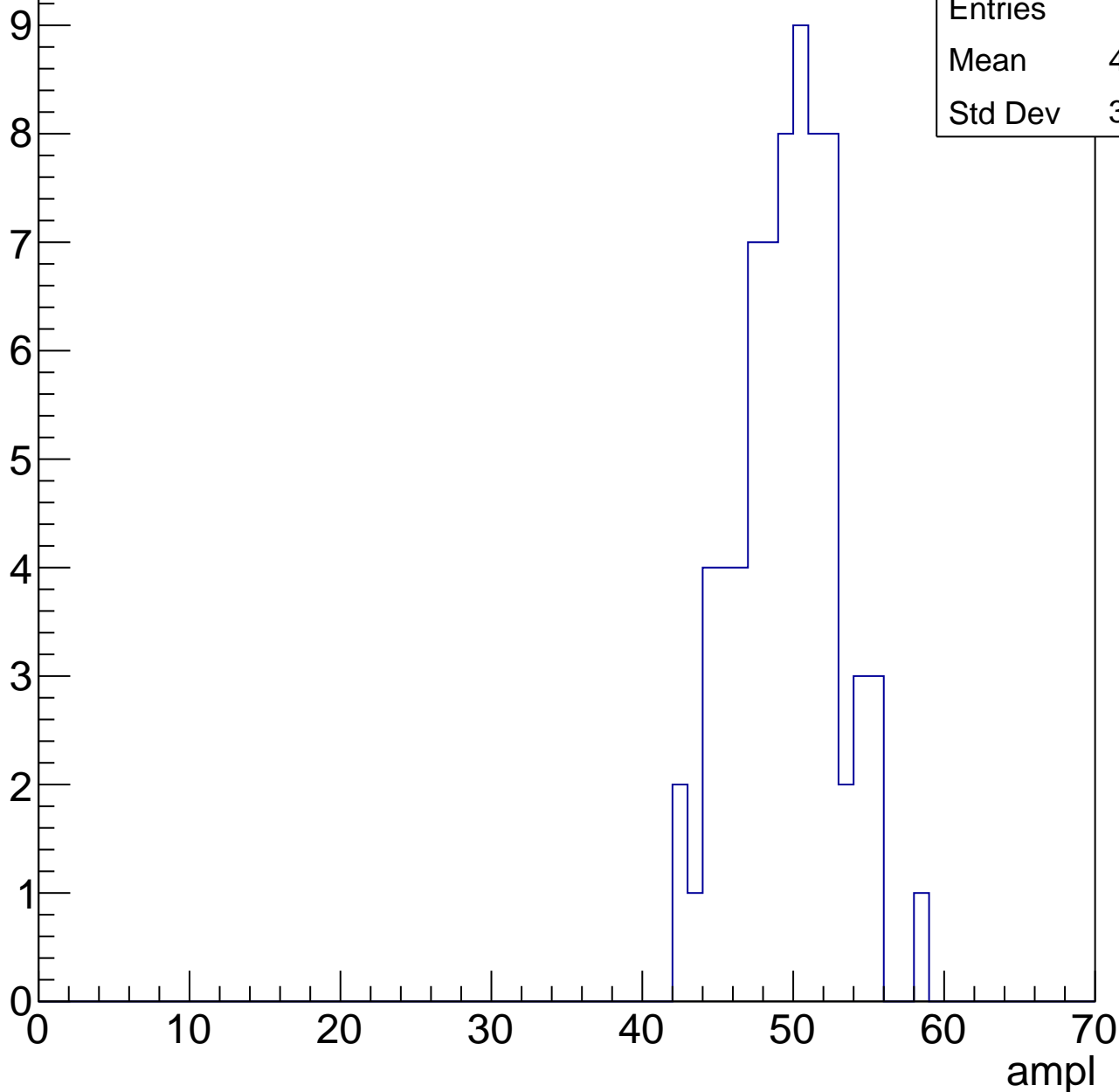
Gaus Width: 4.0715



B1L100S, U10-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

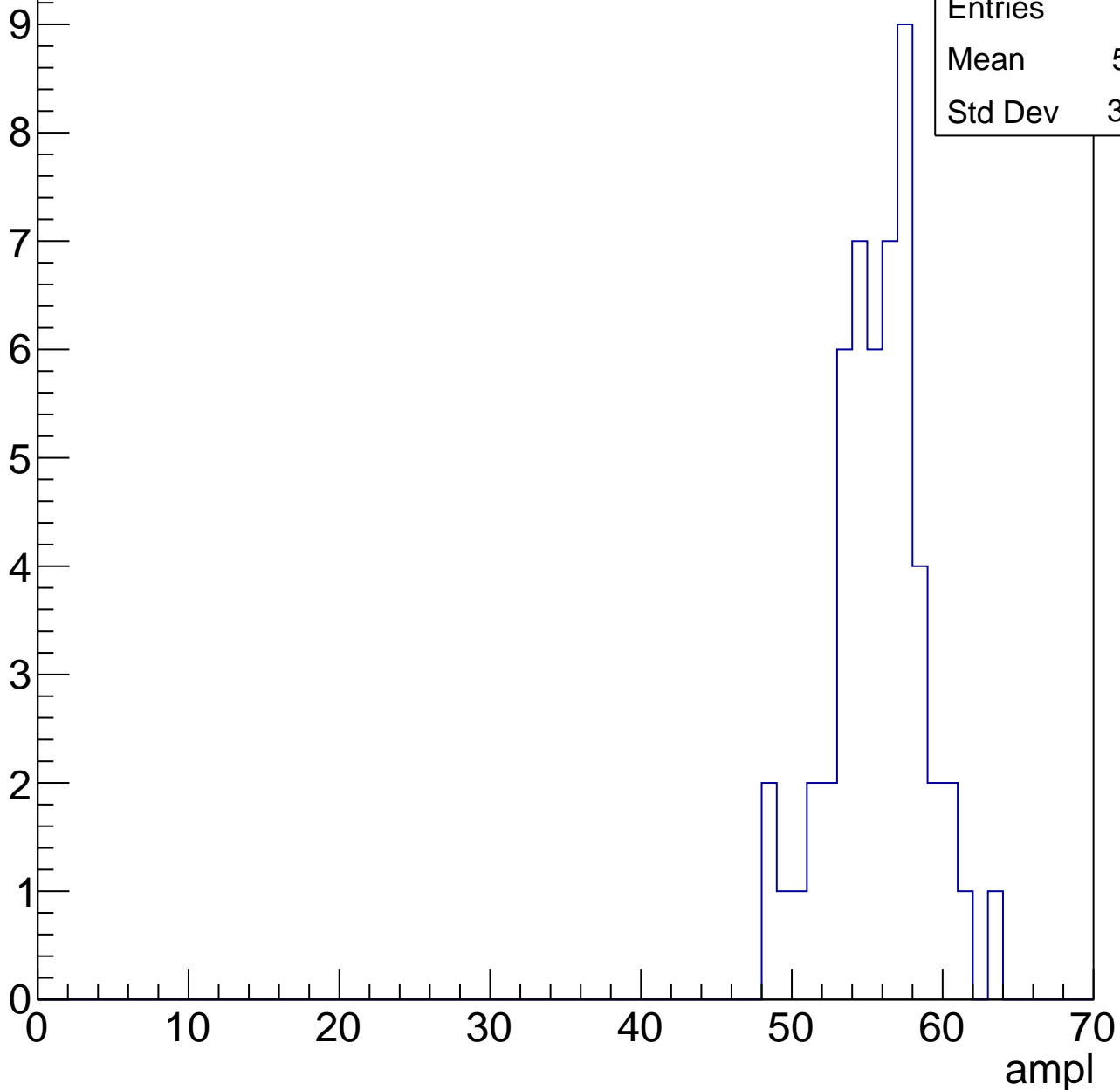


B1L100S, U10-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

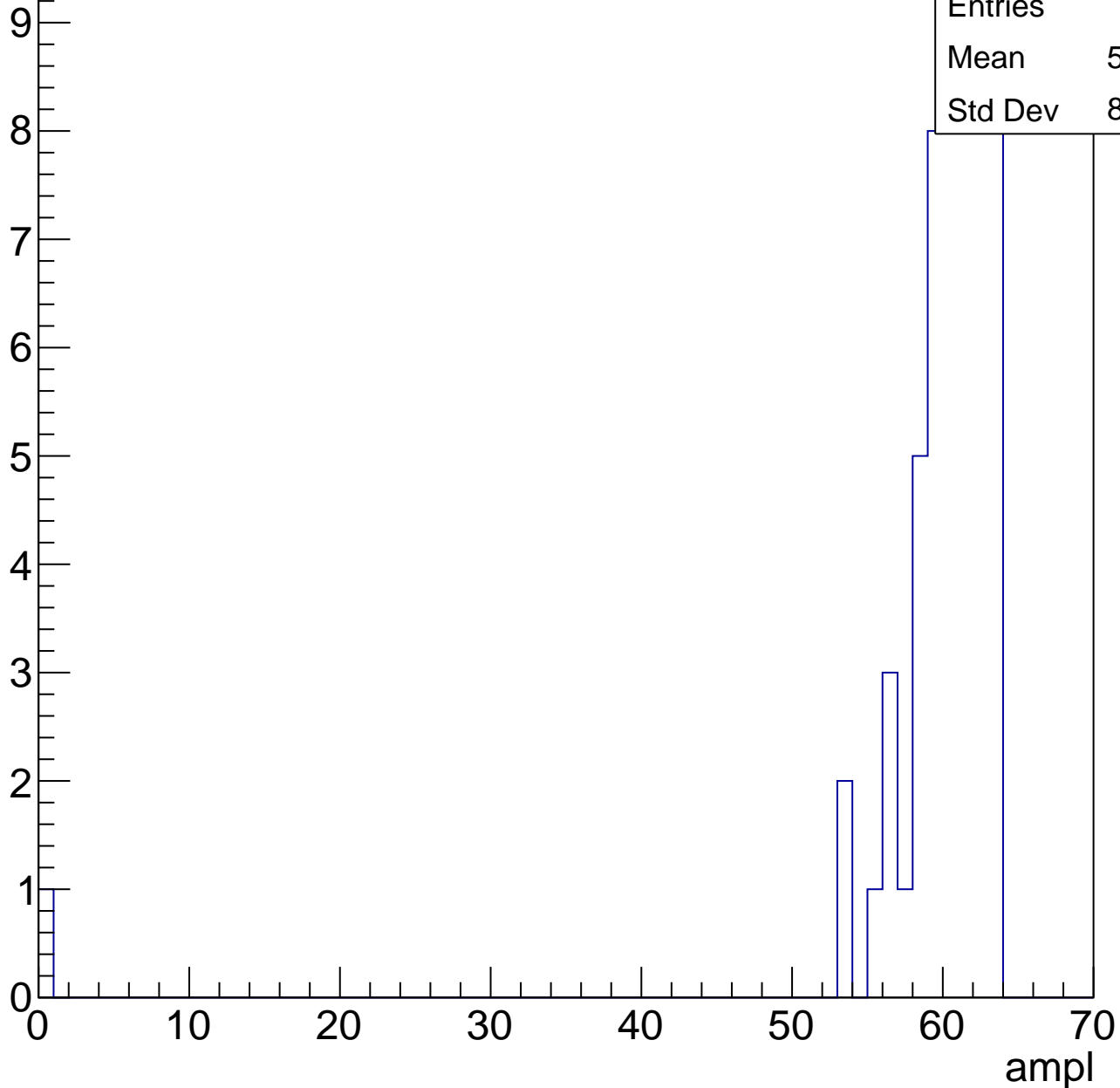
Entries	53
Mean	55.21
Std Dev	3.068



B1L100S, U10-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch3, adc0

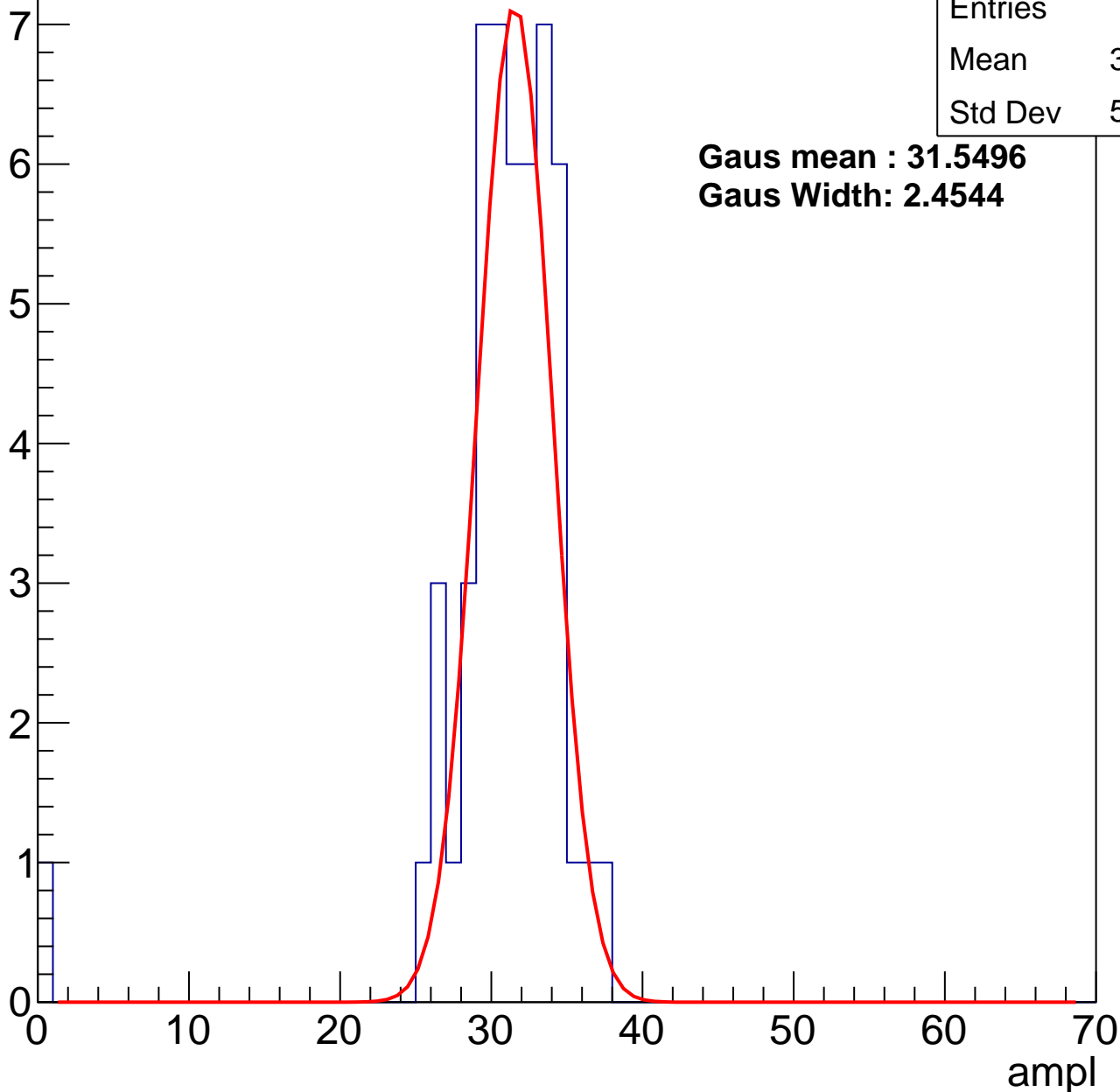
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	30.35
Std Dev	5.036

Gaus mean : 31.5496

Gaus Width: 2.4544



B1L100S, U10-ch3, adc1

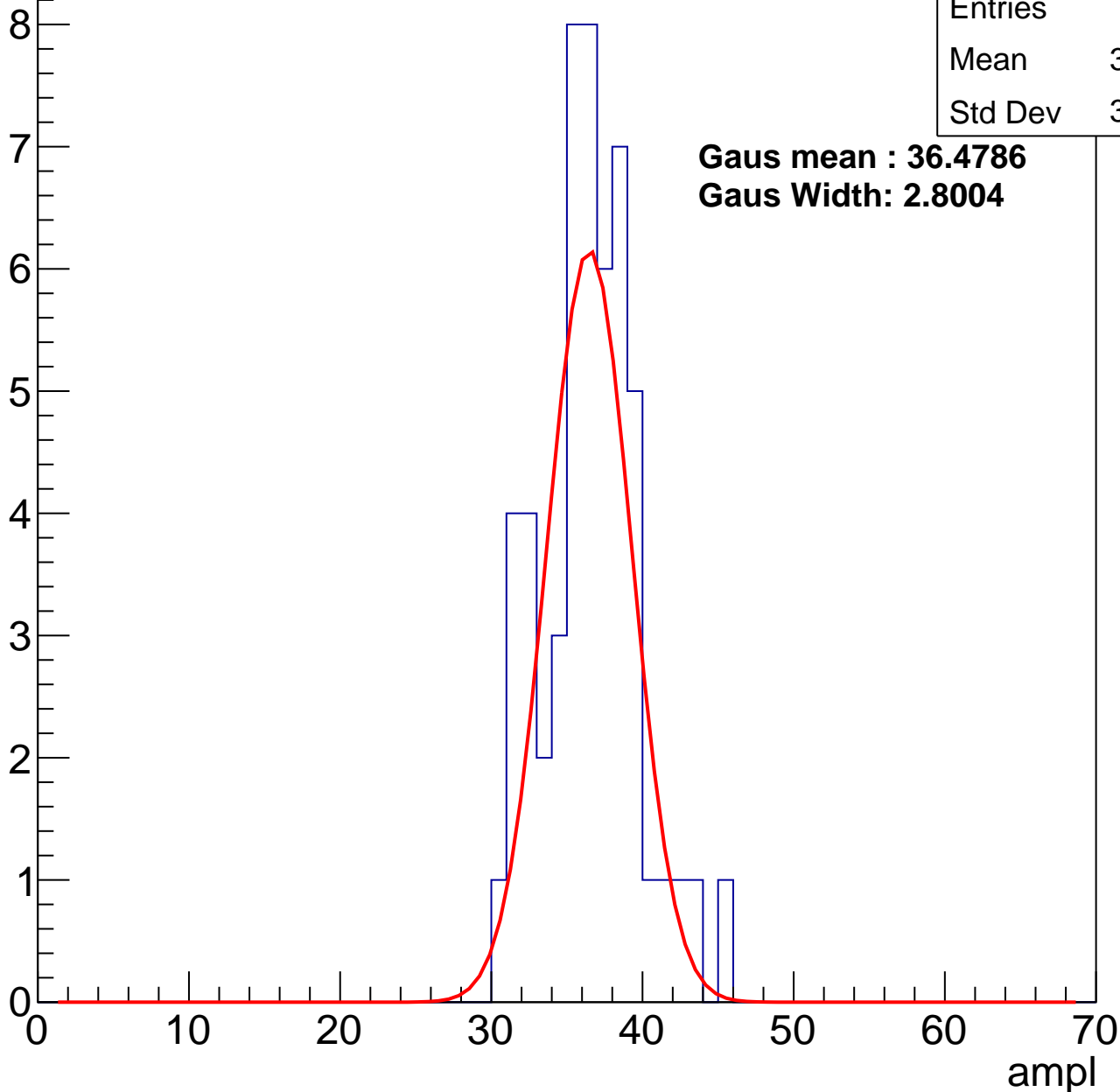
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	36.08
Std Dev	3.143

Gaus mean : 36.4786

Gaus Width: 2.8004



B1L100S, U10-ch3, adc2

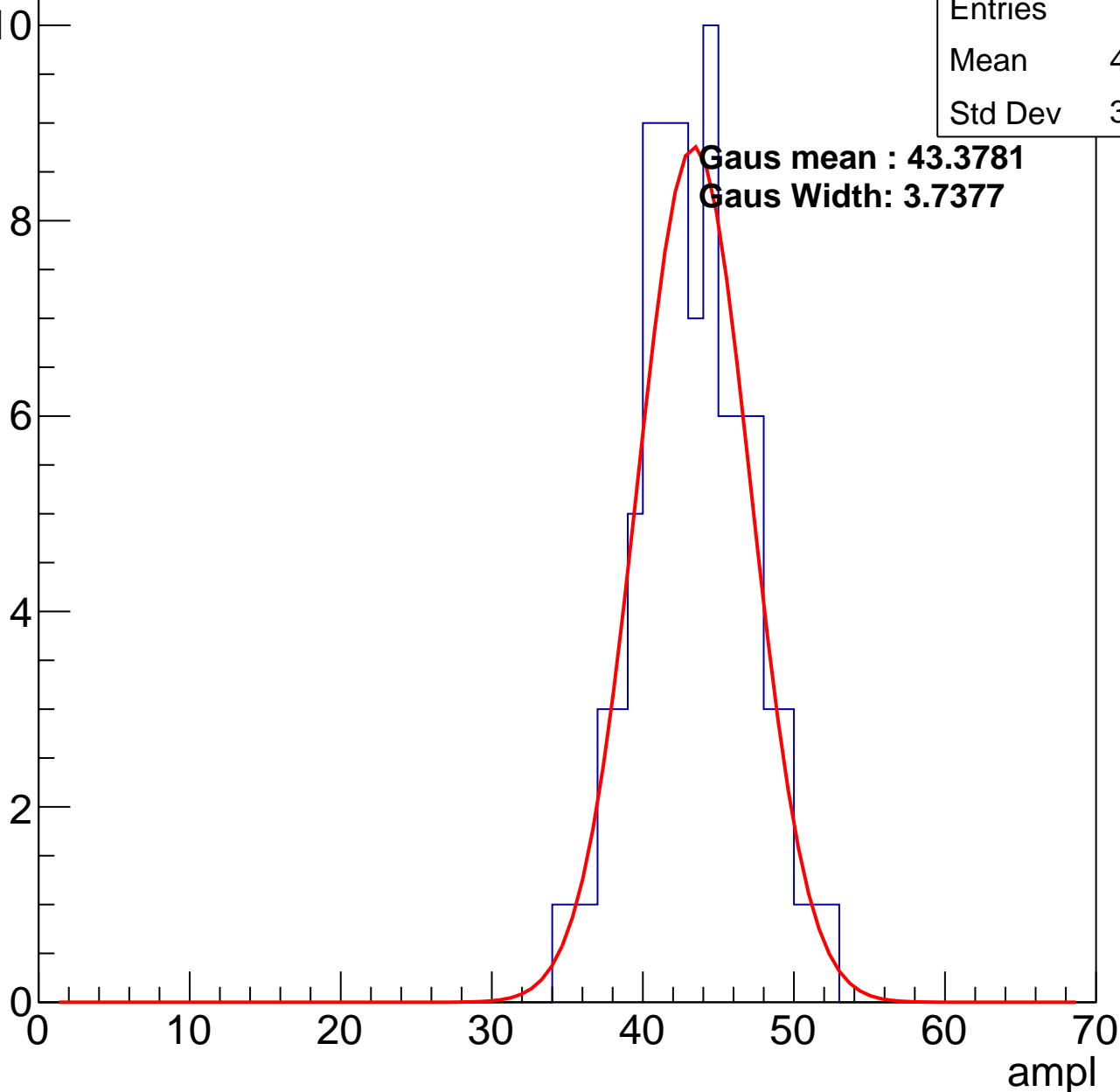
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	42.88
Std Dev	3.667

Gaus mean : 43.3781

Gaus Width: 3.7377

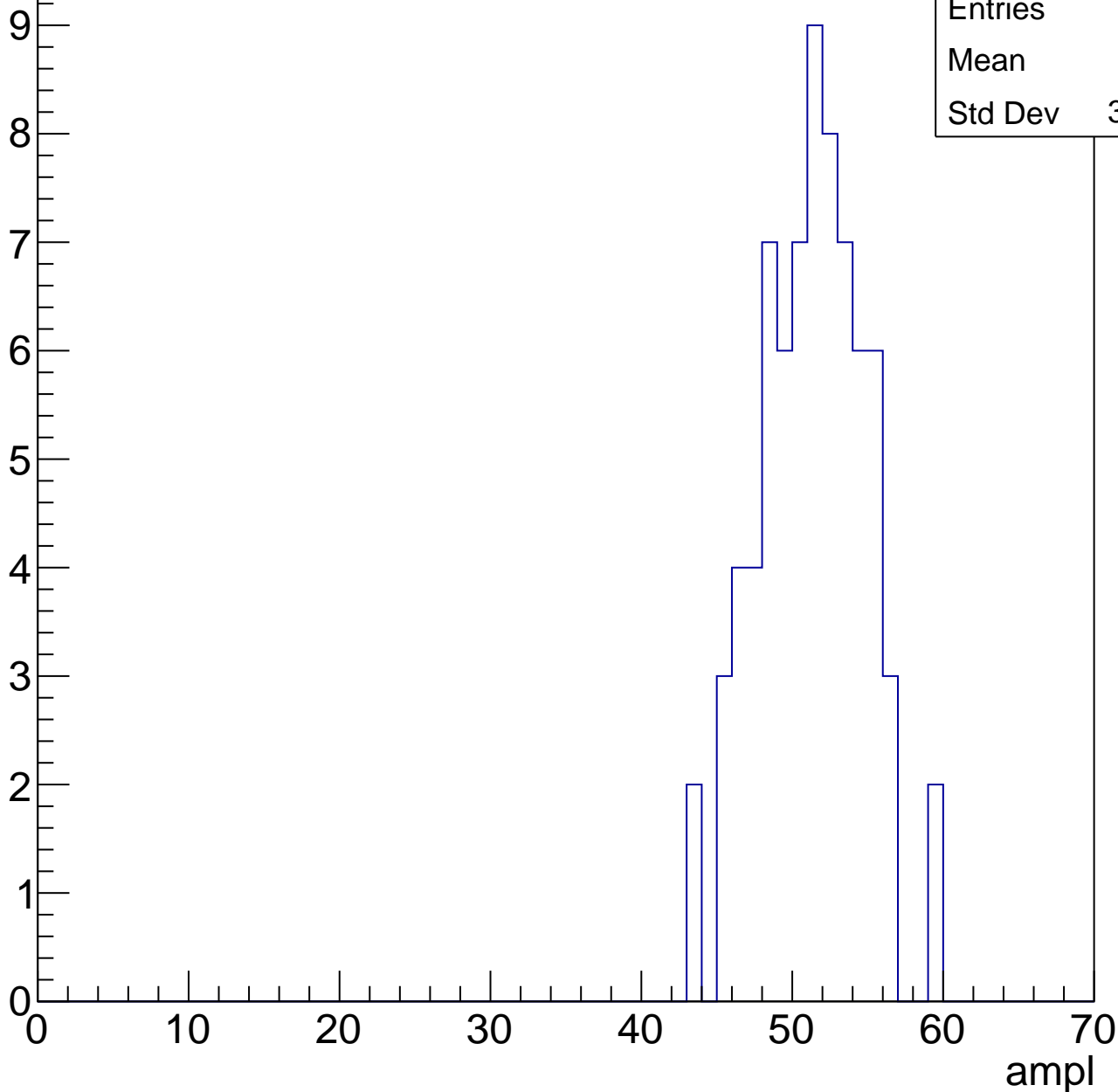


B1L100S, U10-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.8
Std Dev	3.448

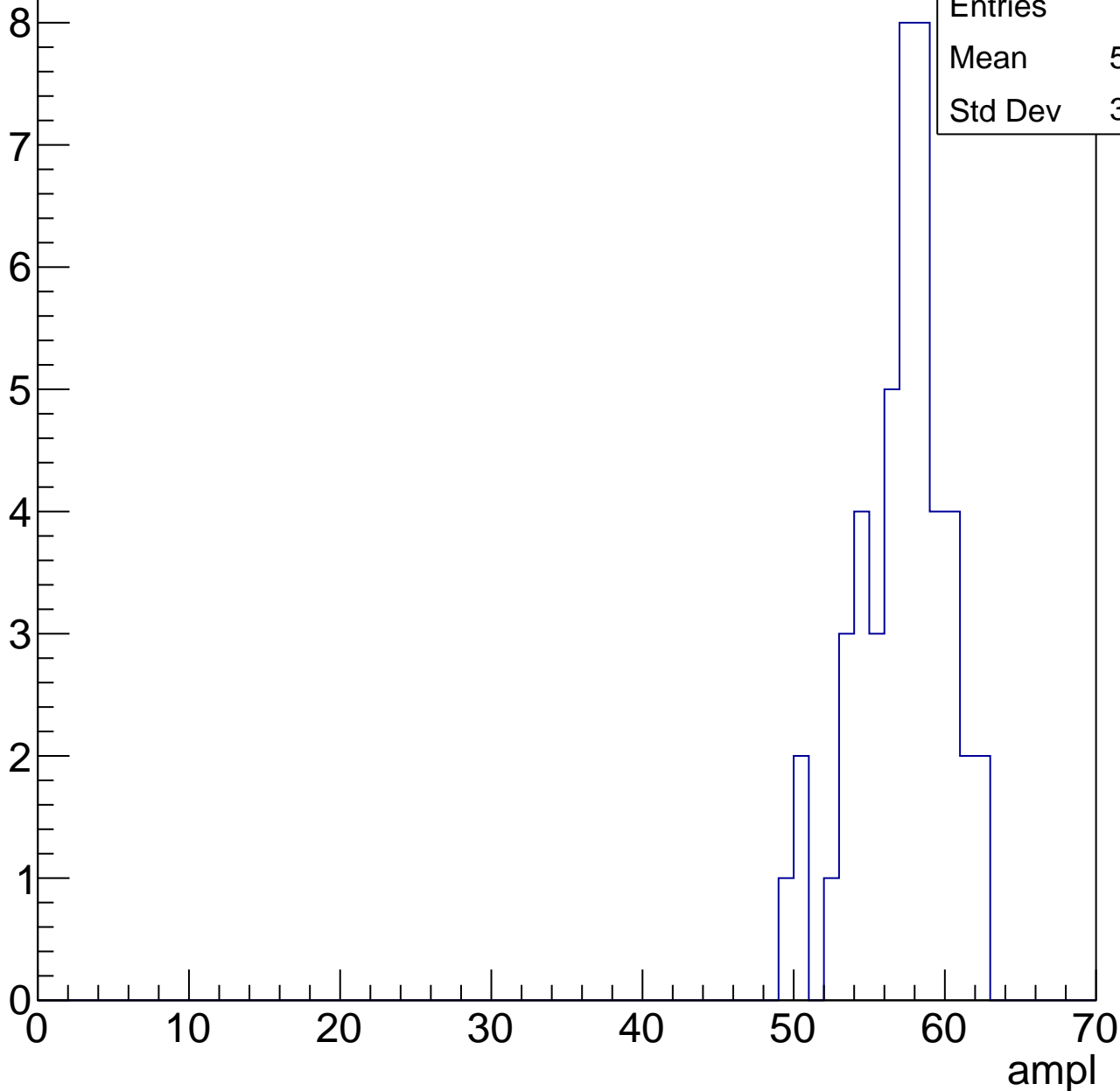


B1L100S, U10-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

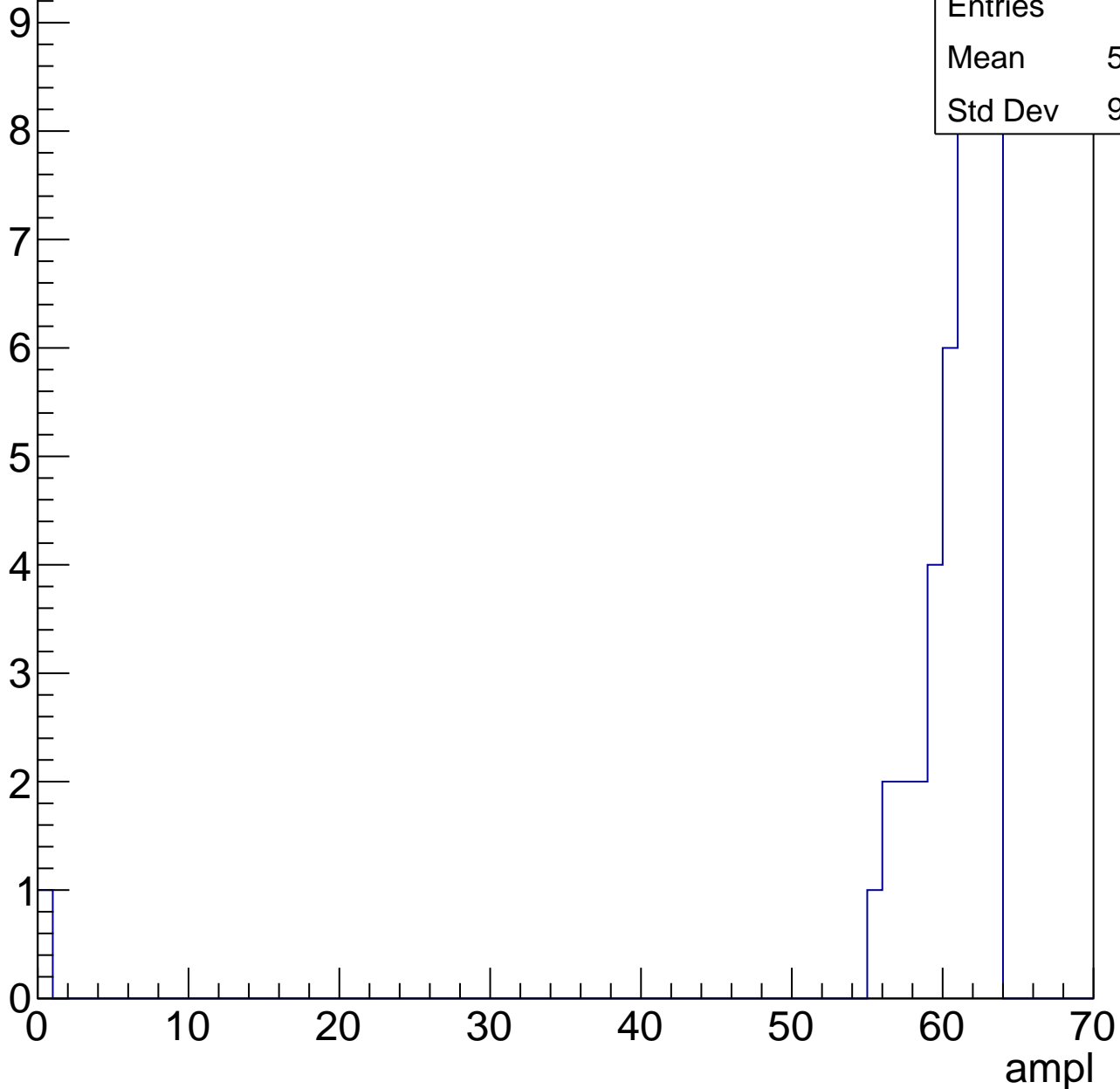
Entries	47
Mean	56.66
Std Dev	3.006



B1L100S, U10-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L100S, U10-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch4, adc0

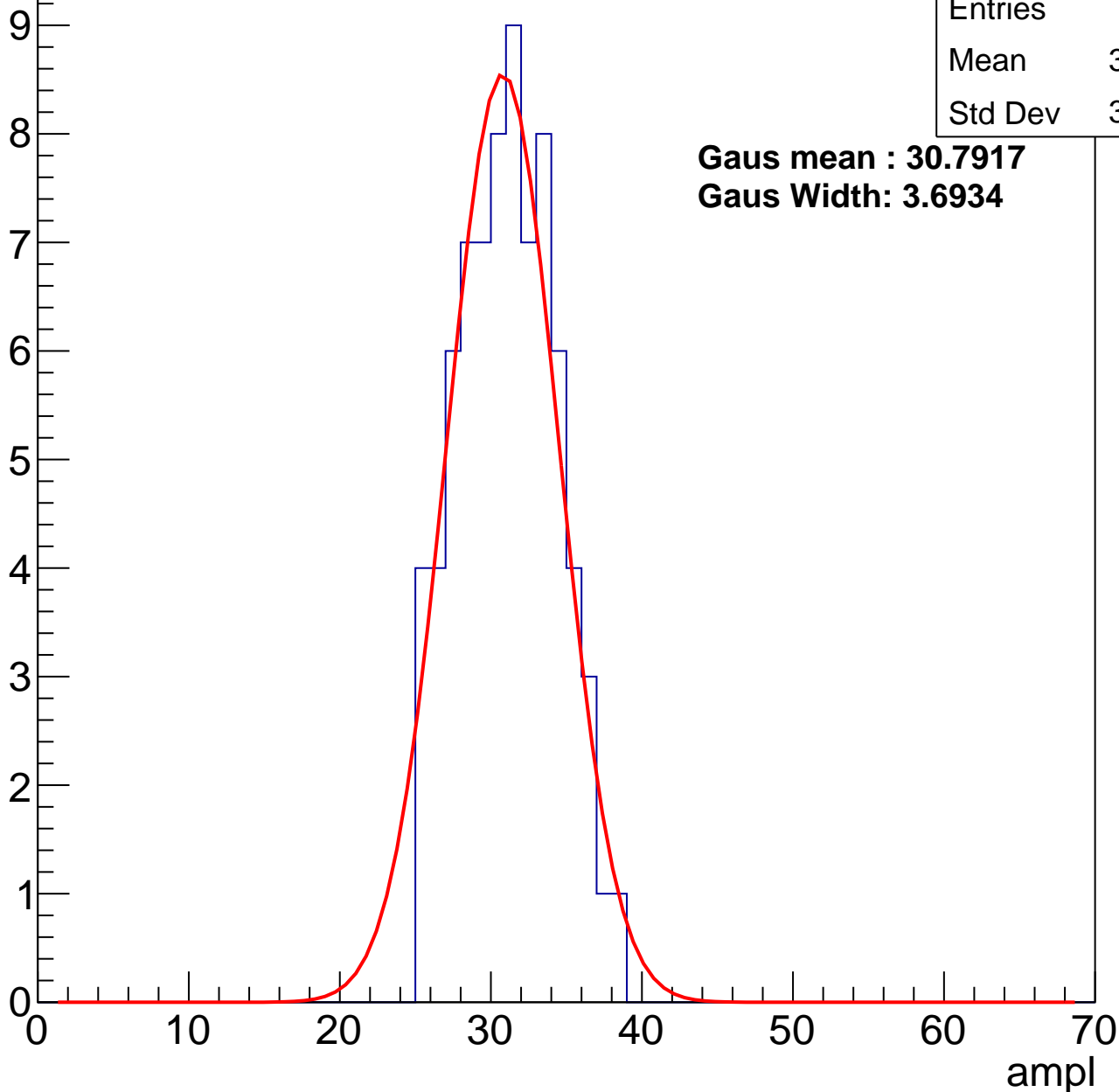
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.65
Std Dev	3.156

Gaus mean : 30.7917

Gaus Width: 3.6934



B1L100S, U10-ch4, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	38.04
Std Dev	3.473

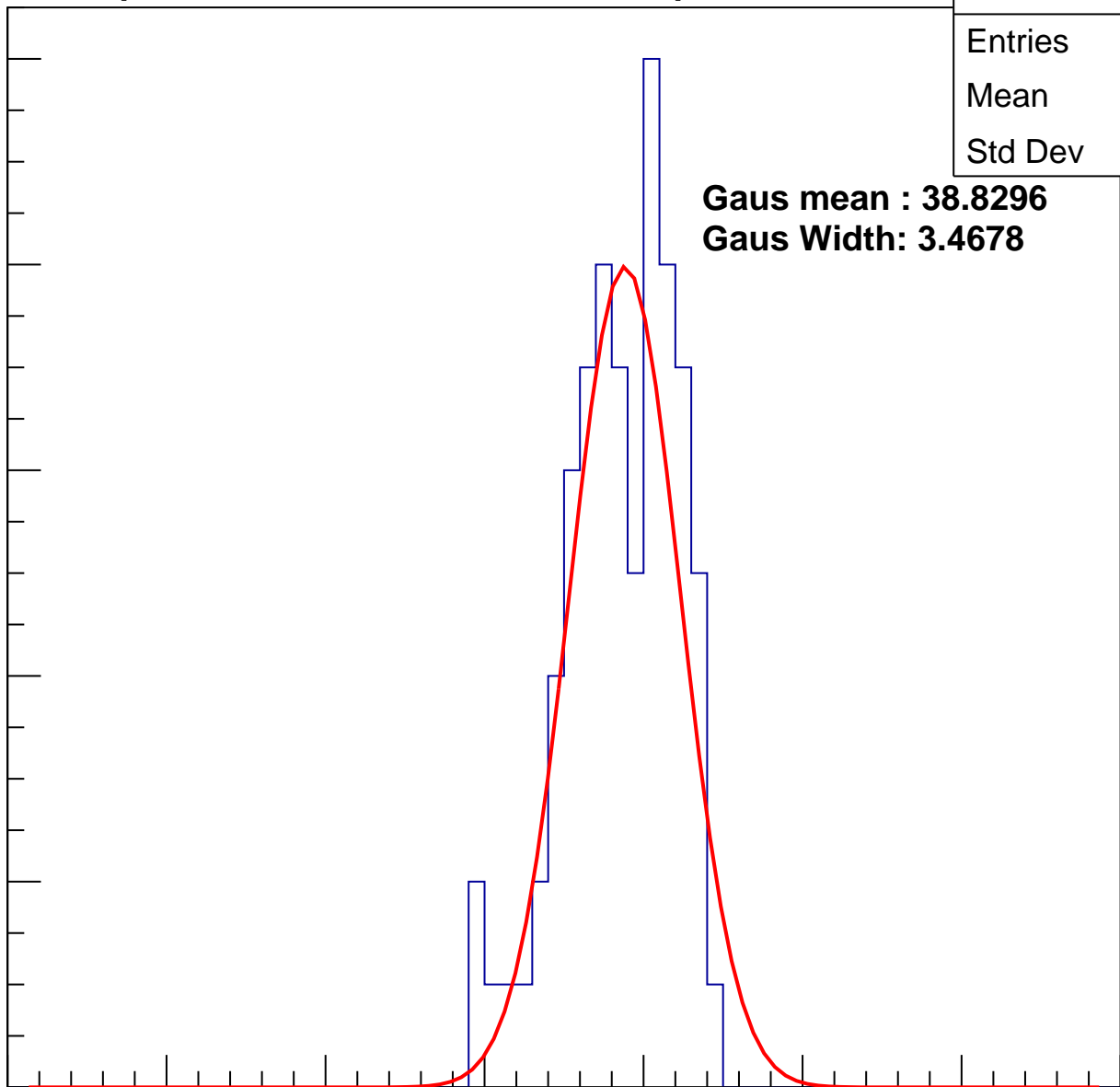
Gaus mean : 38.8296

Gaus Width: 3.4678

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch4, adc2

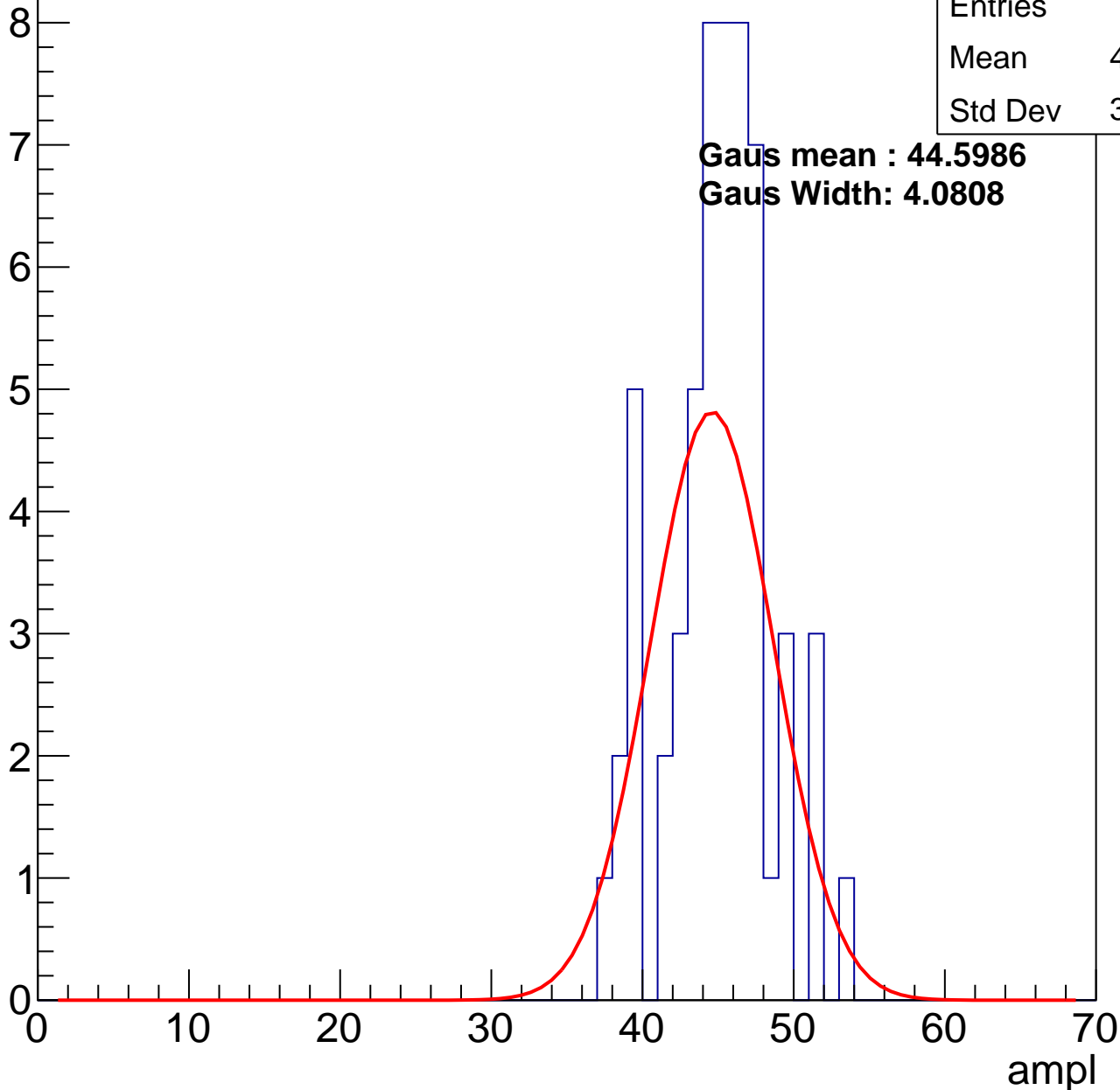
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.58
Std Dev	3.464

Gaus mean : 44.5986

Gaus Width: 4.0808

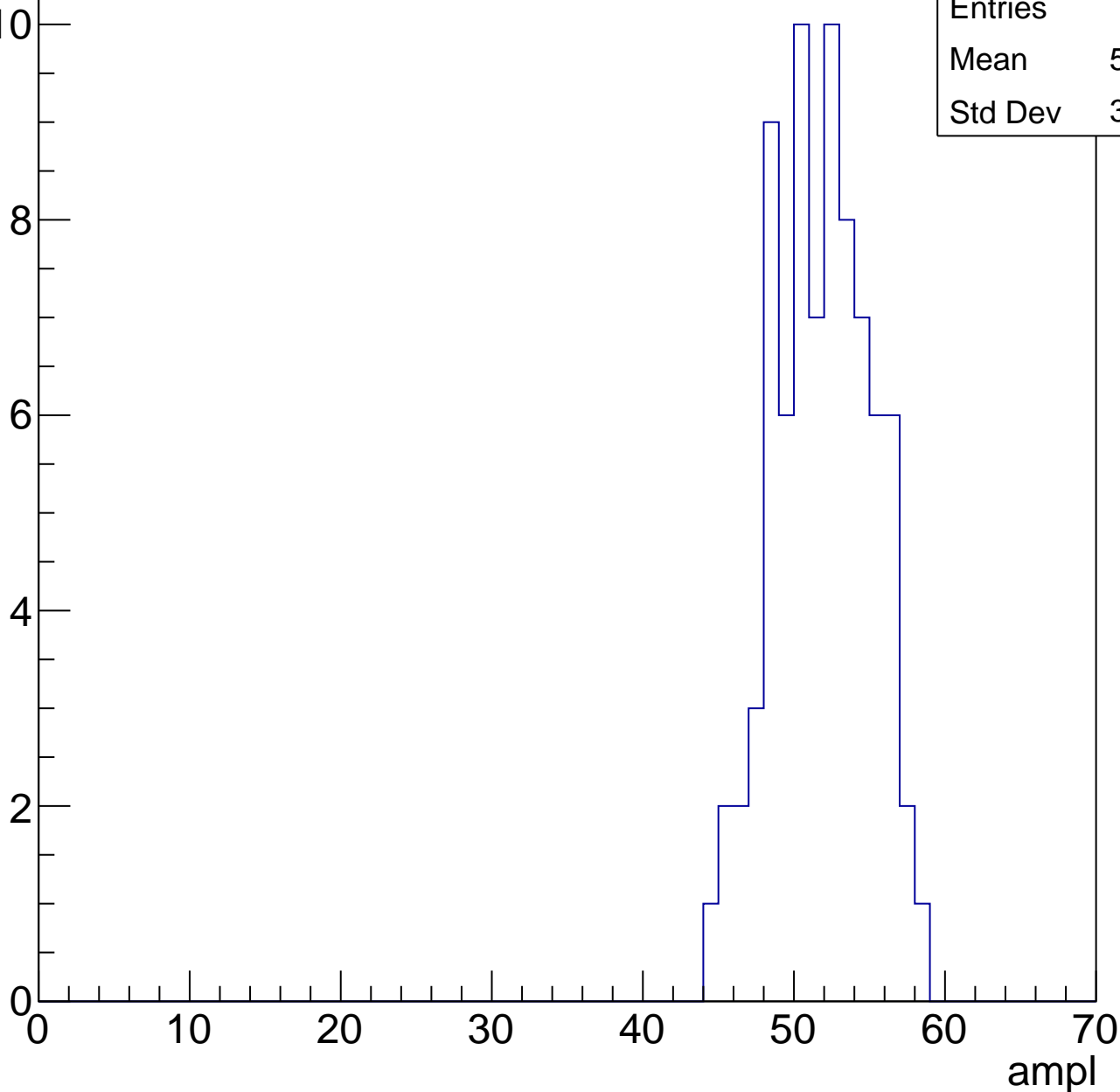


B1L100S, U10-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

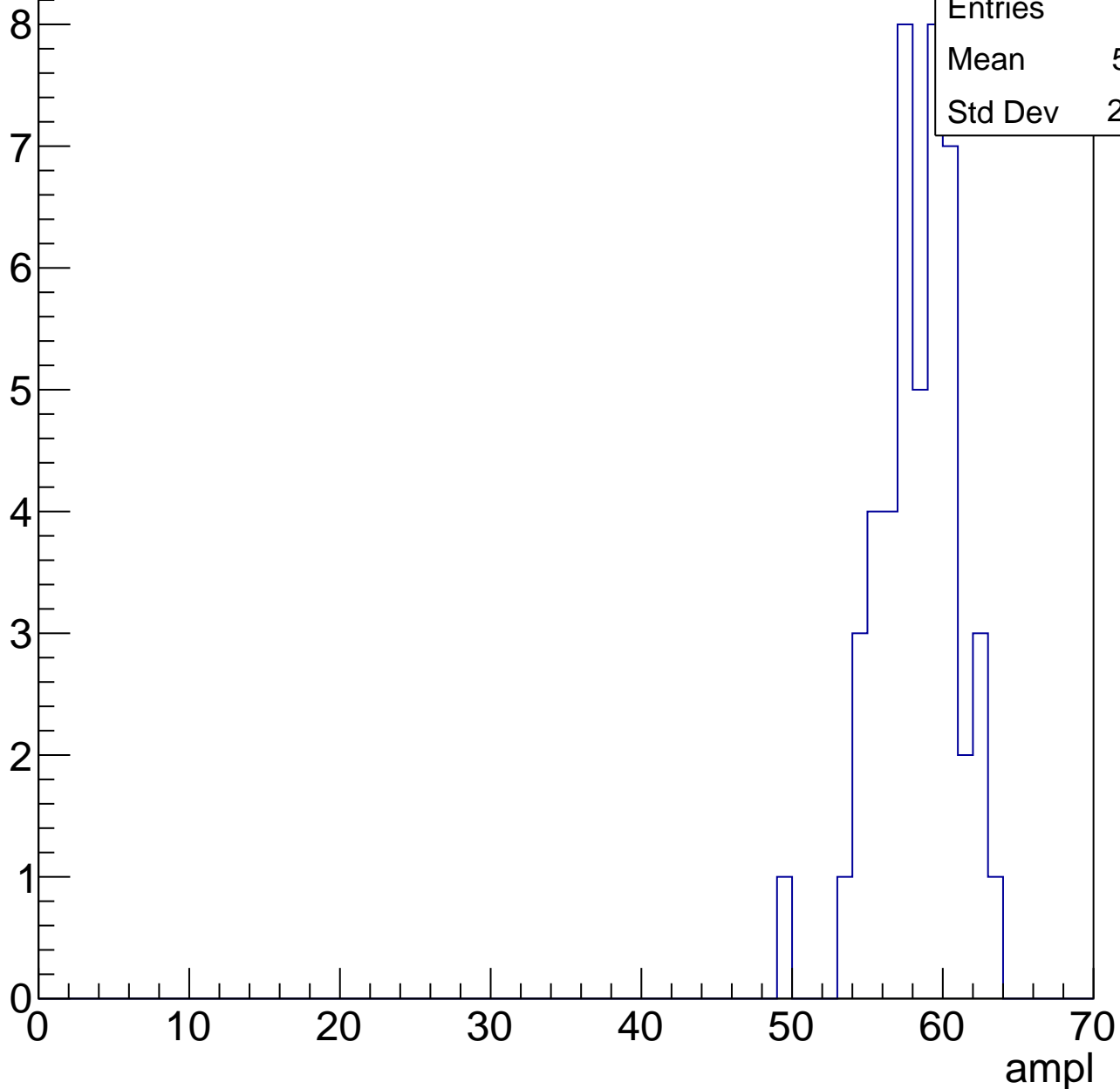
Entries	80
Mean	51.38
Std Dev	3.152



B1L100S, U10-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

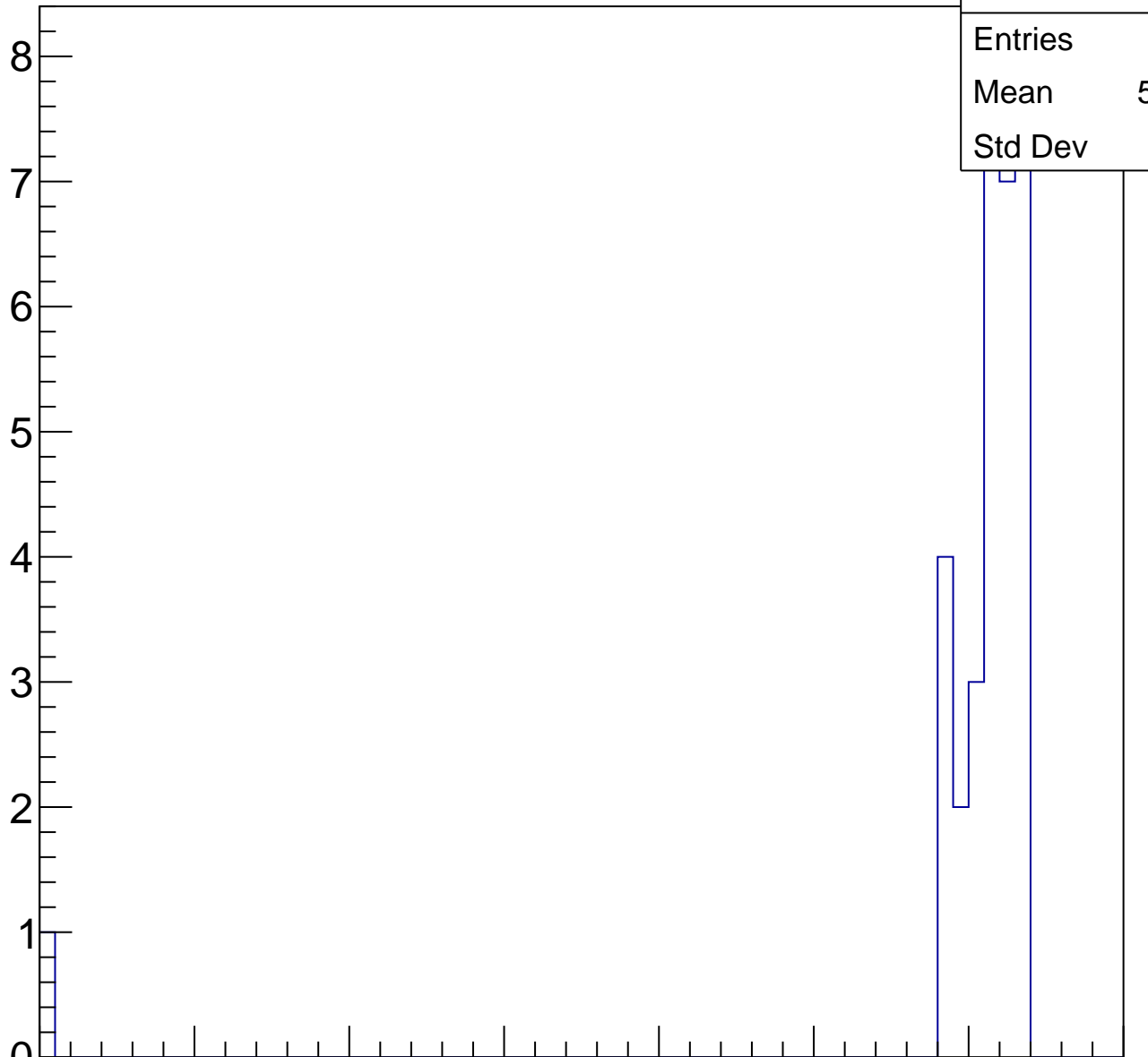
Entry

8
7
6
5
4
3
2
1
0

Entries	33
Mean	59.27
Std Dev	10.6

ampl

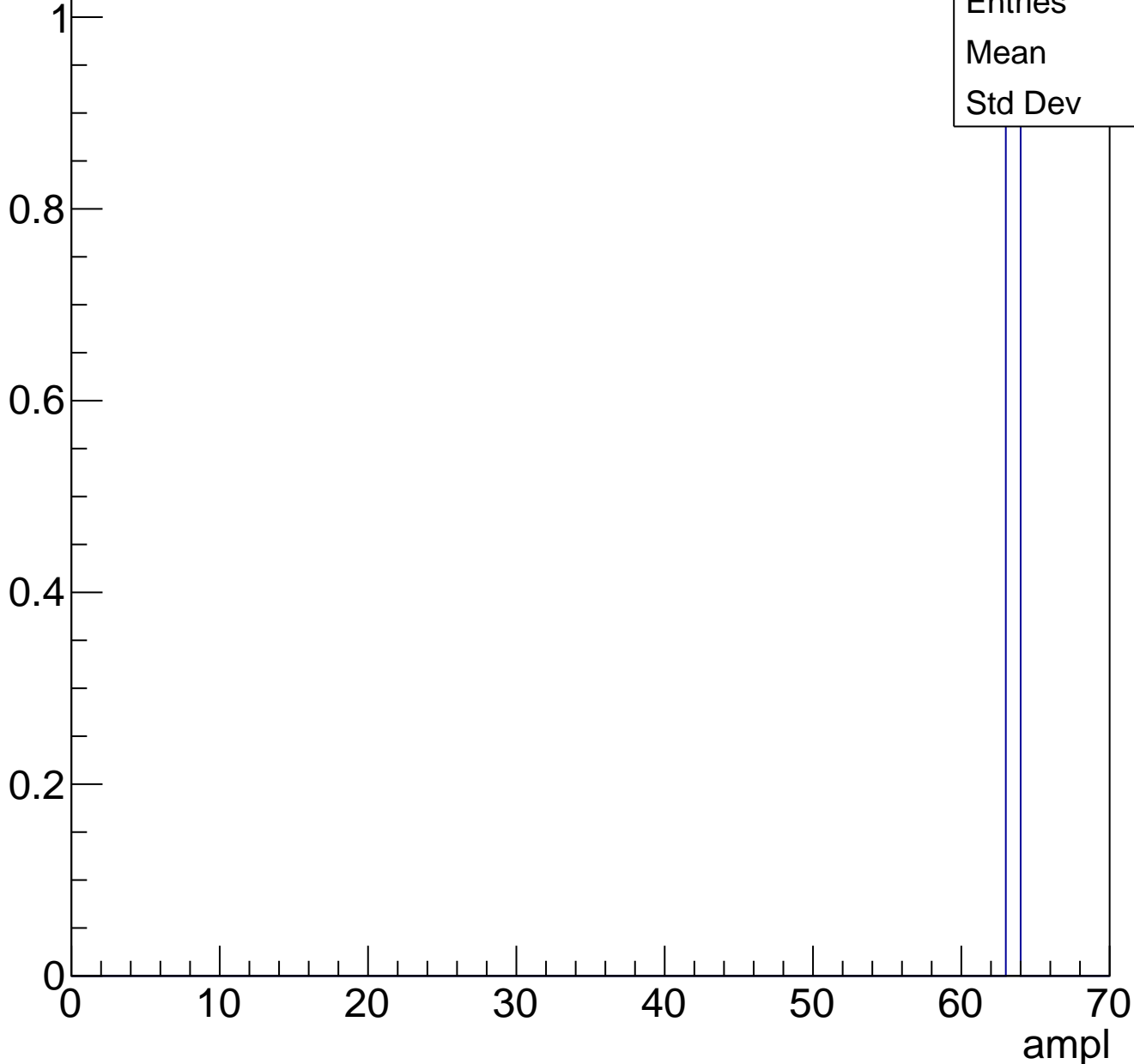
0 10 20 30 40 50 60 70



B1L100S, U10-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch5, adc0

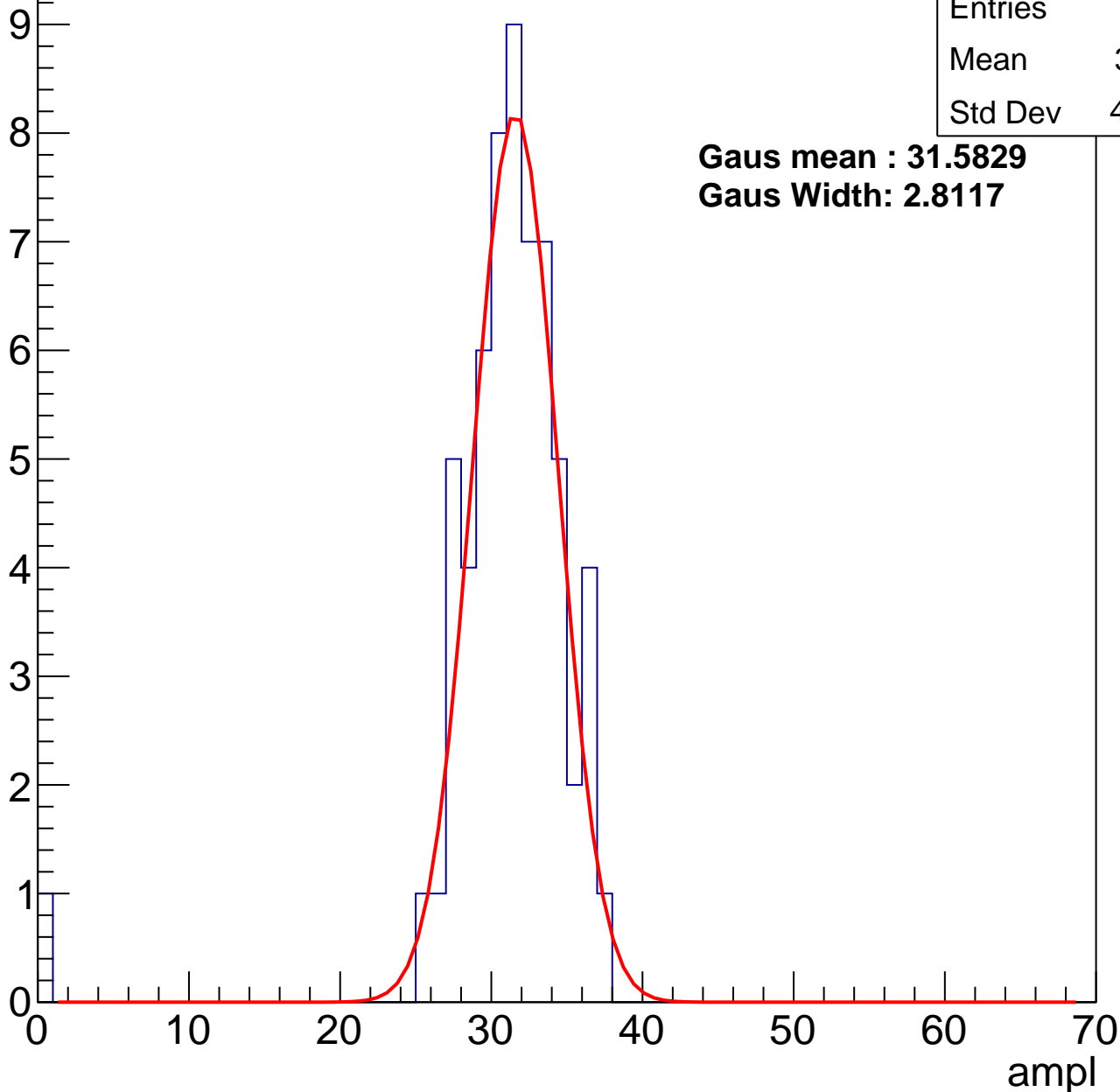
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.61
Std Dev	4.805

Gaus mean : 31.5829

Gaus Width: 2.8117



B1L100S, U10-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	38.73
Std Dev	3.528

Gaus mean : 39.2008

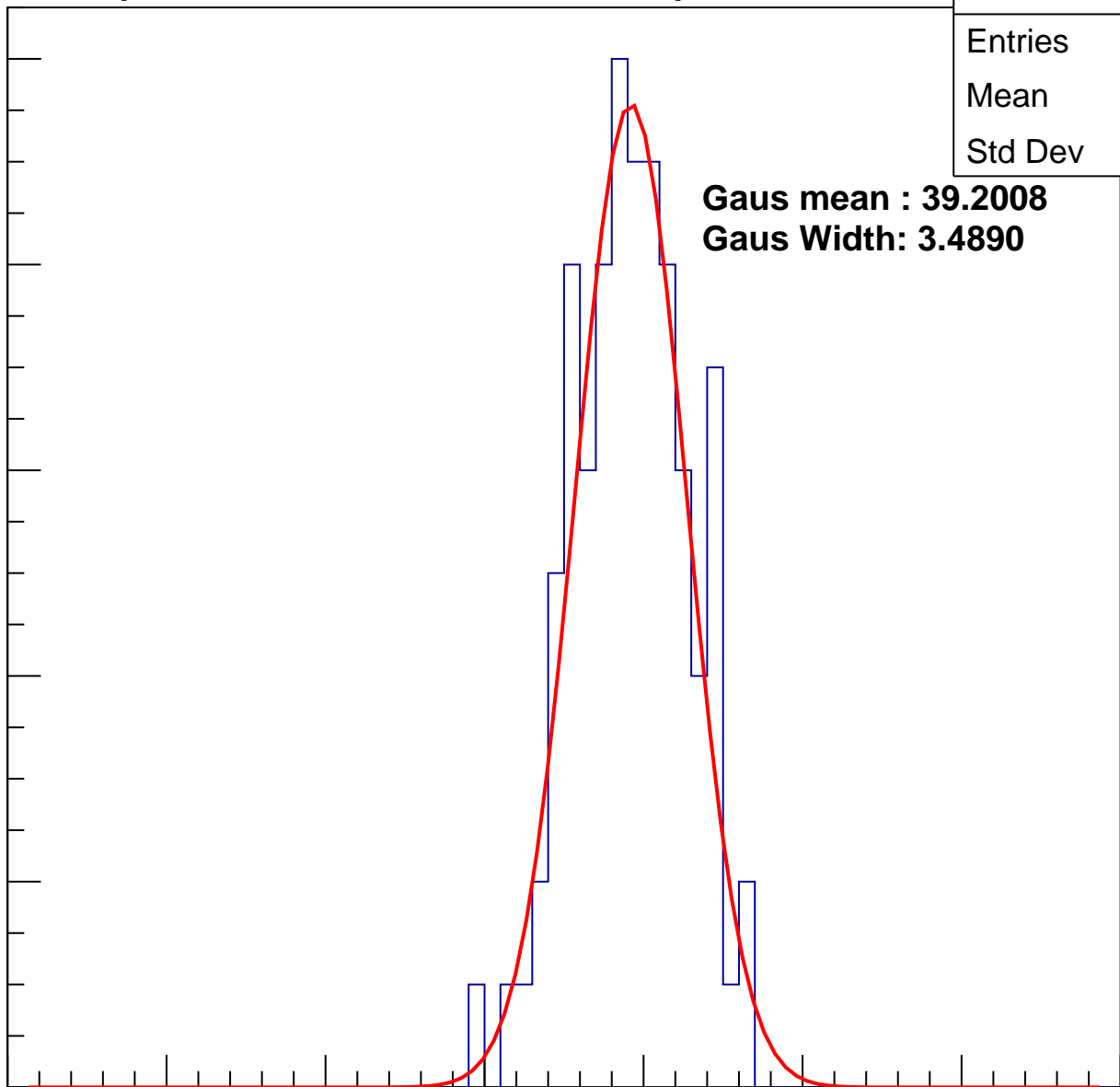
Gaus Width: 3.4890

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch5, adc2

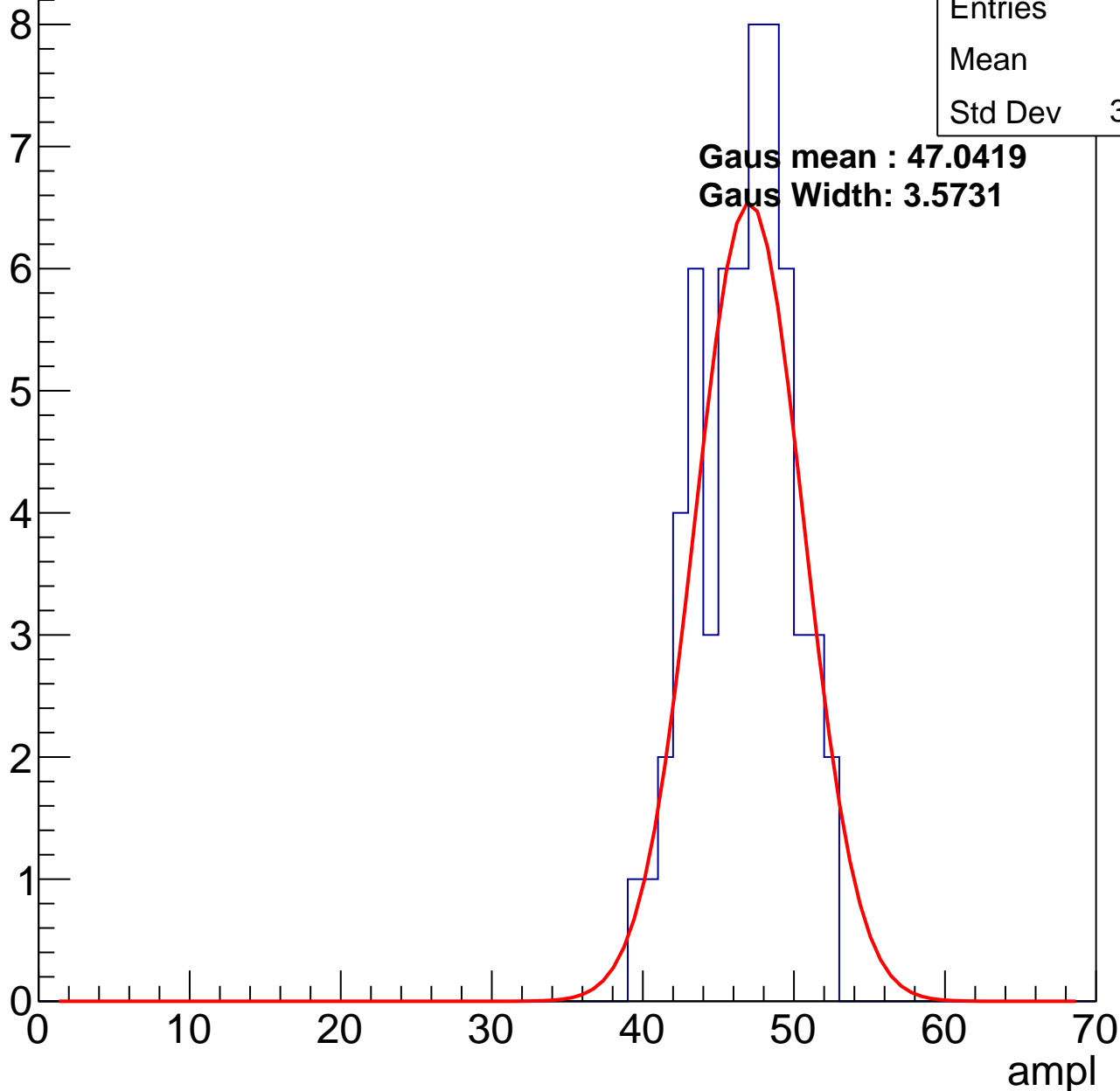
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	46.2
Std Dev	3.074

Gaus mean : 47.0419

Gaus Width: 3.5731

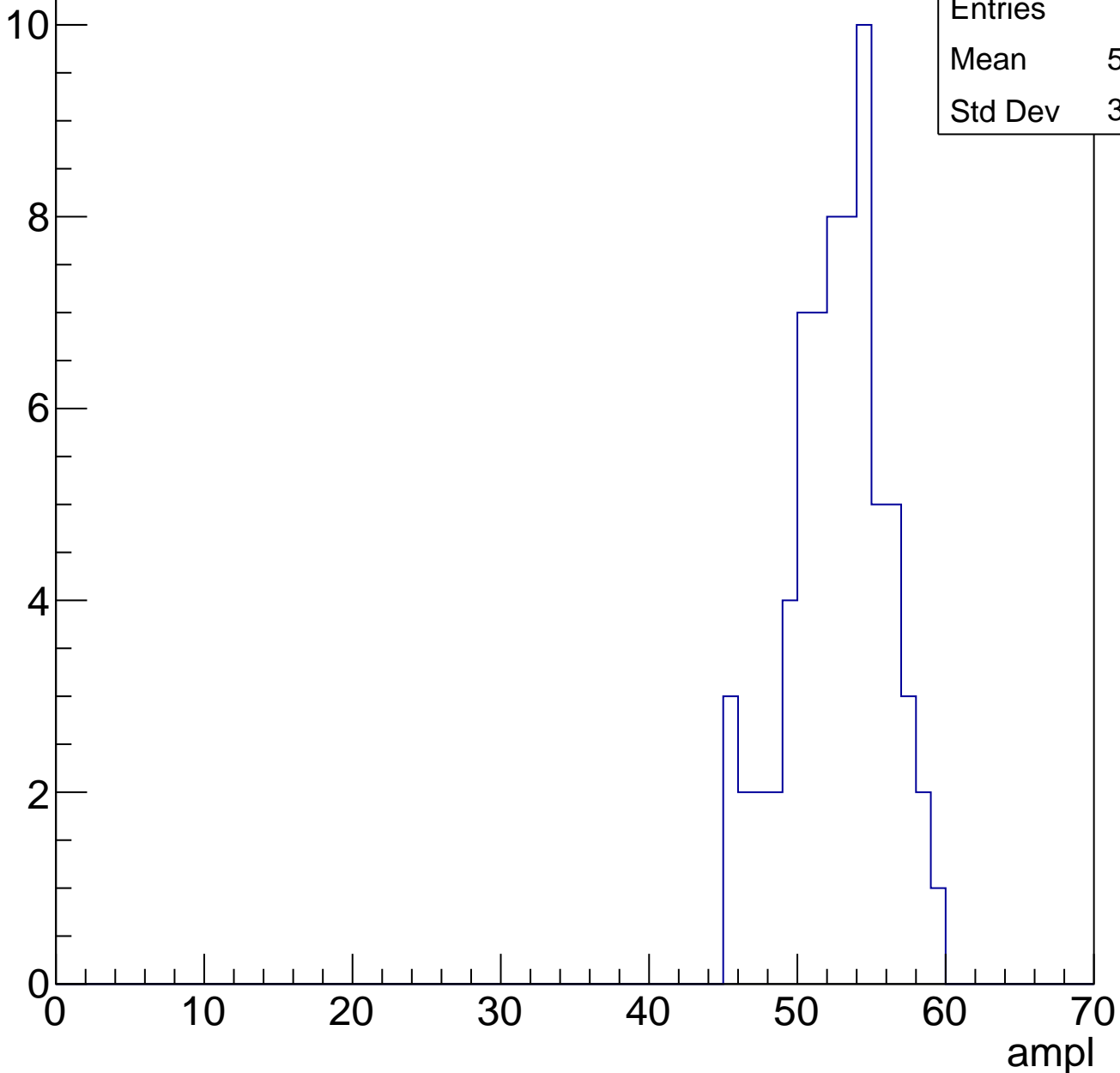


B1L100S, U10-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	52.19
Std Dev	3.272

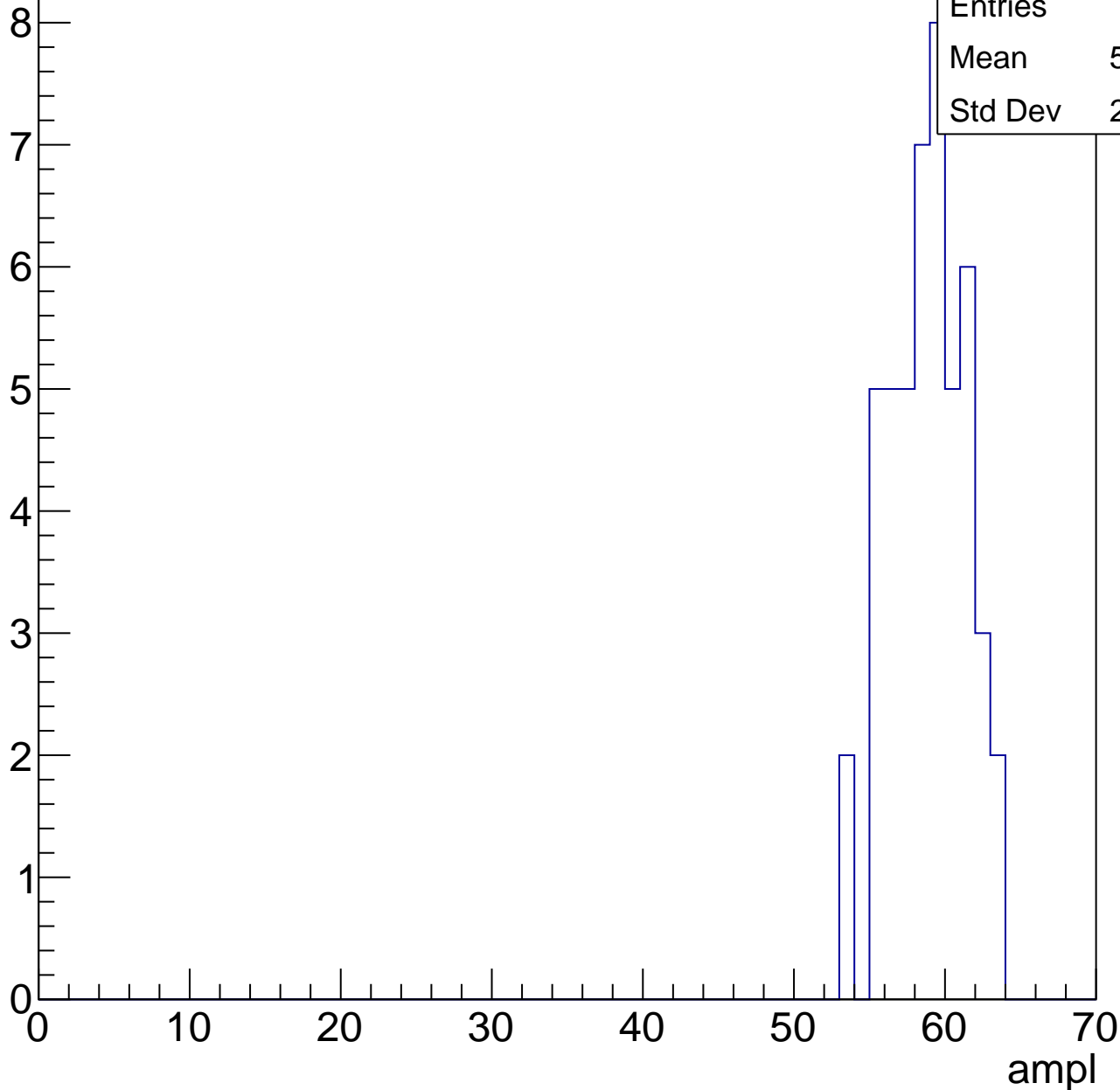


B1L100S, U10-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.38
Std Dev	2.472



B1L100S, U10-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	28
Mean	58.93
Std Dev	11.47

ampl

B1L100S, U10-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch6, adc0

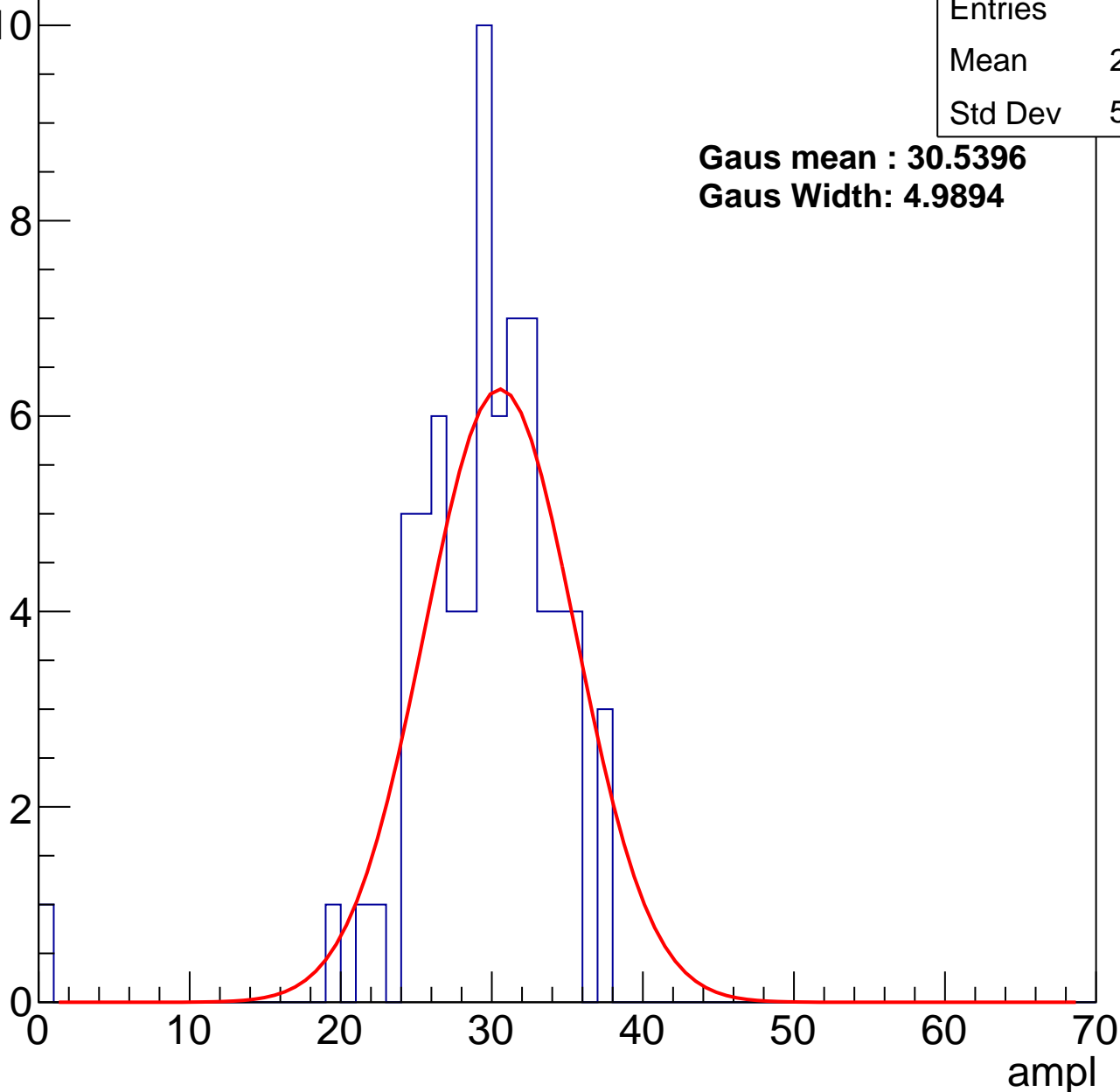
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.95
Std Dev	5.142

Gaus mean : 30.5396

Gaus Width: 4.9894



B1L100S, U10-ch6, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	37.32
Std Dev	3.98

Gaus mean : 37.1319

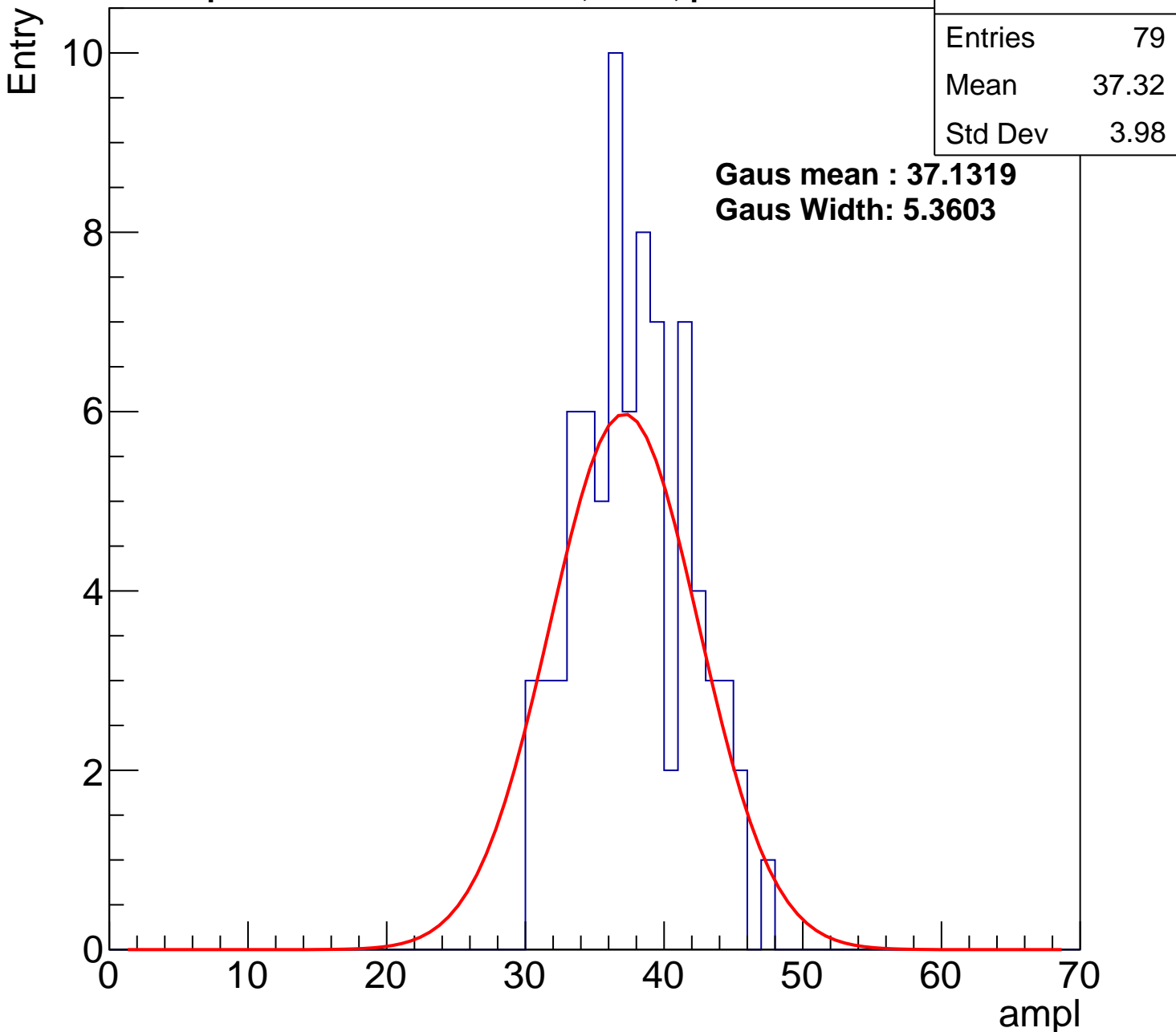
Gaus Width: 5.3603

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch6, adc2

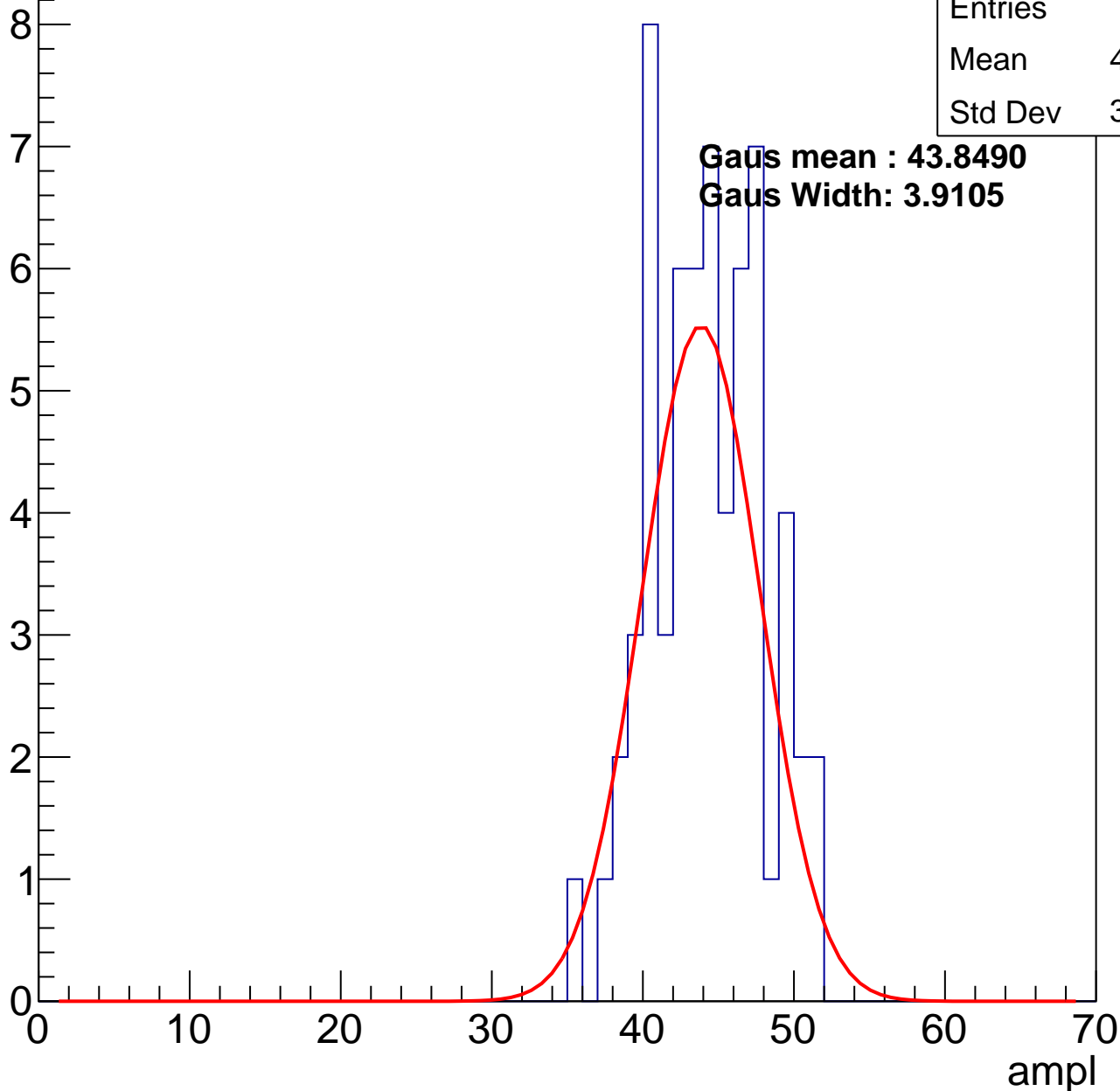
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.76
Std Dev	3.642

Gaus mean : 43.8490

Gaus Width: 3.9105

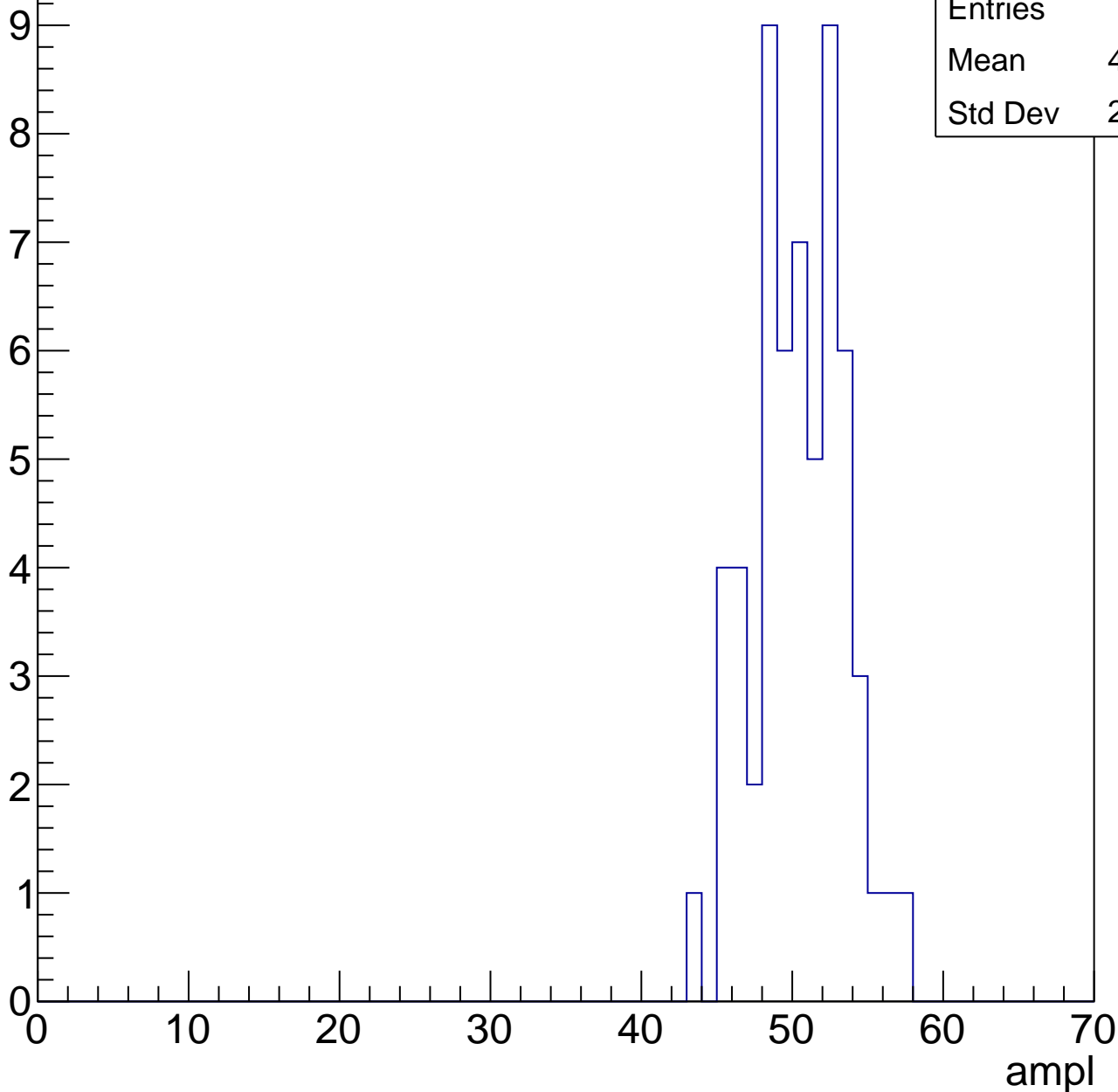


B1L100S, U10-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

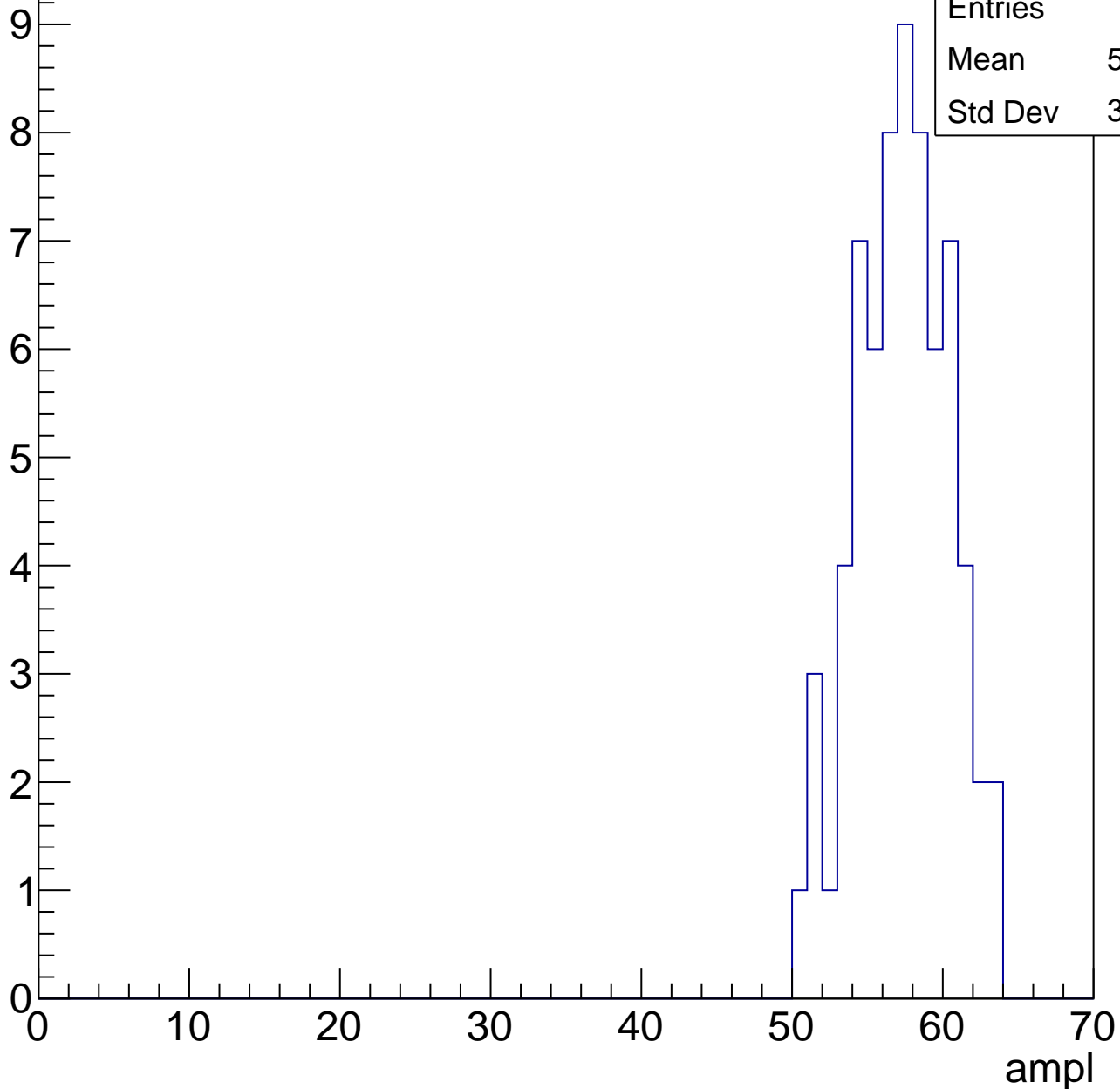
Entries	59
Mean	49.97
Std Dev	2.986



B1L100S, U10-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

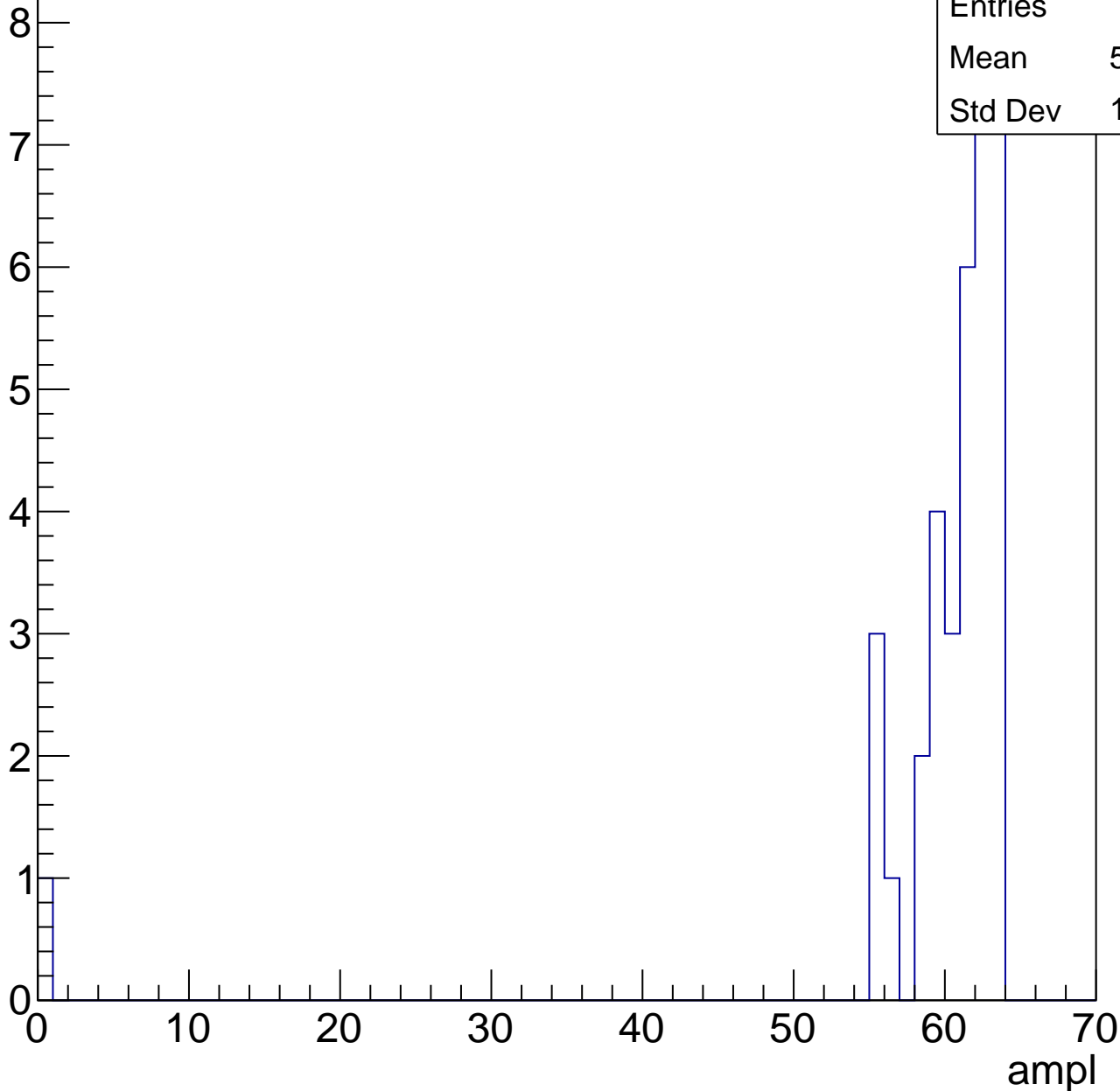


B1L100S, U10-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.86
Std Dev	10.23



B1L100S, U10-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch7, adc0

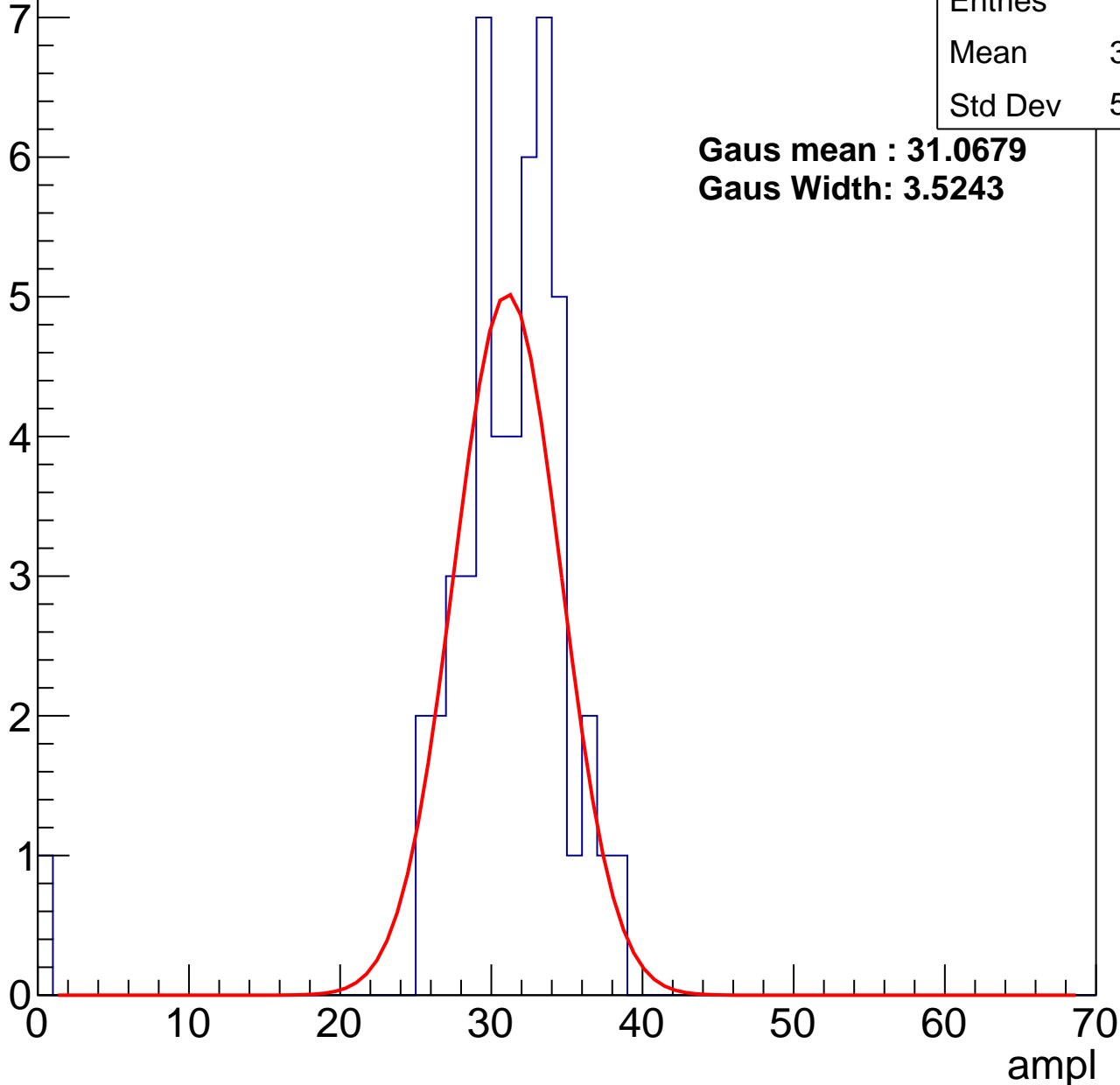
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	30.39
Std Dev	5.348

Gaus mean : 31.0679

Gaus Width: 3.5243



B1L100S, U10-ch7, adc1

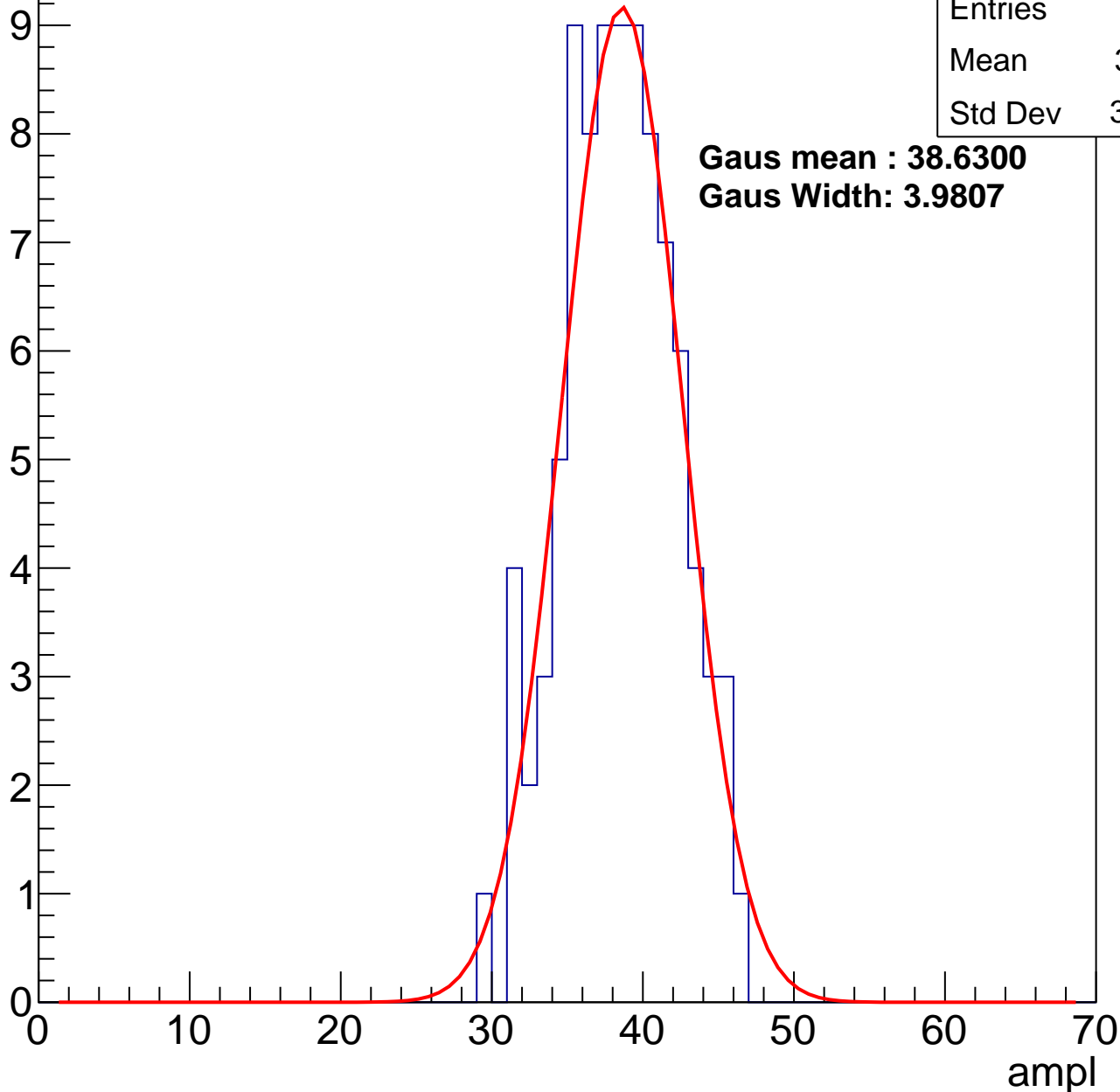
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	91
Mean	38.01
Std Dev	3.699

Gaus mean : 38.6300

Gaus Width: 3.9807



B1L100S, U10-ch7, adc2

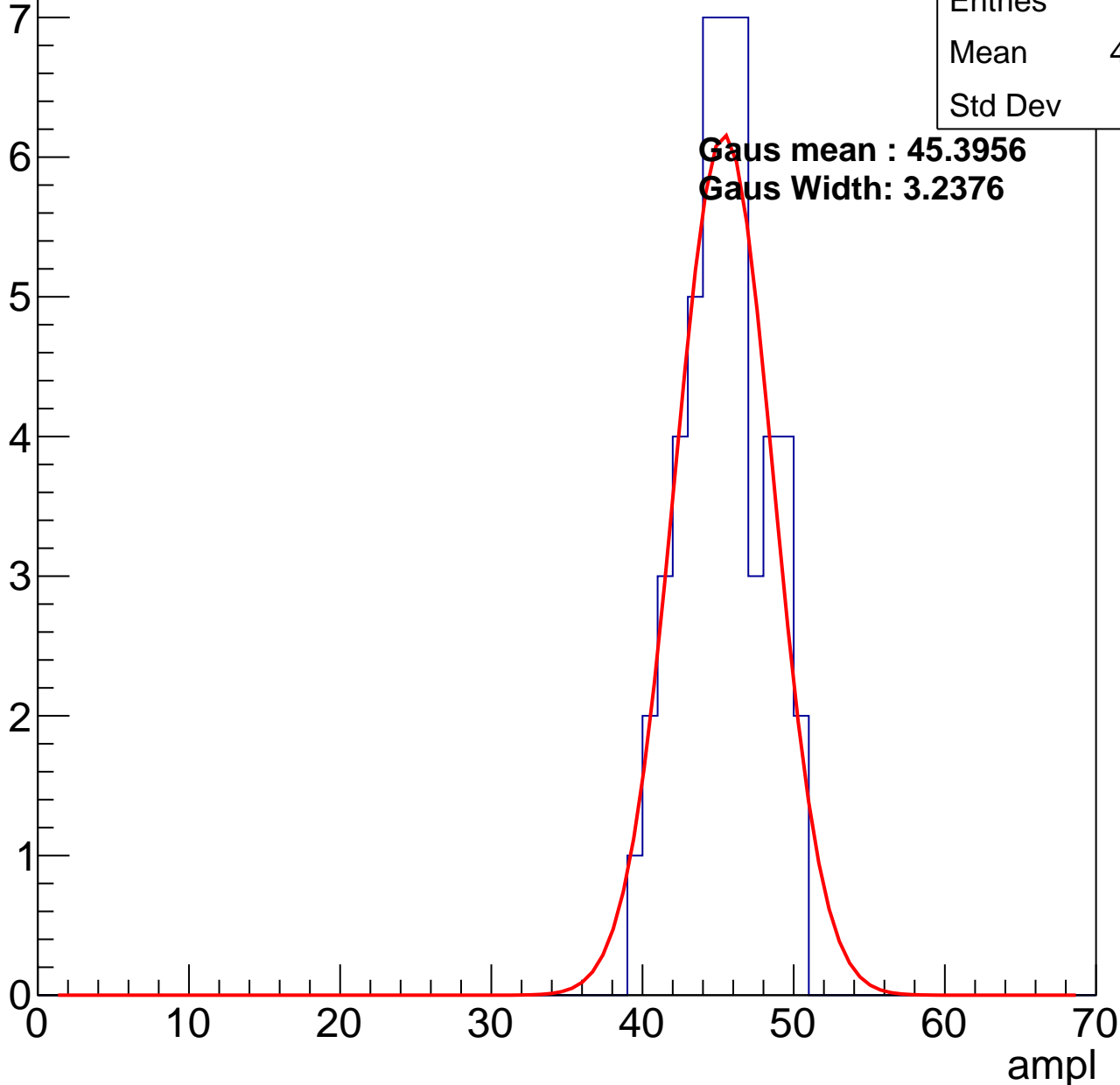
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	44.88
Std Dev	2.73

Gaus mean : 45.3956

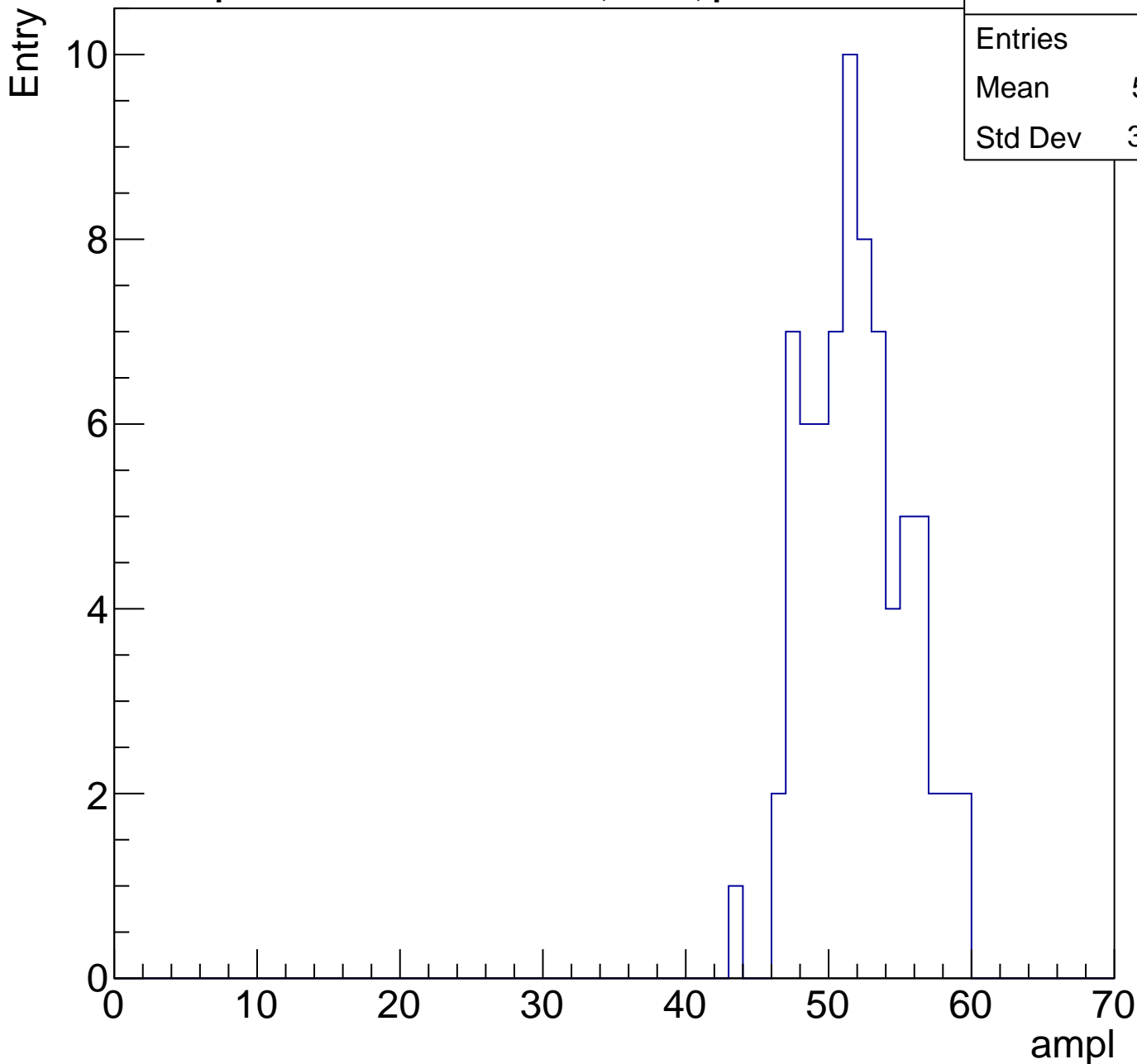
Gaus Width: 3.2376



B1L100S, U10-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	51.51
Std Dev	3.422

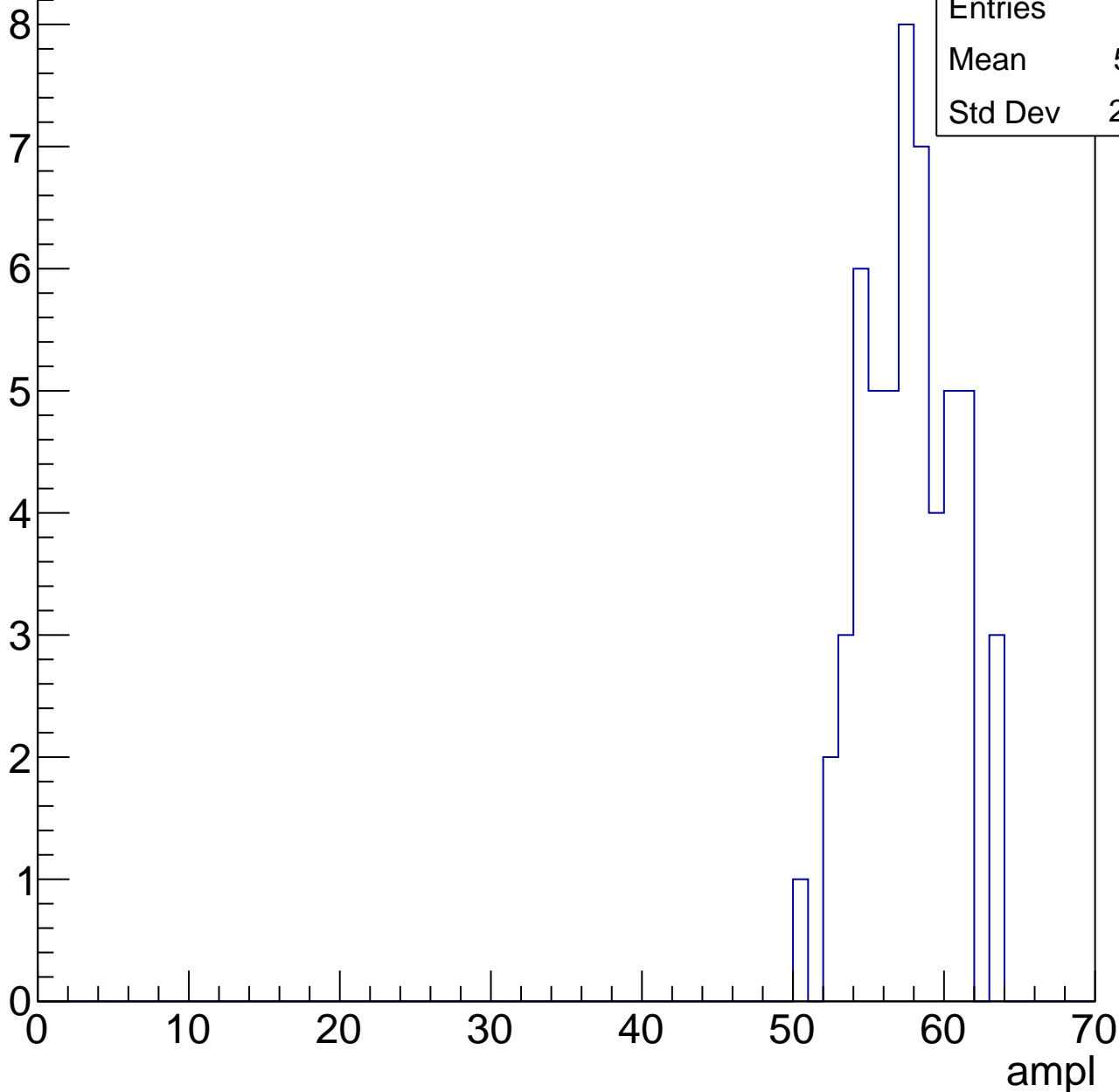


B1L100S, U10-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.11
Std Dev	2.986

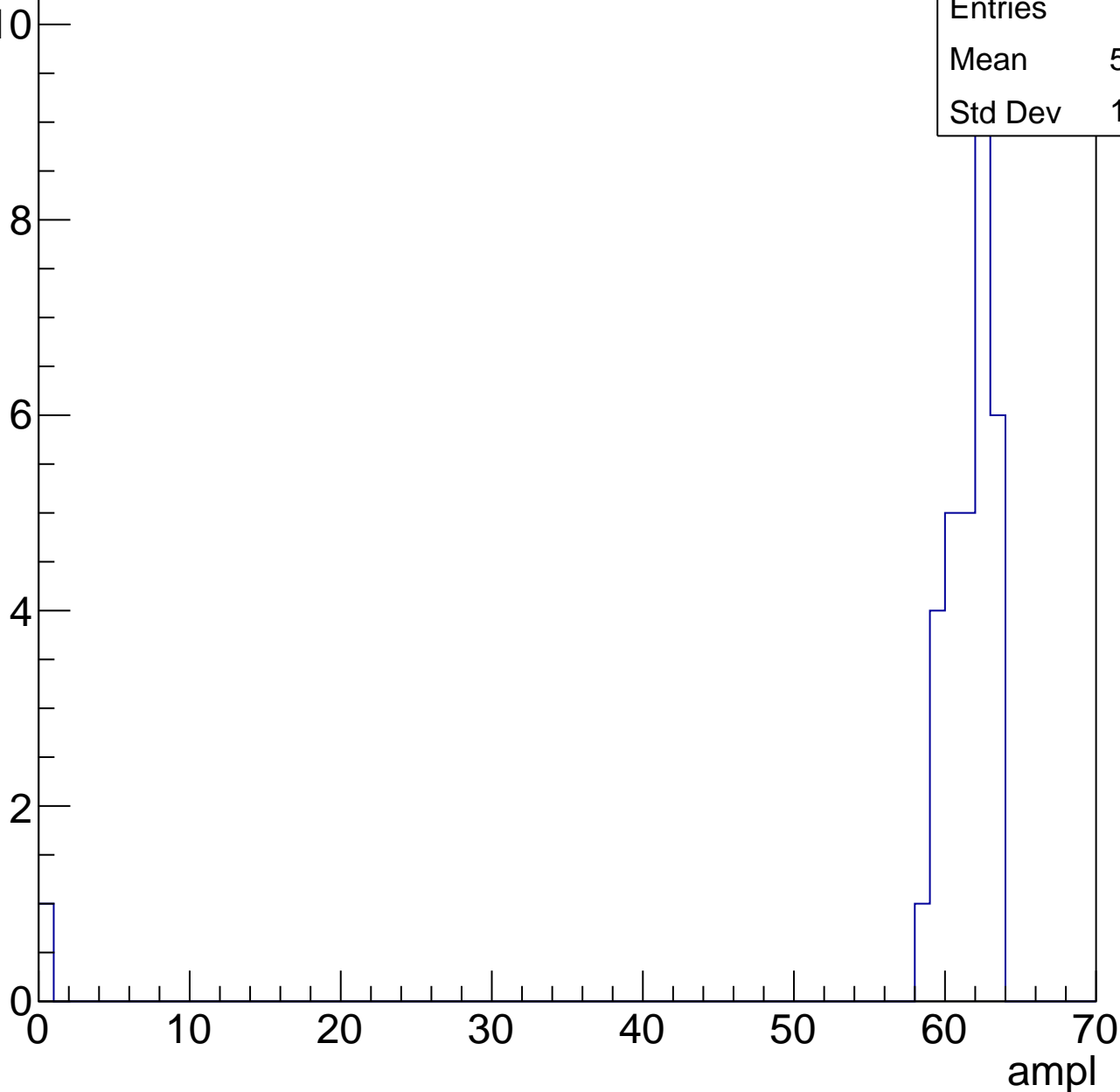


B1L100S, U10-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

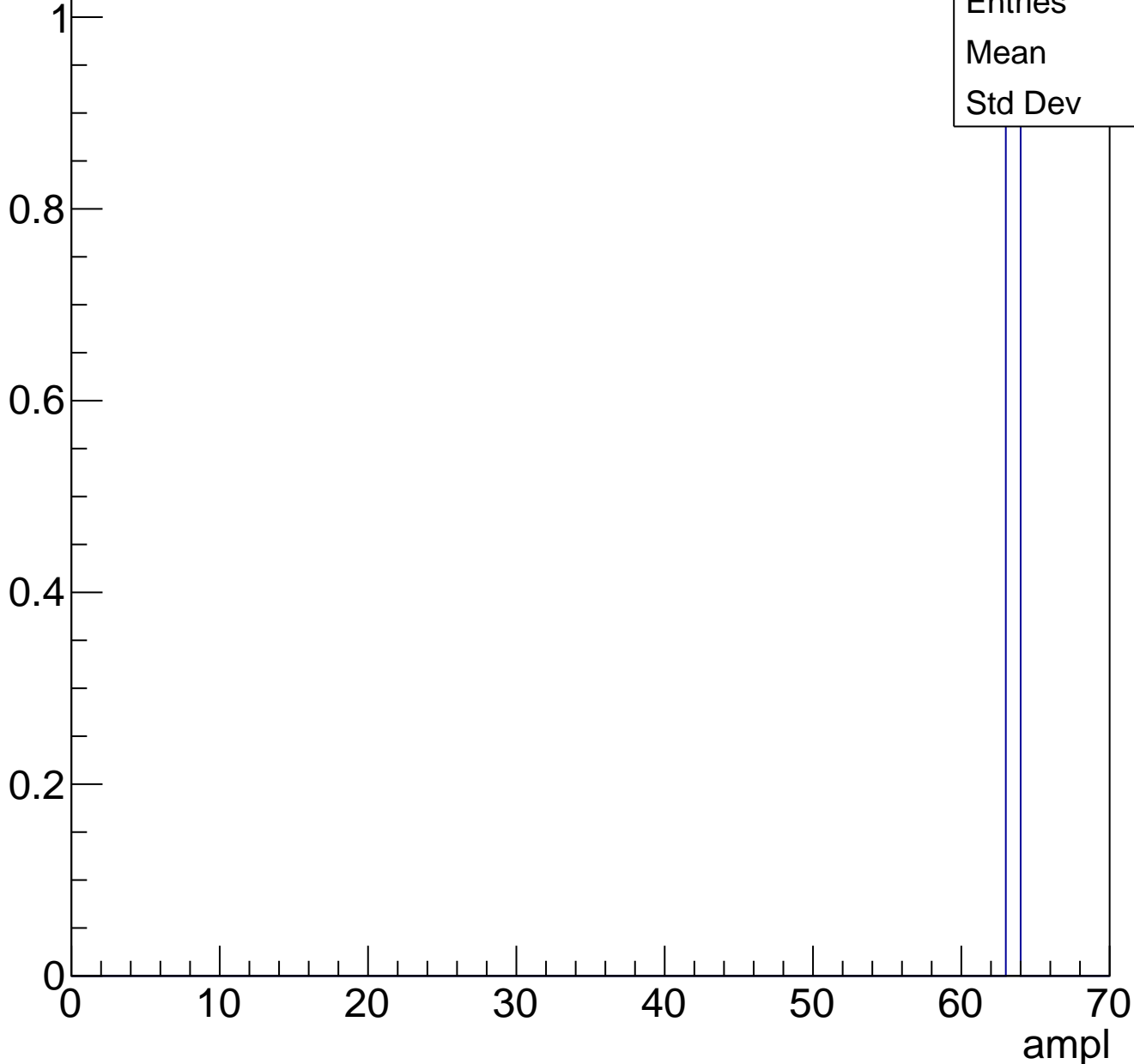
Entries	32
Mean	59.28
Std Dev	10.74



B1L100S, U10-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



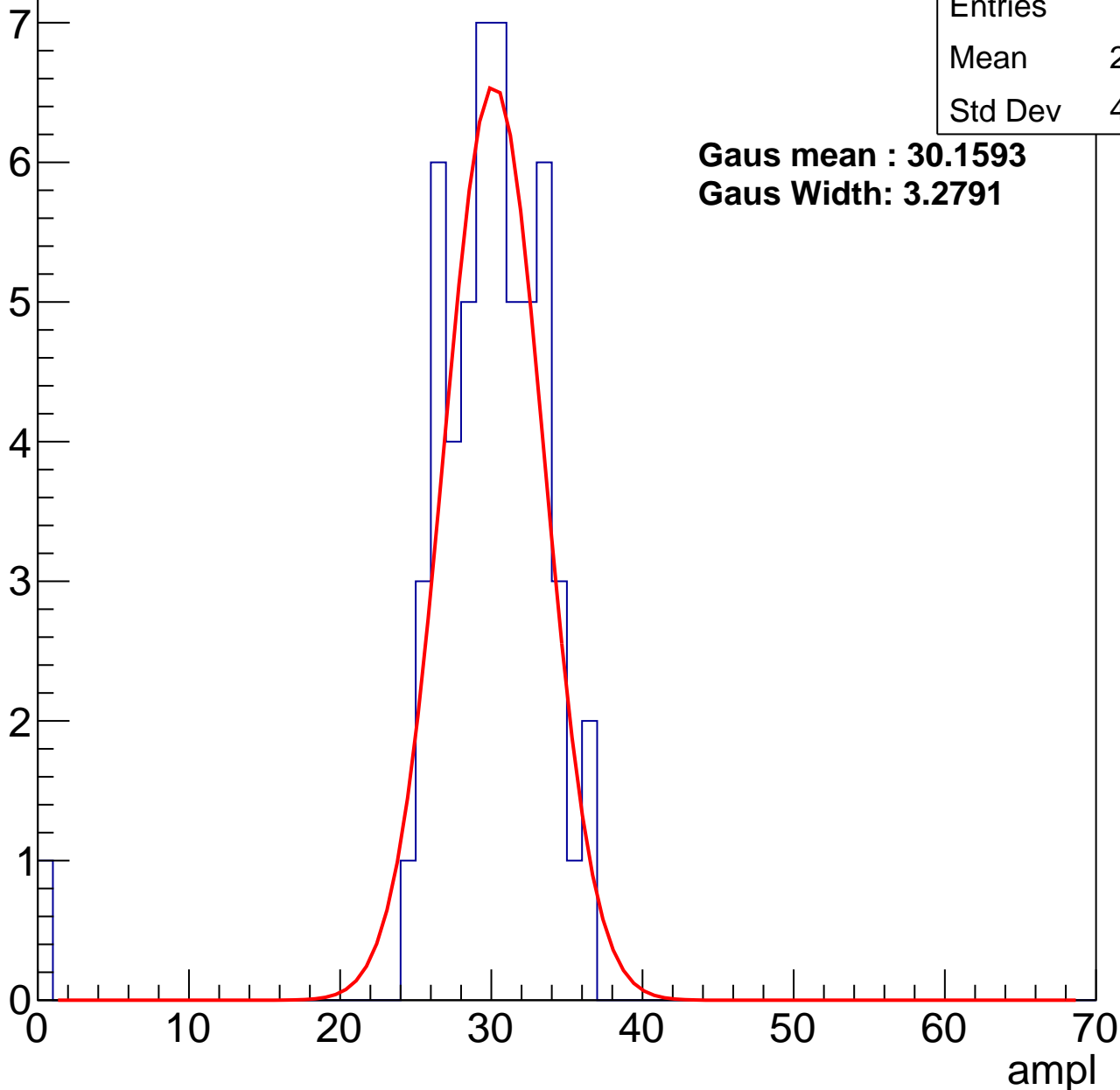
B1L100S, U10-ch8, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.25
Std Dev	4.933

Gaus mean : 30.1593
Gaus Width: 3.2791



B1L100S, U10-ch8, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	36.57
Std Dev	3.639

Gaus mean : 36.8055

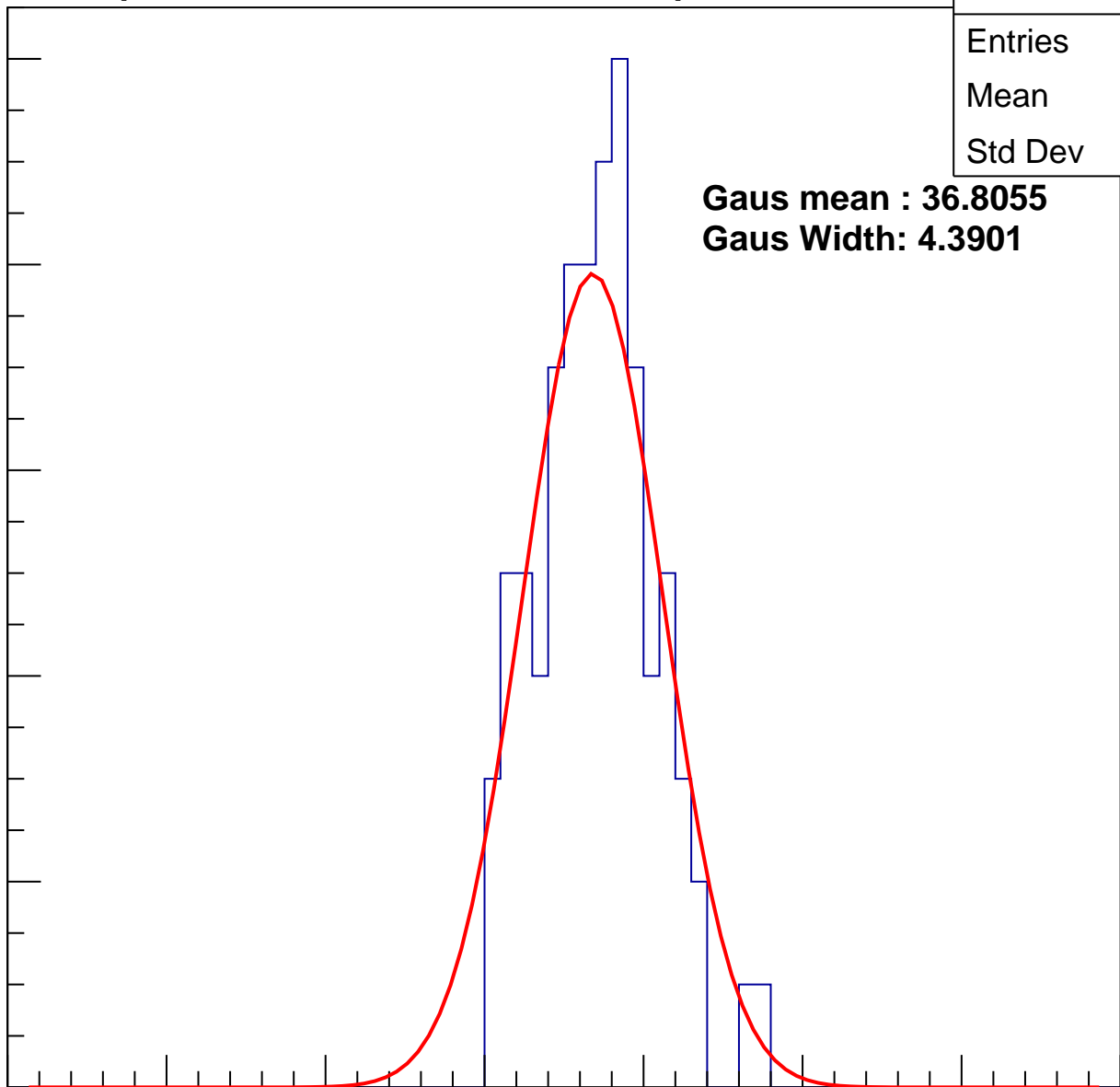
Gaus Width: 4.3901

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch8, adc2

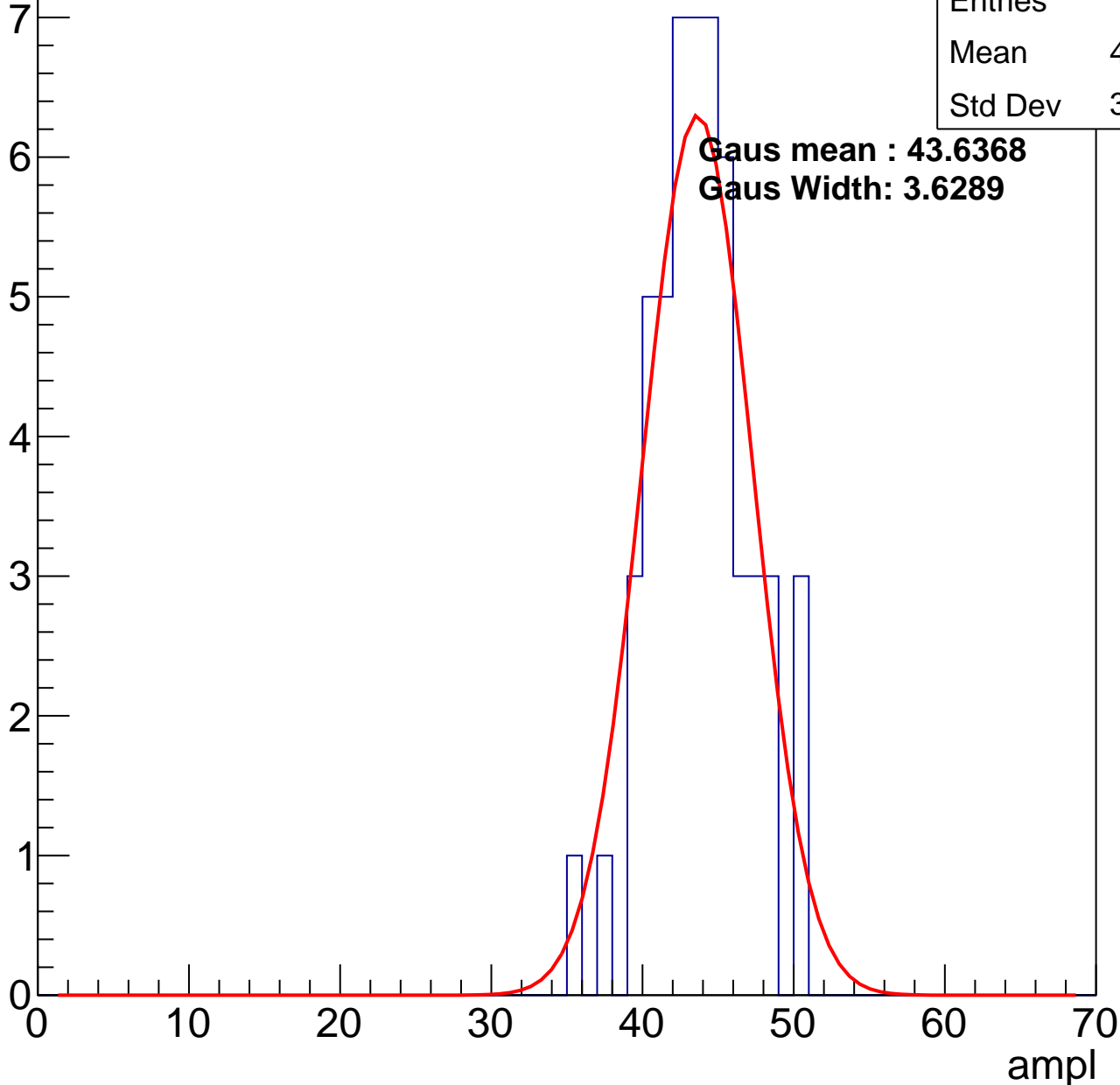
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.33
Std Dev	3.168

Gaus mean : 43.6368

Gaus Width: 3.6289

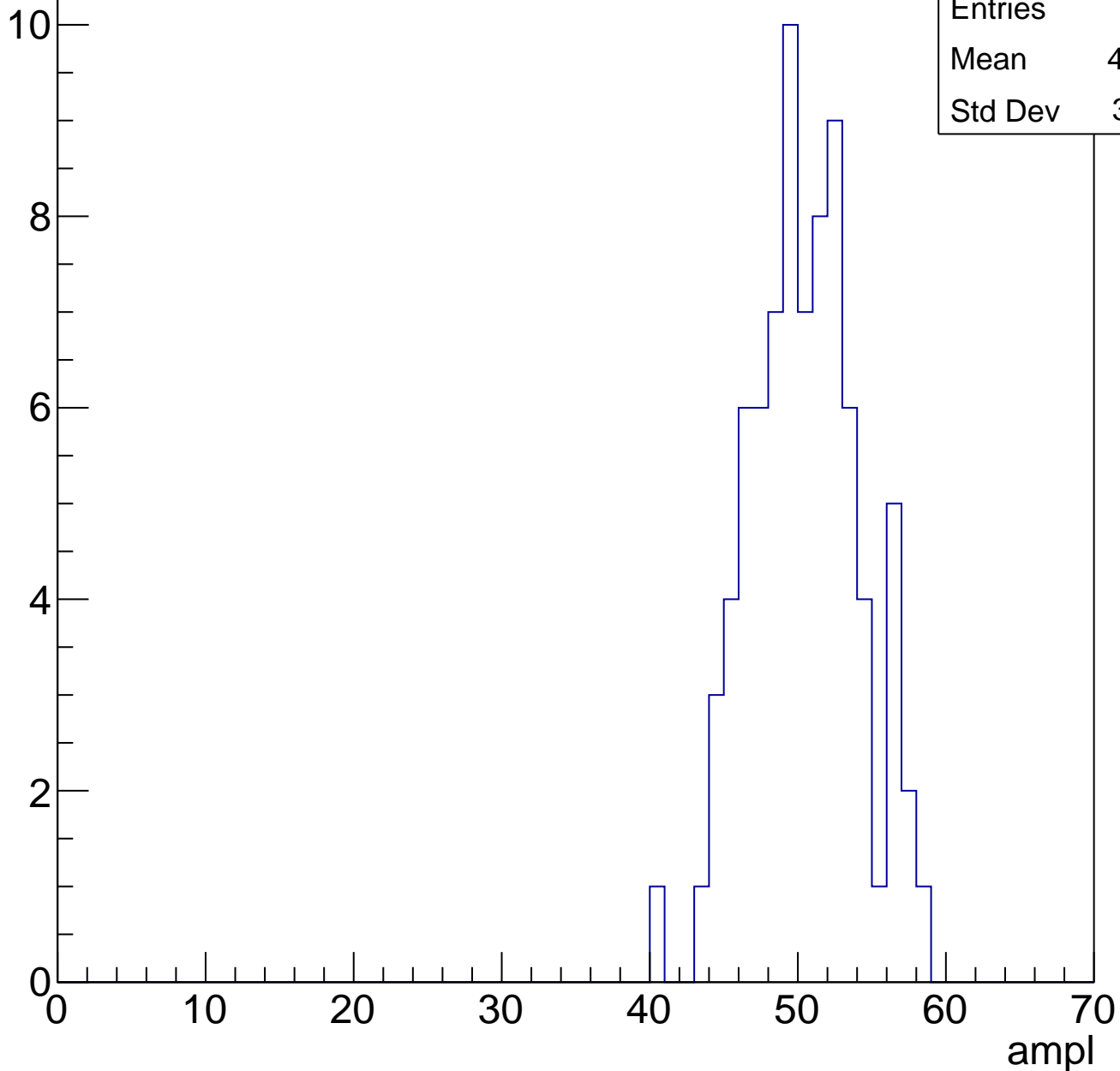


B1L100S, U10-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	49.95
Std Dev	3.641

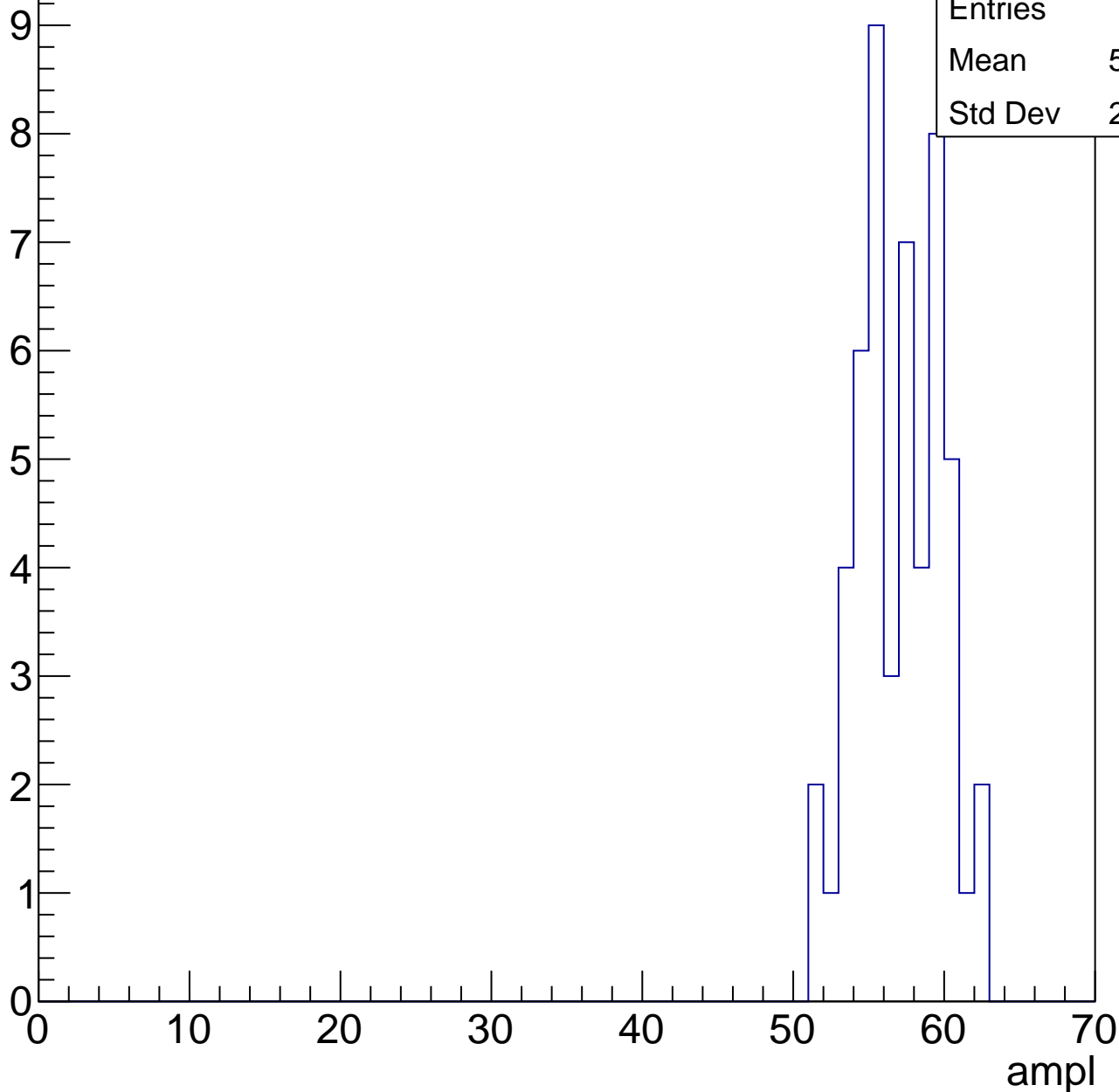
Entry



B1L100S, U10-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

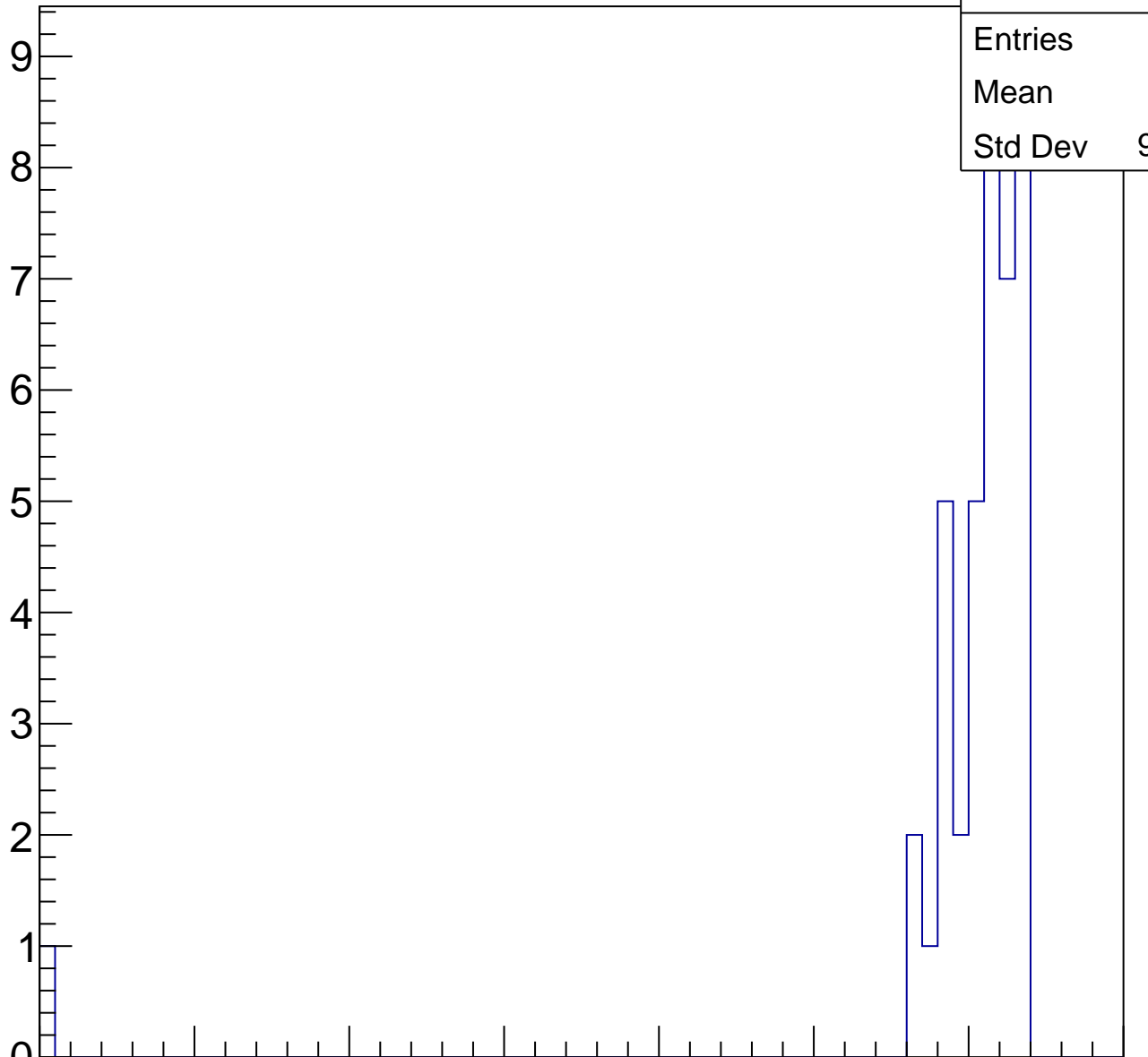
Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.1
Std Dev	9.669

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch9, adc0

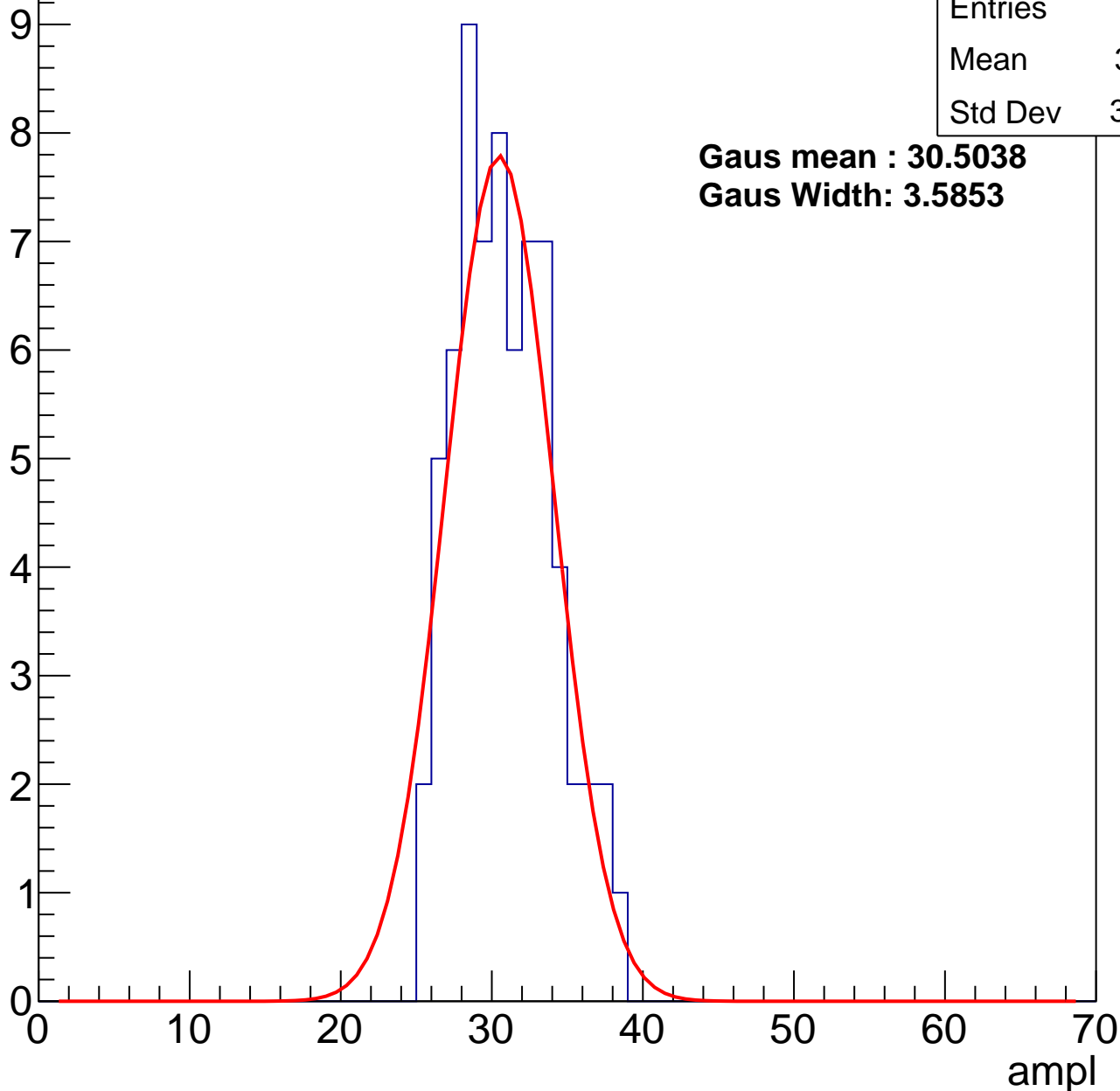
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.41
Std Dev	3.117

Gaus mean : 30.5038

Gaus Width: 3.5853



B1L100S, U10-ch9, adc1

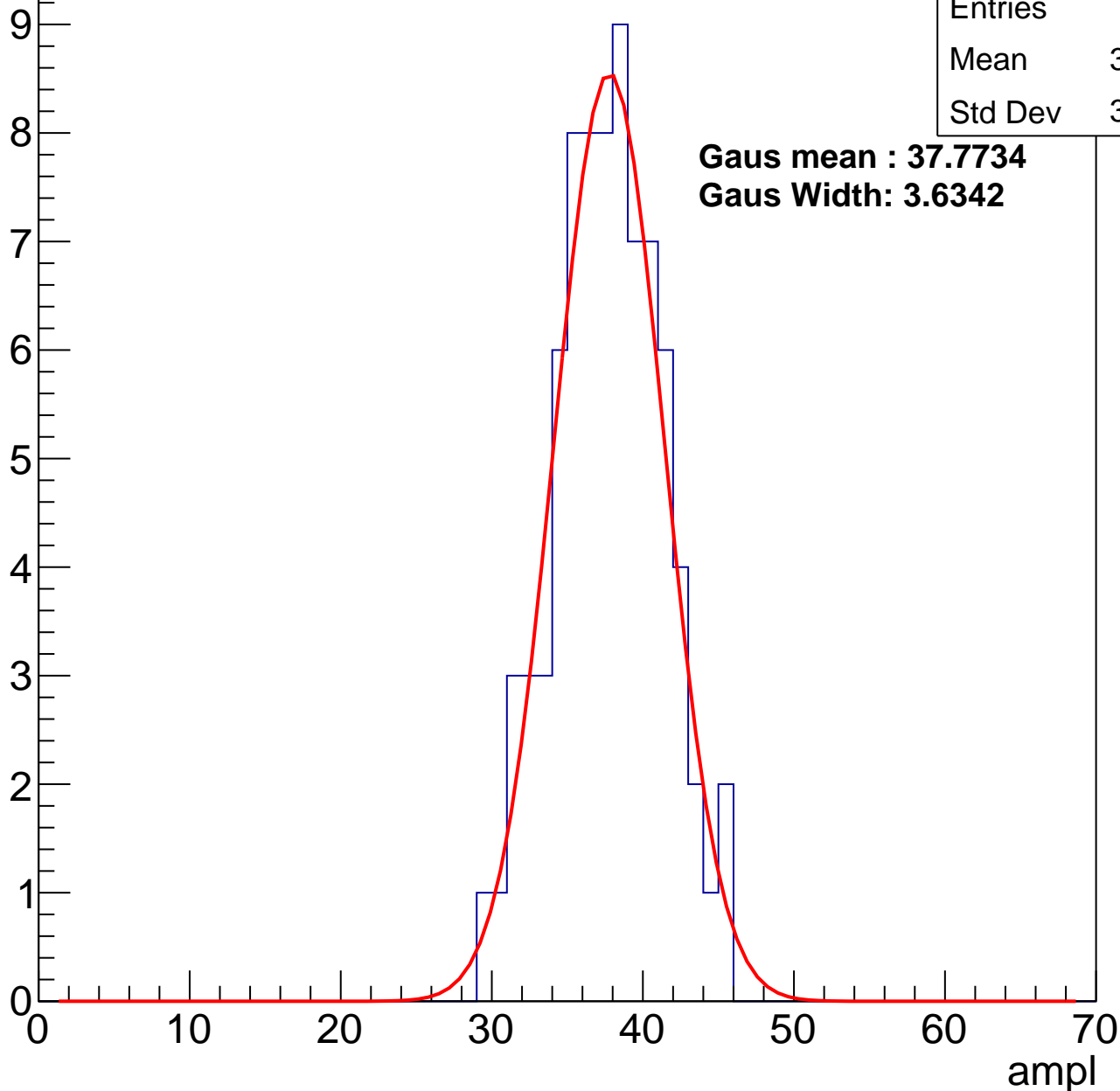
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37.27
Std Dev	3.517

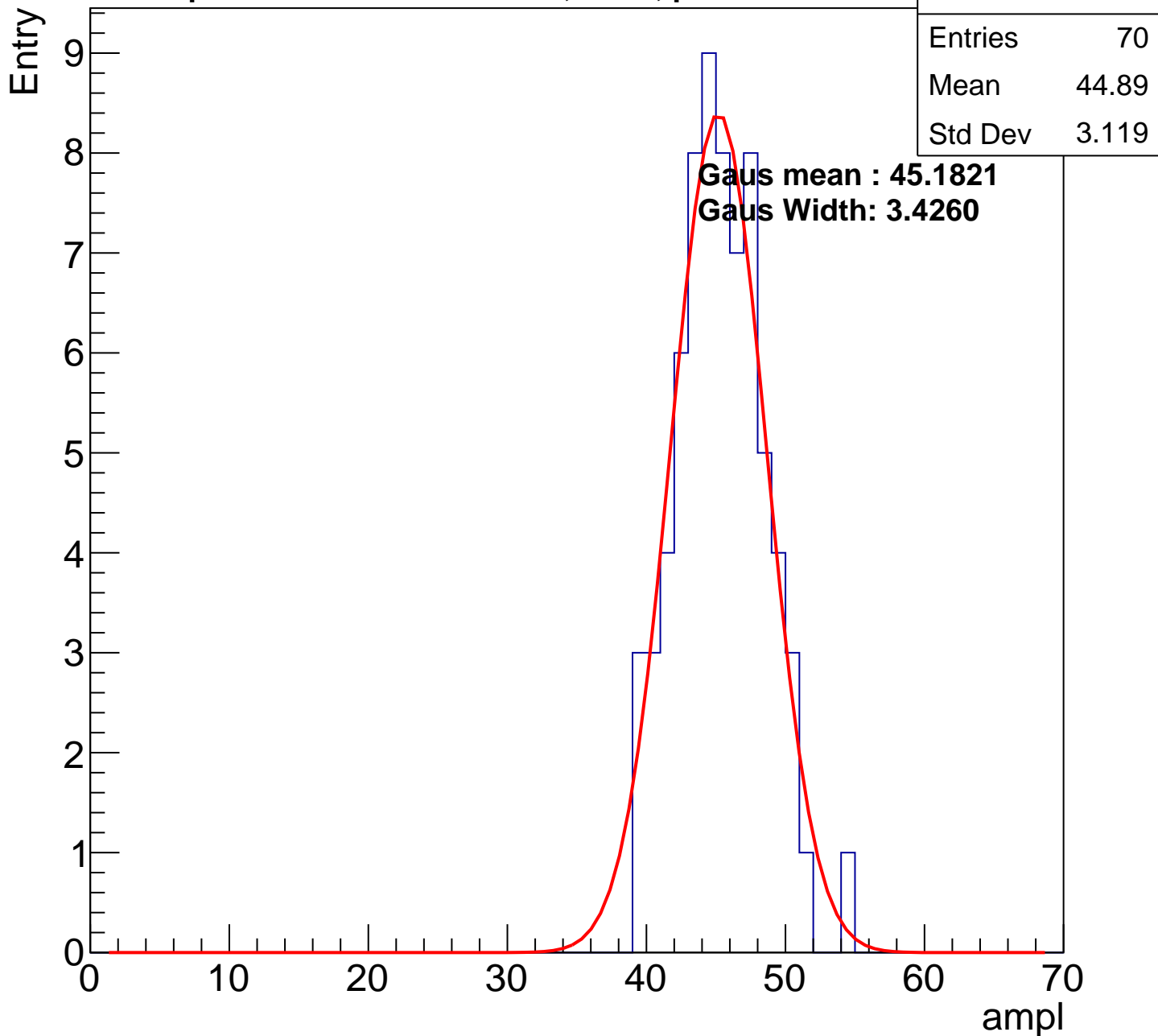
Gaus mean : 37.7734

Gaus Width: 3.6342



B1L100S, U10-ch9, adc2

calib_packv5_042523_0143.root, FC#4, port A2

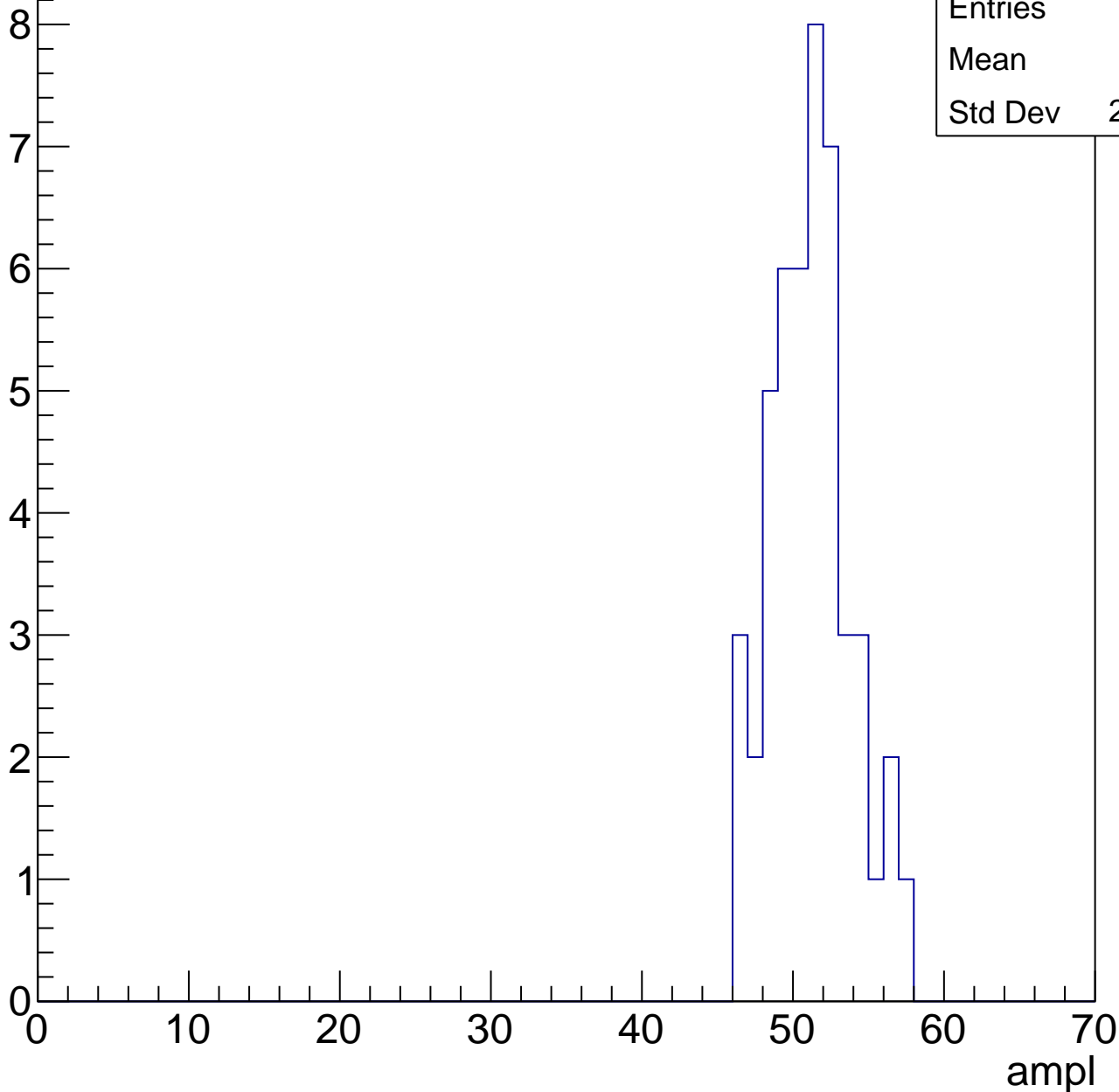


B1L100S, U10-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

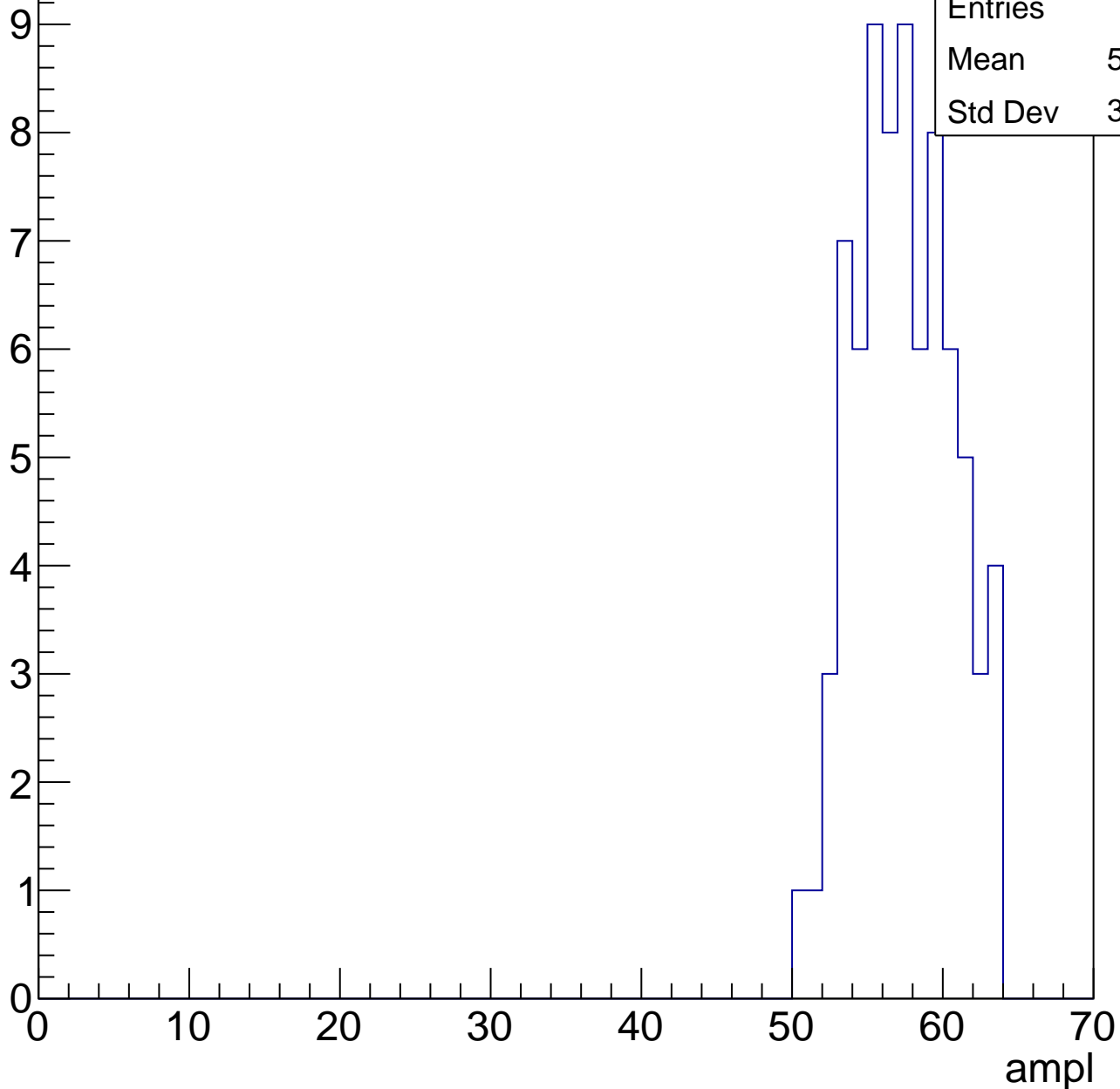
Entries	47
Mean	50.7
Std Dev	2.633



B1L100S, U10-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

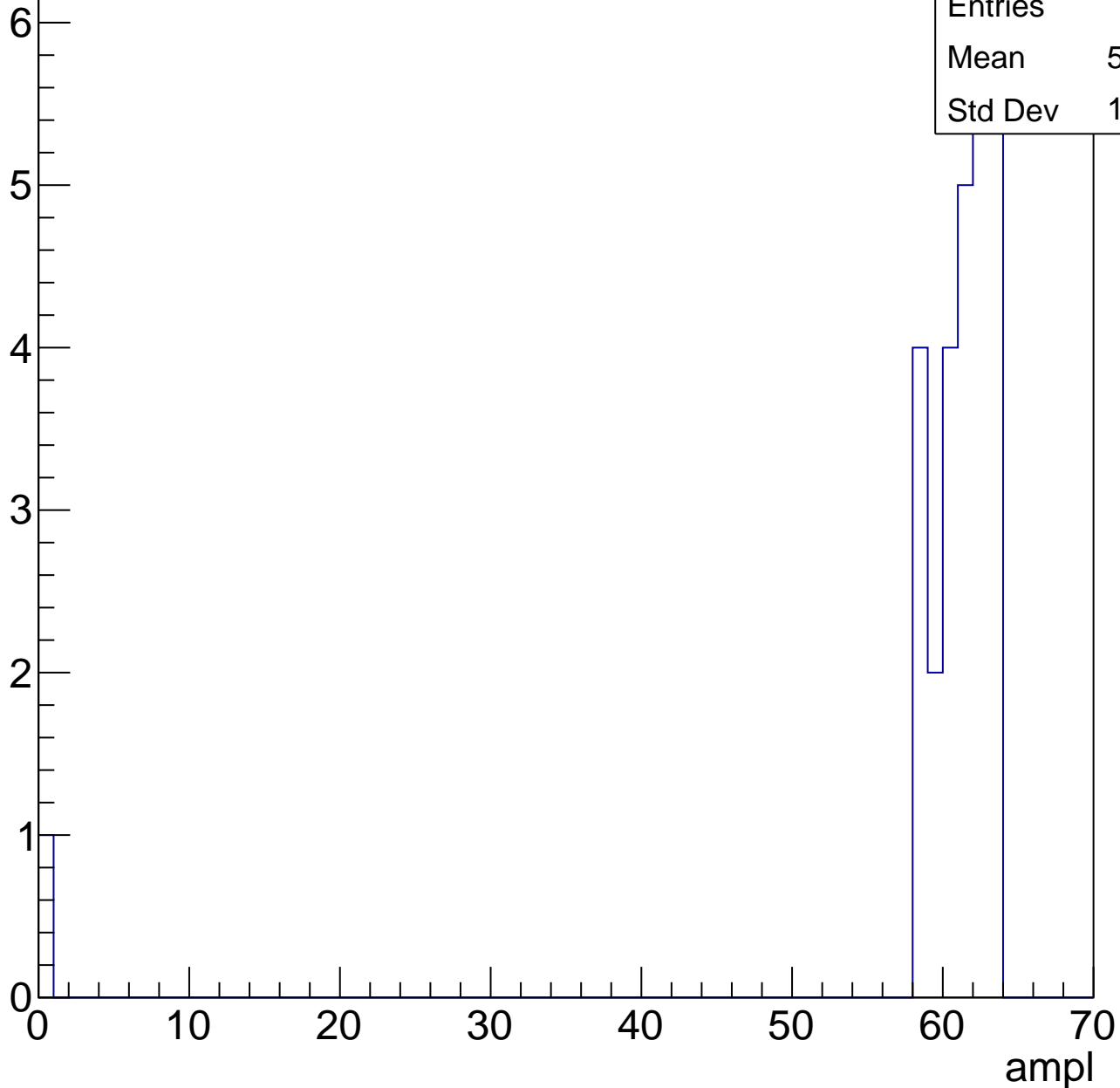


Entries	76
Mean	56.99
Std Dev	3.164

B1L100S, U10-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

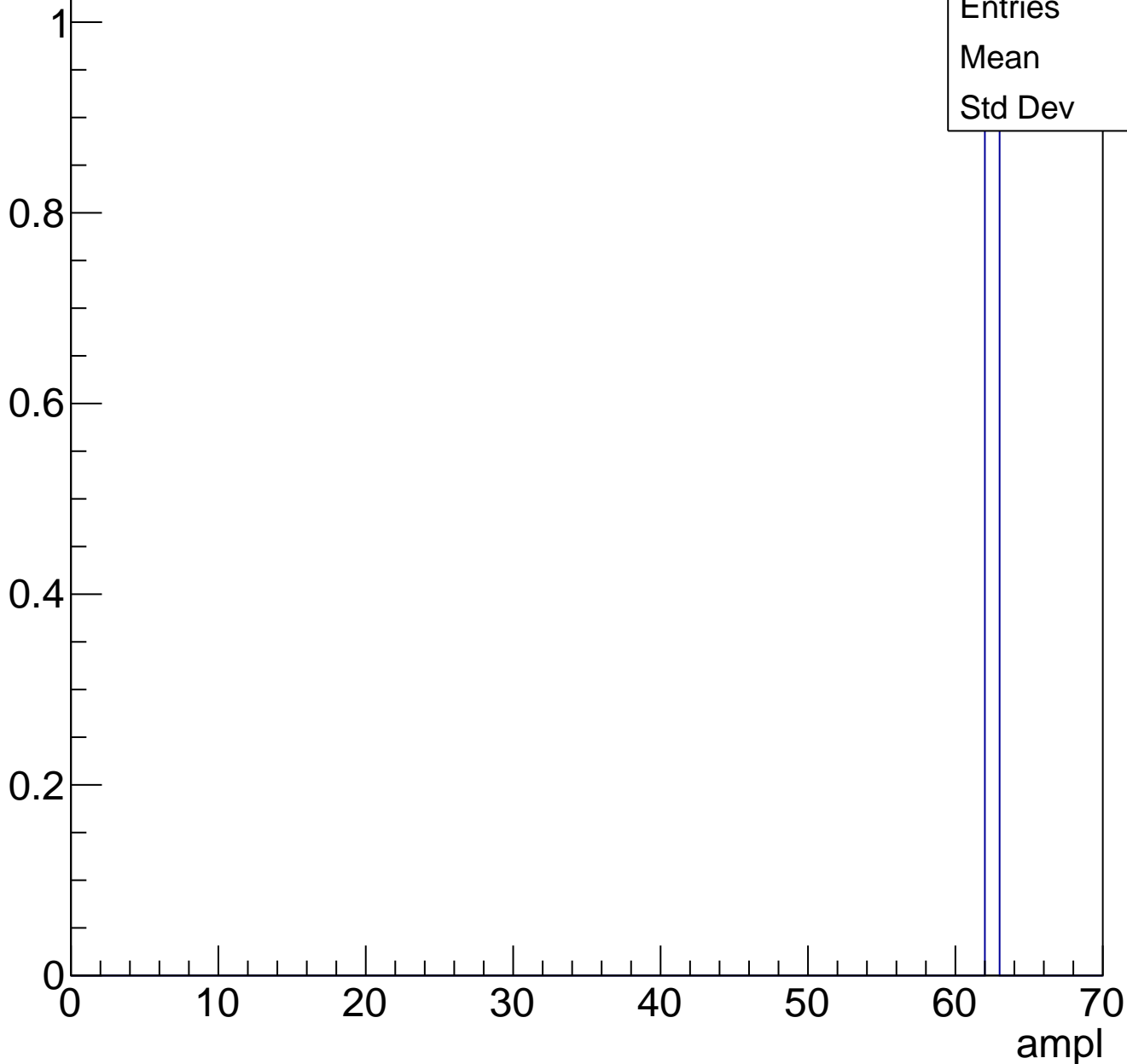
Entry



B1L100S, U10-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L100S, U10-ch10, adc0

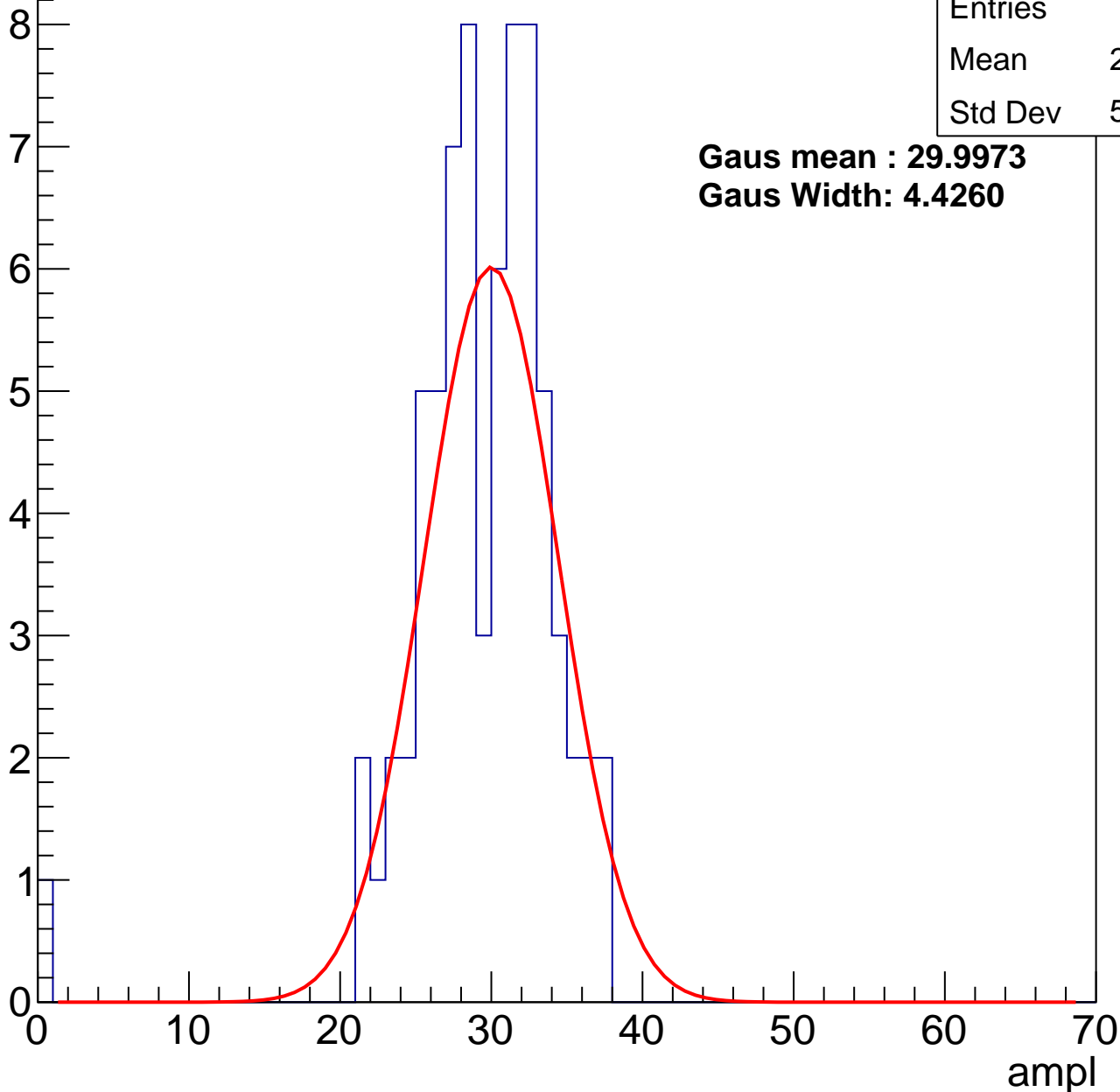
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.89
Std Dev	5.087

Gaus mean : 29.9973

Gaus Width: 4.4260



B1L100S, U10-ch10, adc1

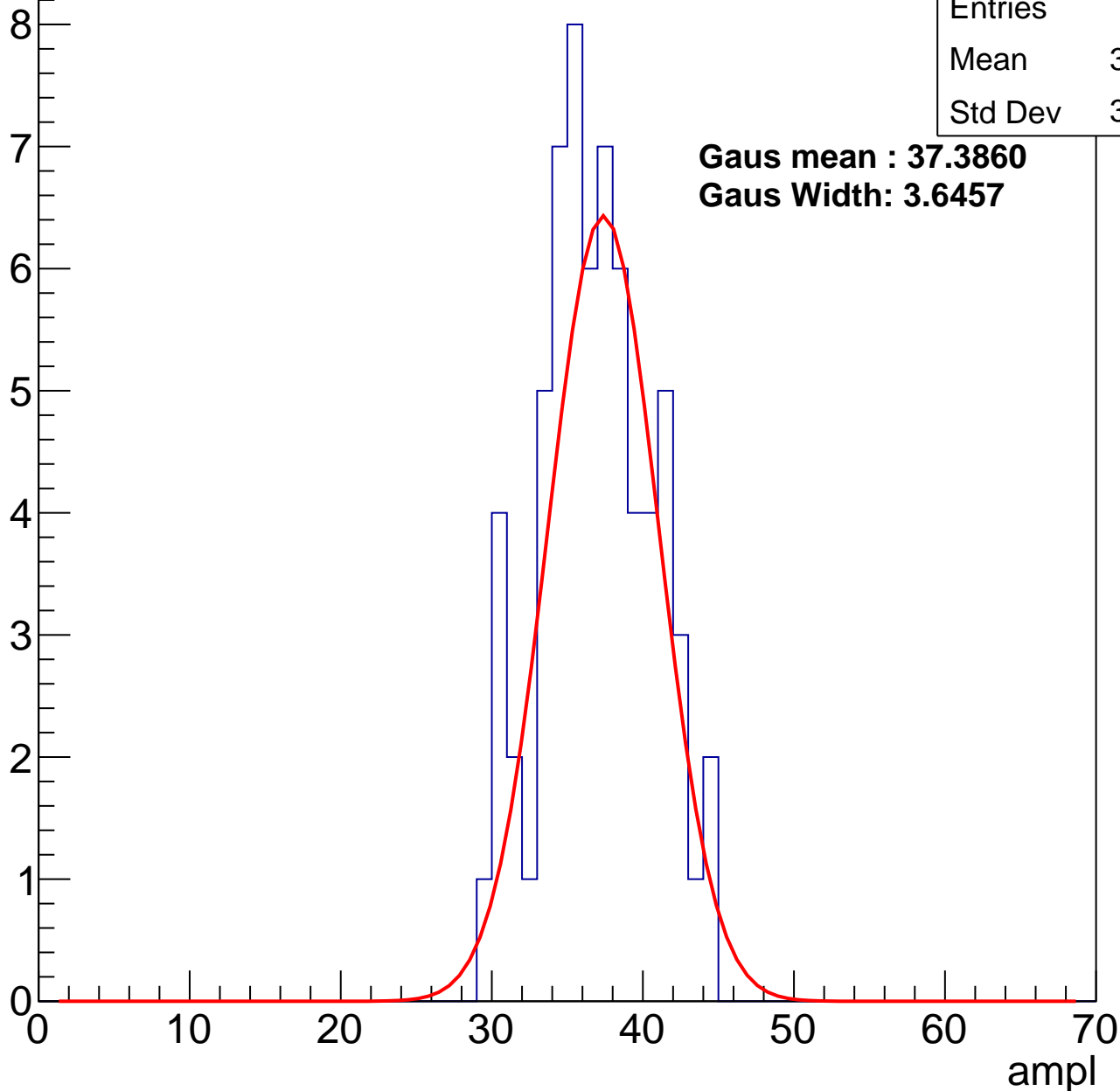
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.47
Std Dev	3.623

Gaus mean : 37.3860

Gaus Width: 3.6457



B1L100S, U10-ch10, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

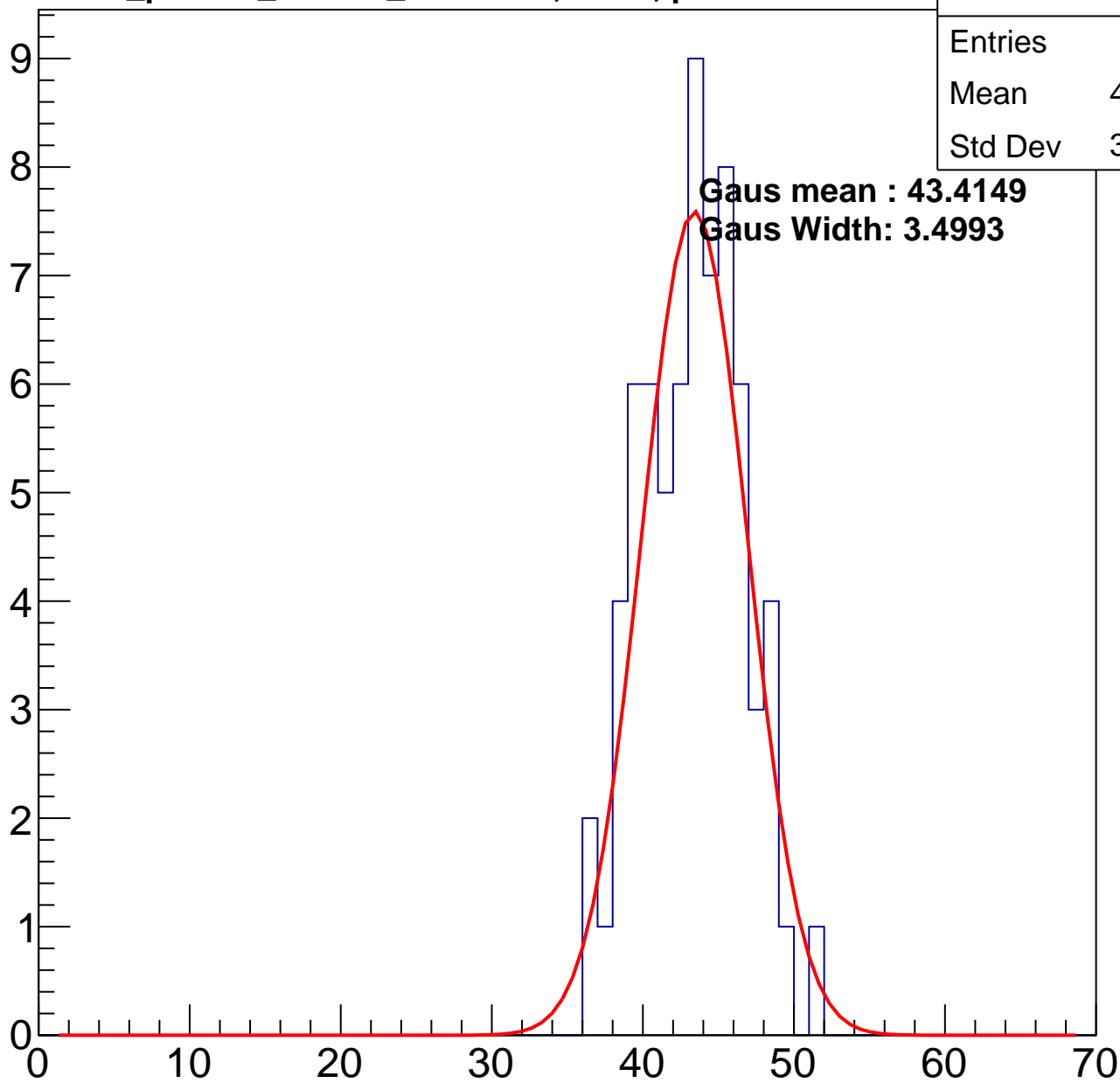
9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	42.84
Std Dev	3.295

Gaus mean : 43.4149

Gaus Width: 3.4993

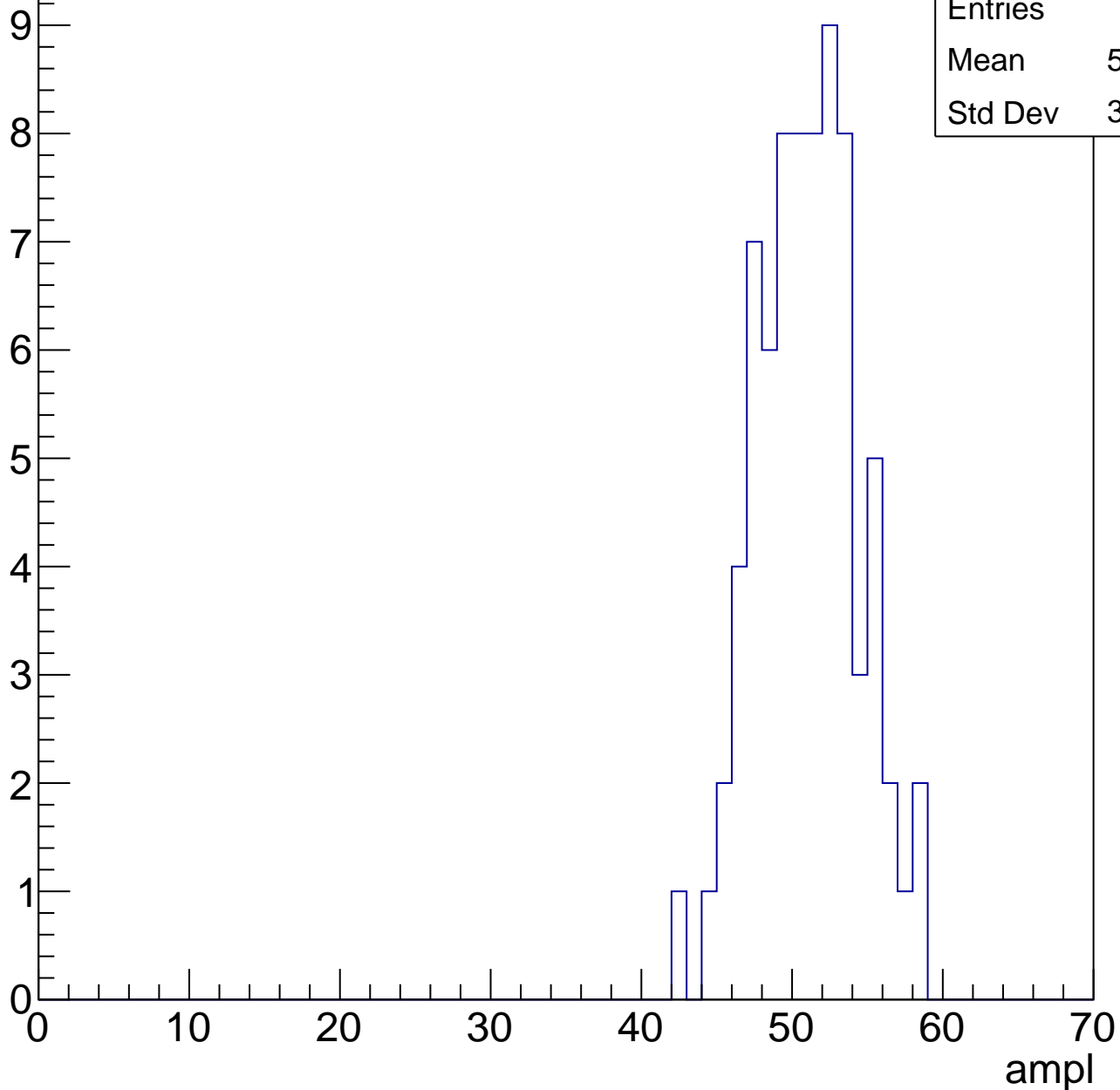
ampl



B1L100S, U10-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



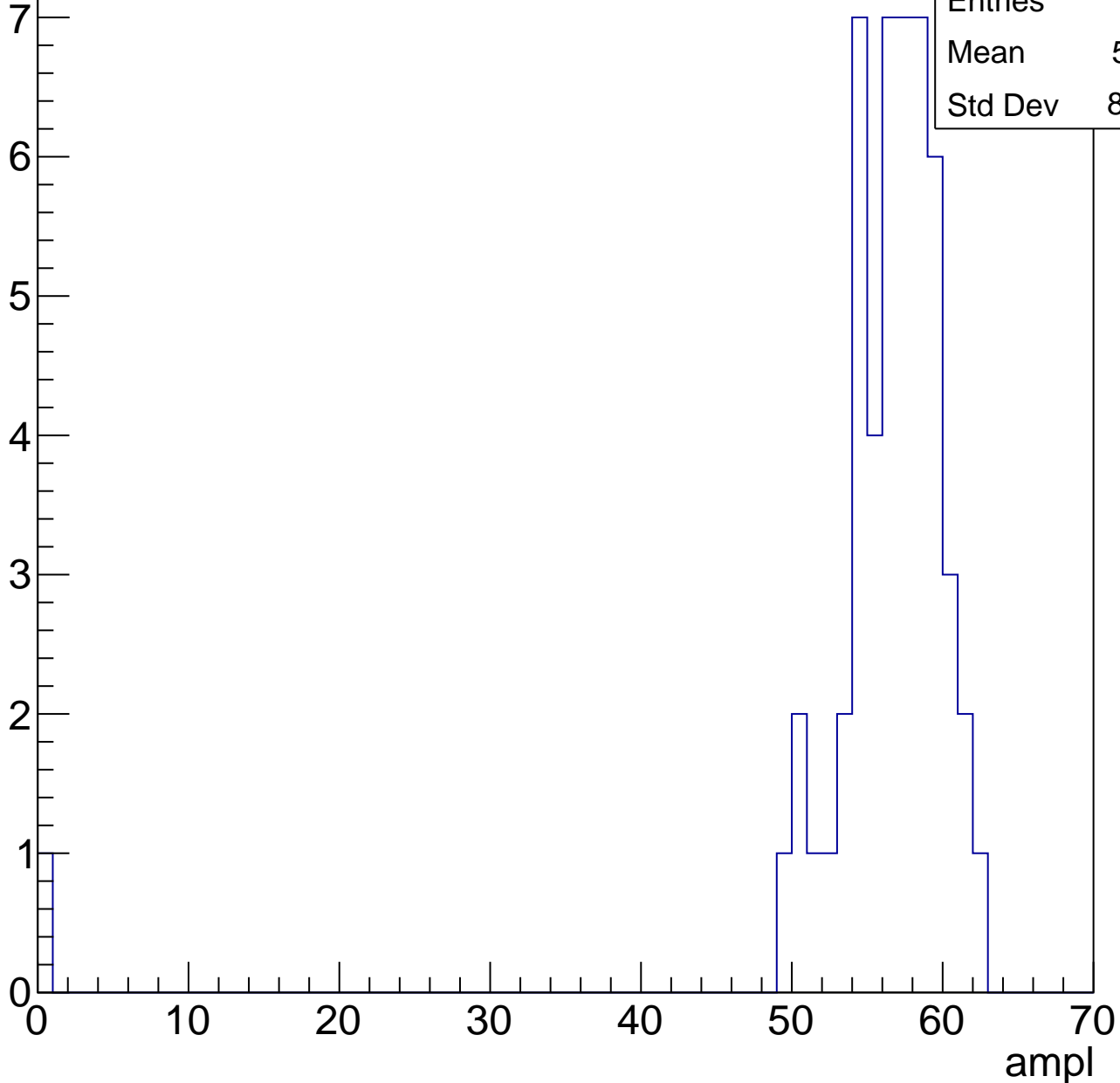
Entries	75
Mean	50.55
Std Dev	3.316

B1L100S, U10-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.21
Std Dev	8.249

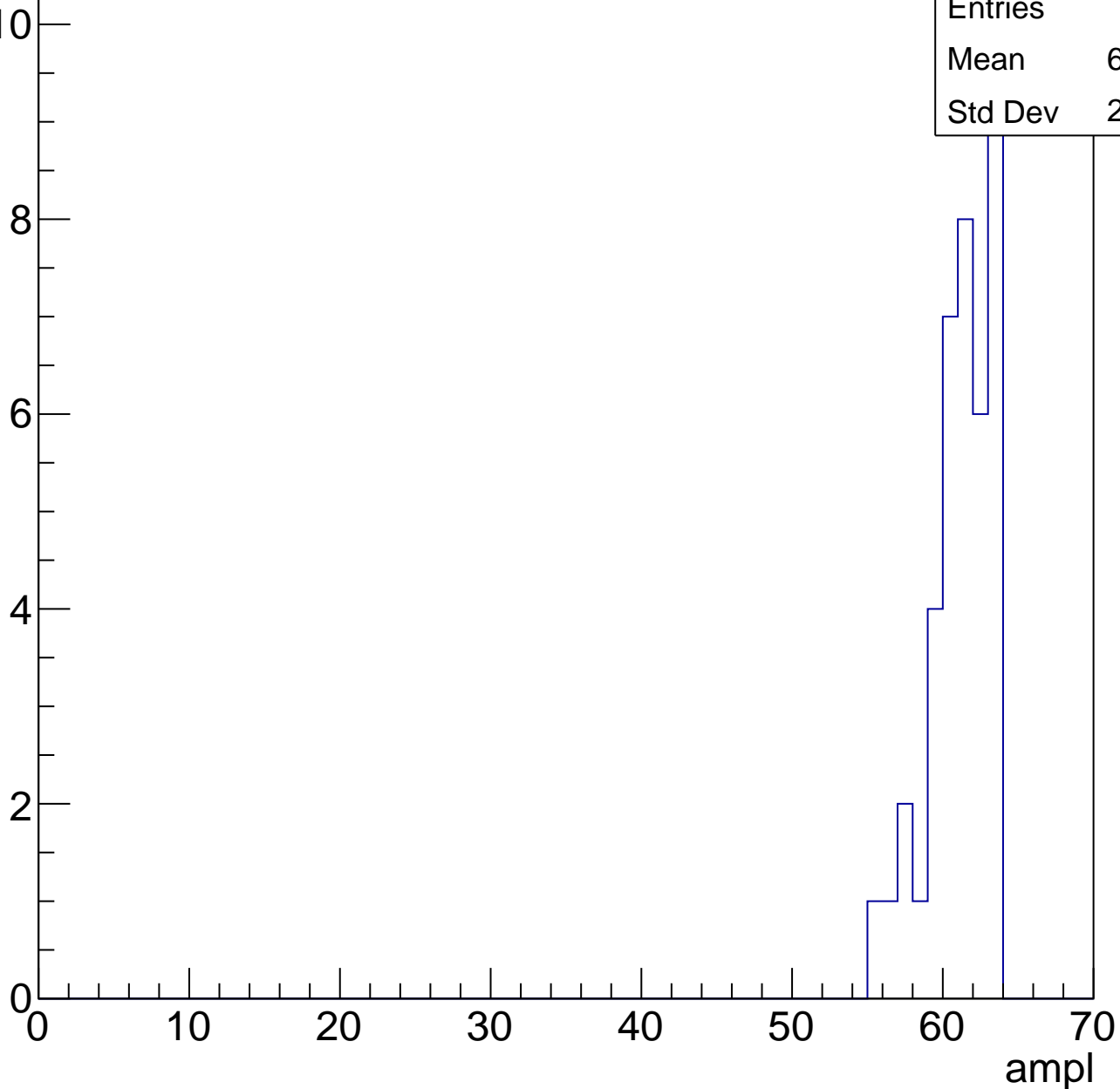


B1L100S, U10-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	60.73
Std Dev	2.049



B1L100S, U10-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch11, adc0

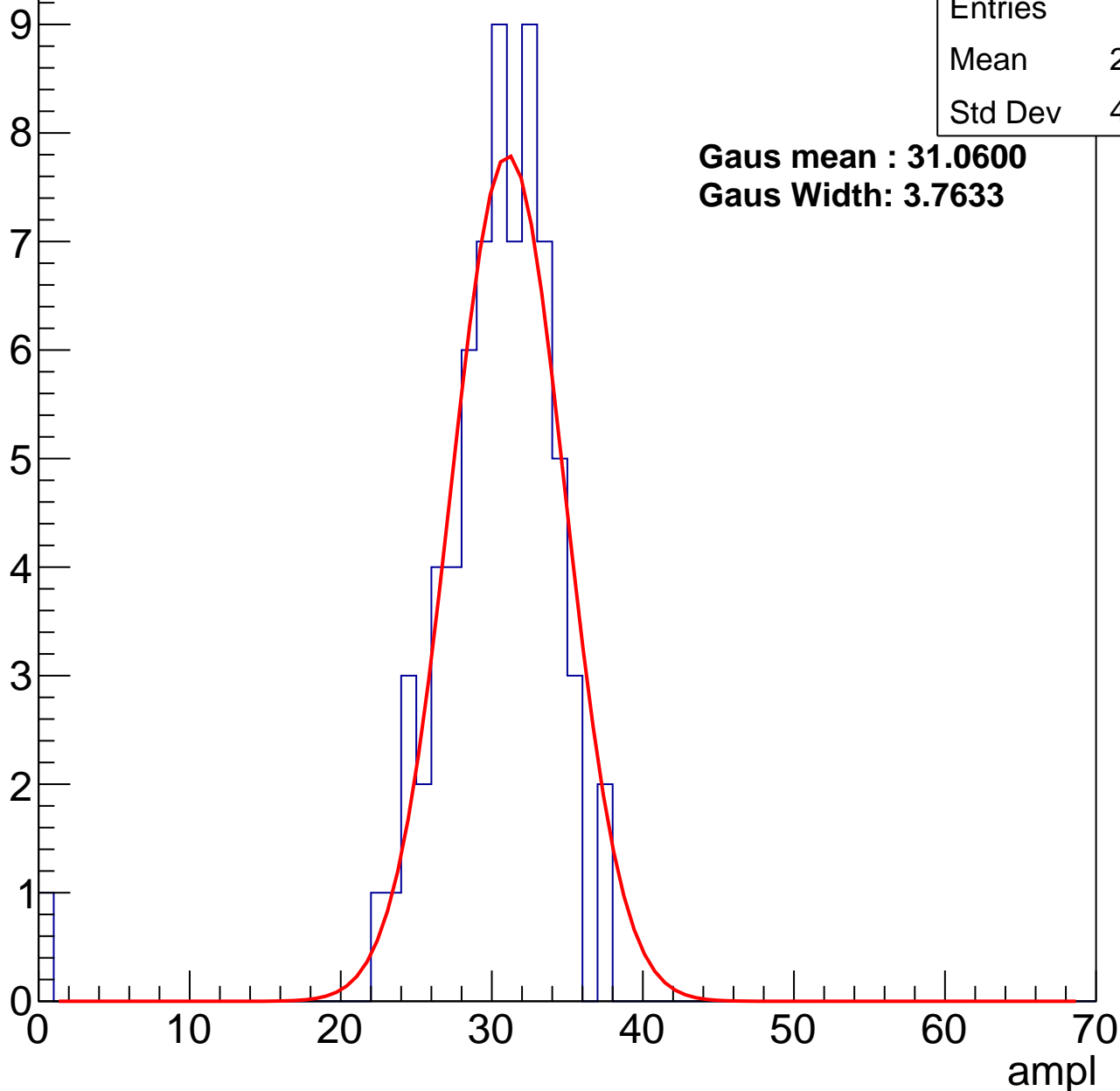
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.65
Std Dev	4.824

Gaus mean : 31.0600

Gaus Width: 3.7633



B1L100S, U10-ch11, adc1

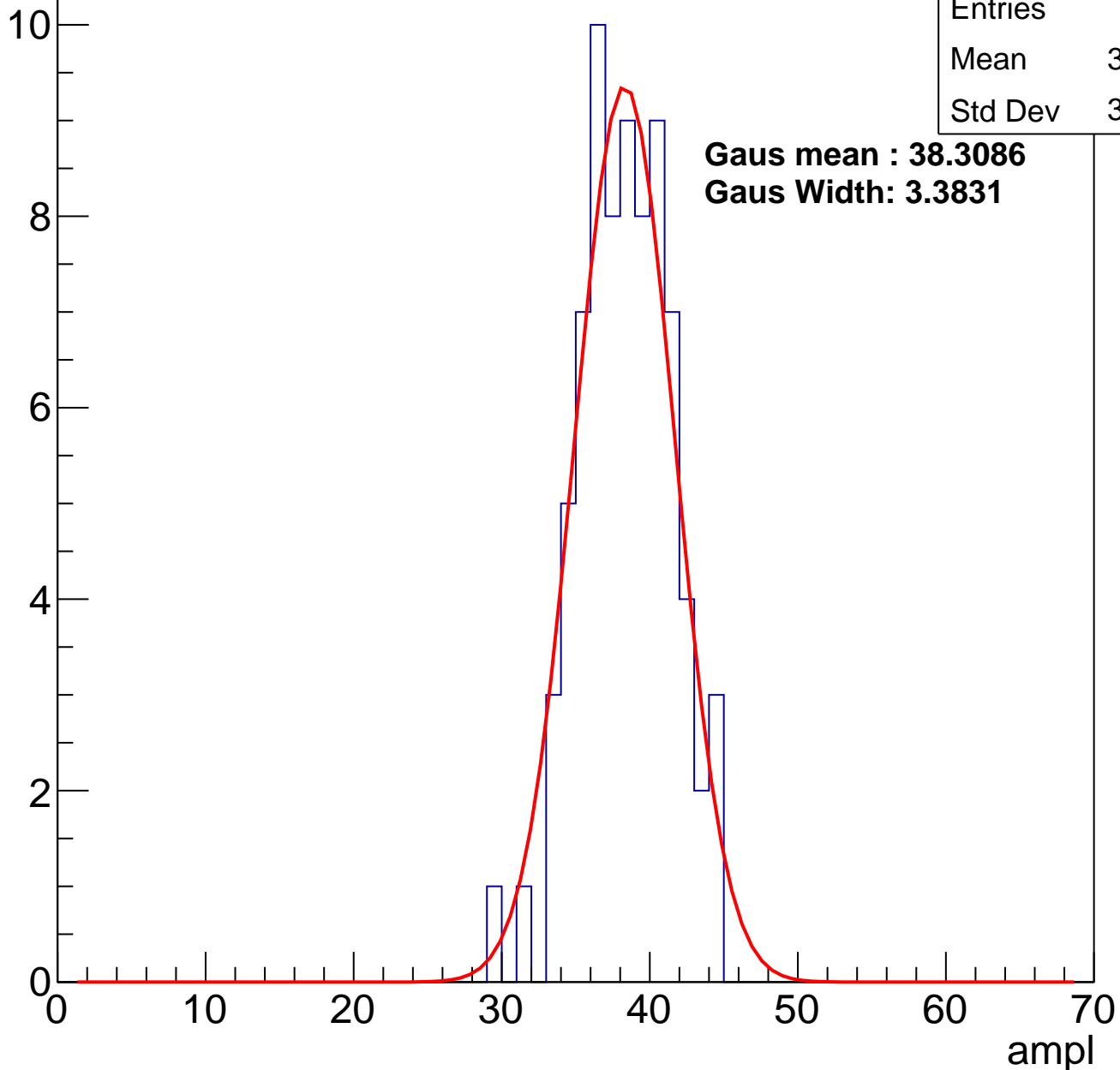
calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	37.88
Std Dev	3.066

Gaus mean : 38.3086

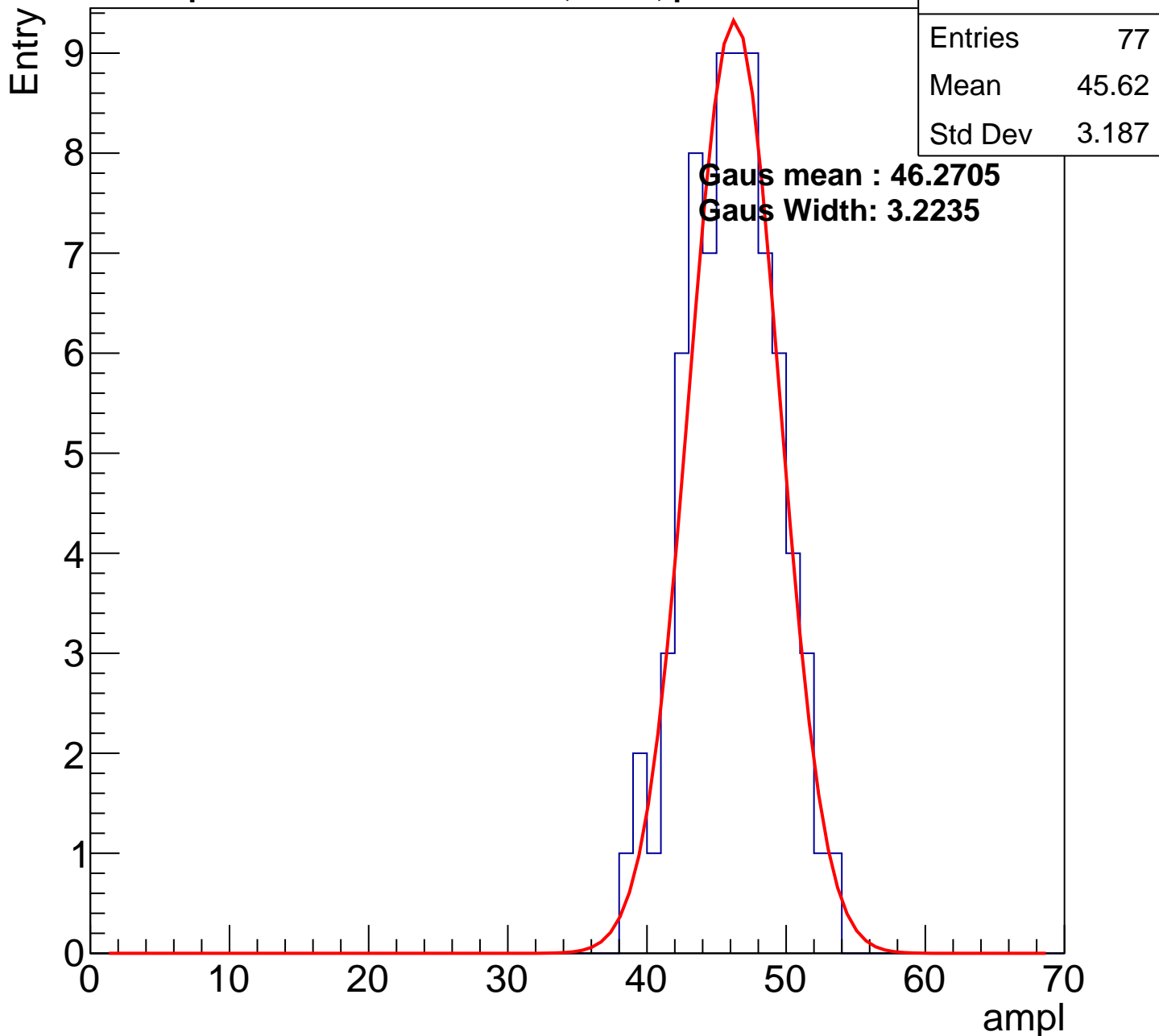
Gaus Width: 3.3831

Entry



B1L100S, U10-ch11, adc2

calib_packv5_042523_0143.root, FC#4, port A2

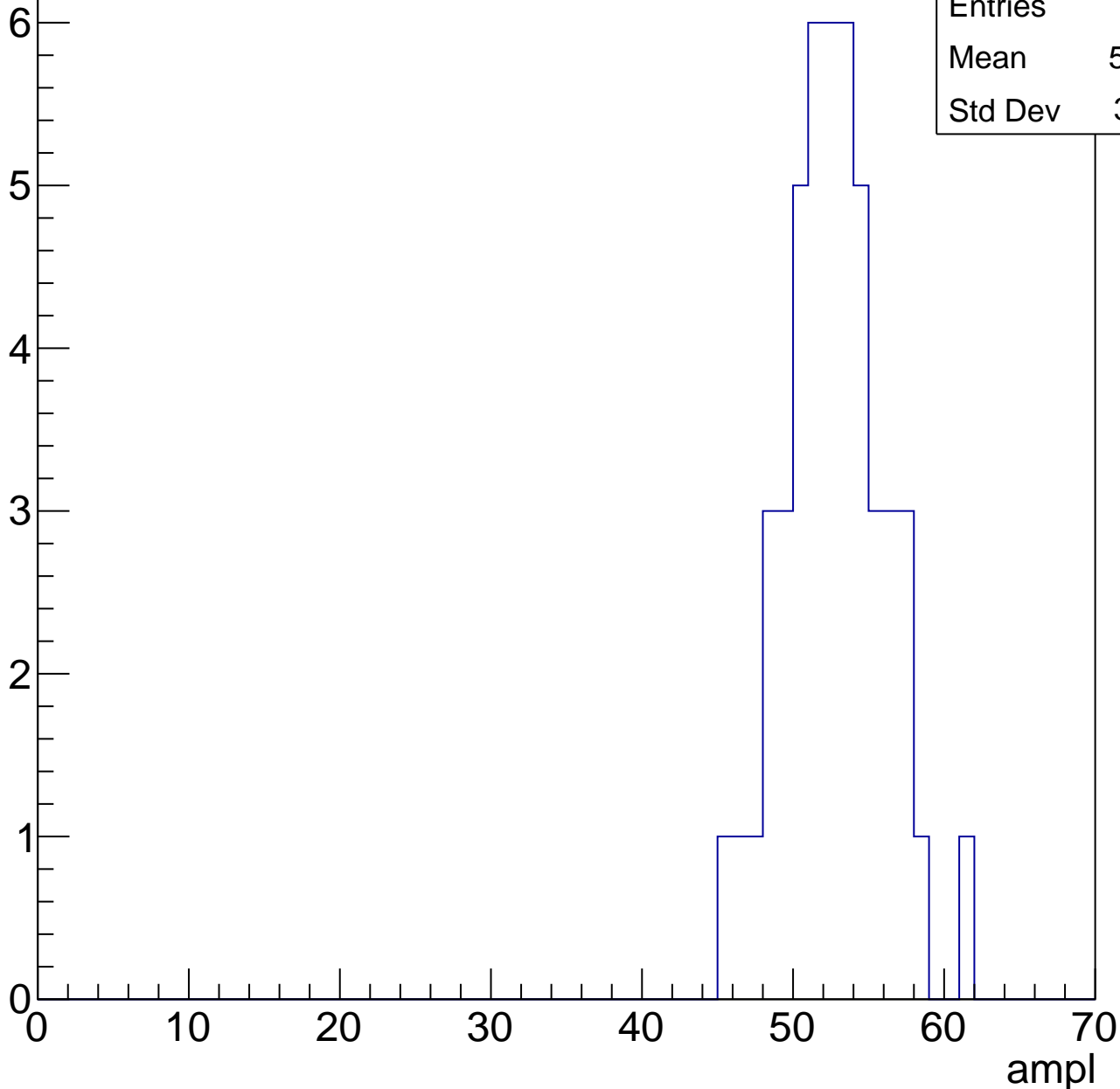


B1L100S, U10-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	52.25
Std Dev	3.231

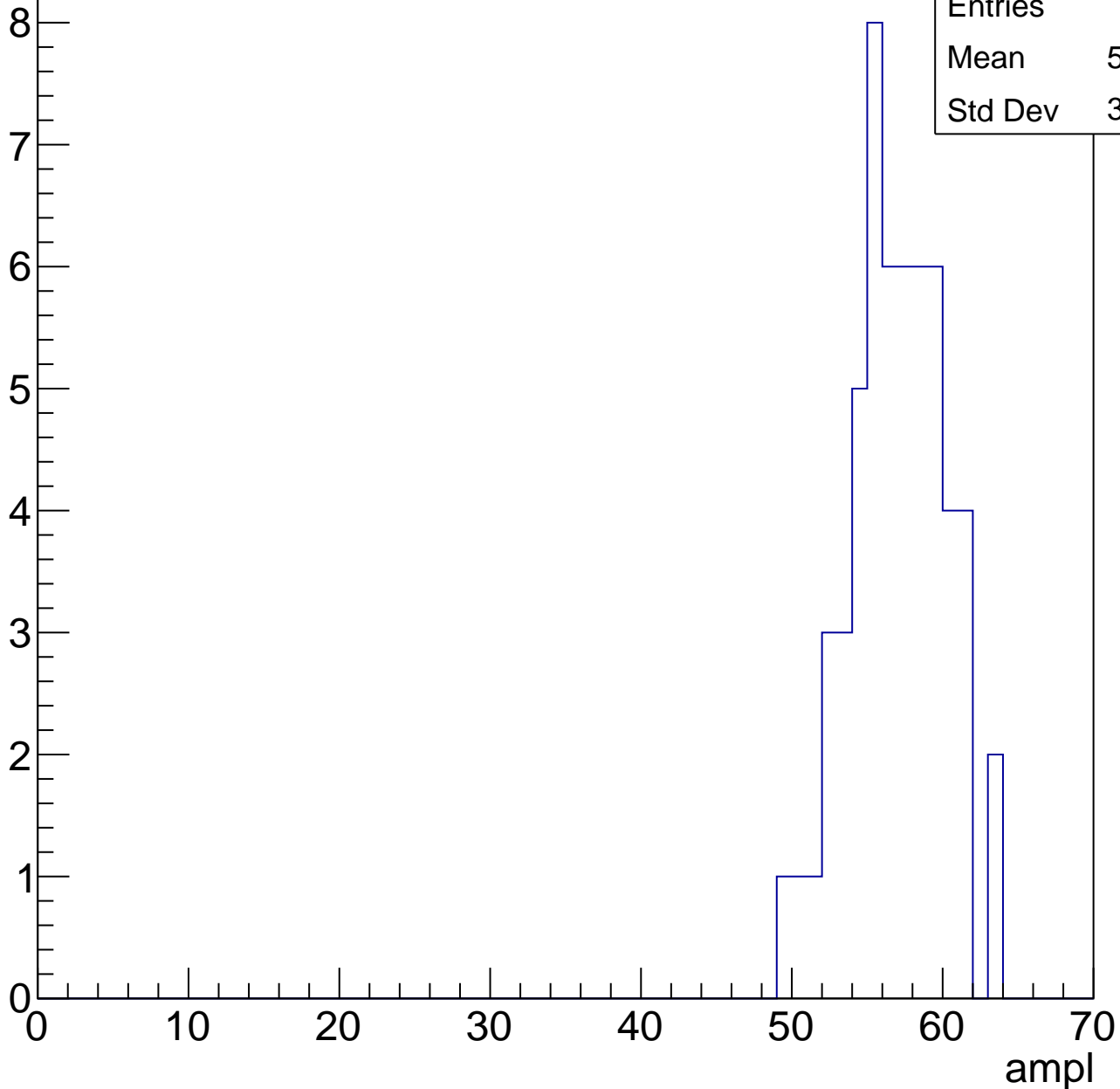


B1L100S, U10-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.52
Std Dev	3.117

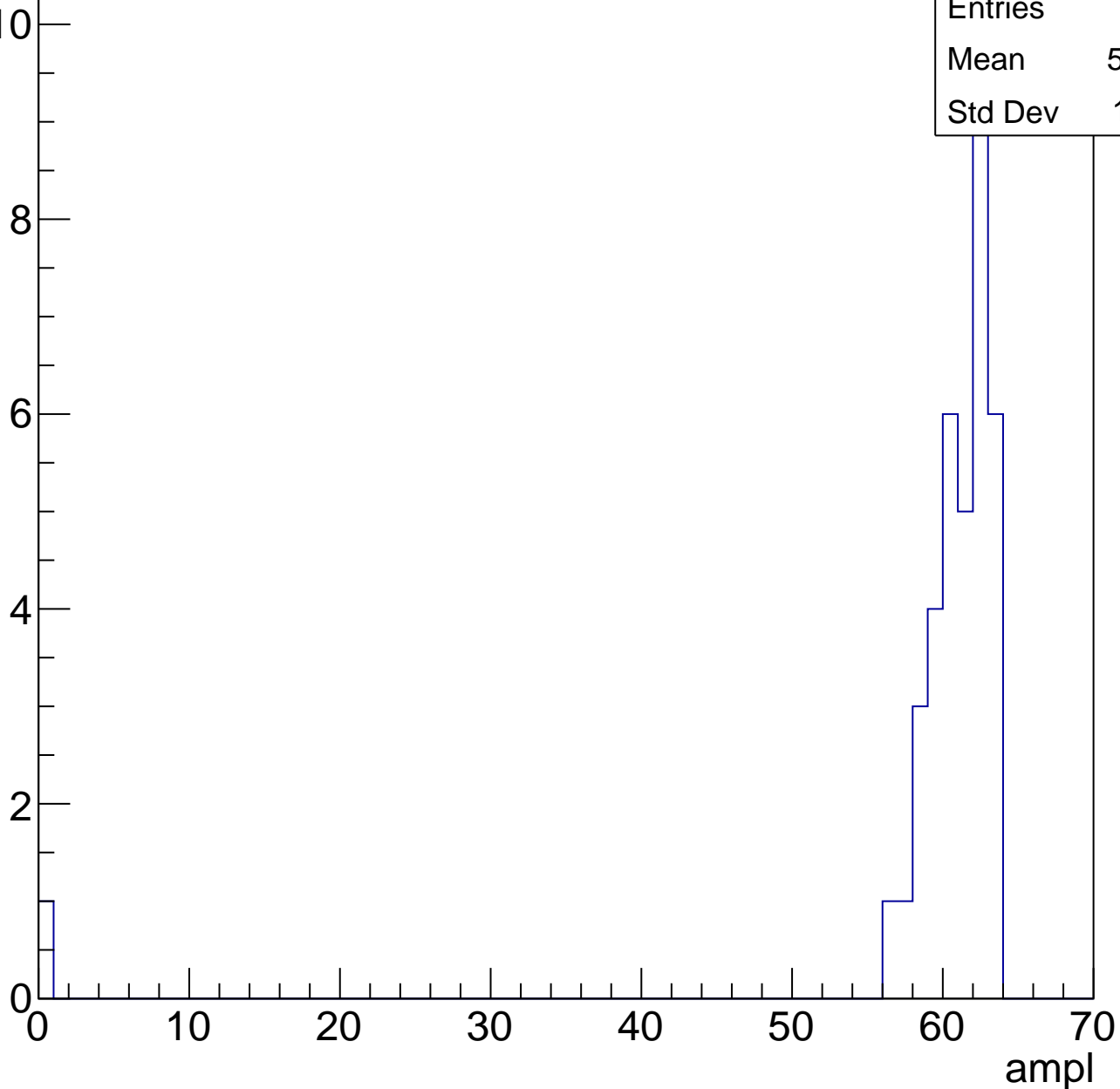


B1L100S, U10-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.08
Std Dev	10.01



B1L100S, U10-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

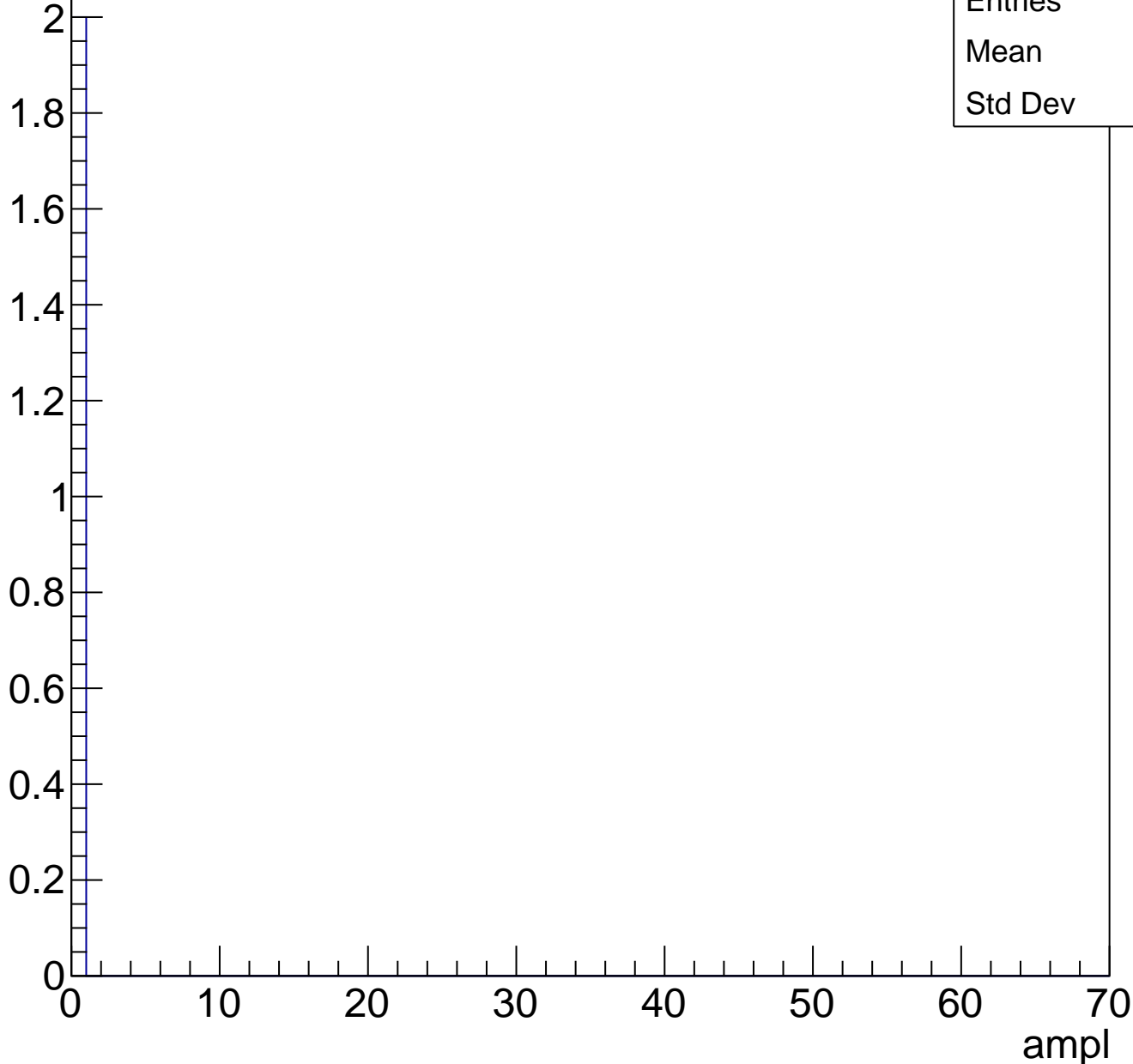
Entry



B1L100S, U10-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch12, adc0

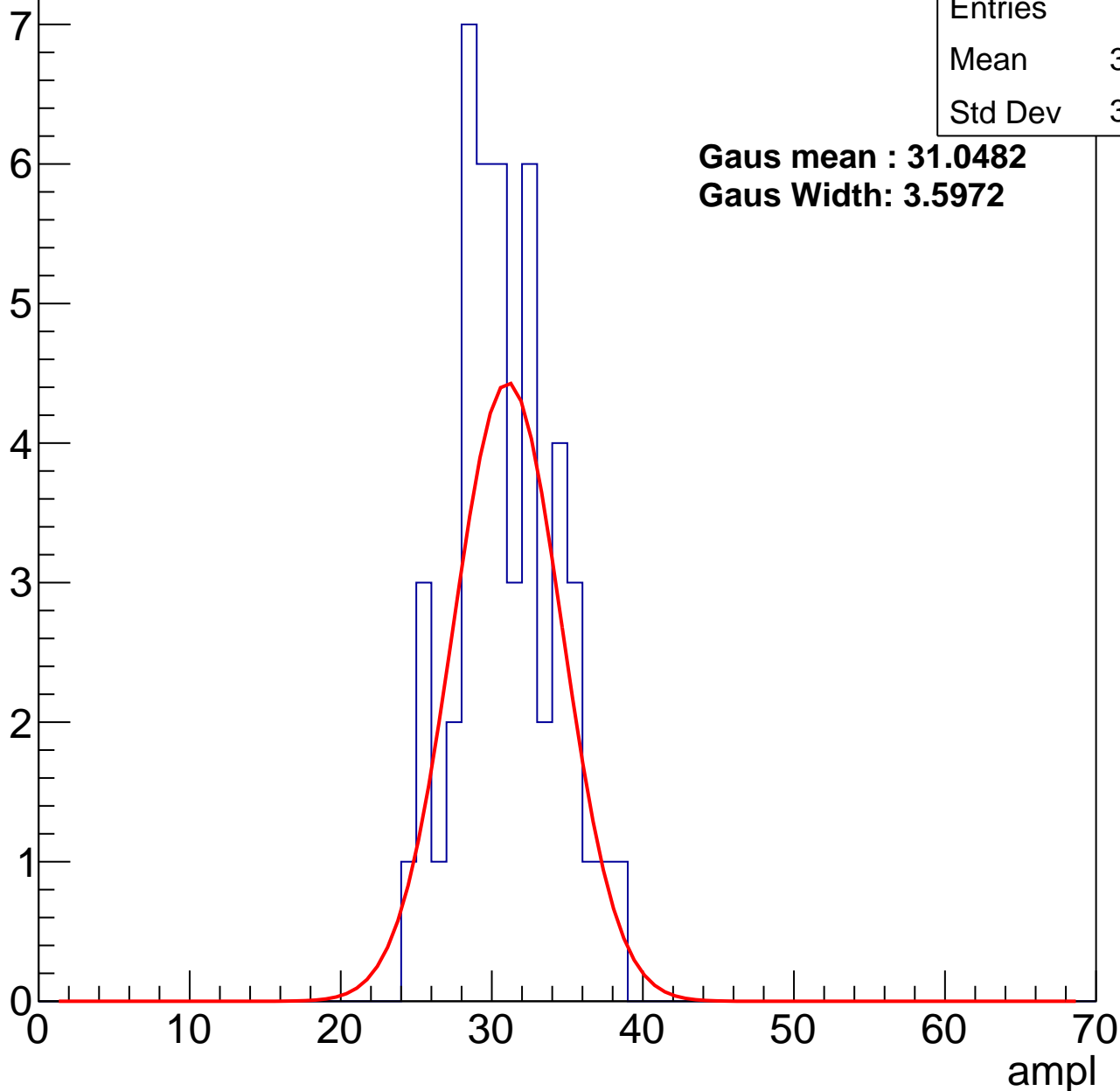
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	30.47
Std Dev	3.267

Gaus mean : 31.0482

Gaus Width: 3.5972



B1L100S, U10-ch12, adc1

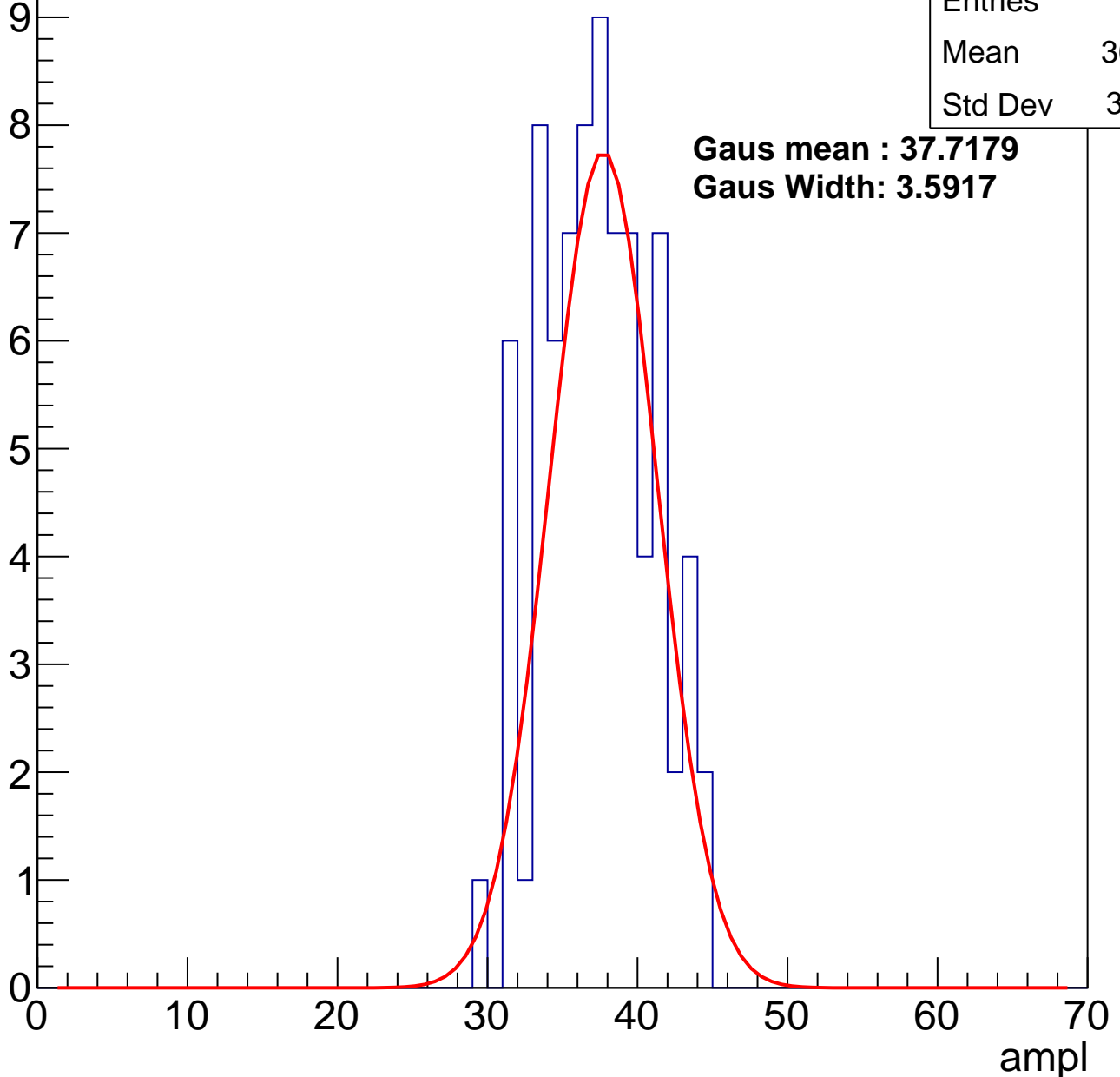
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	36.85
Std Dev	3.551

Gaus mean : 37.7179

Gaus Width: 3.5917



B1L100S, U10-ch12, adc2

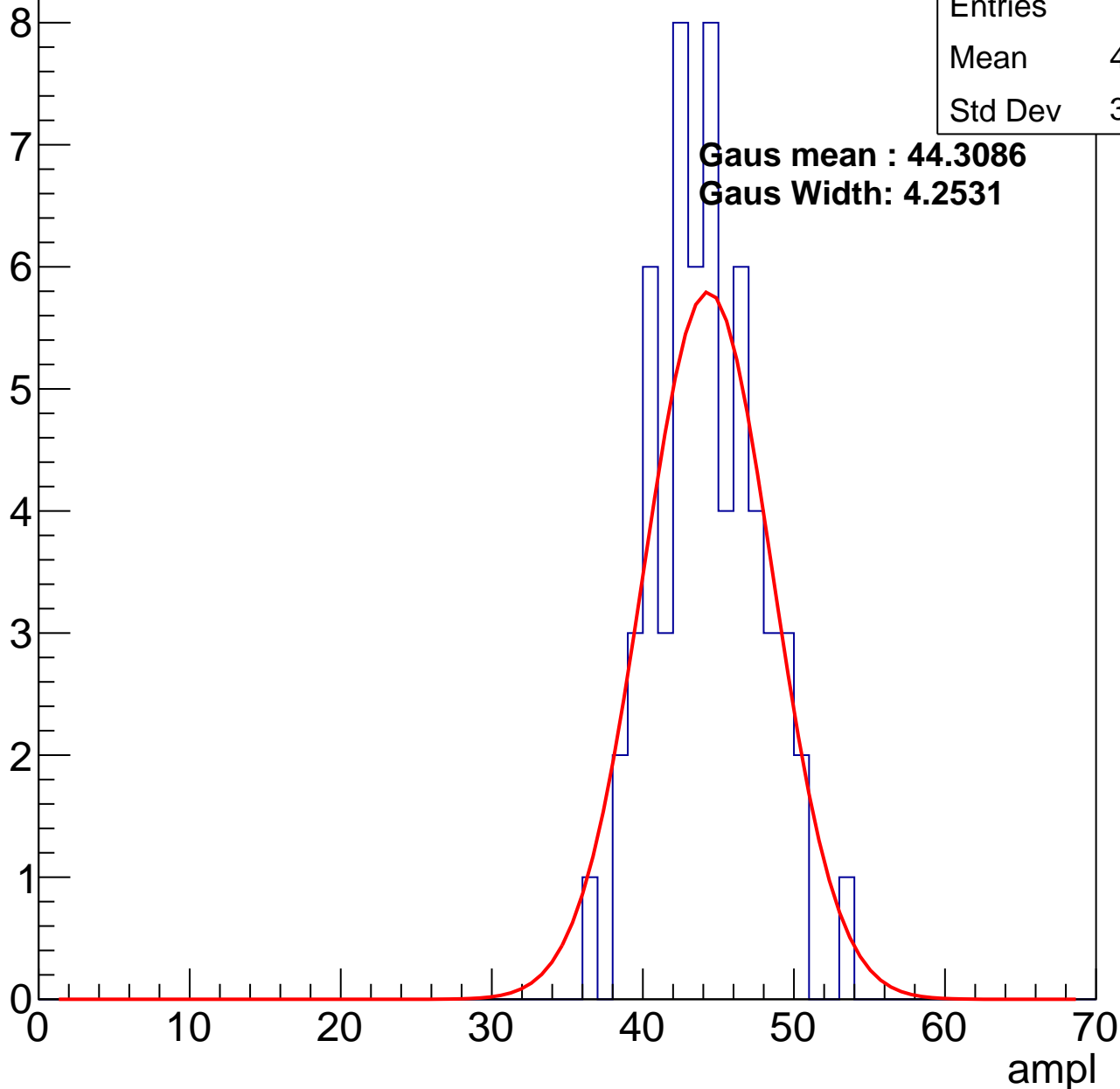
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.77
Std Dev	3.437

Gaus mean : 44.3086

Gaus Width: 4.2531

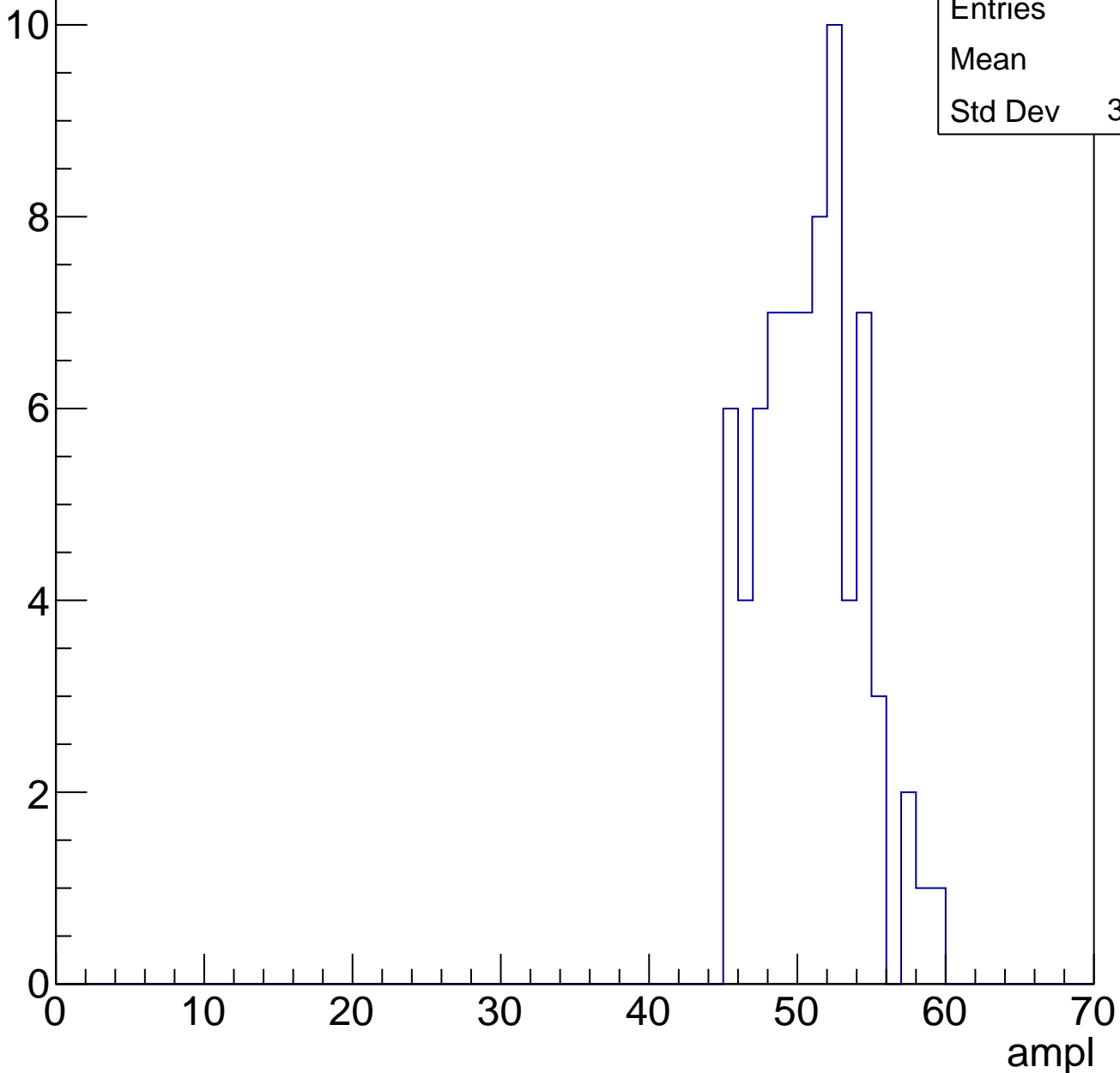


B1L100S, U10-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.4
Std Dev	3.322

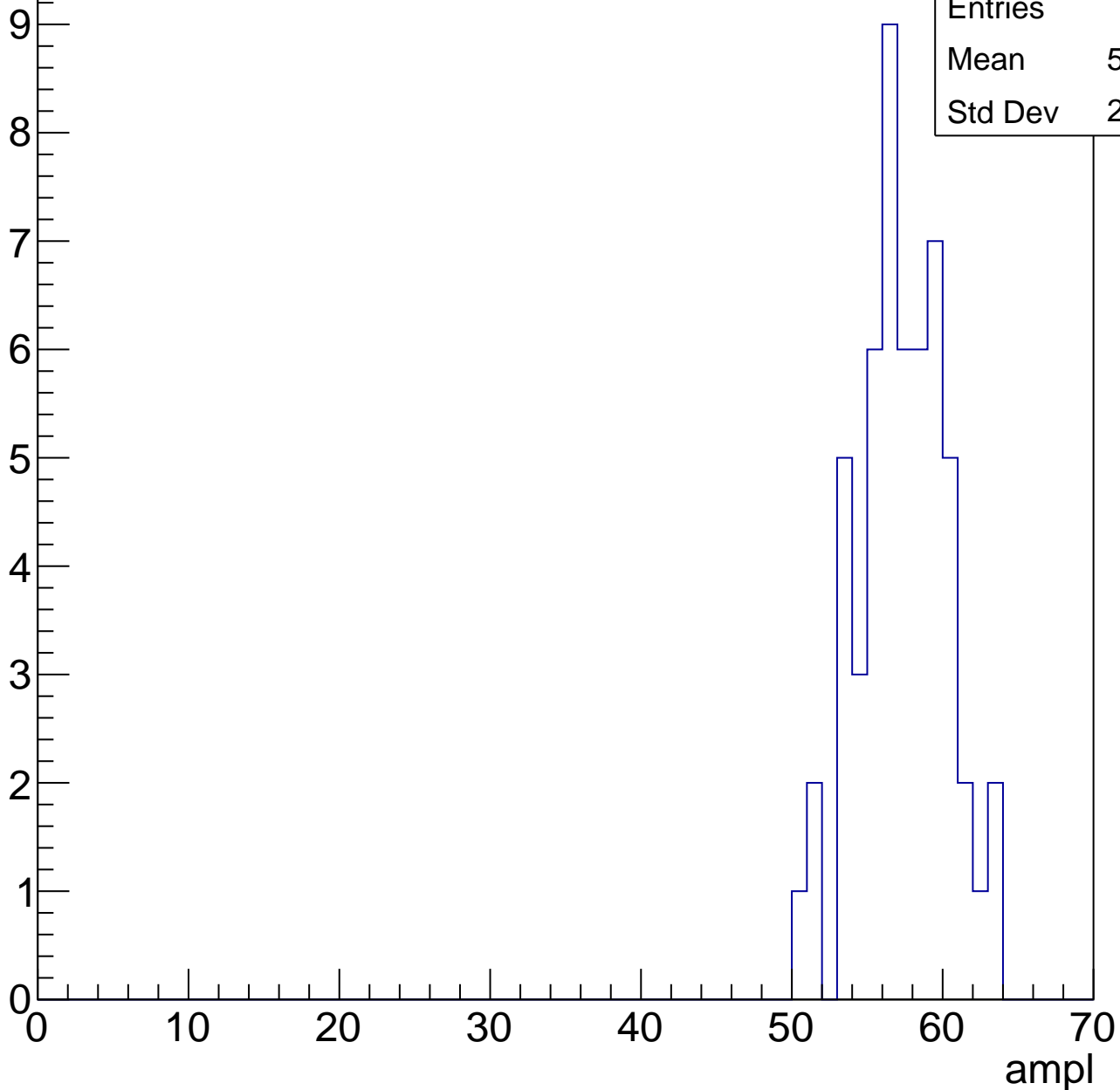


B1L100S, U10-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

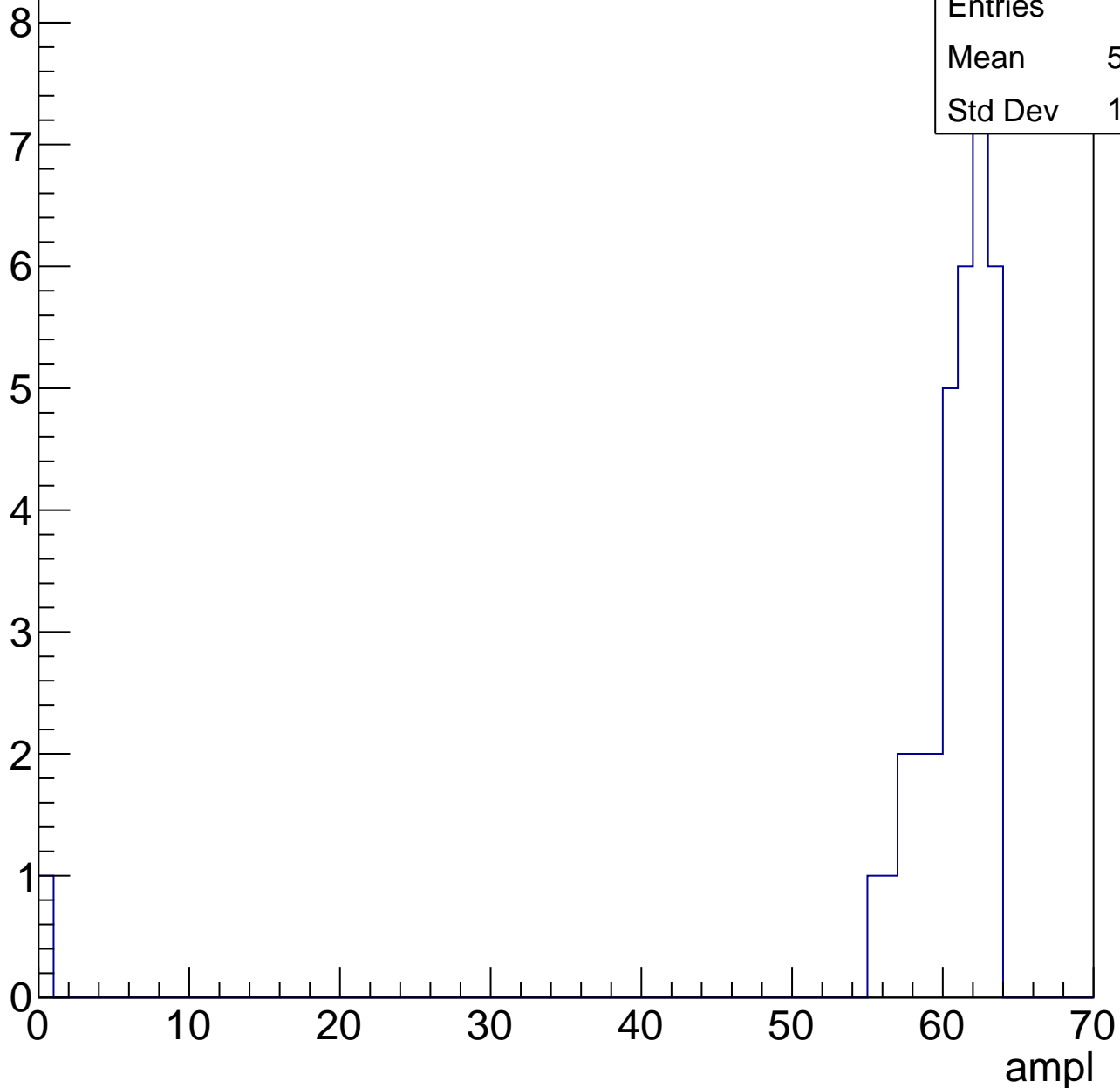
Entries	55
Mean	56.84
Std Dev	2.916



B1L100S, U10-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

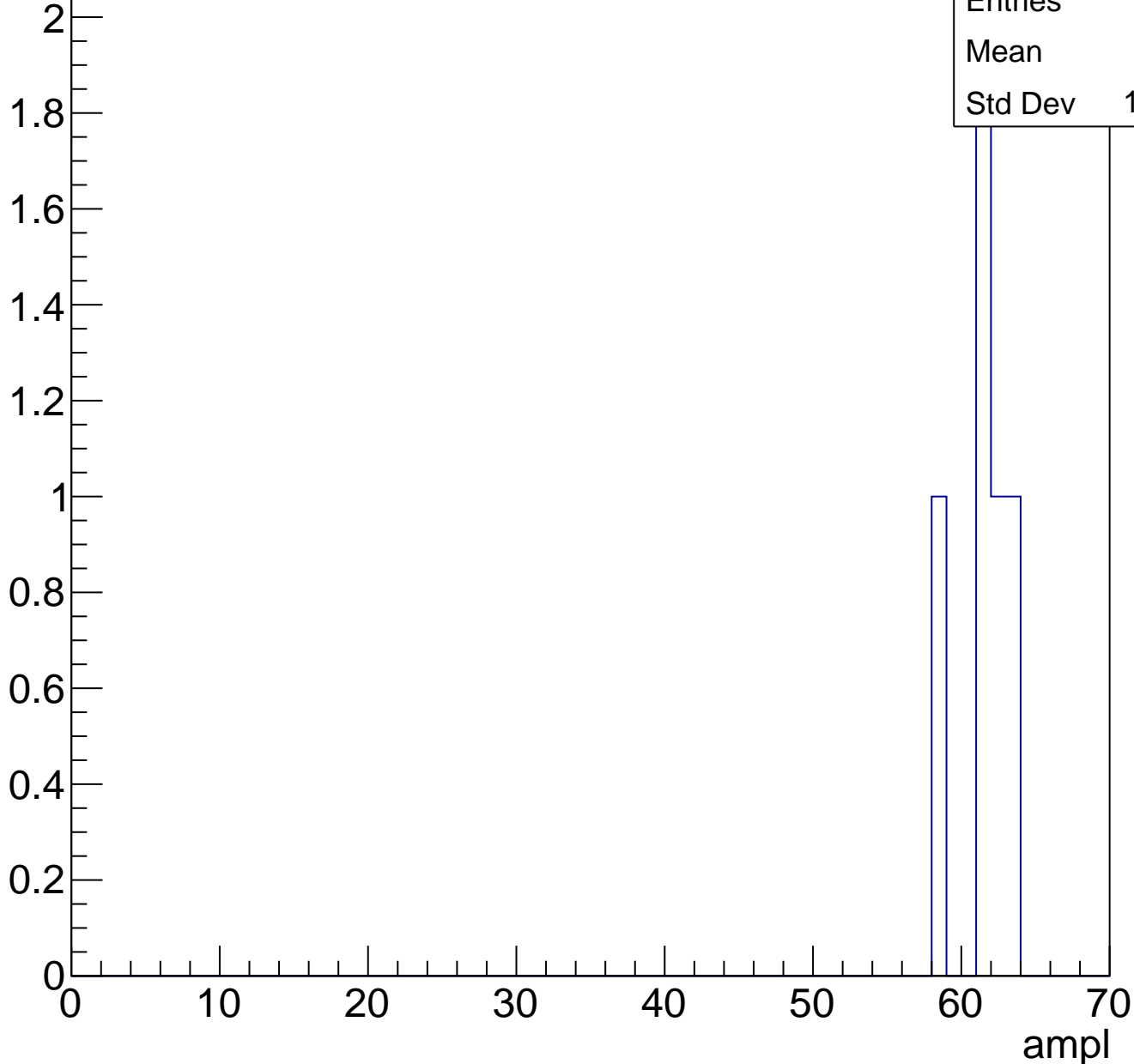
Entry



B1L100S, U10-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U10-ch13, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	29.21
Std Dev	5.659

Gaus mean : 30.3630

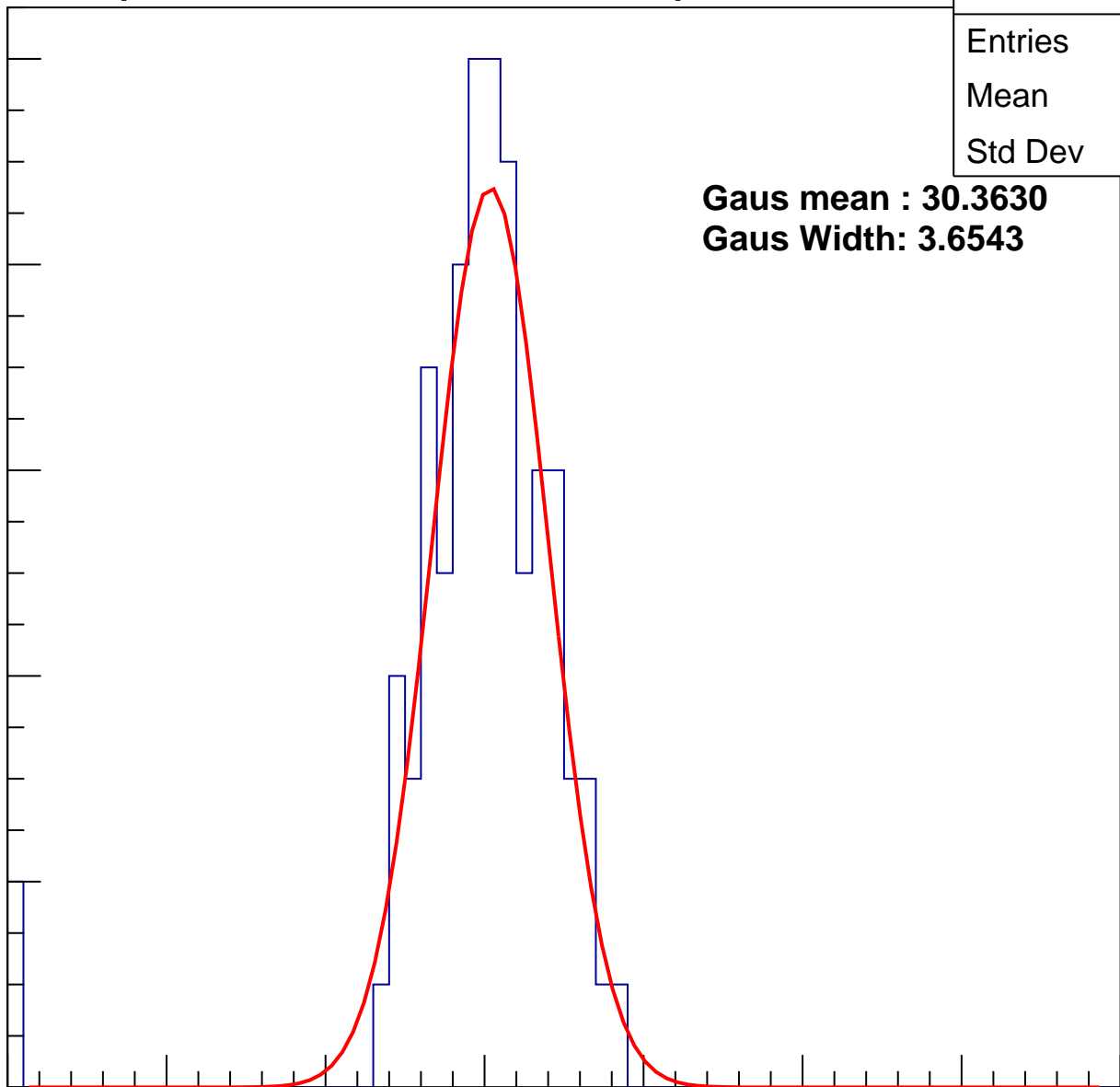
Gaus Width: 3.6543

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch13, adc1

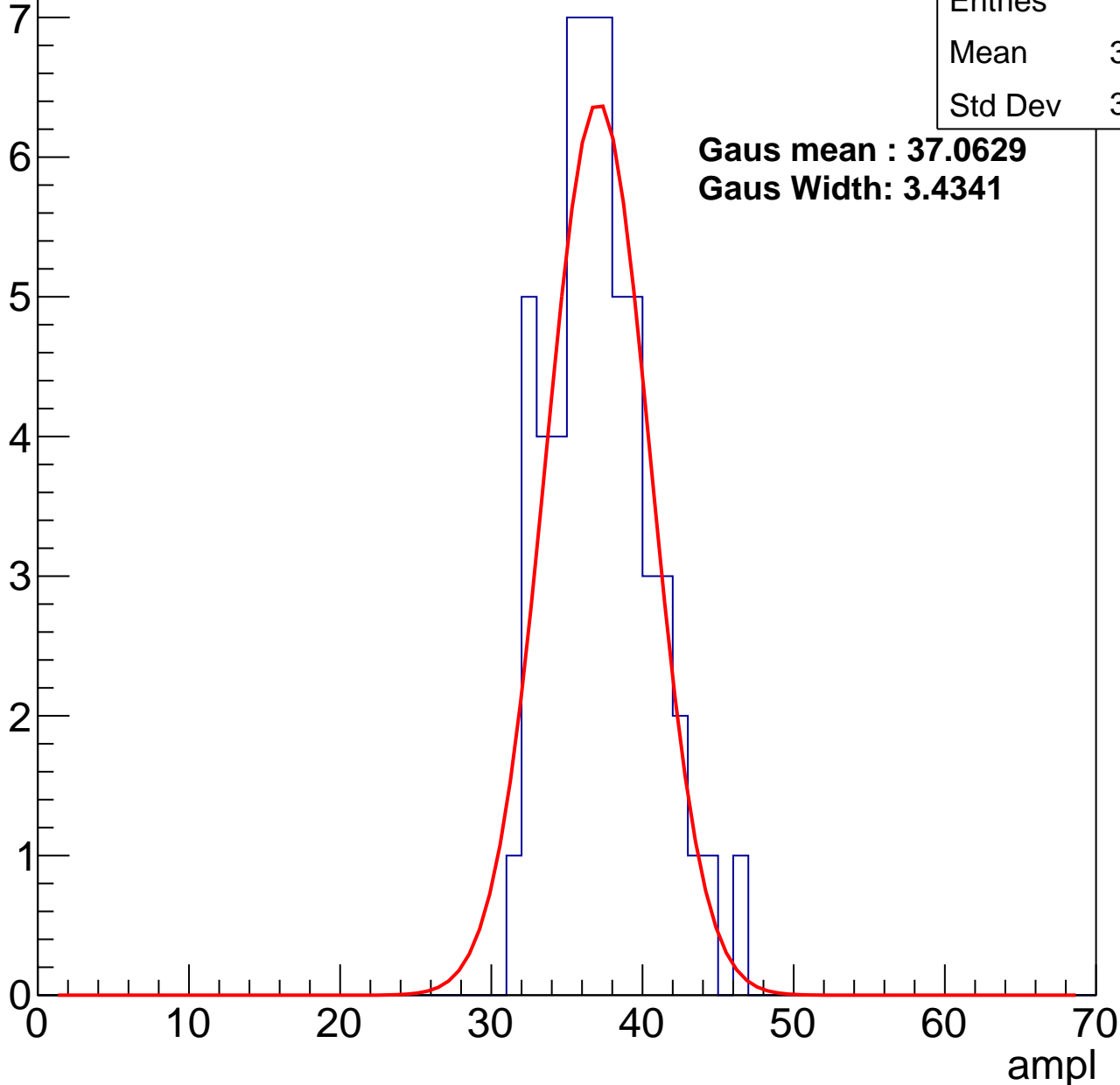
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	36.79
Std Dev	3.299

Gaus mean : 37.0629

Gaus Width: 3.4341



B1L100S, U10-ch13, adc2

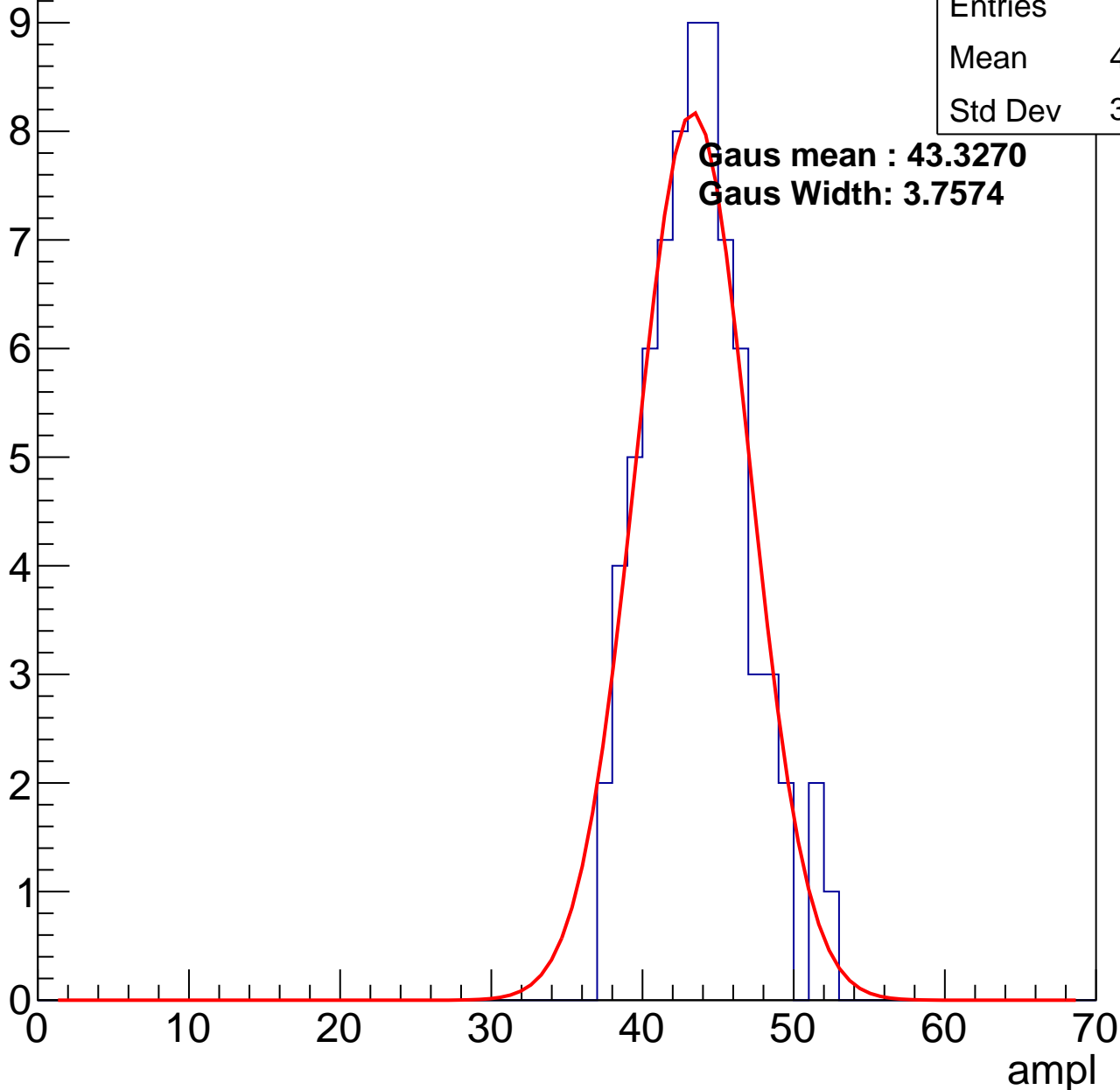
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.18
Std Dev	3.359

Gaus mean : 43.3270

Gaus Width: 3.7574

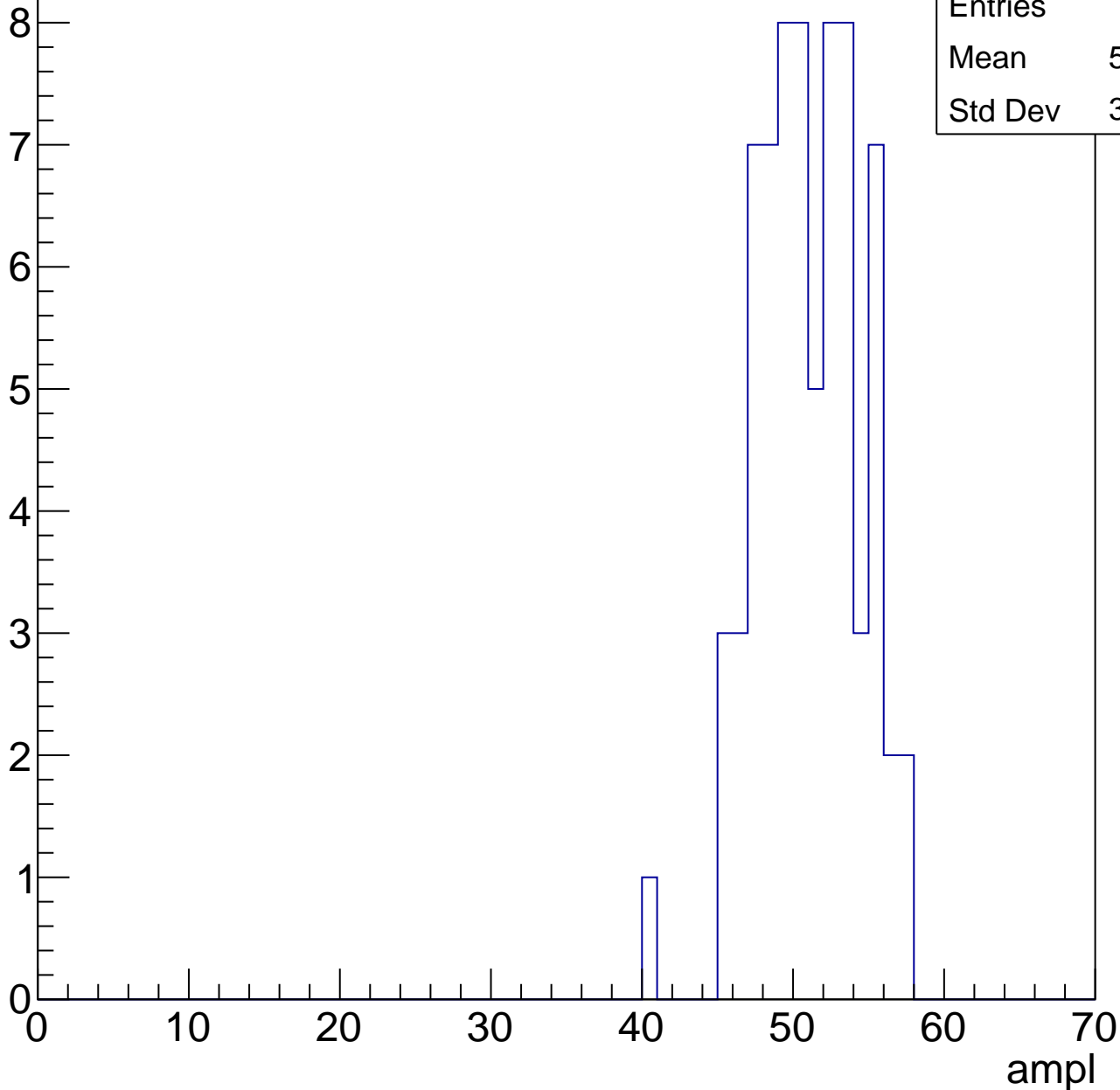


B1L100S, U10-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

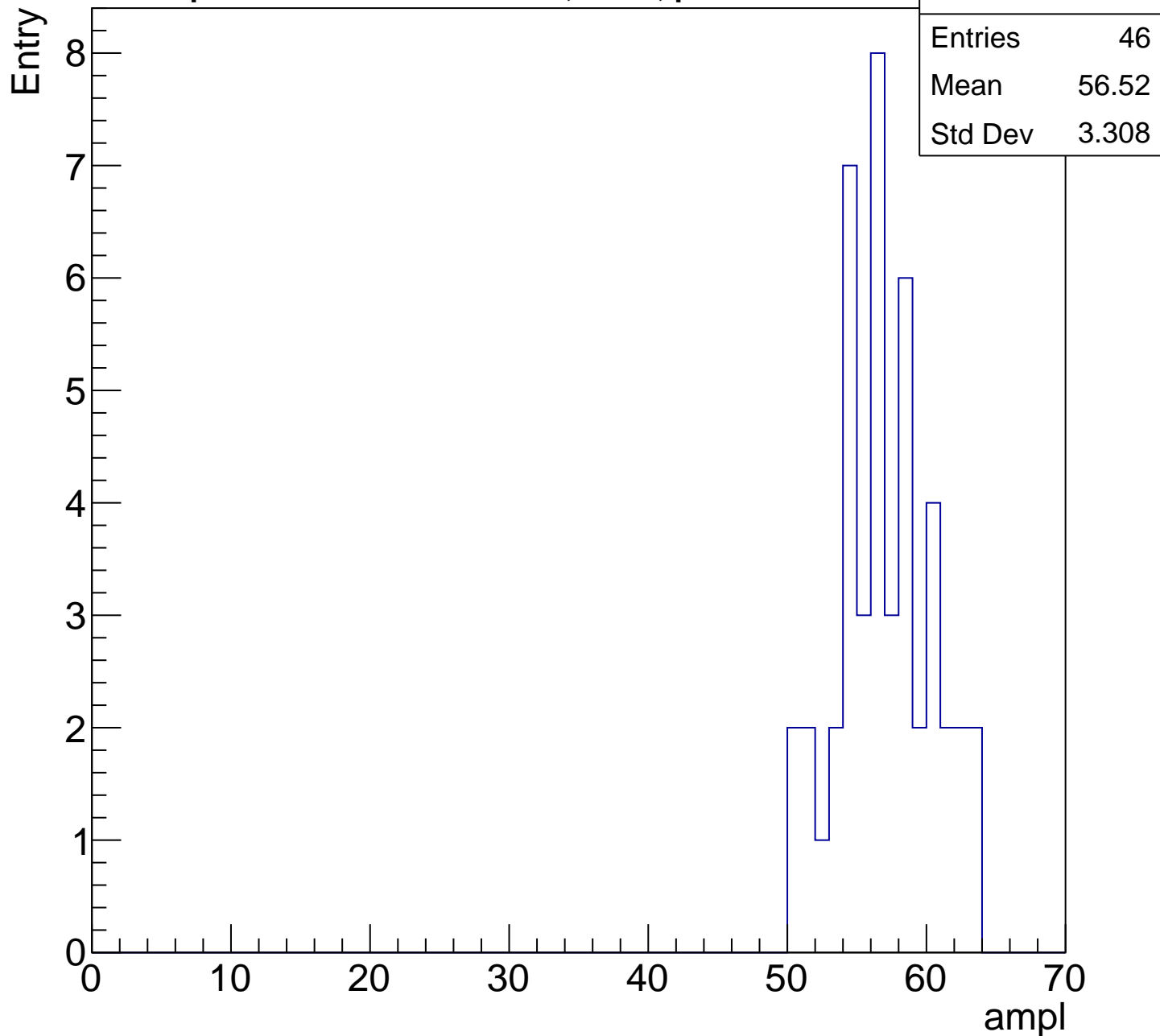
Entry

Entries	72
Mean	50.53
Std Dev	3.342



B1L100S, U10-ch13, adc4

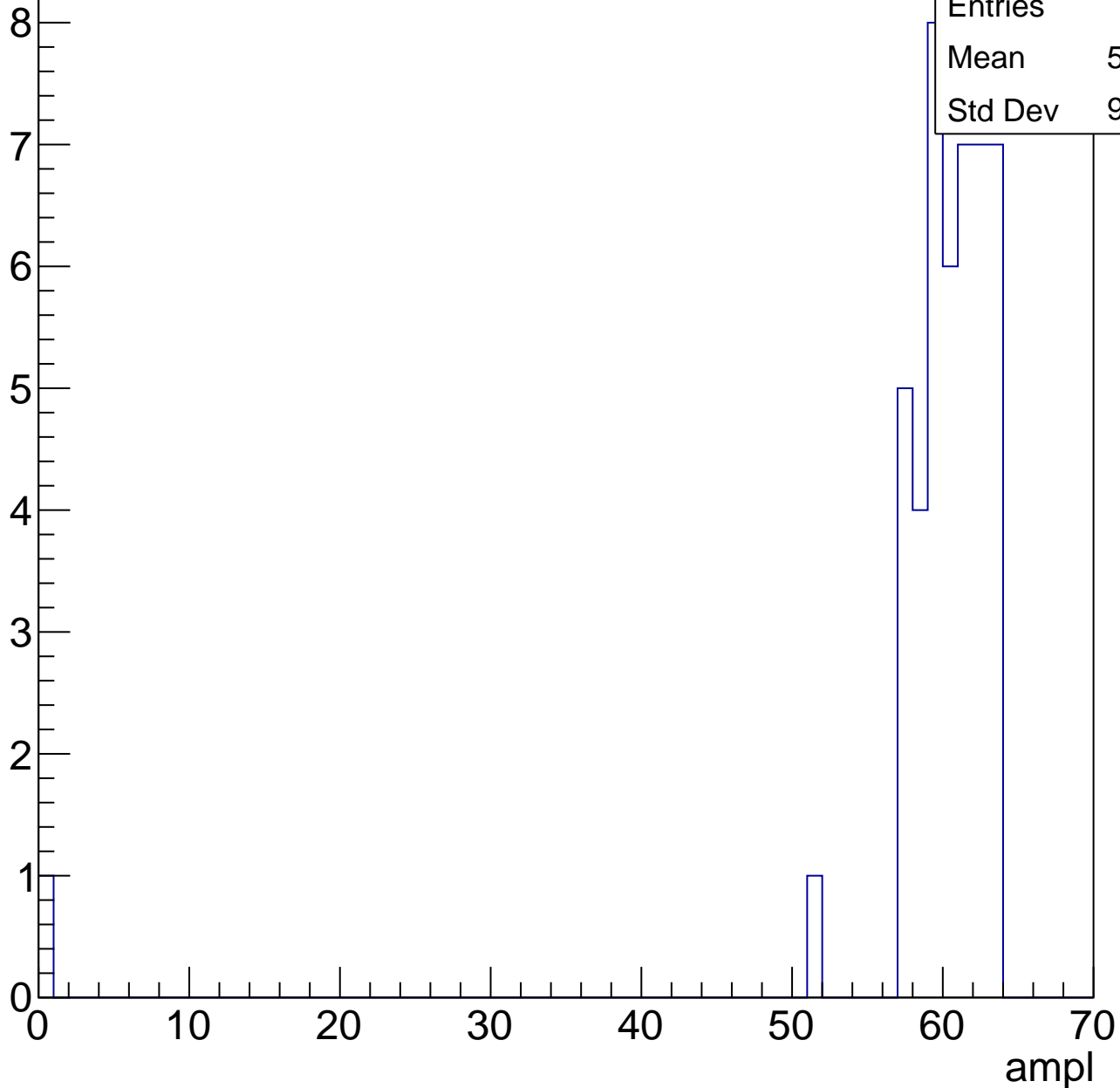
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch14, adc0

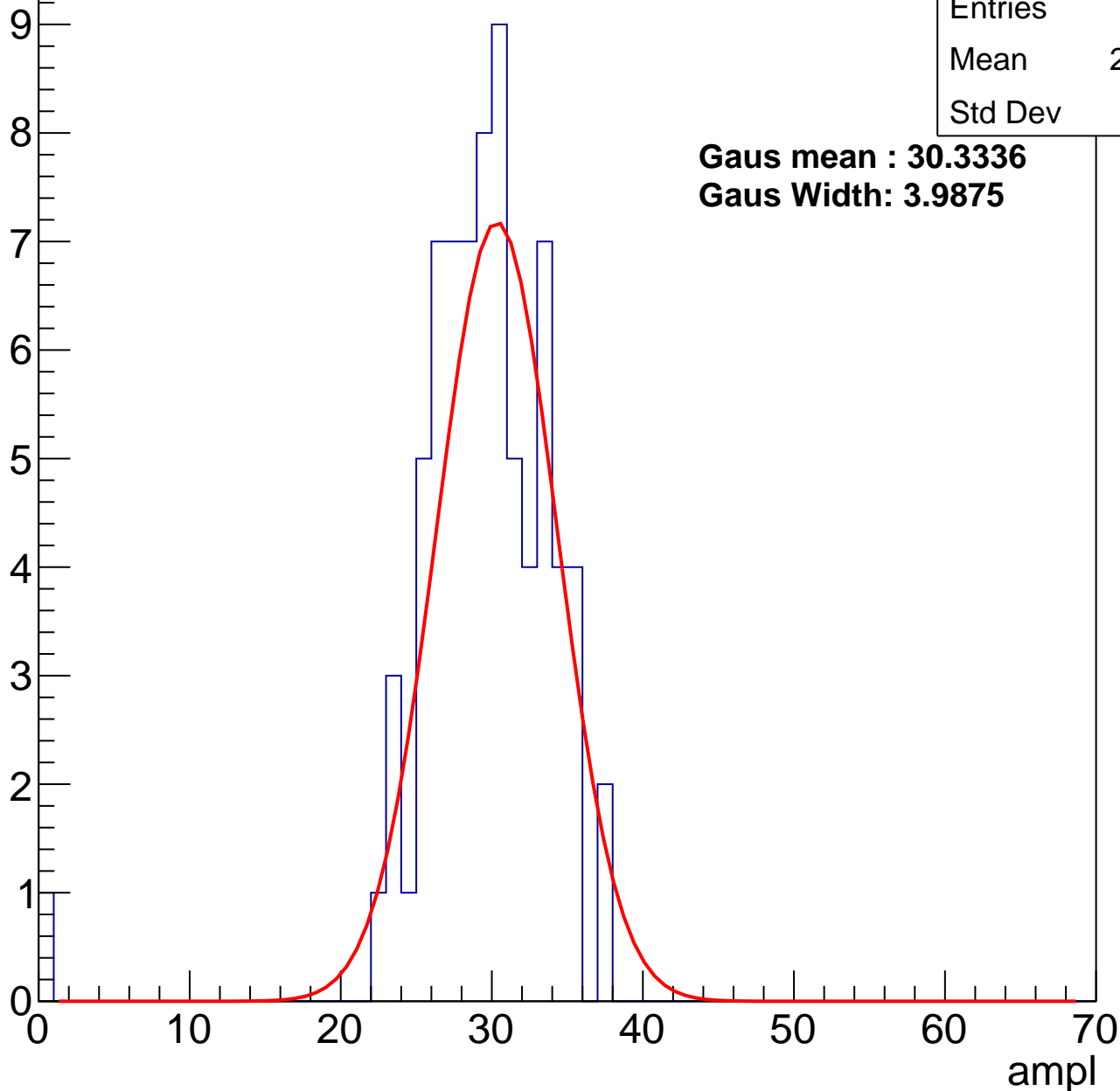
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	28.97
Std Dev	4.83

Gaus mean : 30.3336

Gaus Width: 3.9875



B1L100S, U10-ch14, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	36.31
Std Dev	3.211

Gaus mean : 36.6395

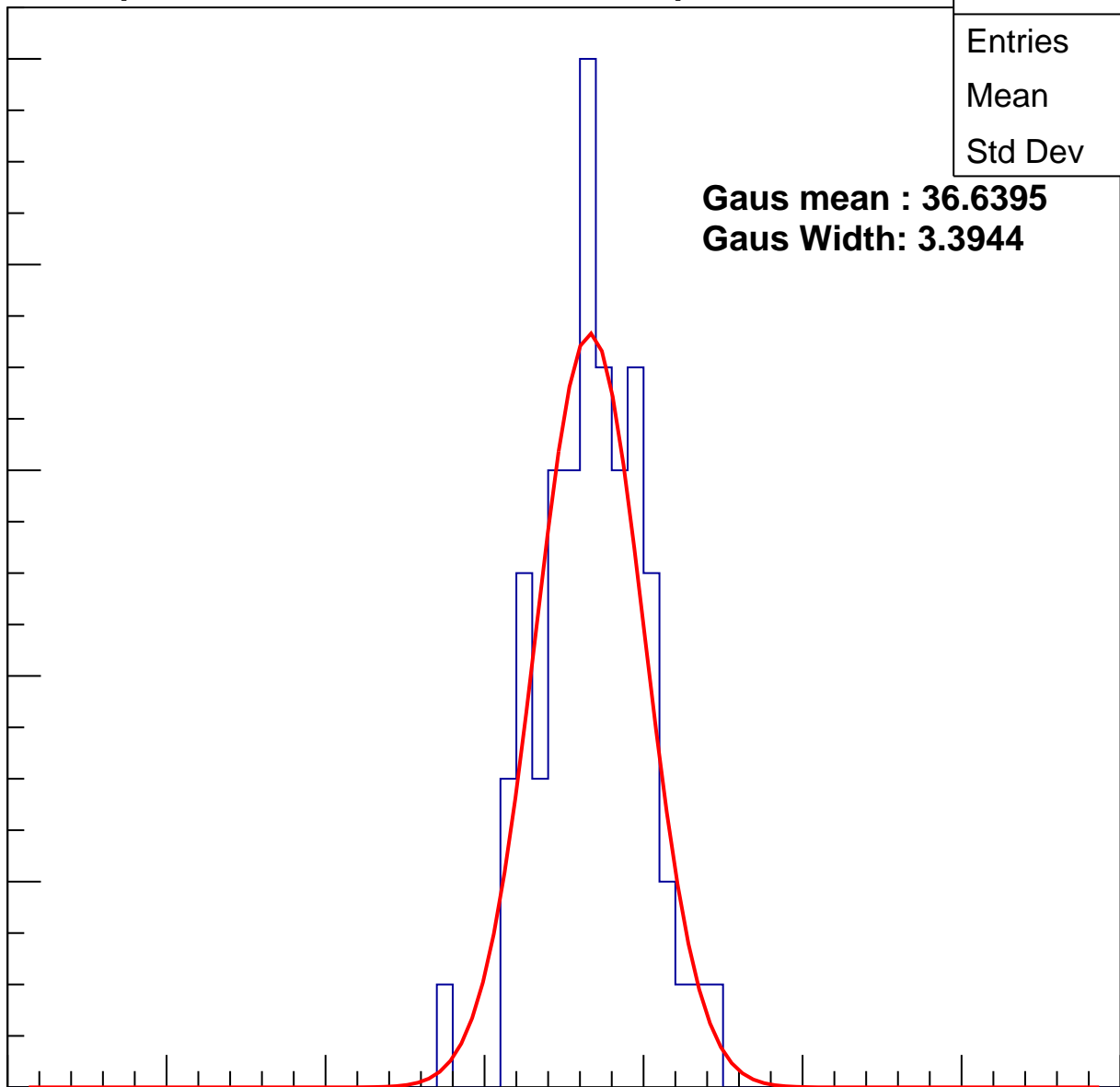
Gaus Width: 3.3944

Entry

10
8
6
4
2
0

ampl

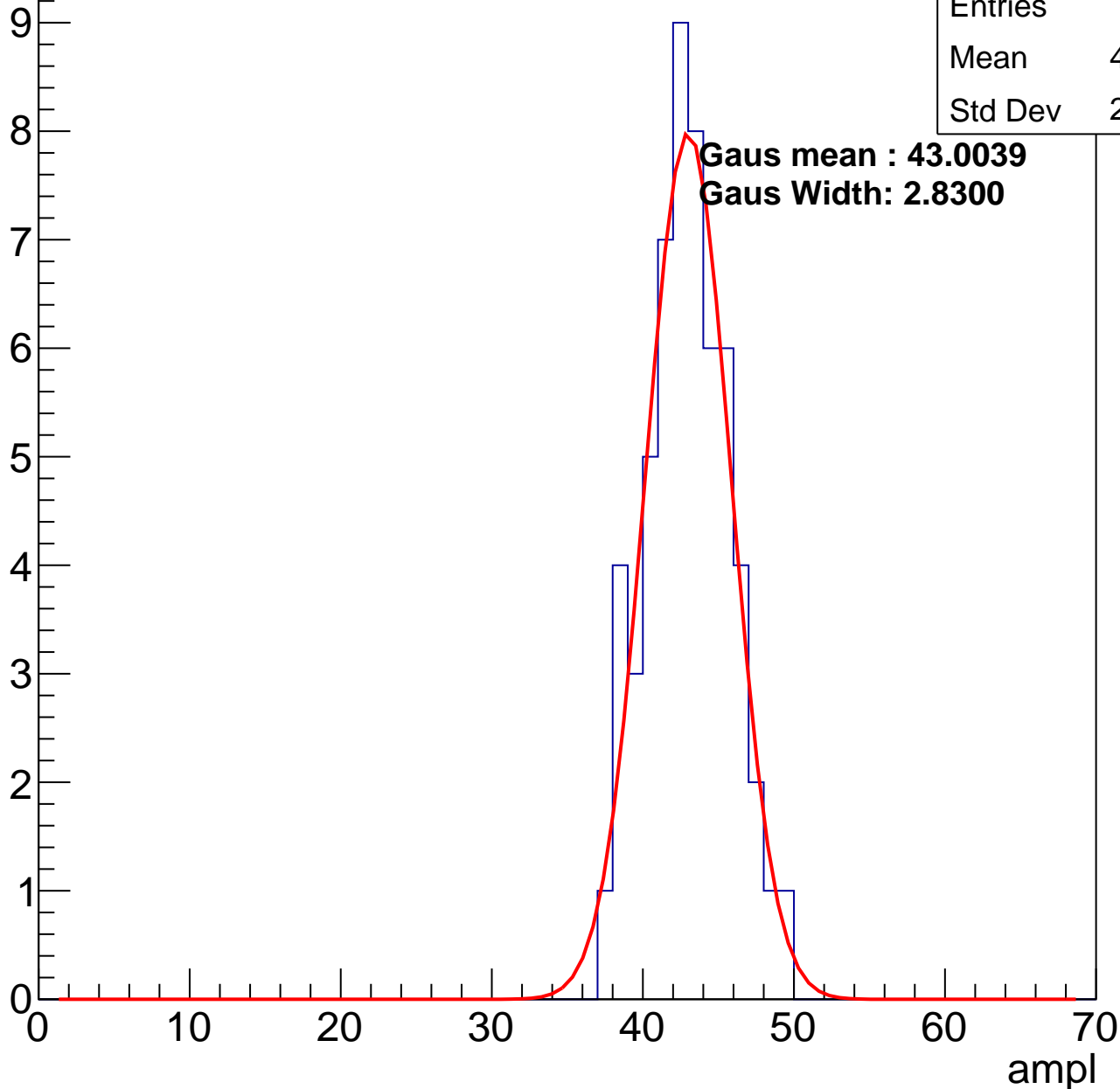
0 10 20 30 40 50 60 70



B1L100S, U10-ch14, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

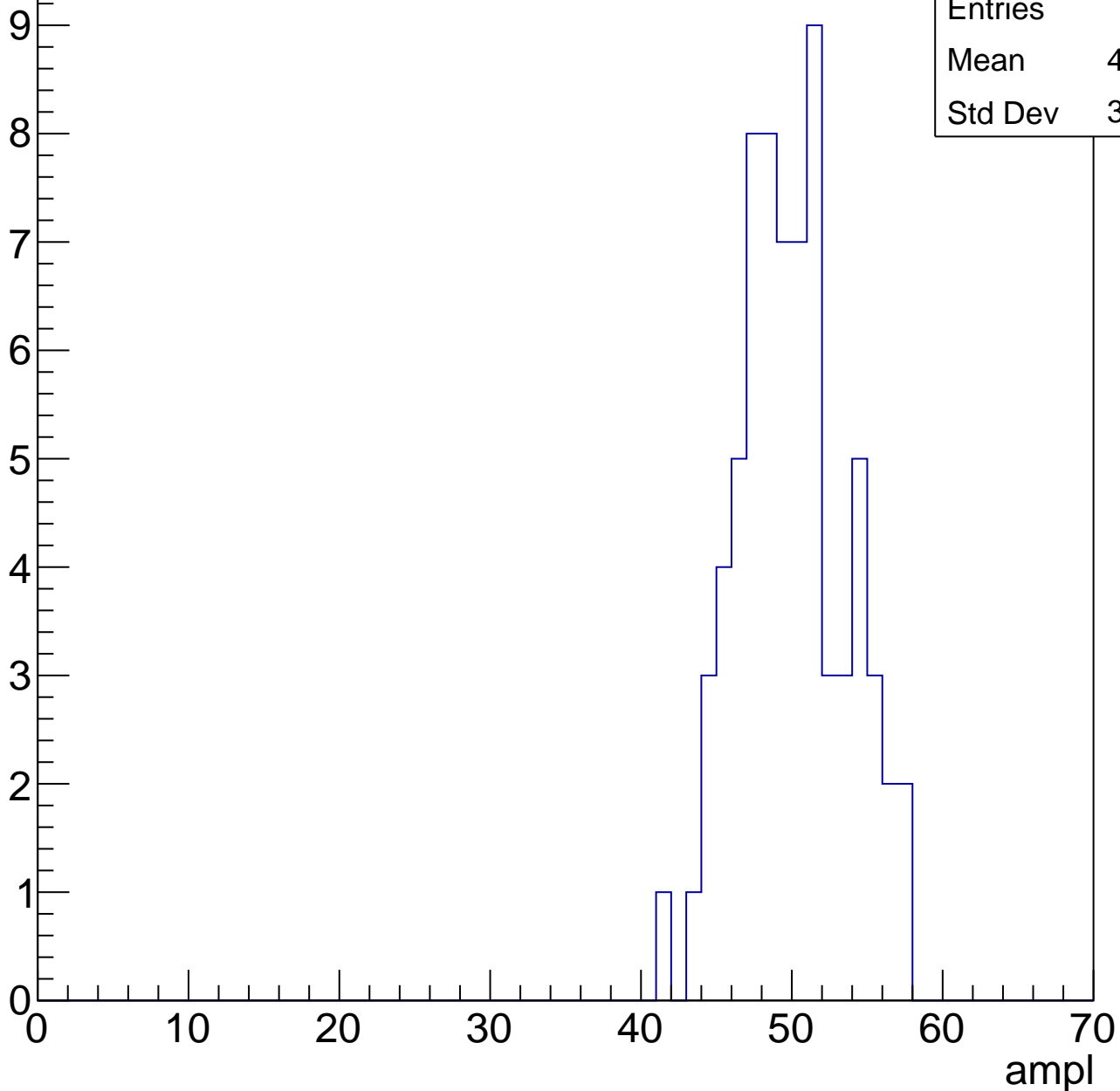


B1L100S, U10-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	49.49
Std Dev	3.528

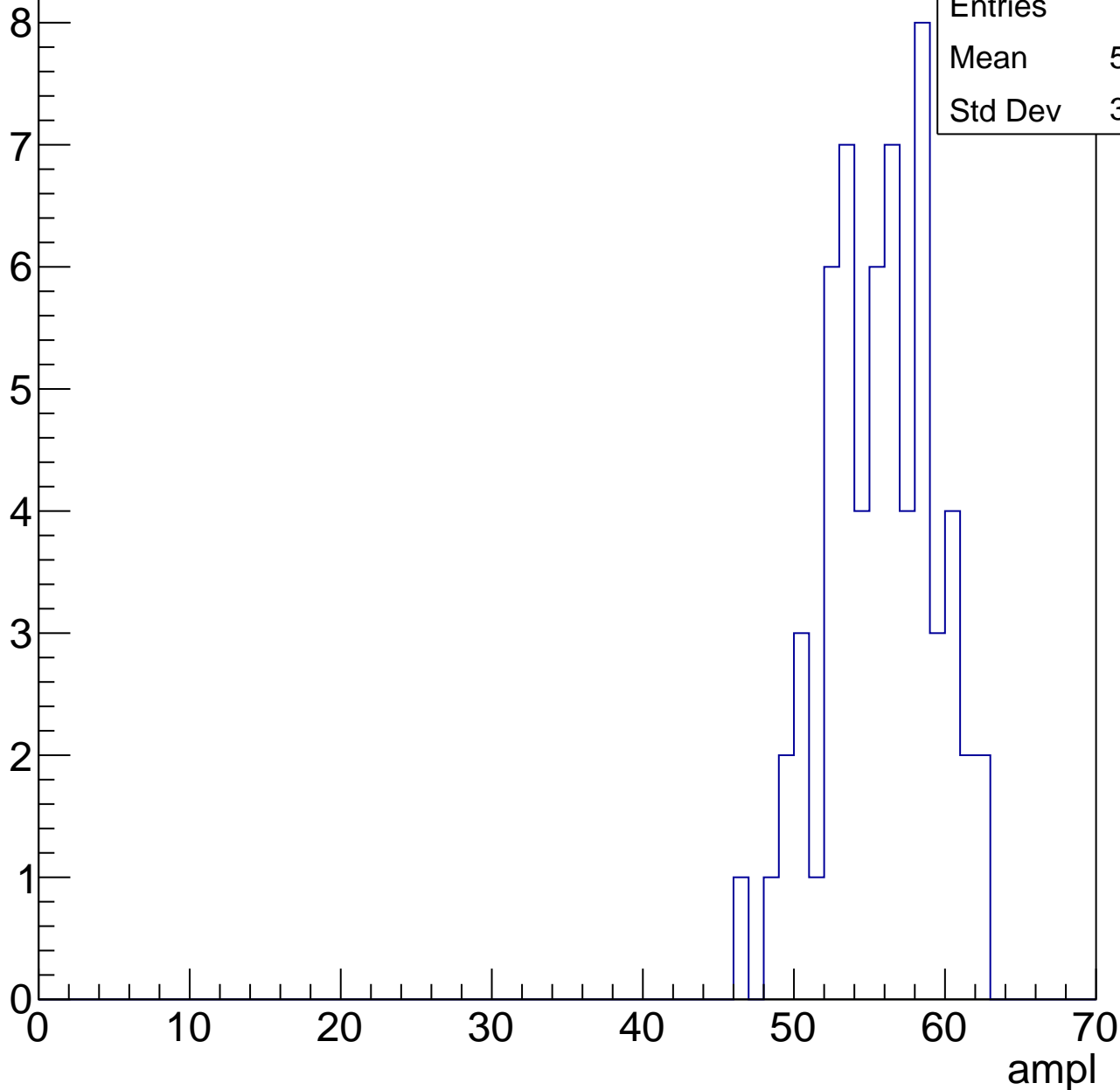


B1L100S, U10-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	55.23
Std Dev	3.596

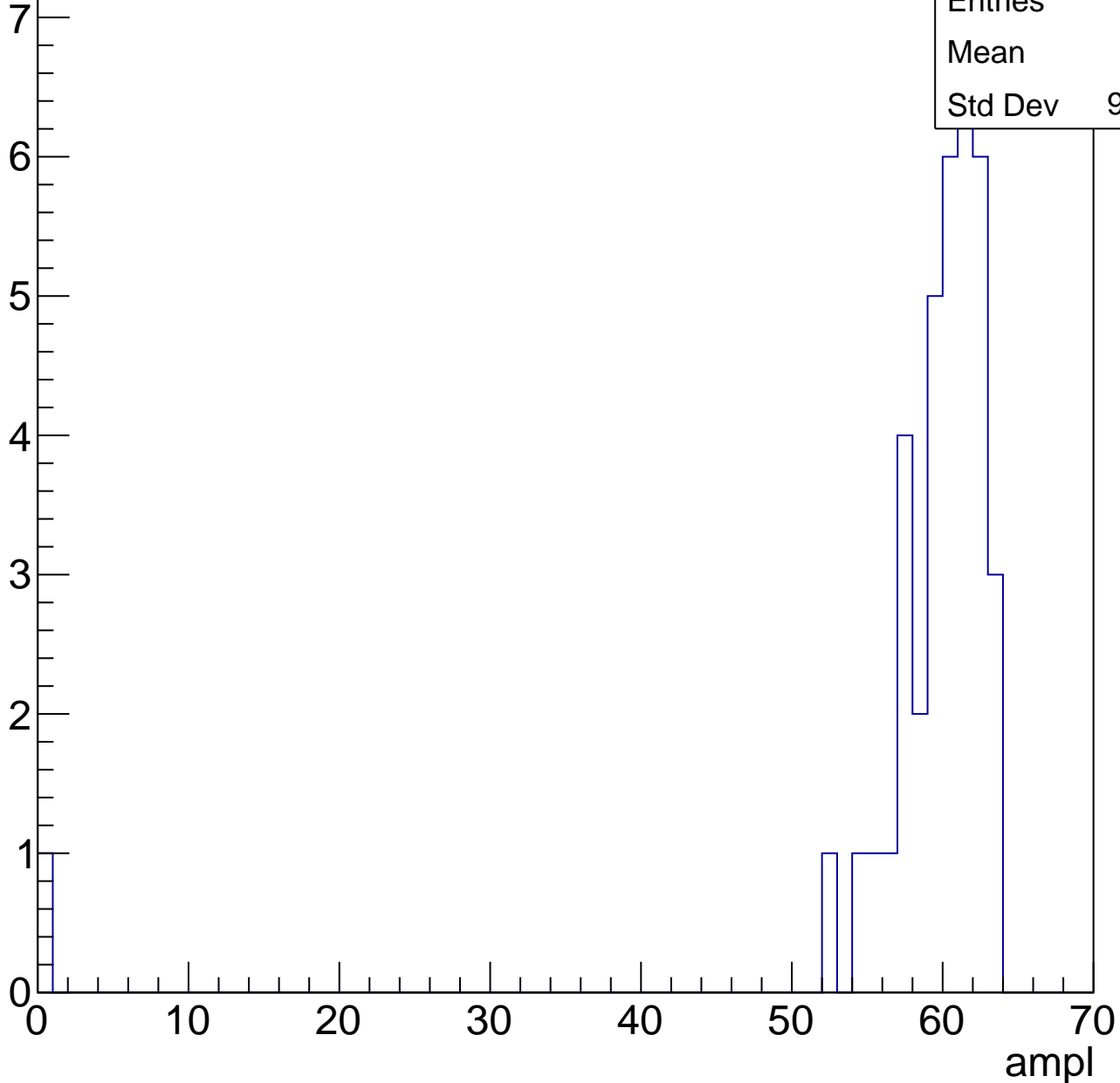


B1L100S, U10-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58
Std Dev	9.862

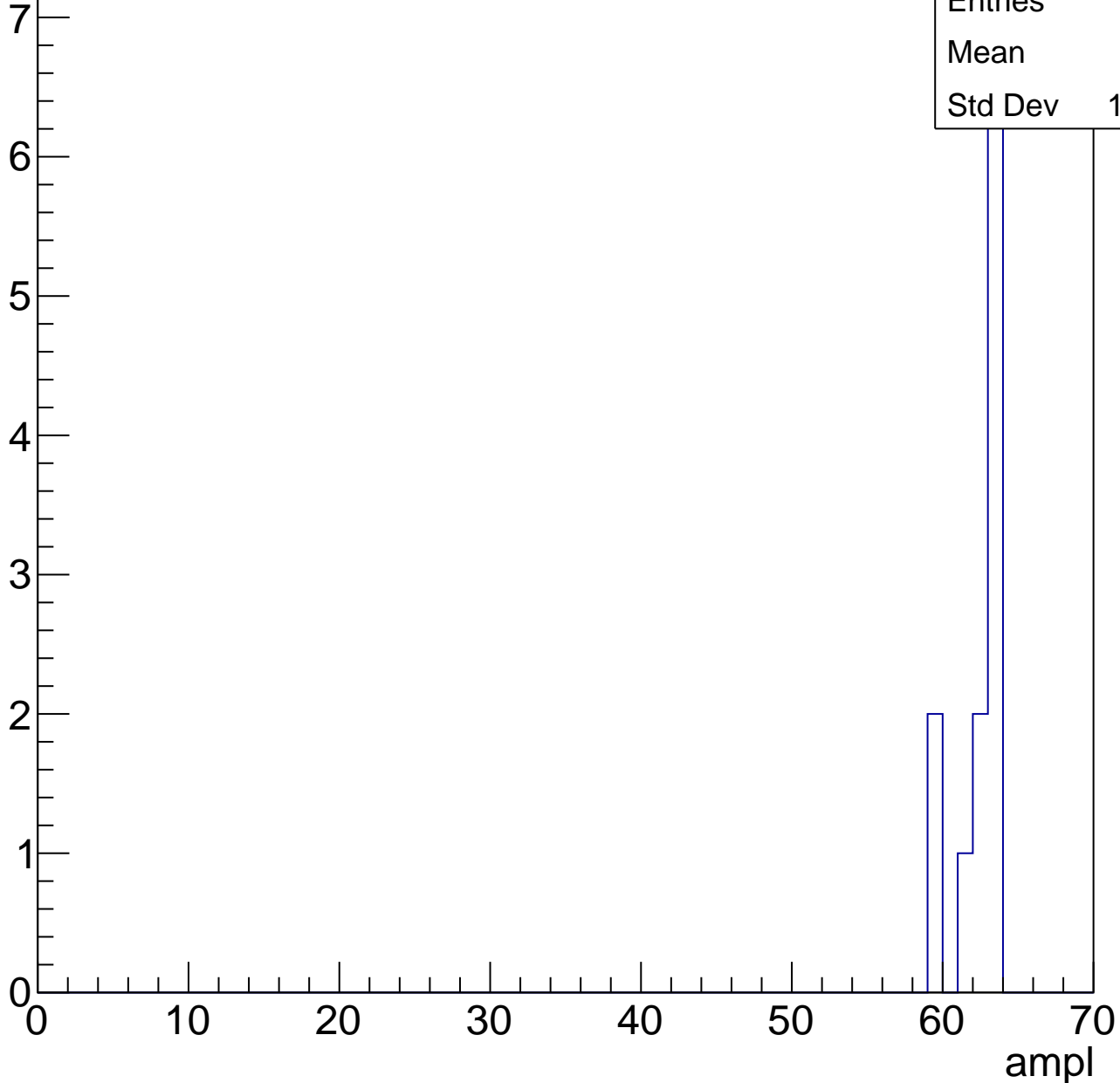


B1L100S, U10-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	62
Std Dev	1.472



B1L100S, U10-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch15, adc0

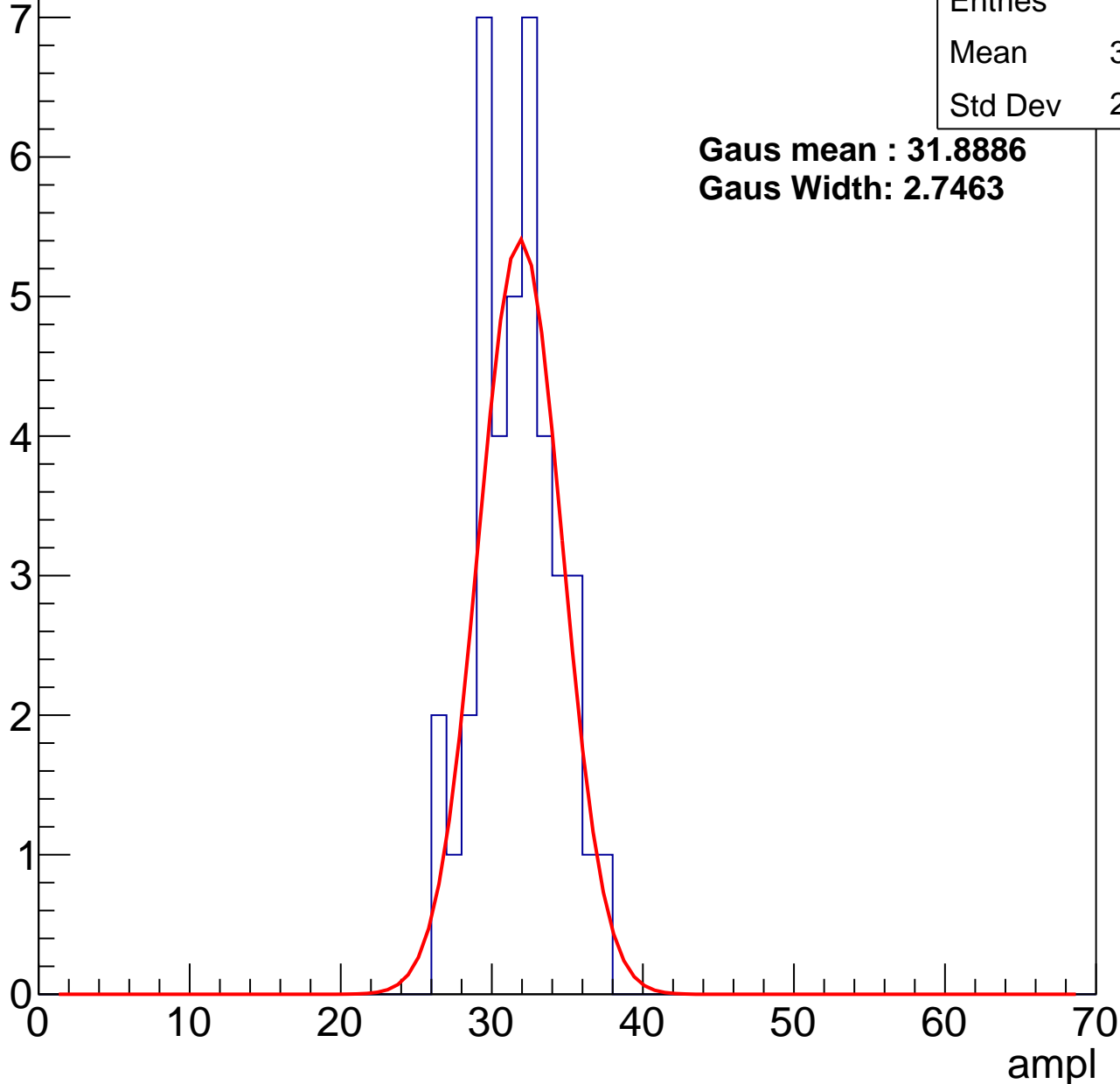
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	31.23
Std Dev	2.612

Gaus mean : 31.8886

Gaus Width: 2.7463



B1L100S, U10-ch15, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.7
Std Dev	3.516

Gaus mean : 37.0744

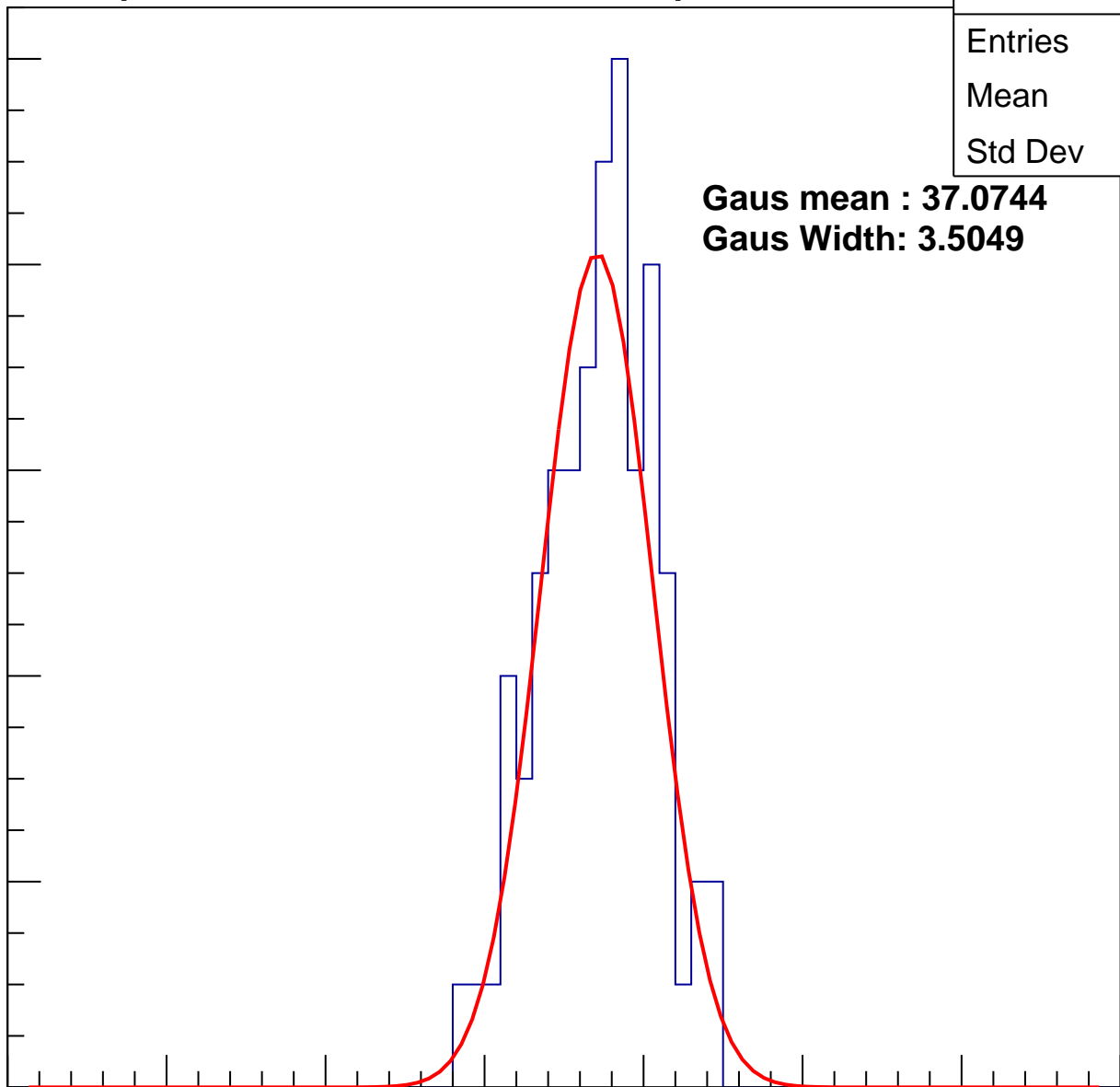
Gaus Width: 3.5049

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch15, adc2

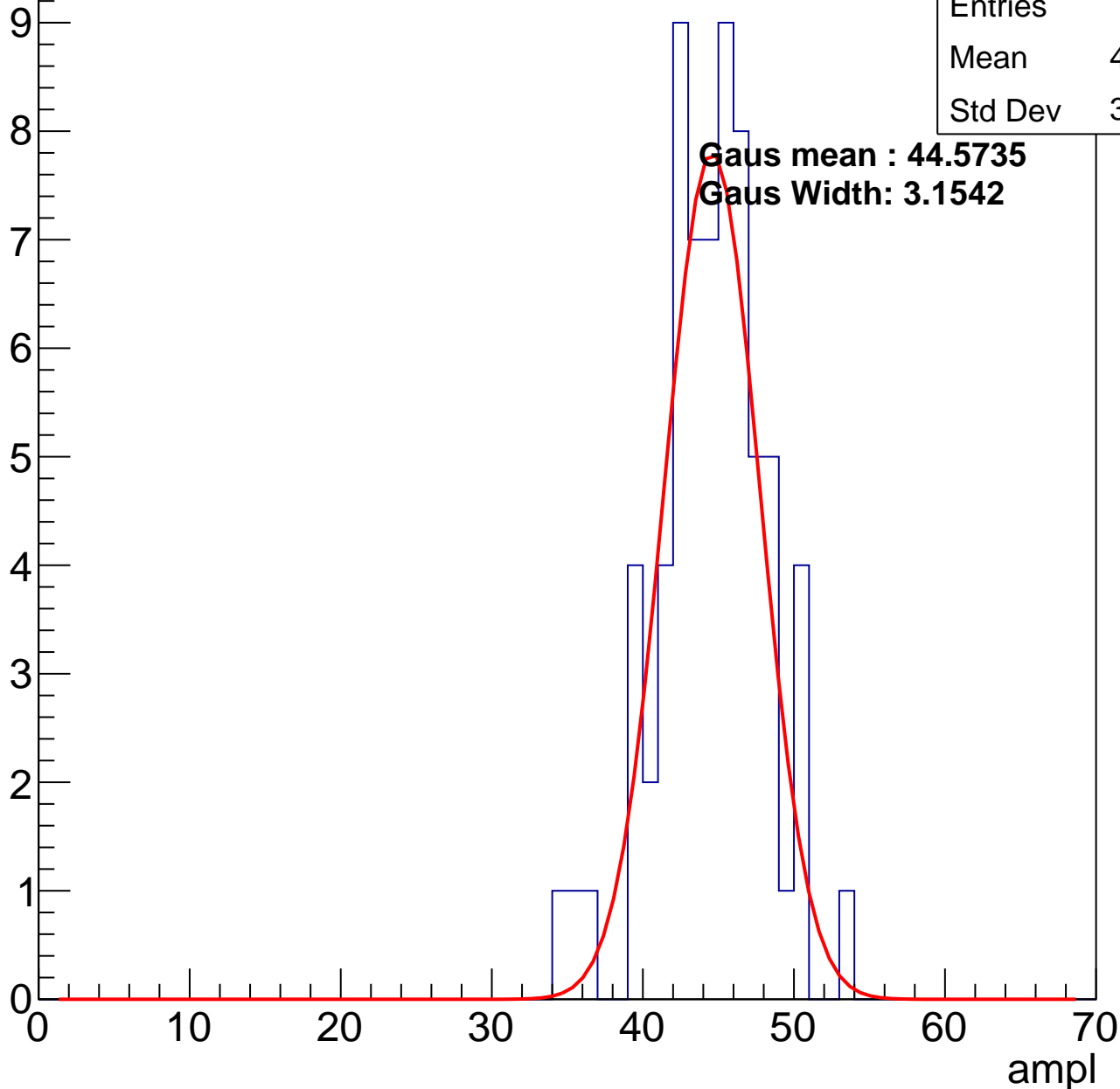
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.09
Std Dev	3.554

Gaus mean : 44.5735

Gaus Width: 3.1542

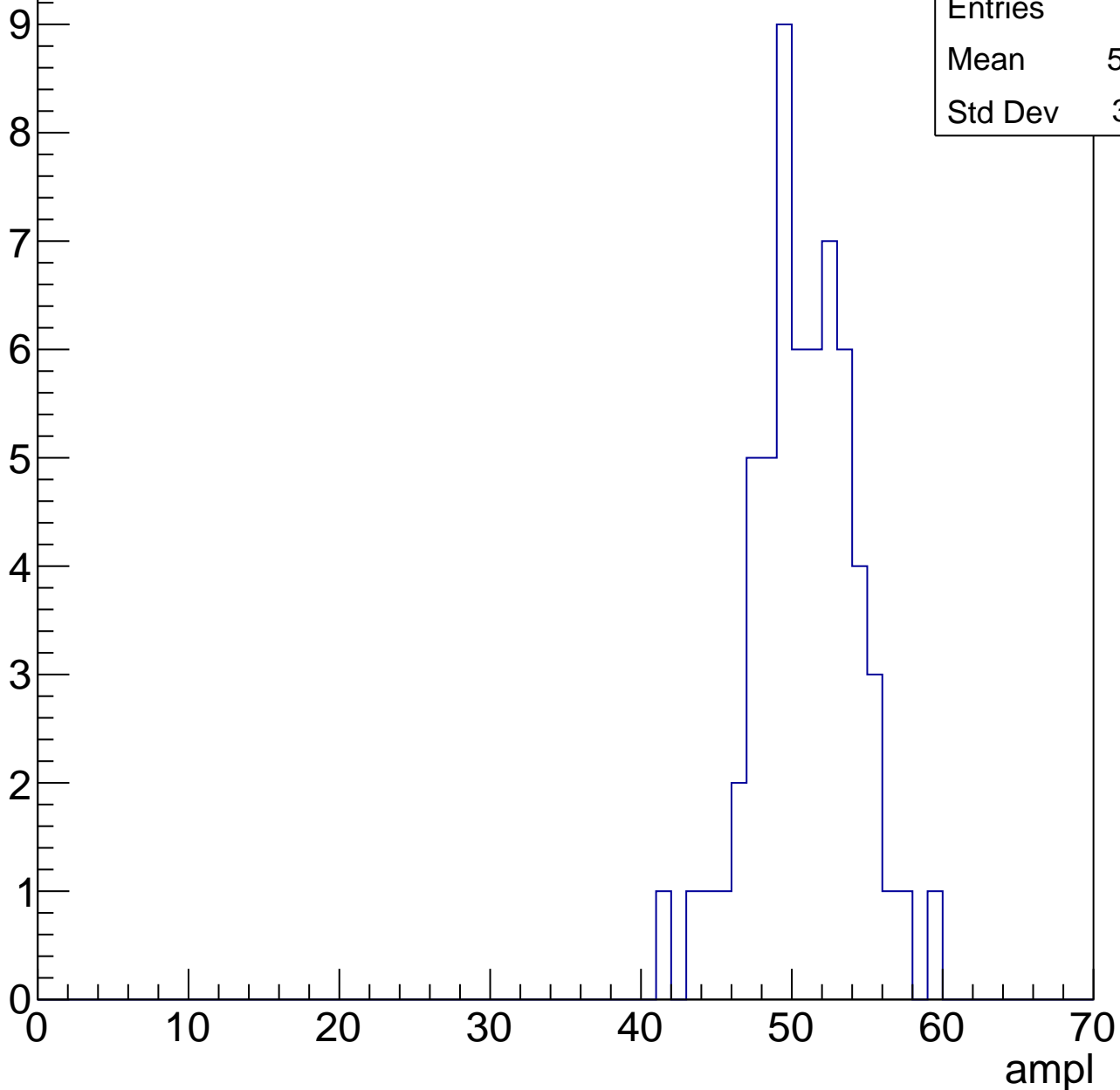


B1L100S, U10-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

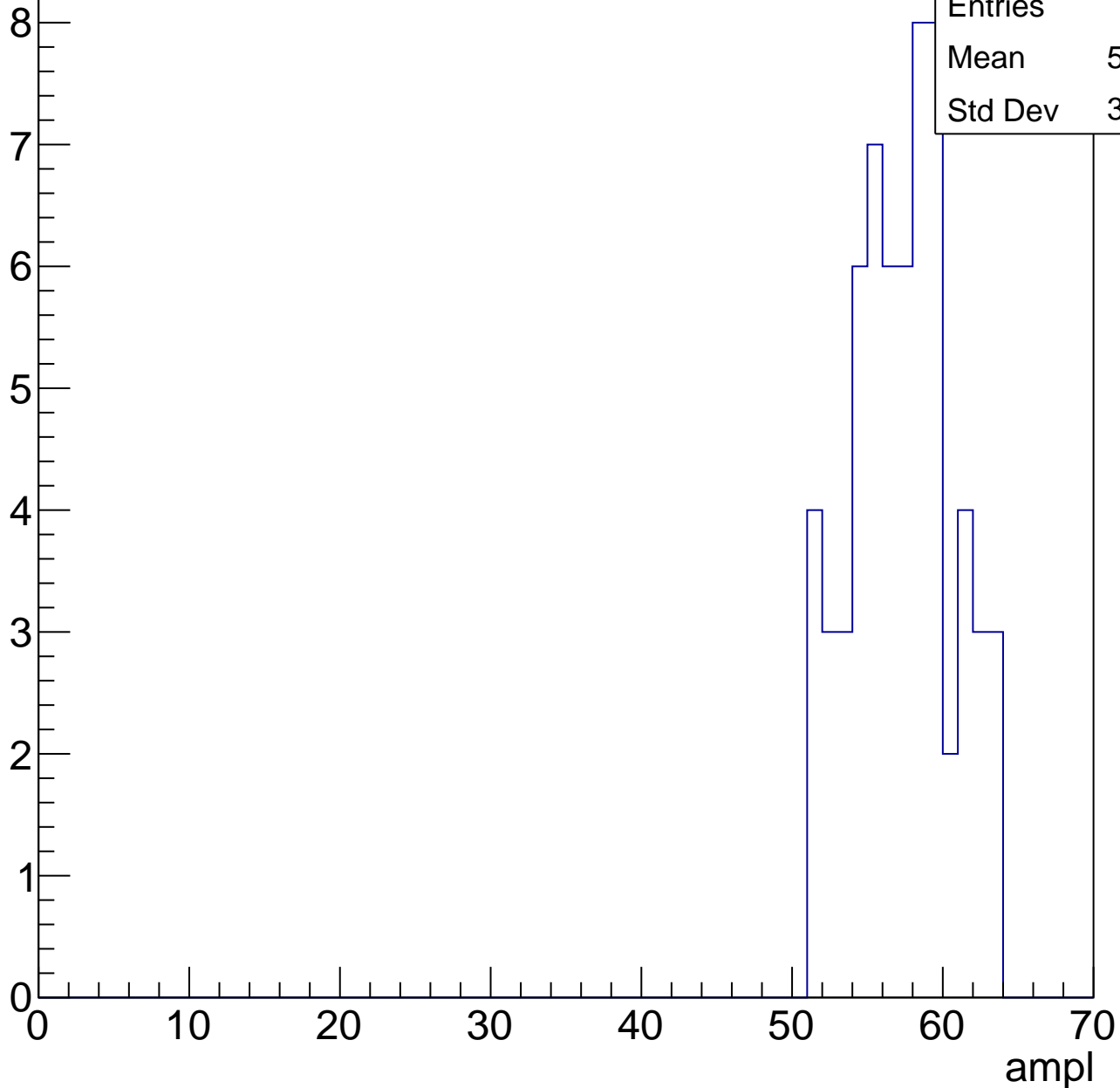
Entries	60
Mean	50.37
Std Dev	3.371



B1L100S, U10-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

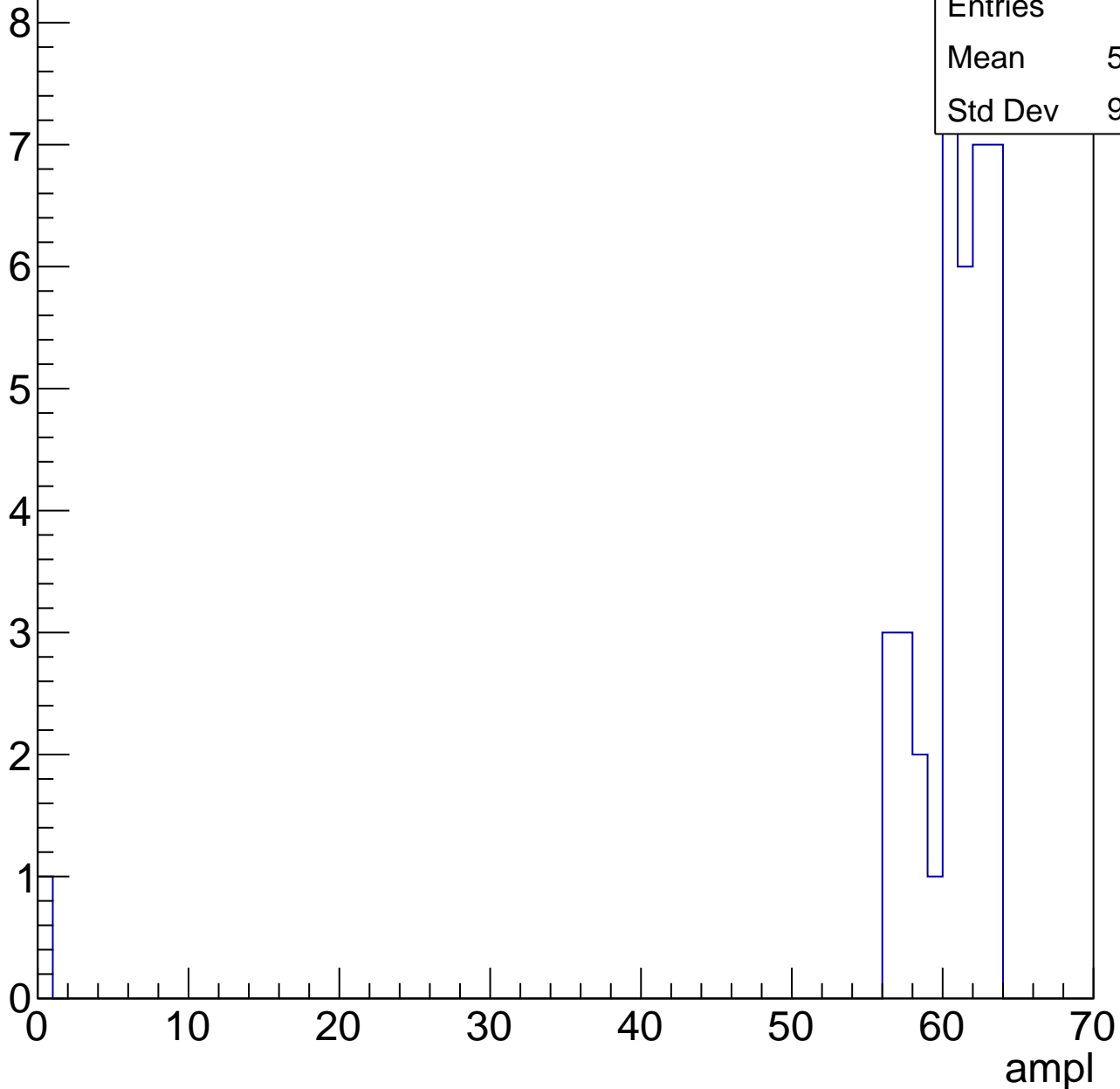


B1L100S, U10-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.82
Std Dev	9.904



B1L100S, U10-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

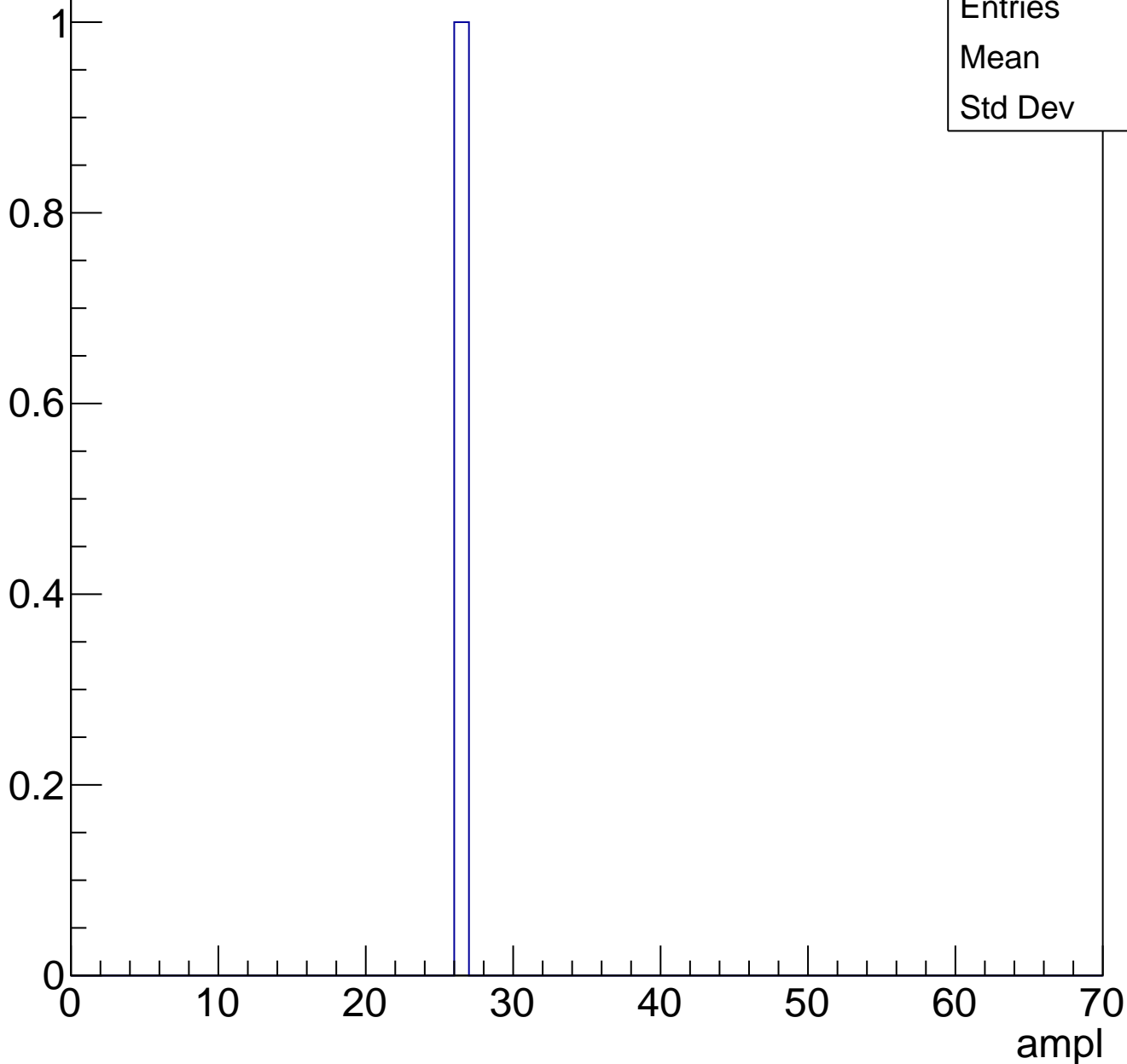


Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch16, adc0

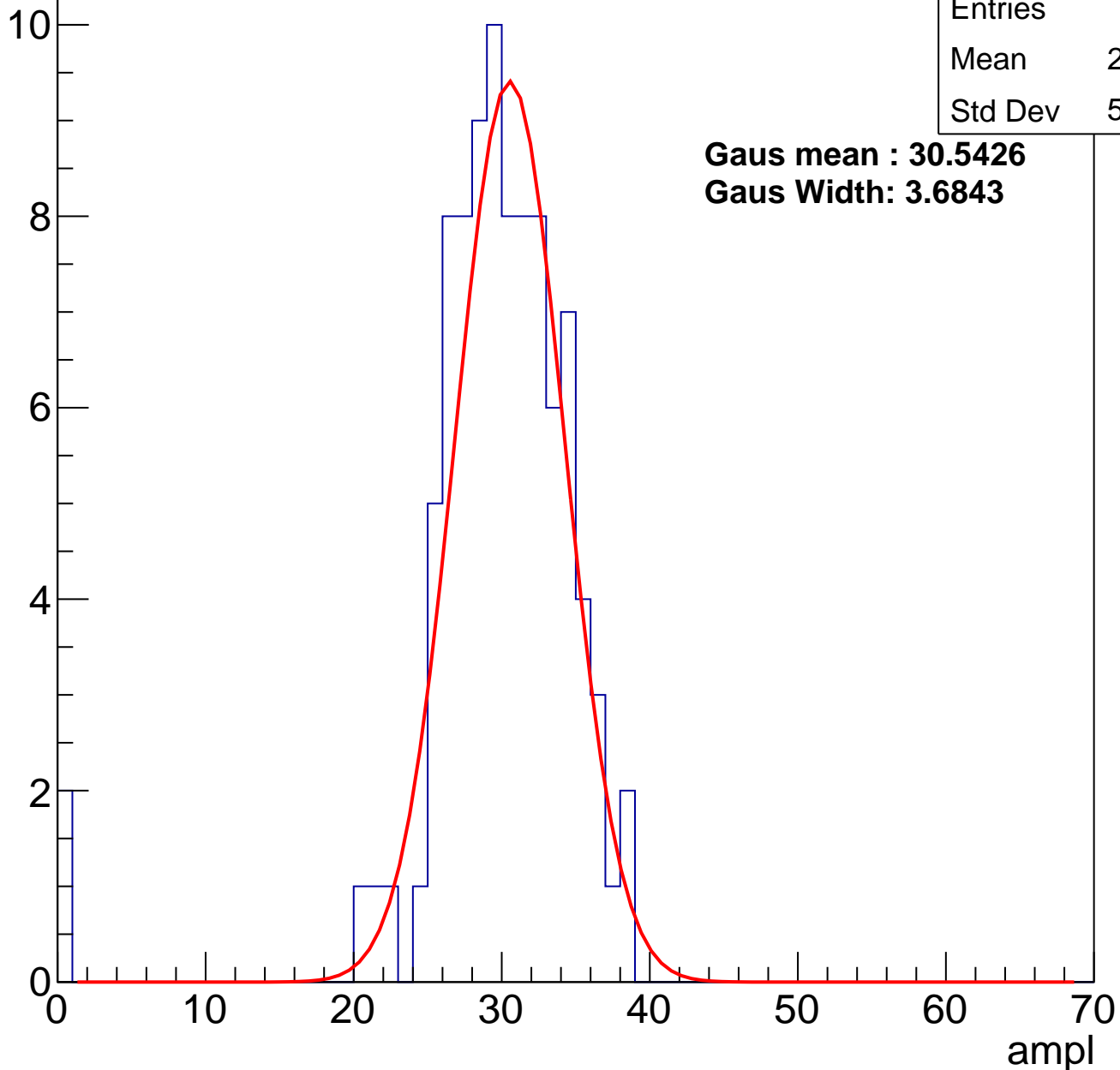
calib_packv5_042523_0143.root, FC#4, port A2

Entries	93
Mean	29.24
Std Dev	5.669

Gaus mean : 30.5426

Gaus Width: 3.6843

Entry



B1L100S, U10-ch16, adc1

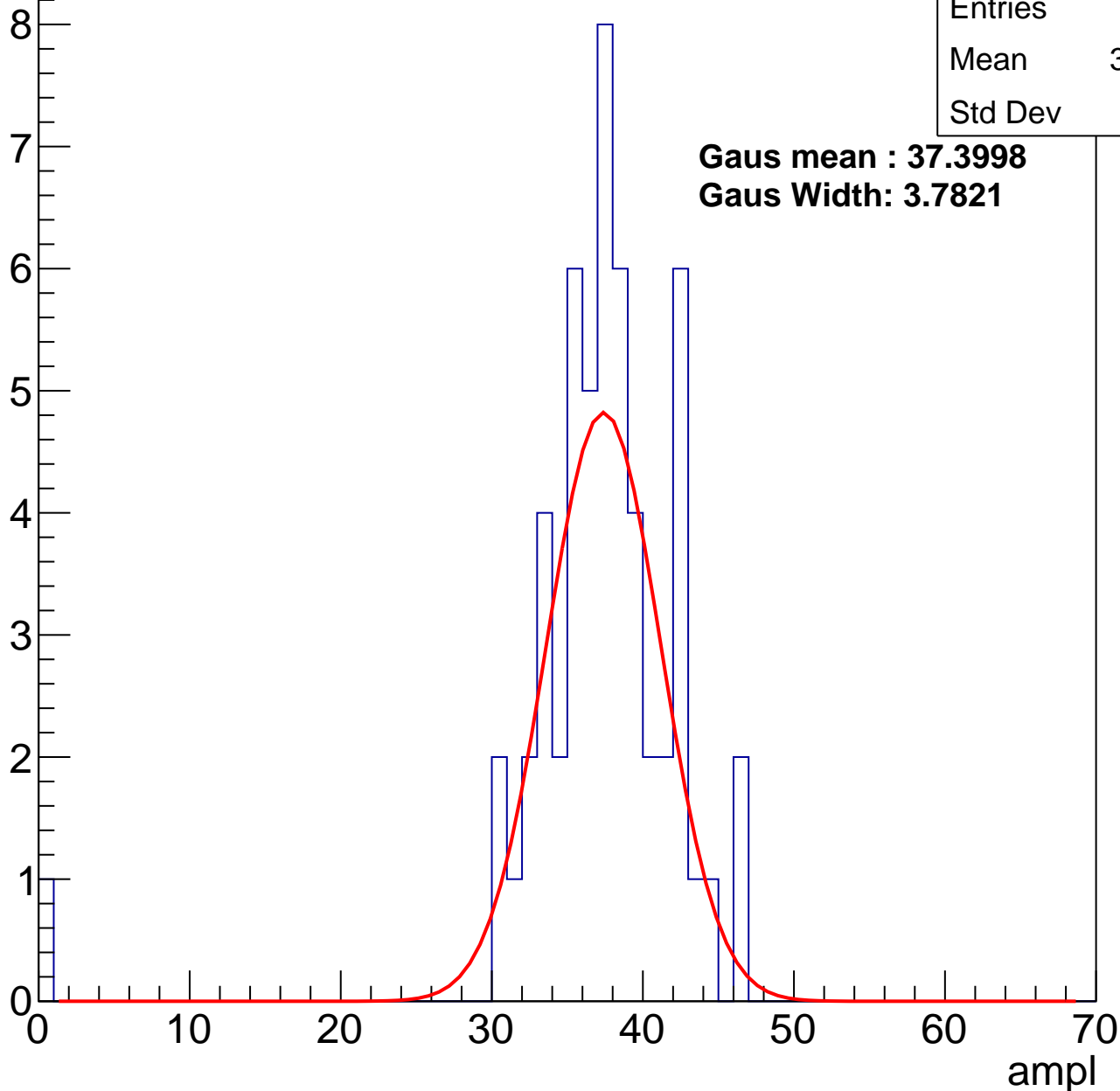
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	36.69
Std Dev	6.22

Gaus mean : 37.3998

Gaus Width: 3.7821



B1L100S, U10-ch16, adc2

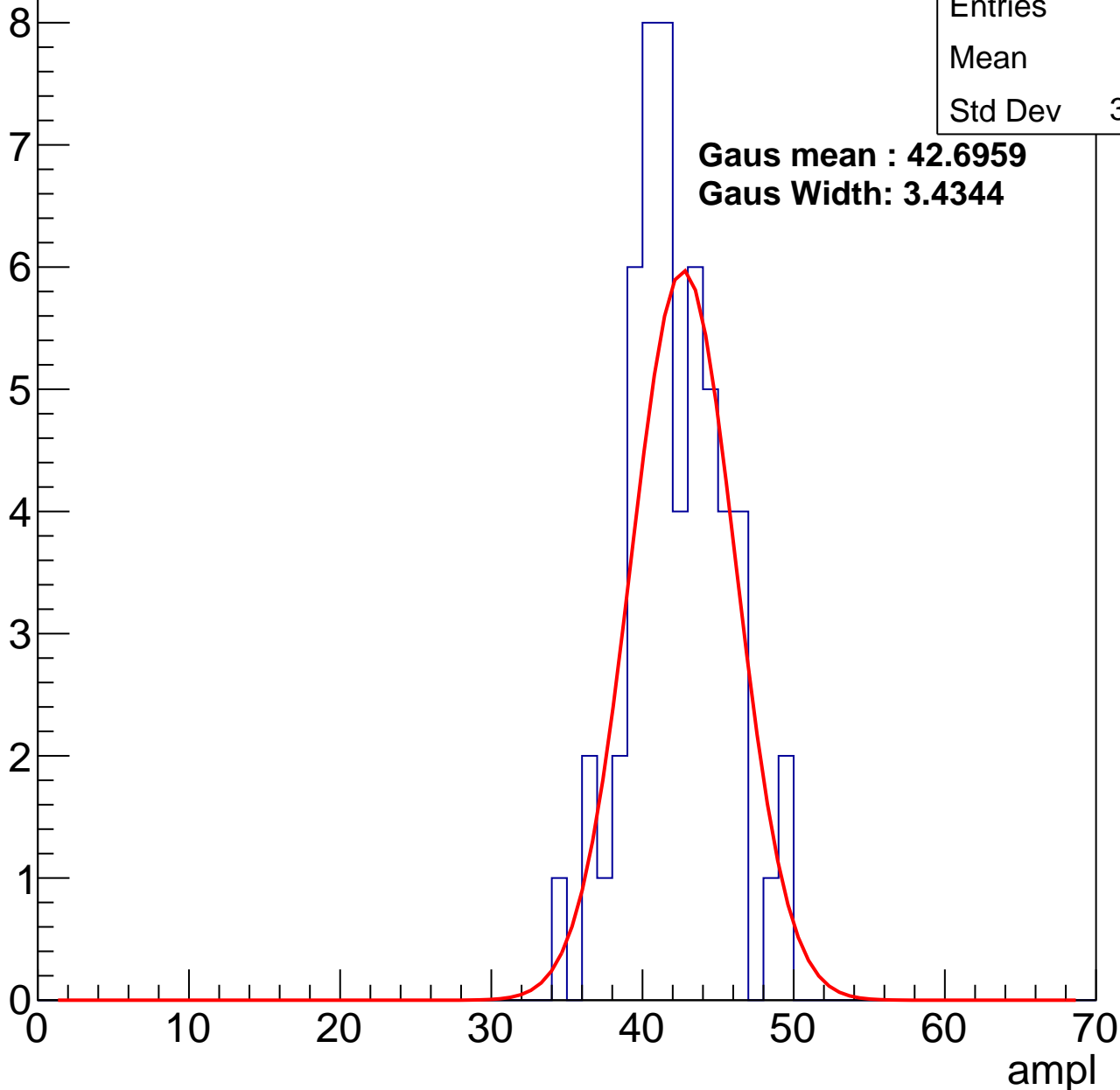
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	41.8
Std Dev	3.176

Gaus mean : 42.6959

Gaus Width: 3.4344

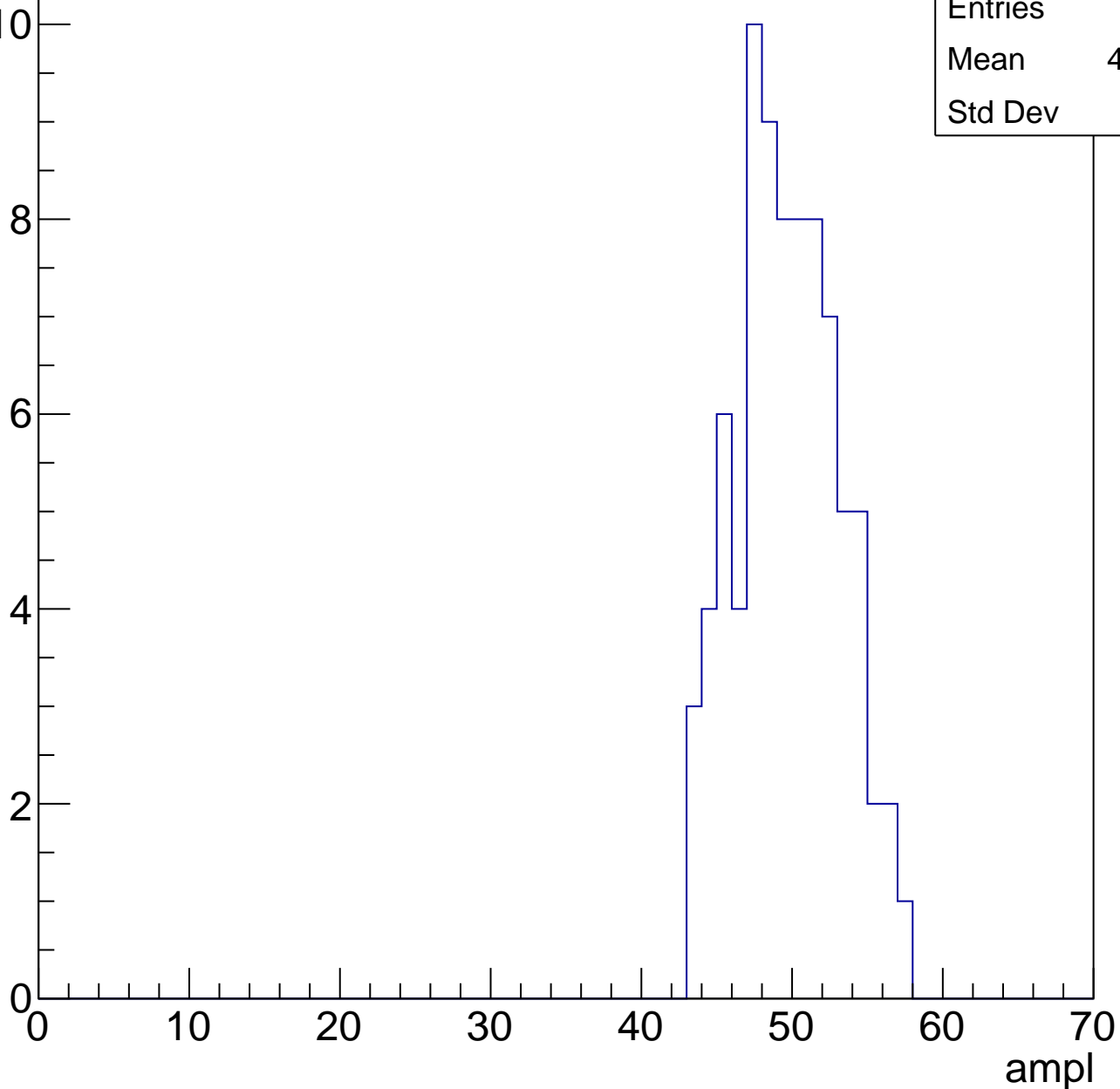


B1L100S, U10-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

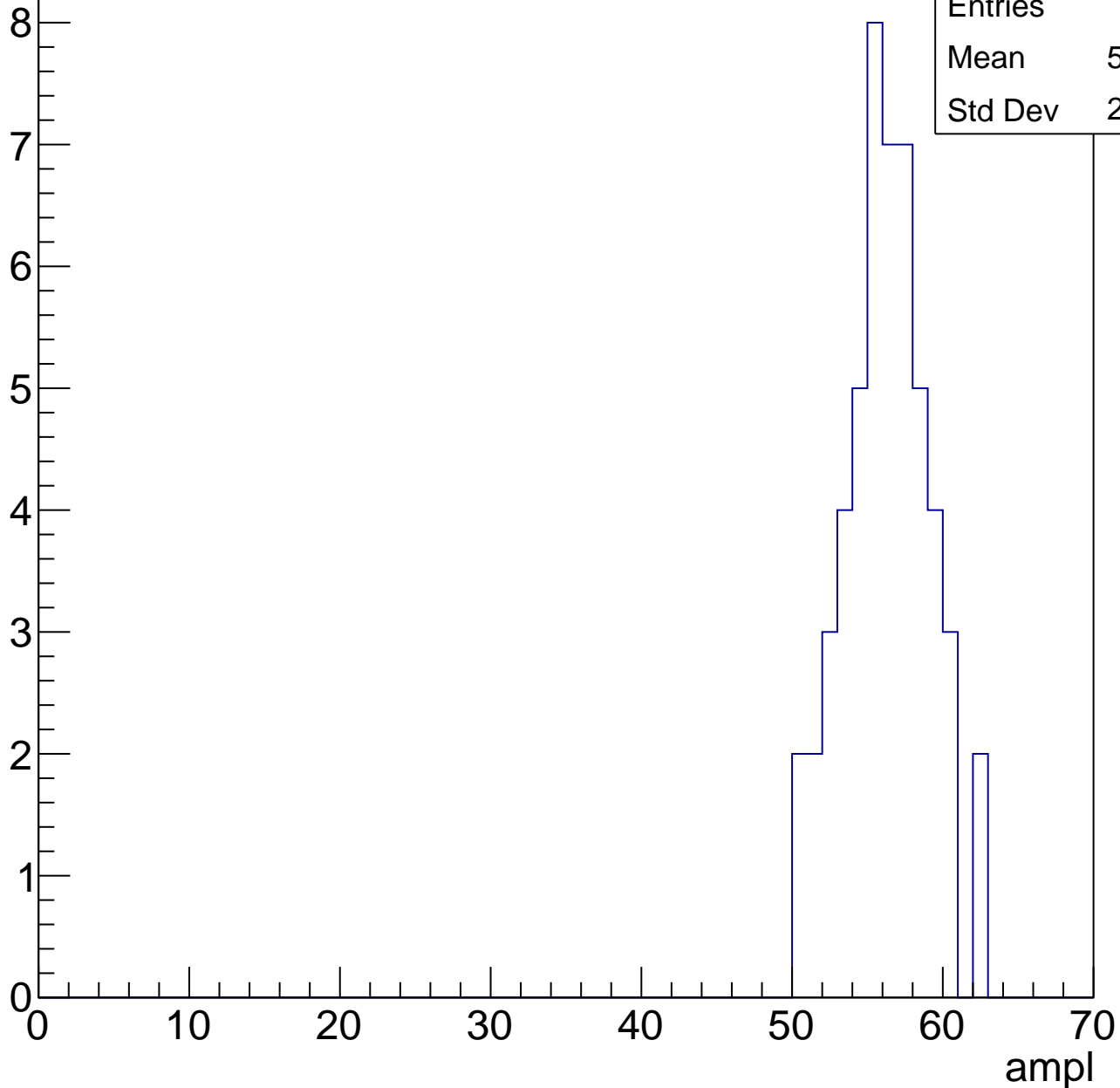
Entries	82
Mean	49.26
Std Dev	3.36



B1L100S, U10-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

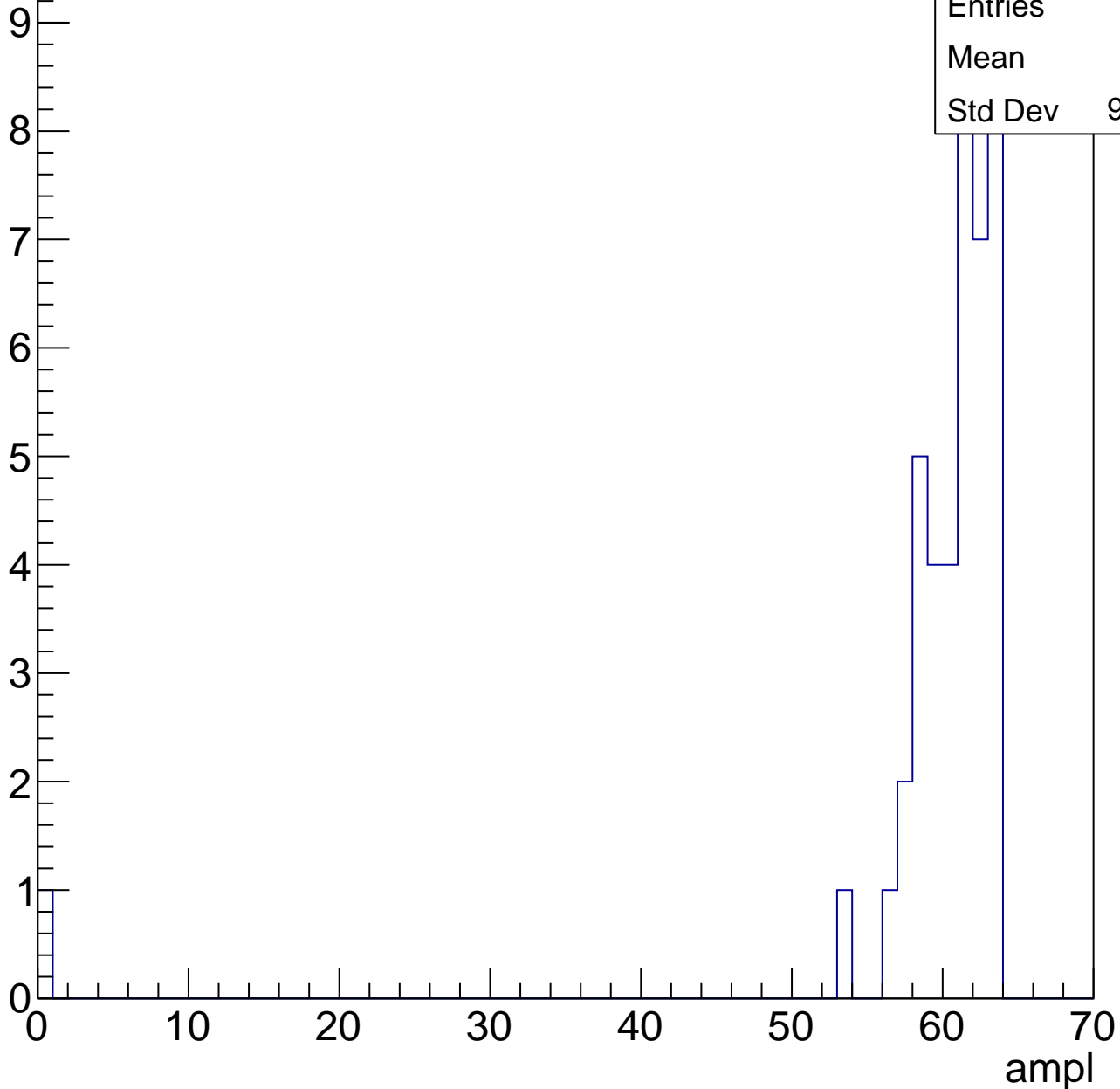


B1L100S, U10-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

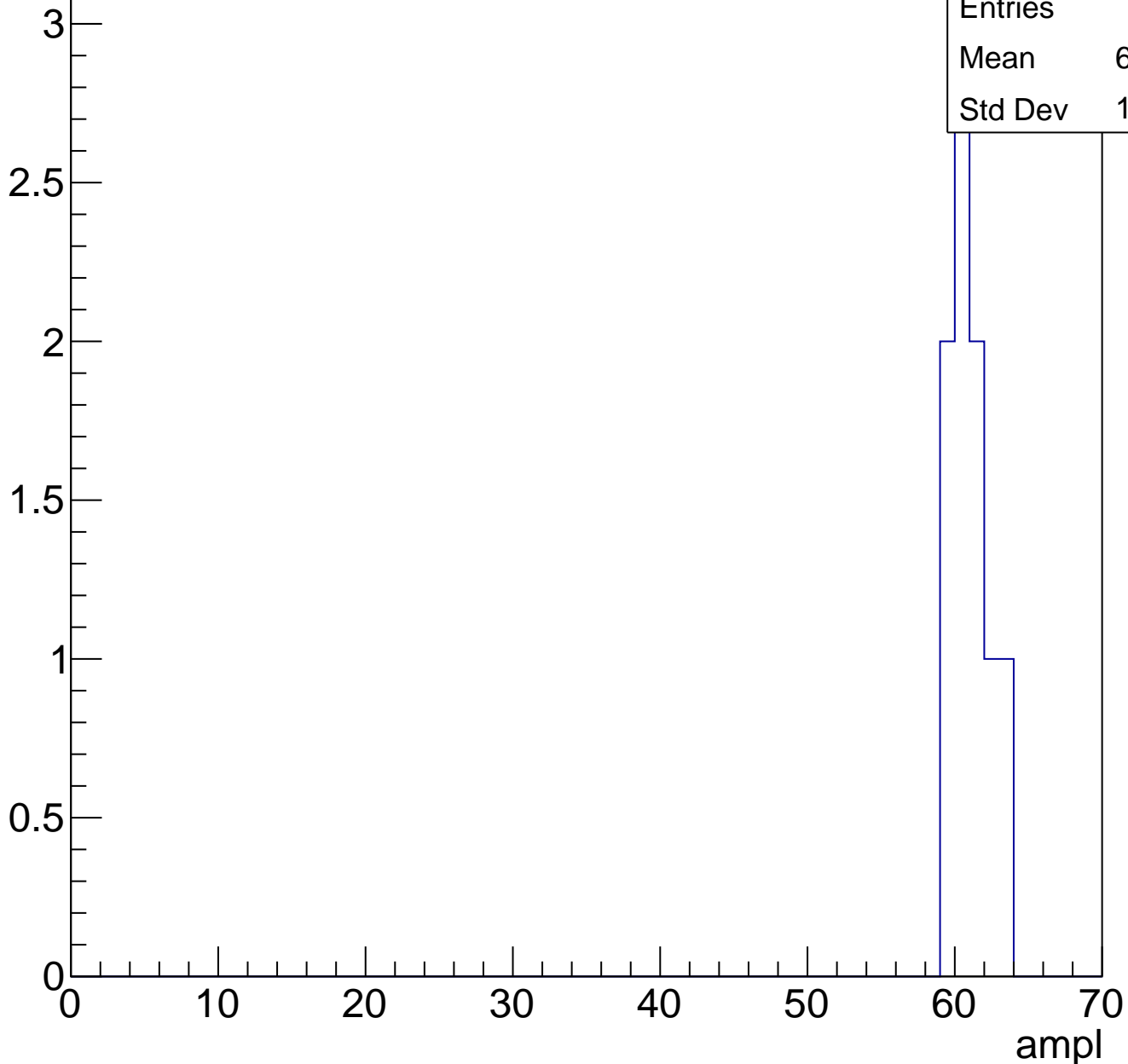
Entries	42
Mean	59
Std Dev	9.489



B1L100S, U10-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	9
Mean	60.56
Std Dev	1.257

B1L100S, U10-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch17, adc0

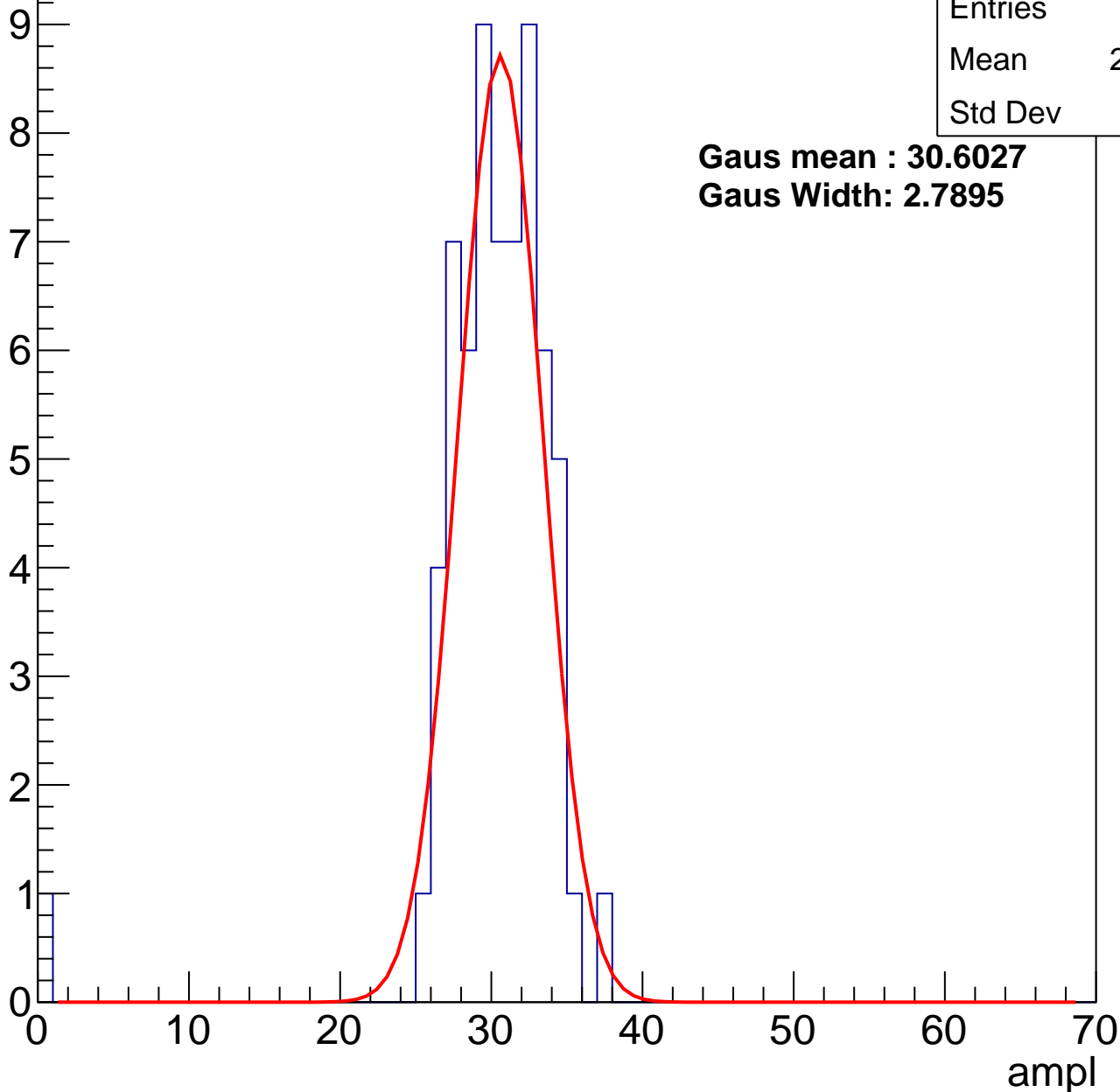
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.72
Std Dev	4.56

Gaus mean : 30.6027

Gaus Width: 2.7895



B1L100S, U10-ch17, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	37.47
Std Dev	3.222

Gaus mean : 38.1502

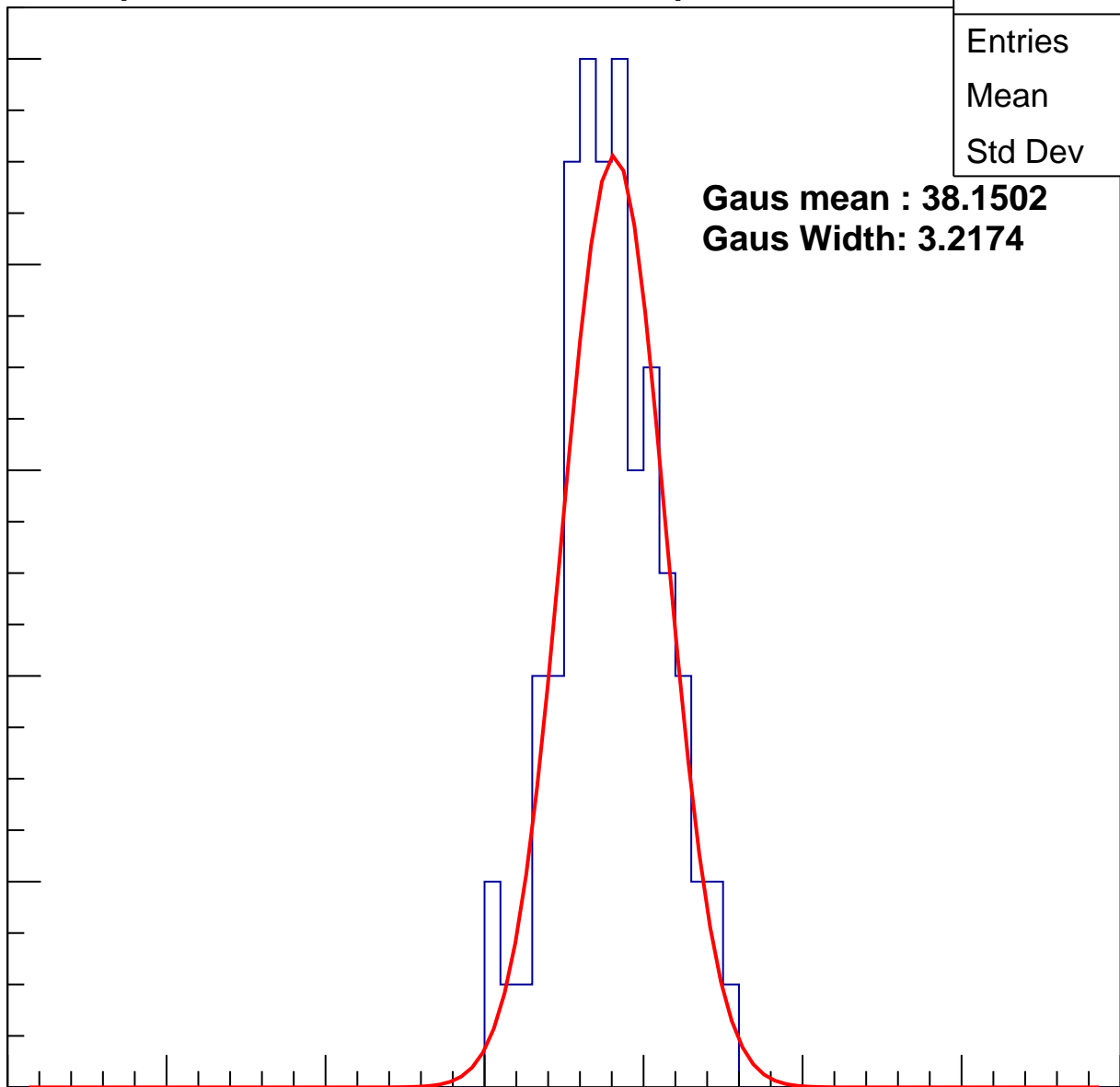
Gaus Width: 3.2174

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch17, adc2

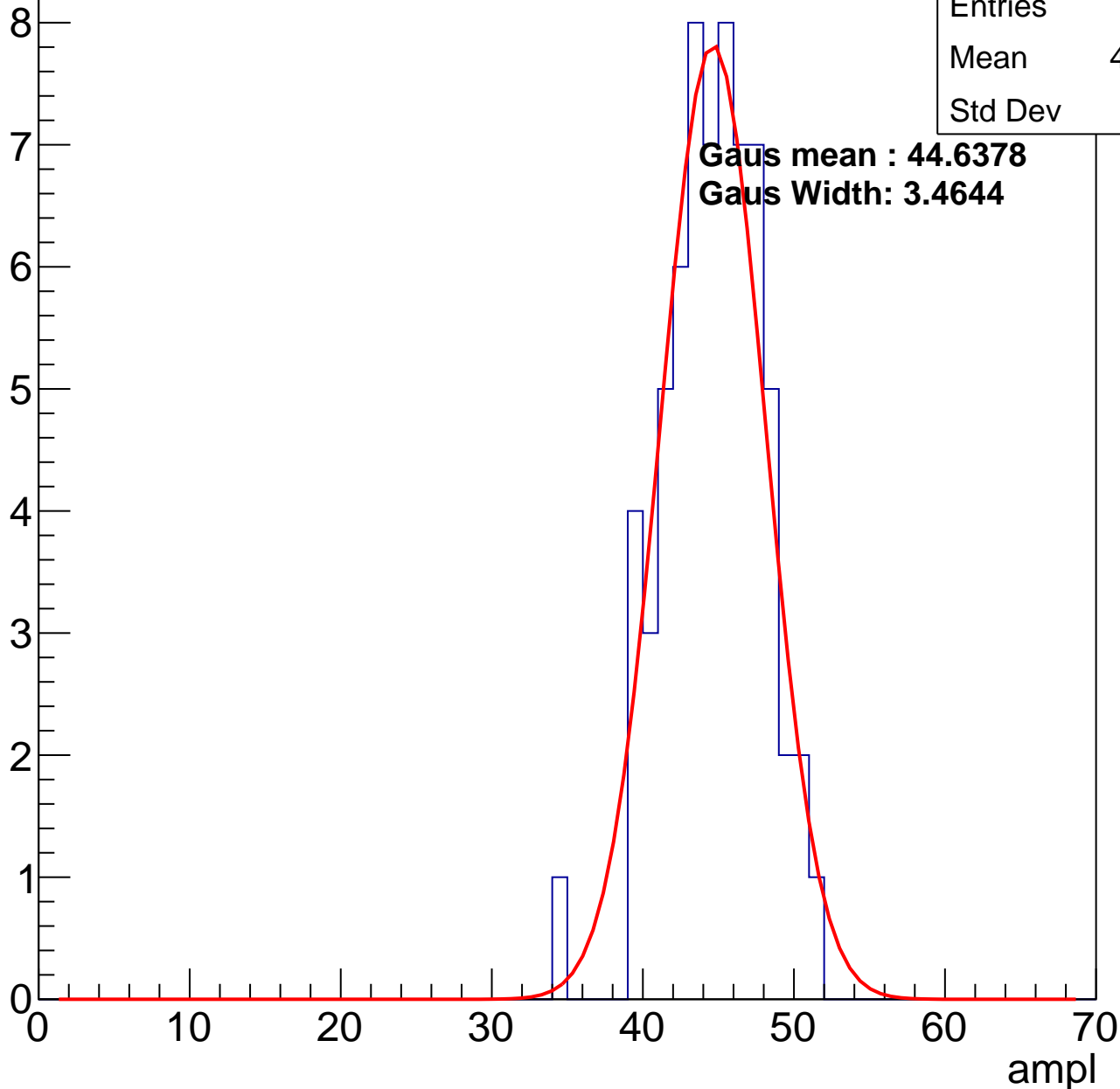
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.23
Std Dev	3.19

Gaus mean : 44.6378

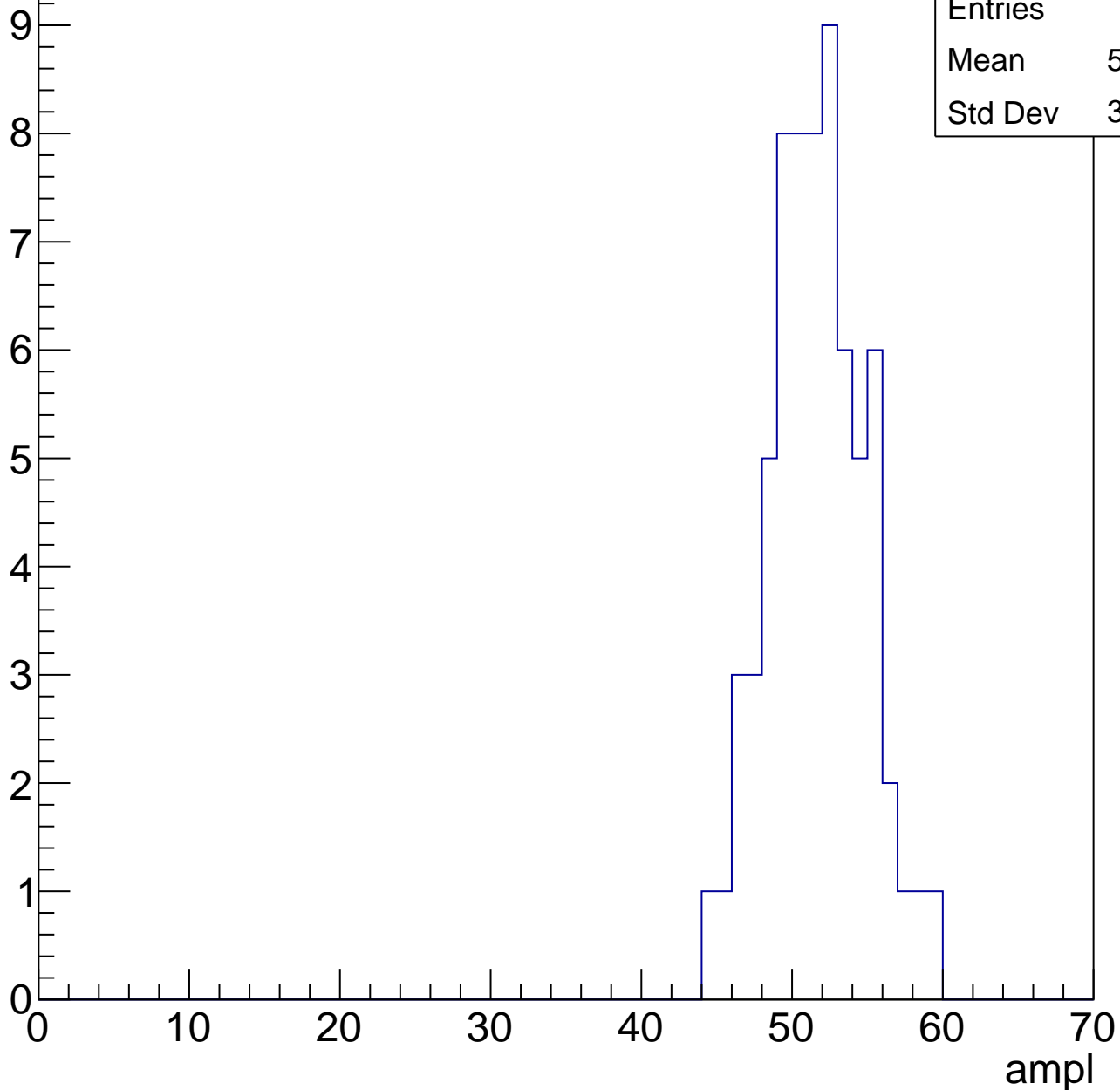
Gaus Width: 3.4644



B1L100S, U10-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

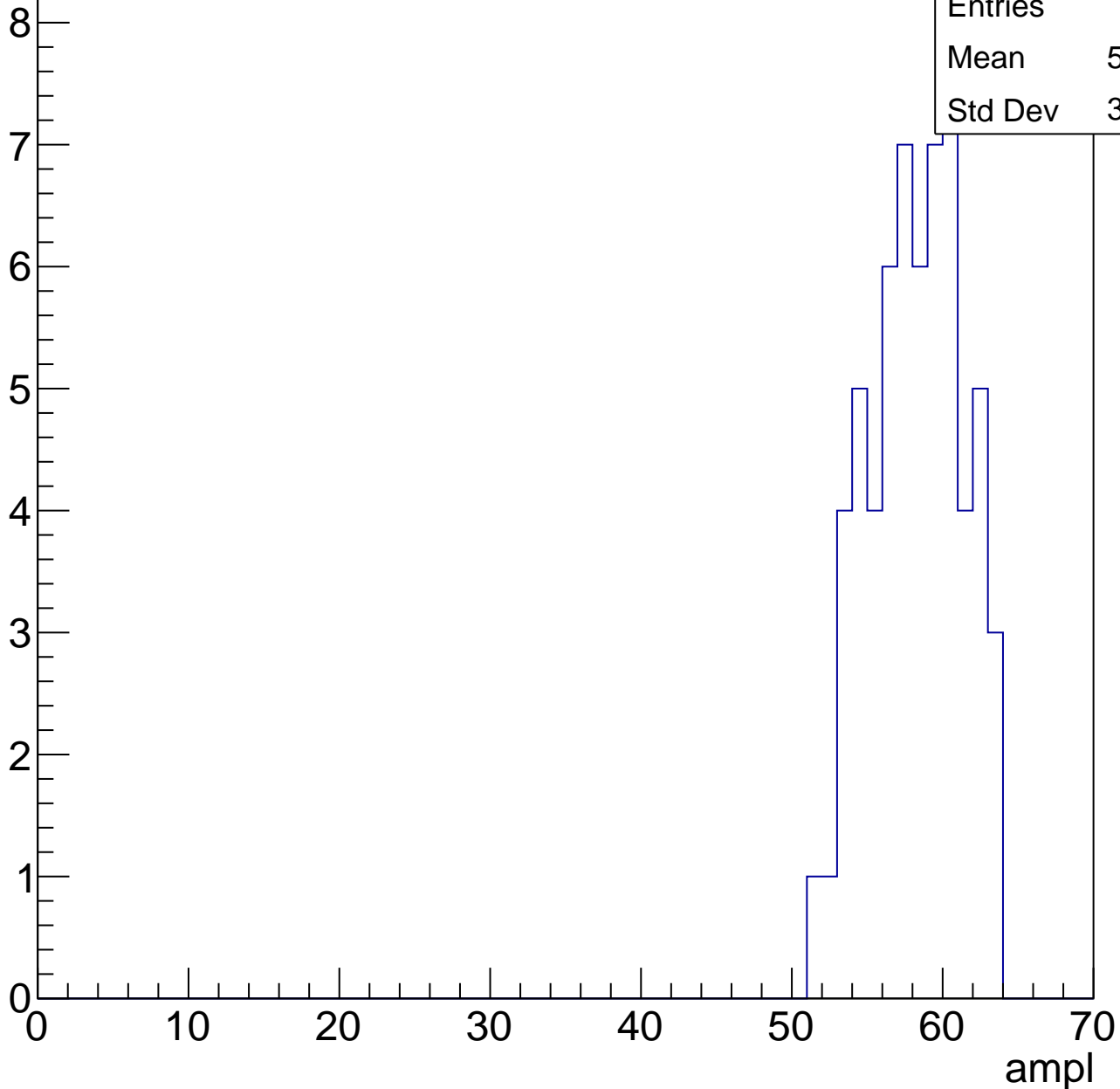


B1L100S, U10-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.77
Std Dev	3.027

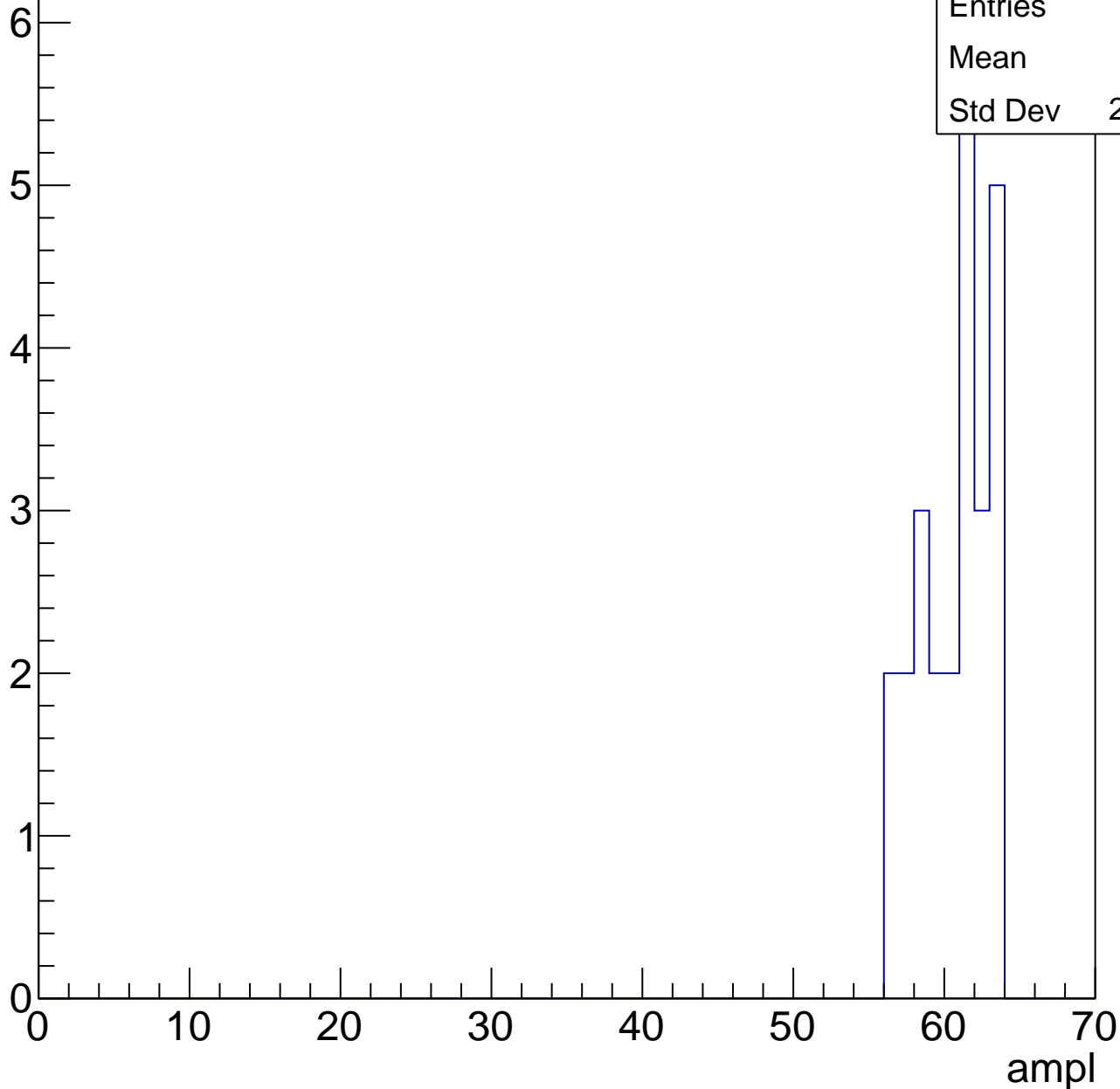


B1L100S, U10-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	60.2
Std Dev	2.245



B1L100S, U10-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	50
Std Dev	25

B1L100S, U10-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch18, adc0

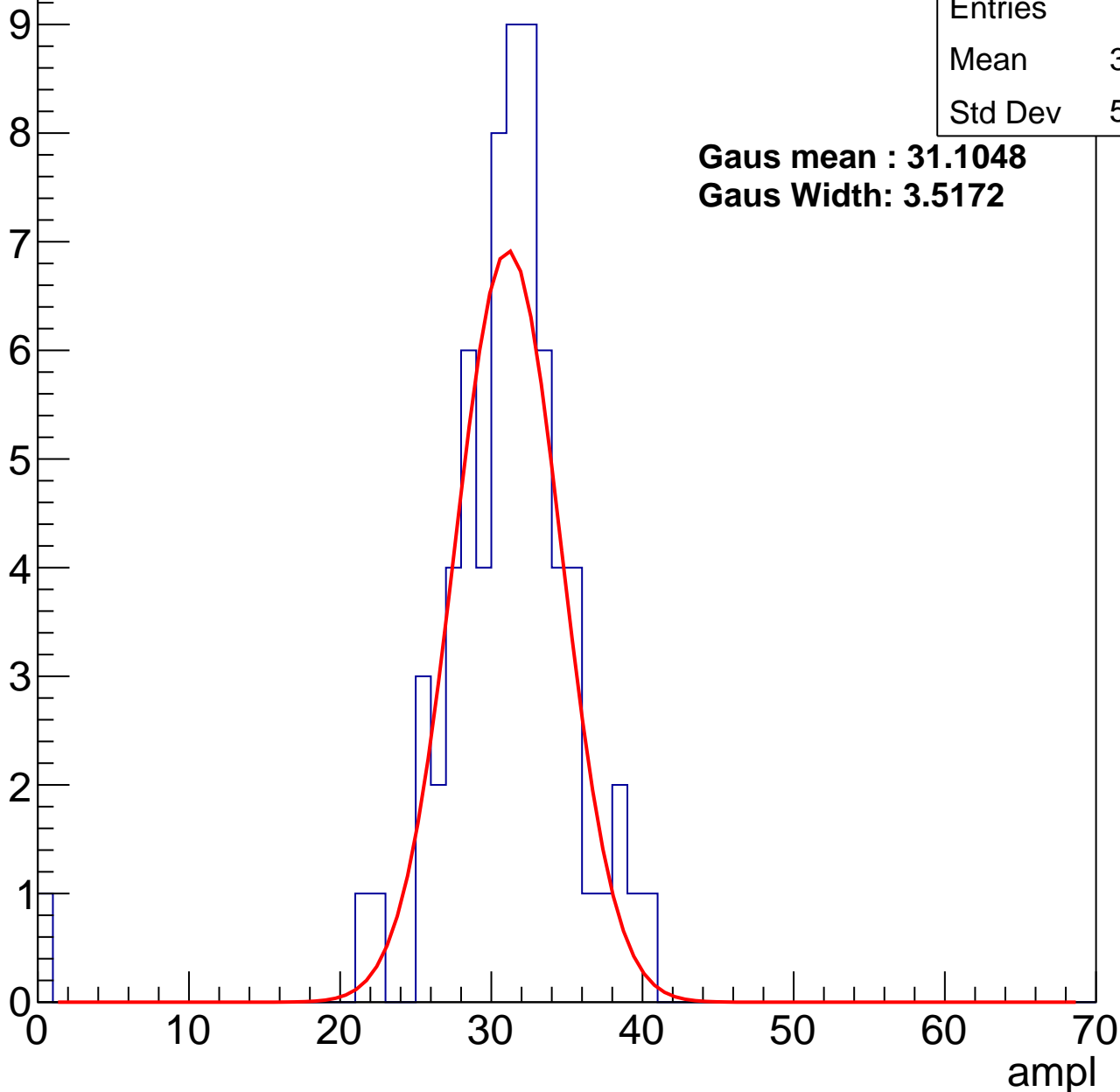
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.46
Std Dev	5.228

Gaus mean : 31.1048

Gaus Width: 3.5172



B1L100S, U10-ch18, adc1

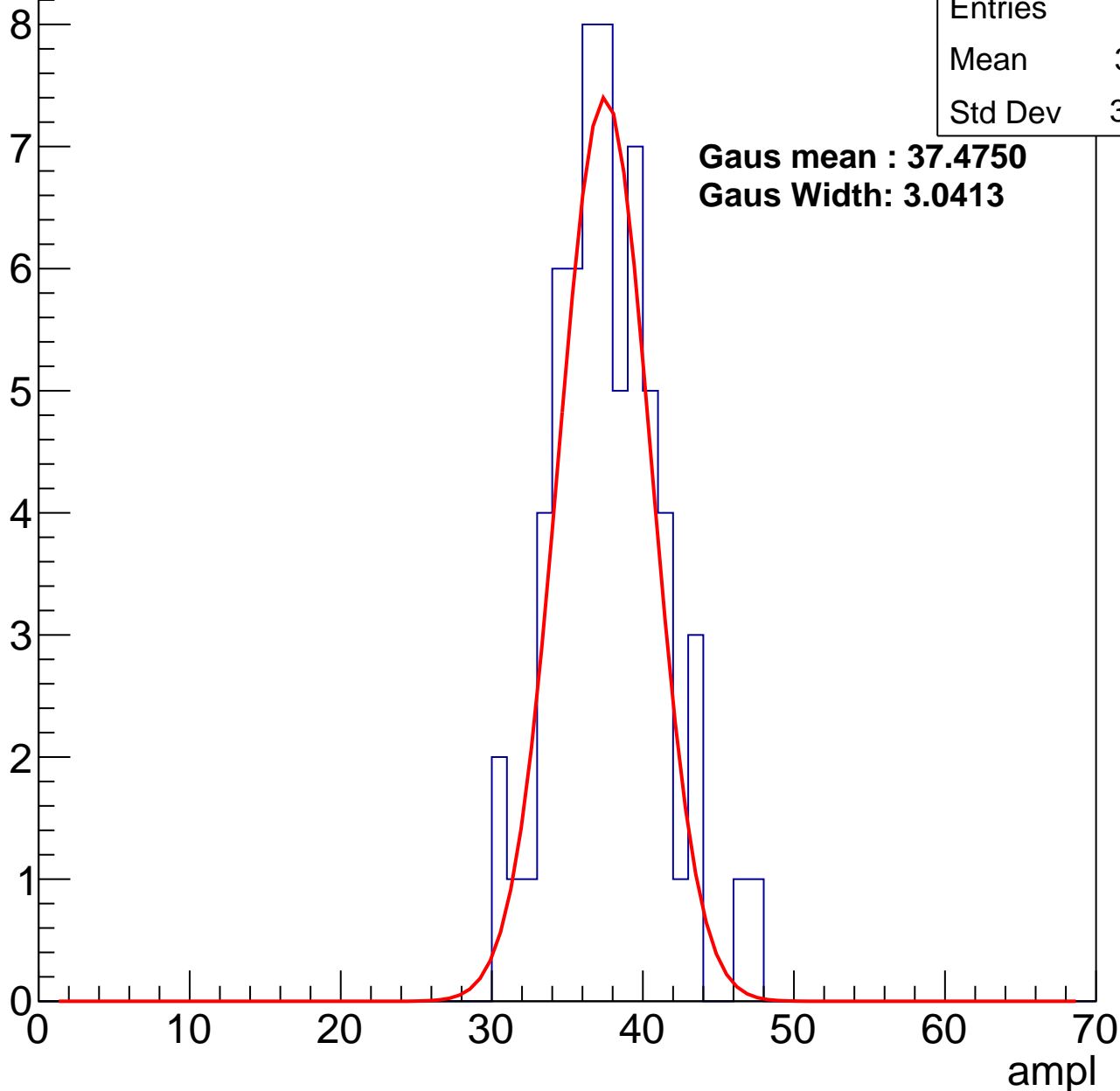
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.21
Std Dev	3.479

Gaus mean : 37.4750

Gaus Width: 3.0413



B1L100S, U10-ch18, adc2

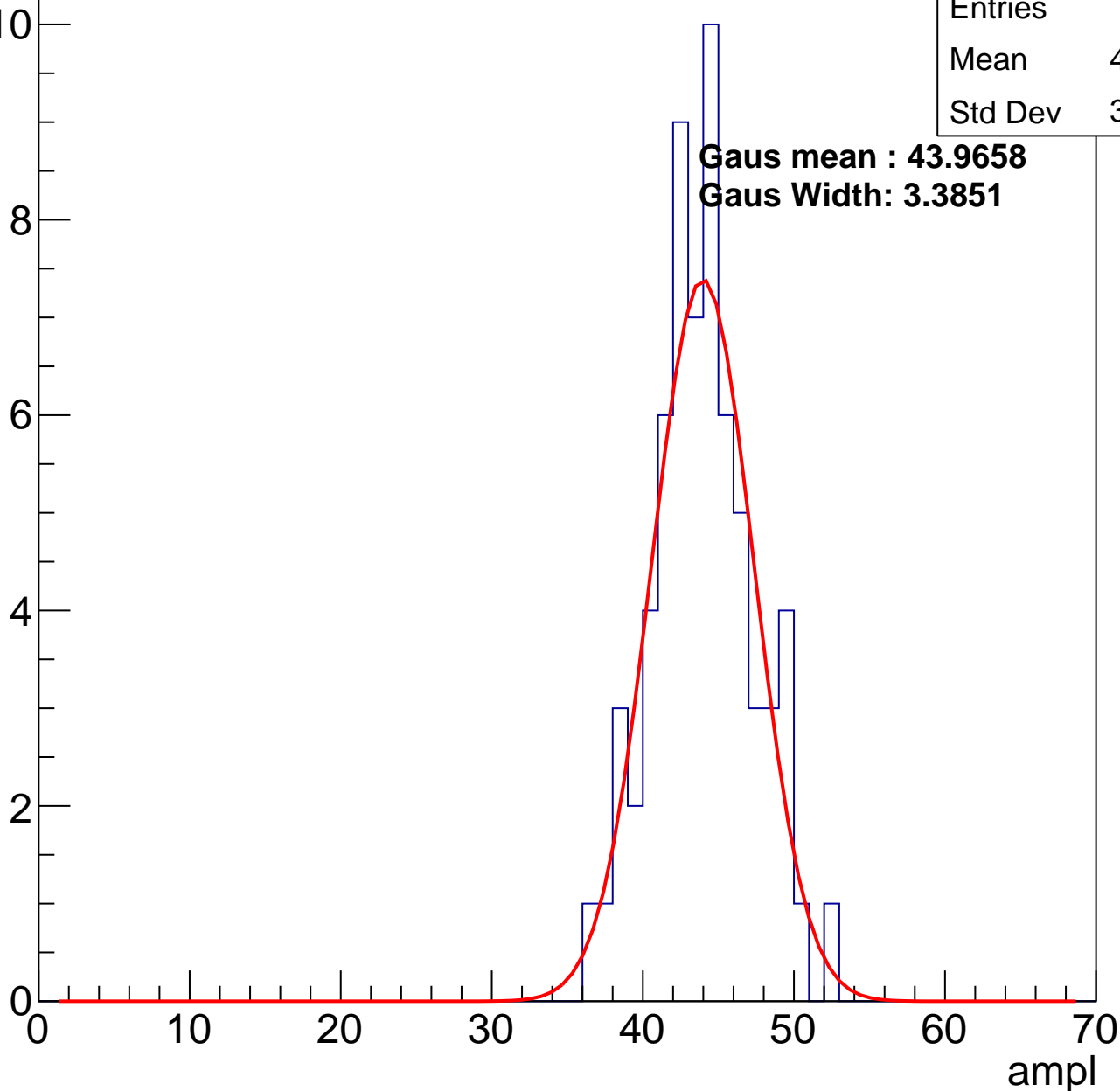
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.53
Std Dev	3.299

Gaus mean : 43.9658

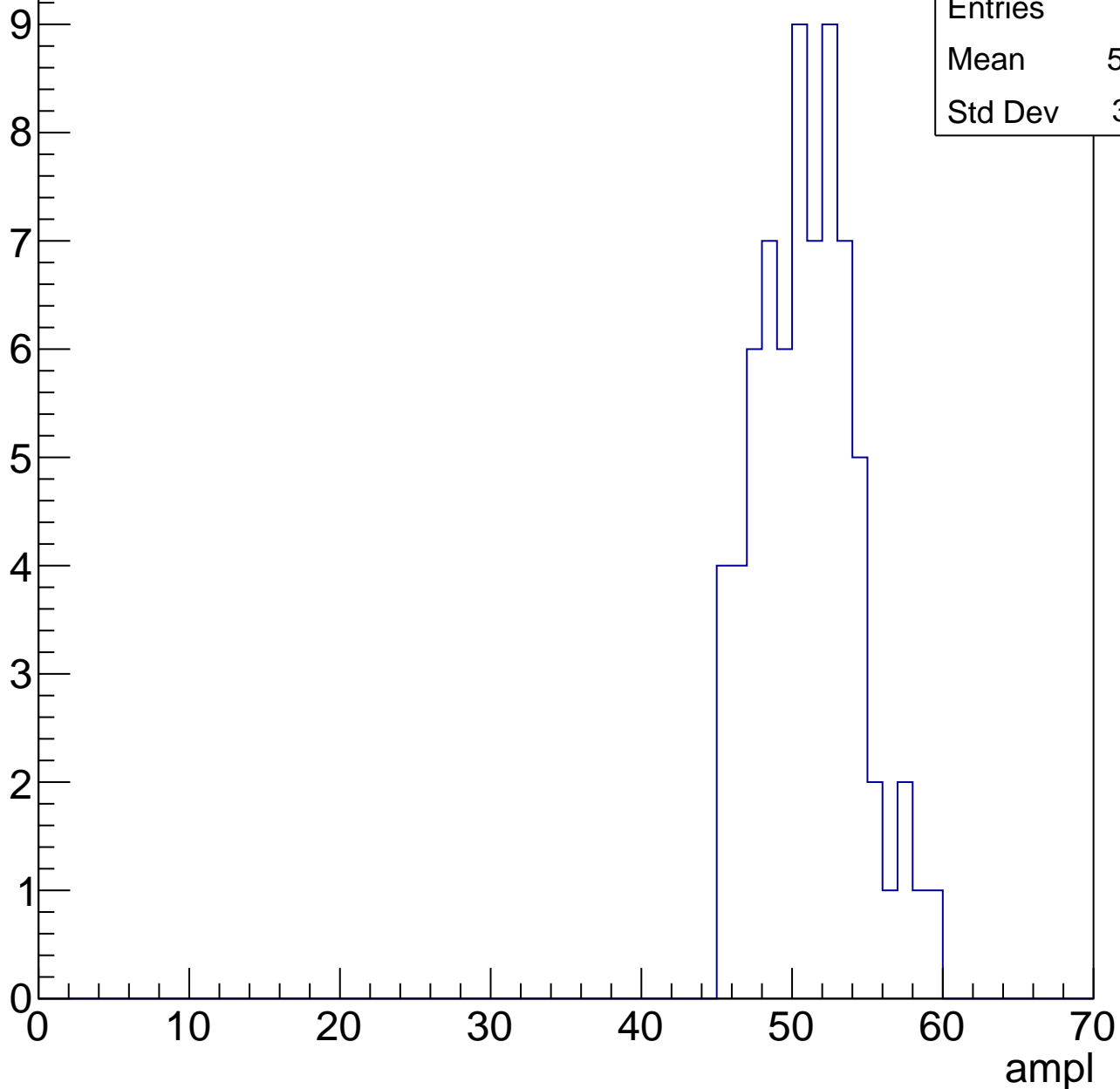
Gaus Width: 3.3851



B1L100S, U10-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

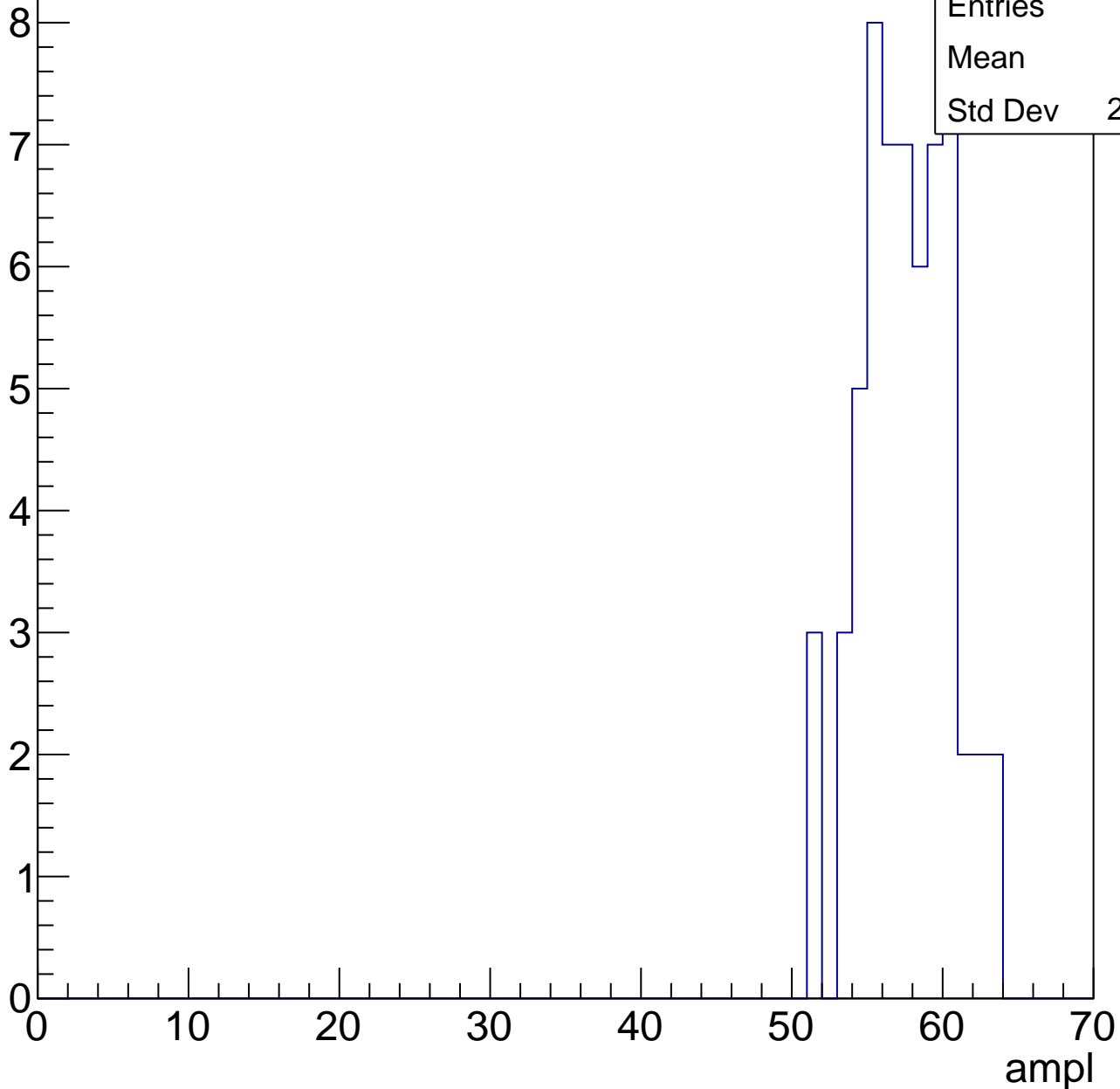


B1L100S, U10-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	57.1
Std Dev	2.885

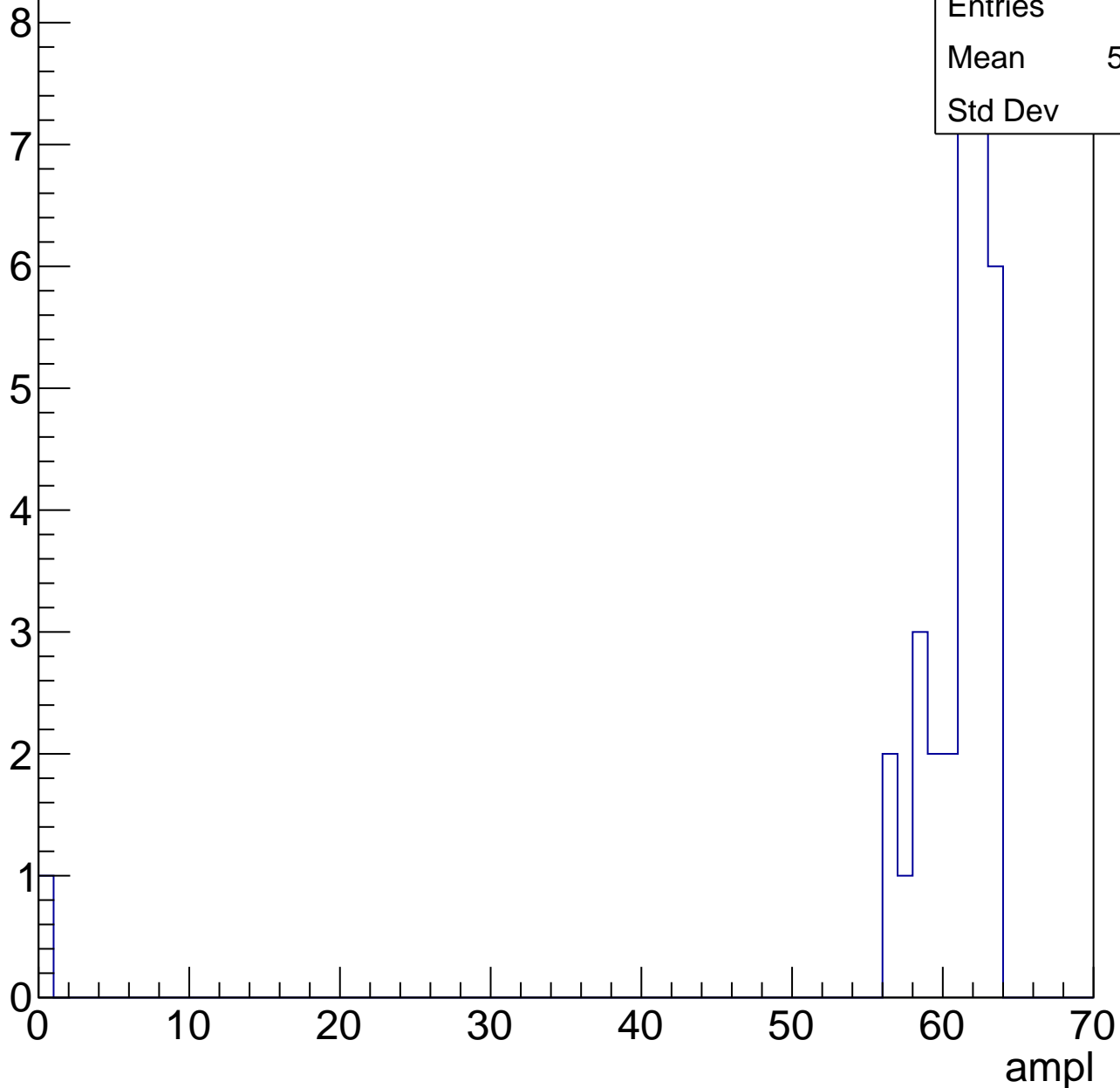


B1L100S, U10-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.88
Std Dev	10.6



B1L100S, U10-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U10-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch19, adc0

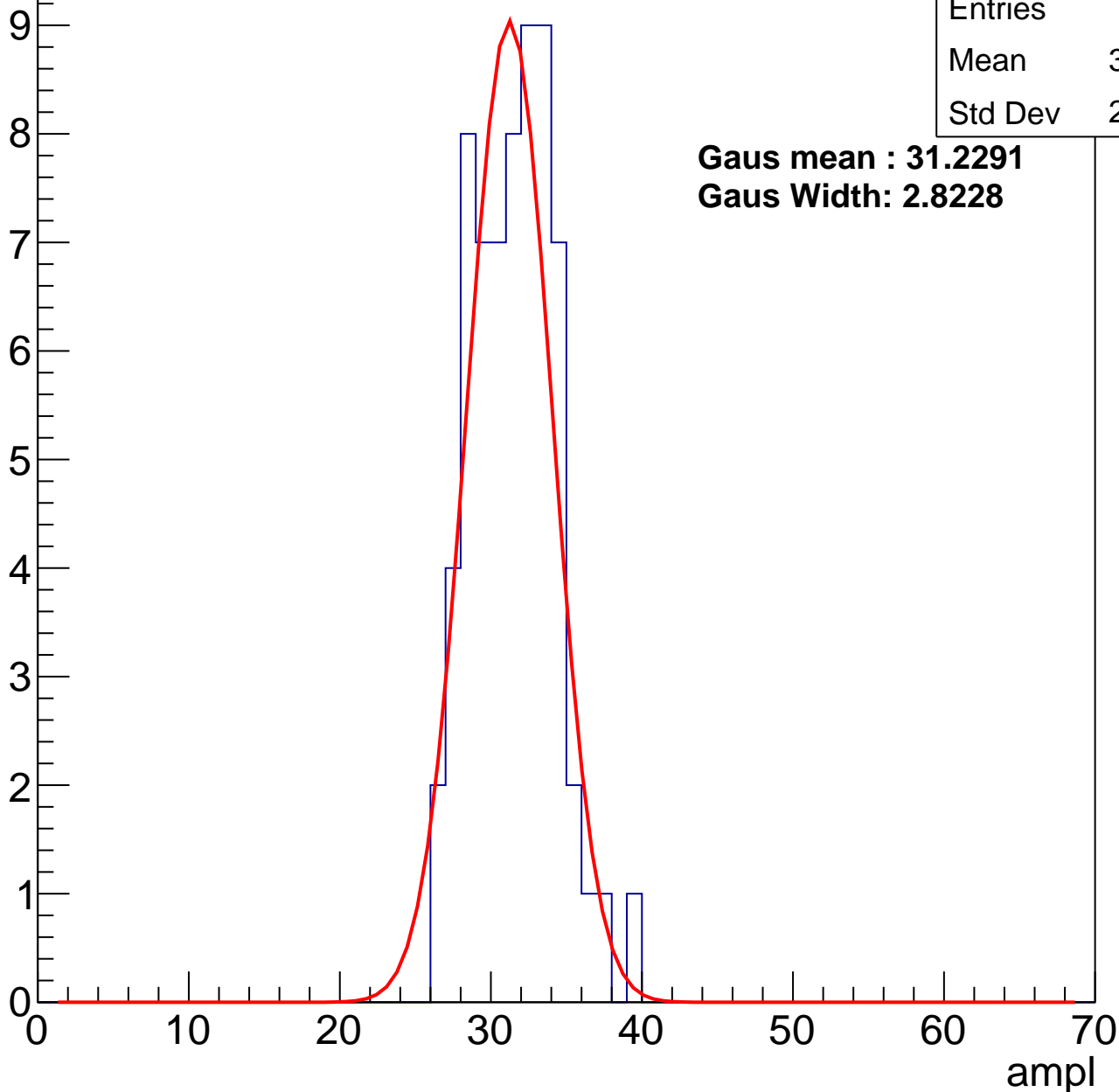
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	31.06
Std Dev	2.713

Gaus mean : 31.2291

Gaus Width: 2.8228



B1L100S, U10-ch19, adc1

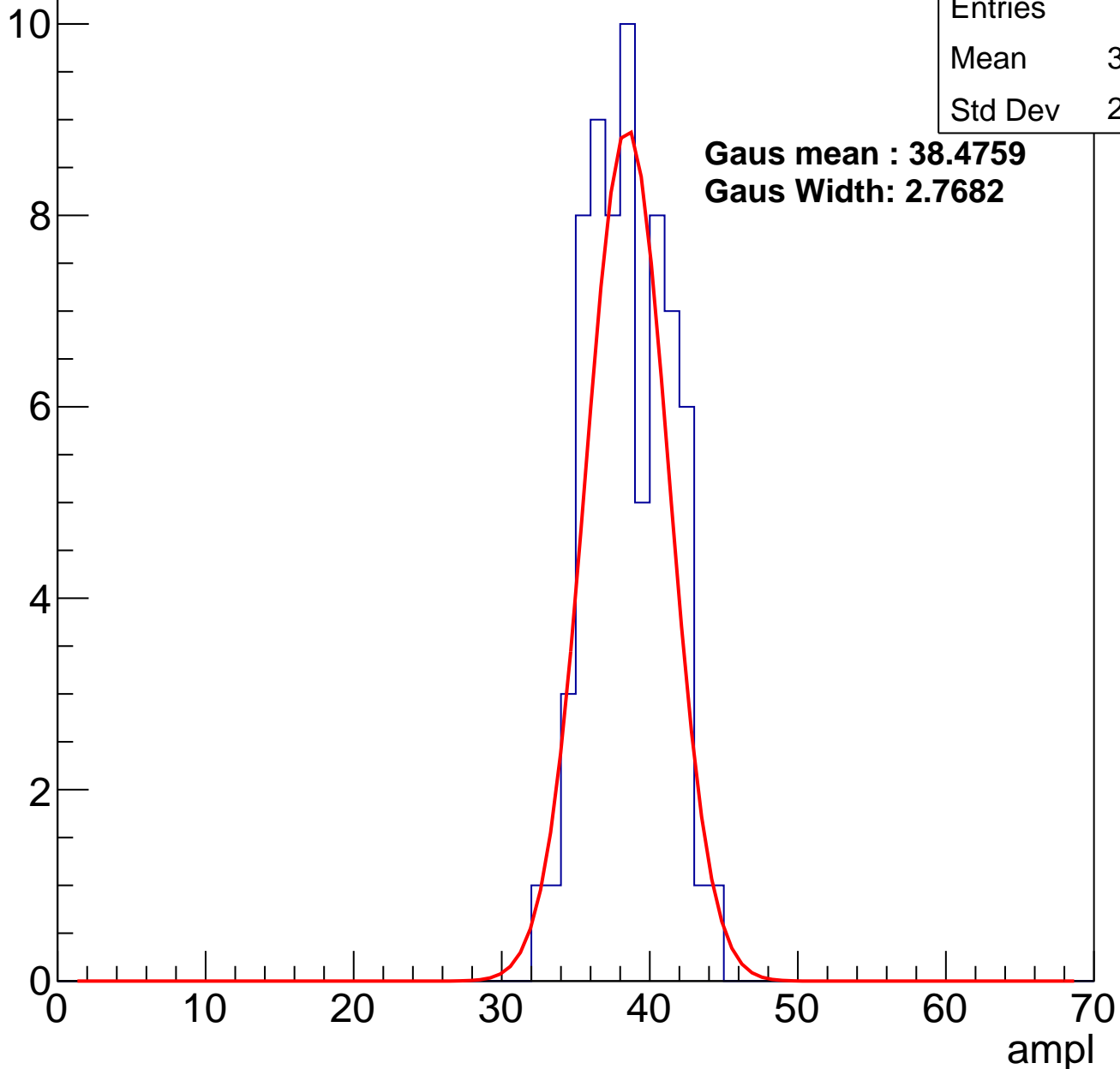
calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	38.06
Std Dev	2.662

Gaus mean : 38.4759

Gaus Width: 2.7682

Entry



B1L100S, U10-ch19, adc2

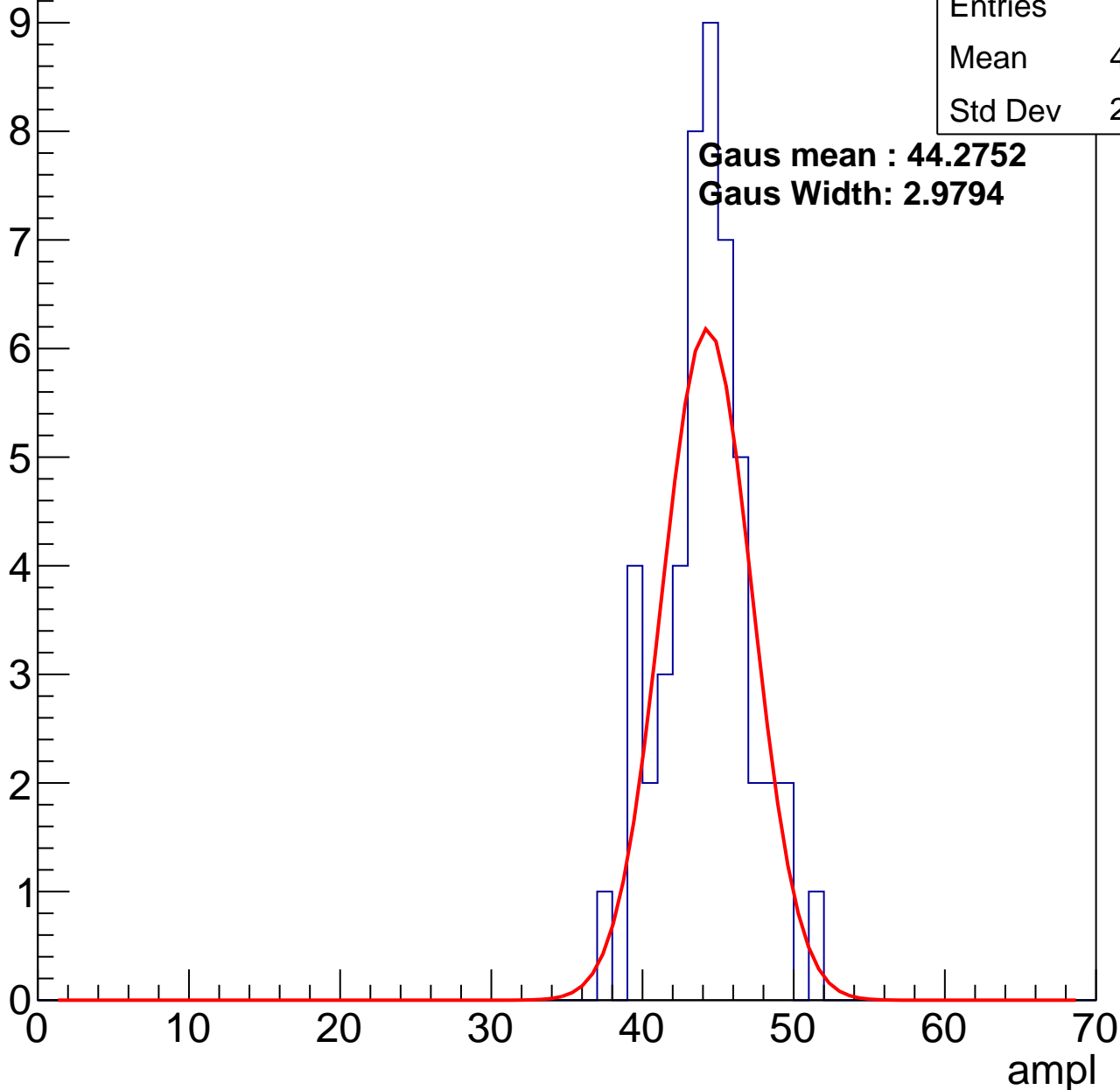
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	43.76
Std Dev	2.846

Gaus mean : 44.2752

Gaus Width: 2.9794



B1L100S, U10-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	94
Mean	51.09
Std Dev	3.616

Entry

10

8

6

4

2

0

0

10

20

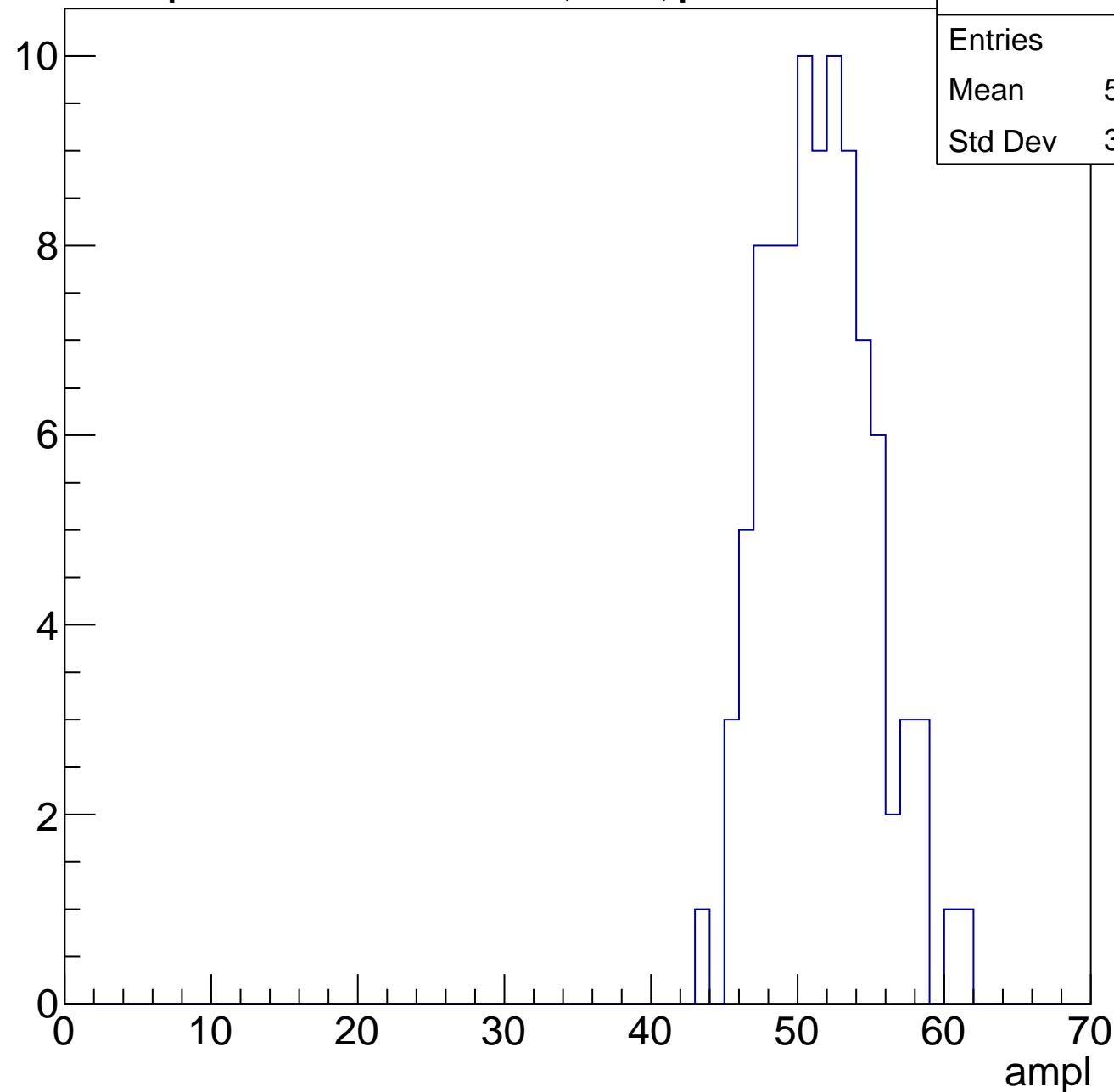
30

40

50

60

ampl

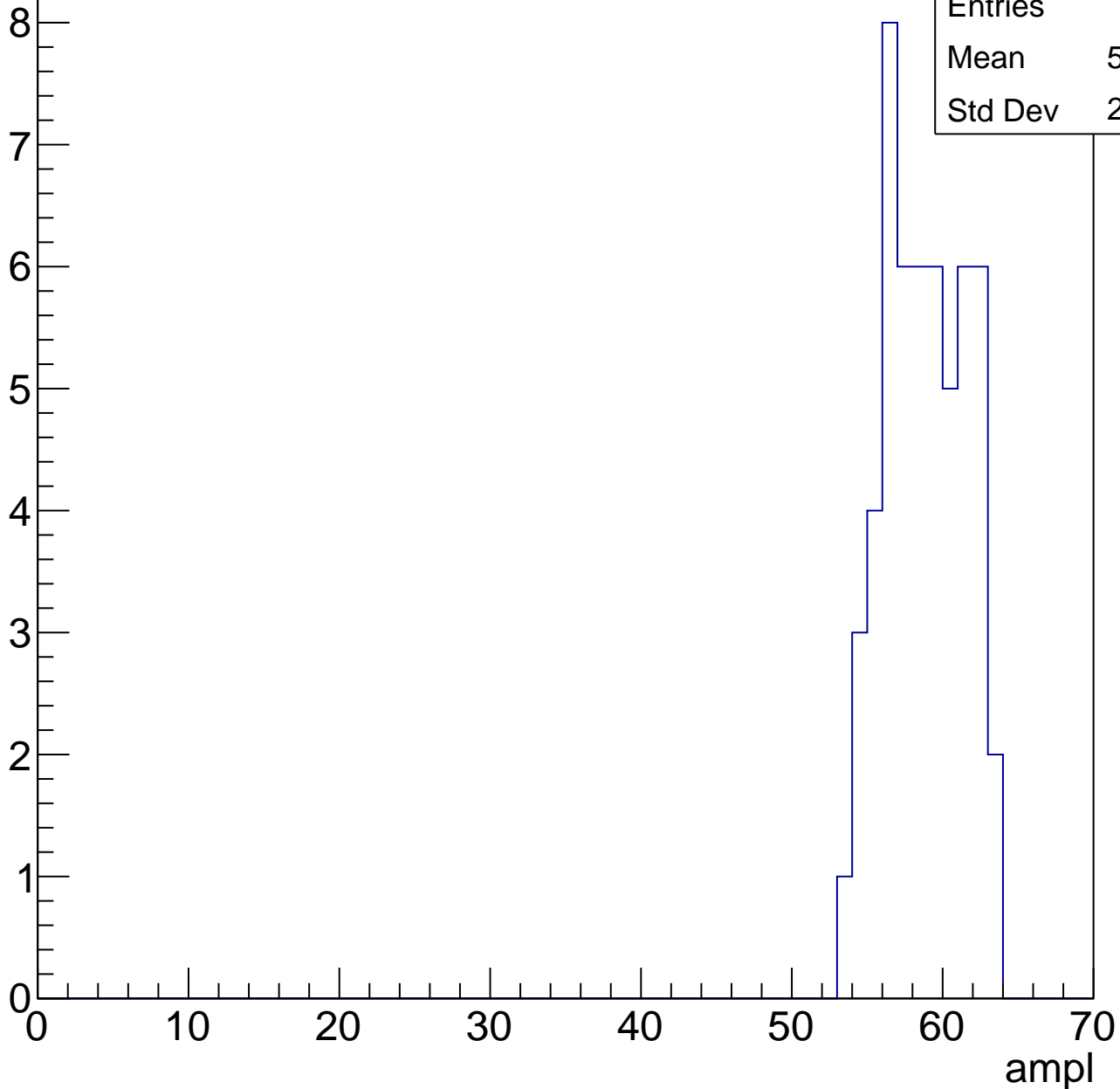


B1L100S, U10-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.32
Std Dev	2.633

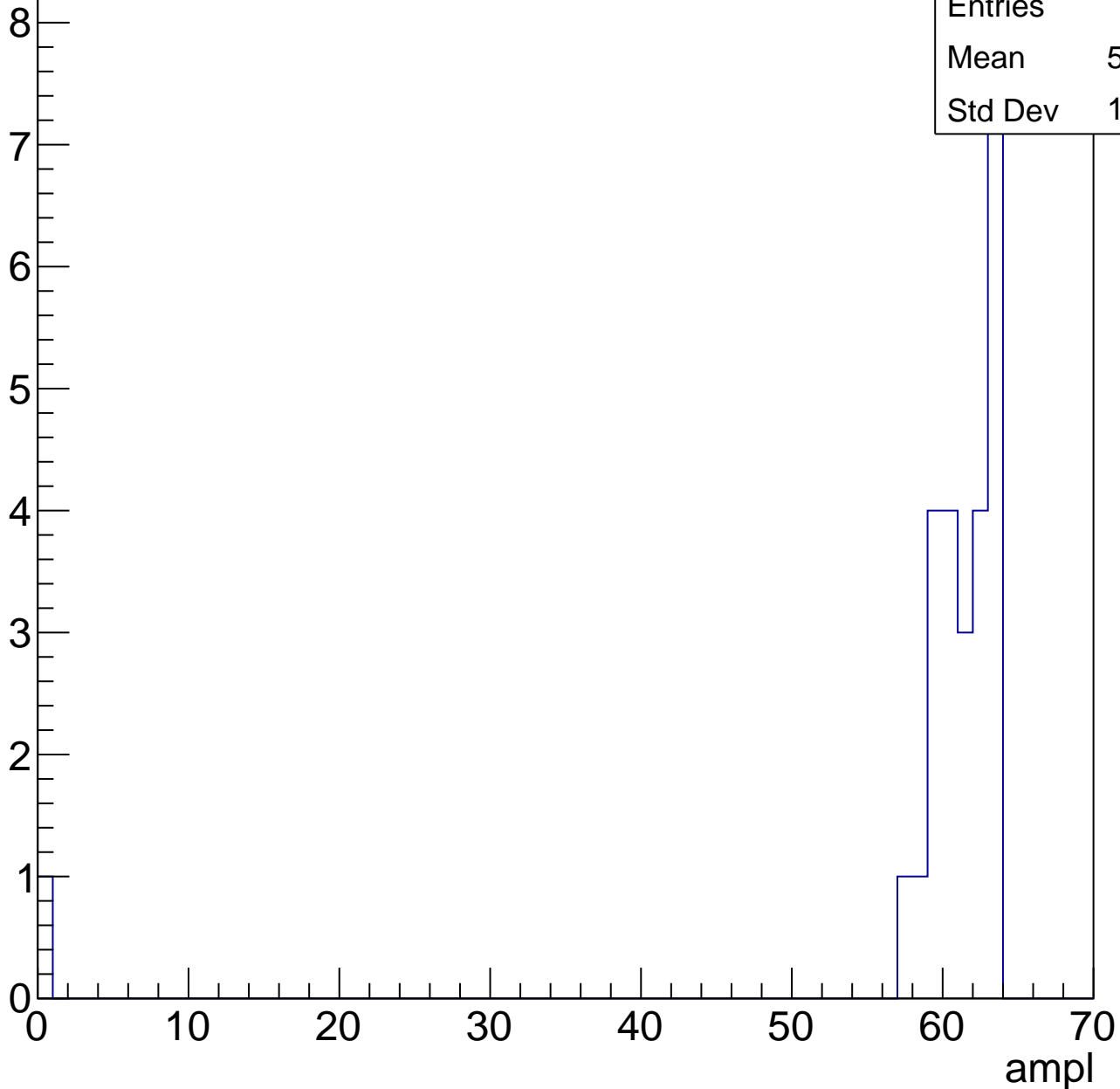


B1L100S, U10-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.69
Std Dev	11.87



B1L100S, U10-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch19, adc7

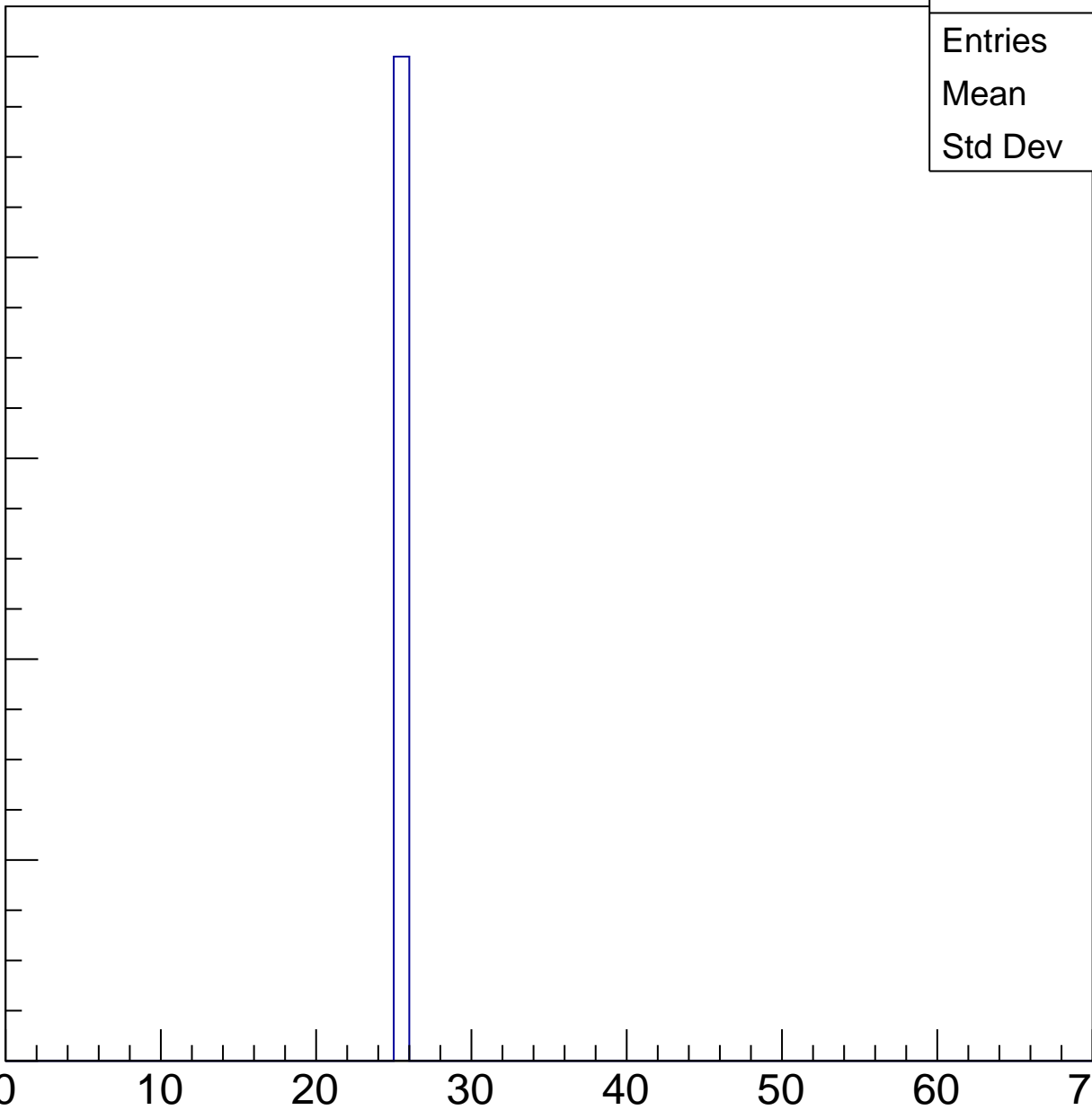
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L100S, U10-ch20, adc0

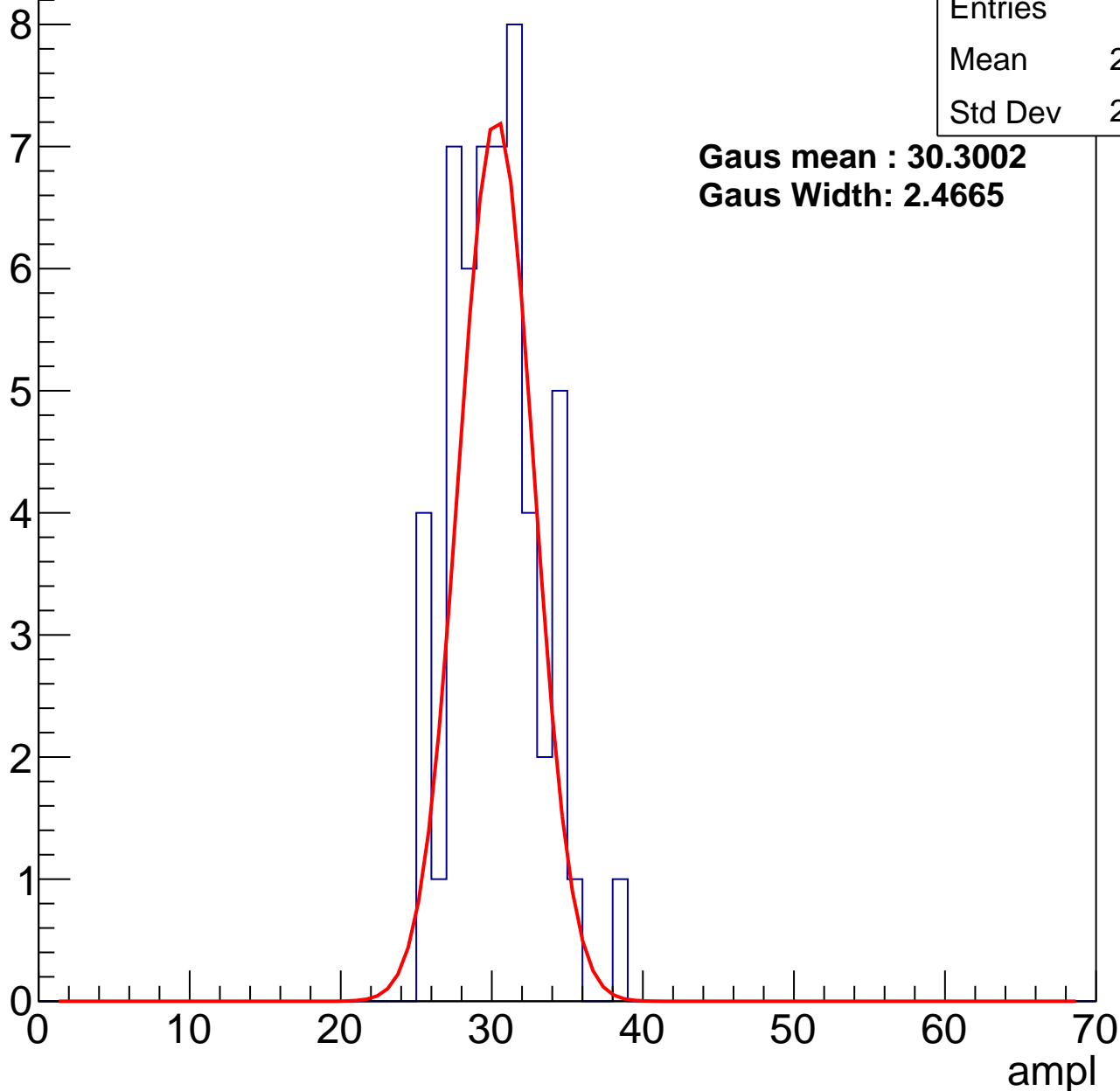
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	29.83
Std Dev	2.813

Gaus mean : 30.3002

Gaus Width: 2.4665



B1L100S, U10-ch20, adc1

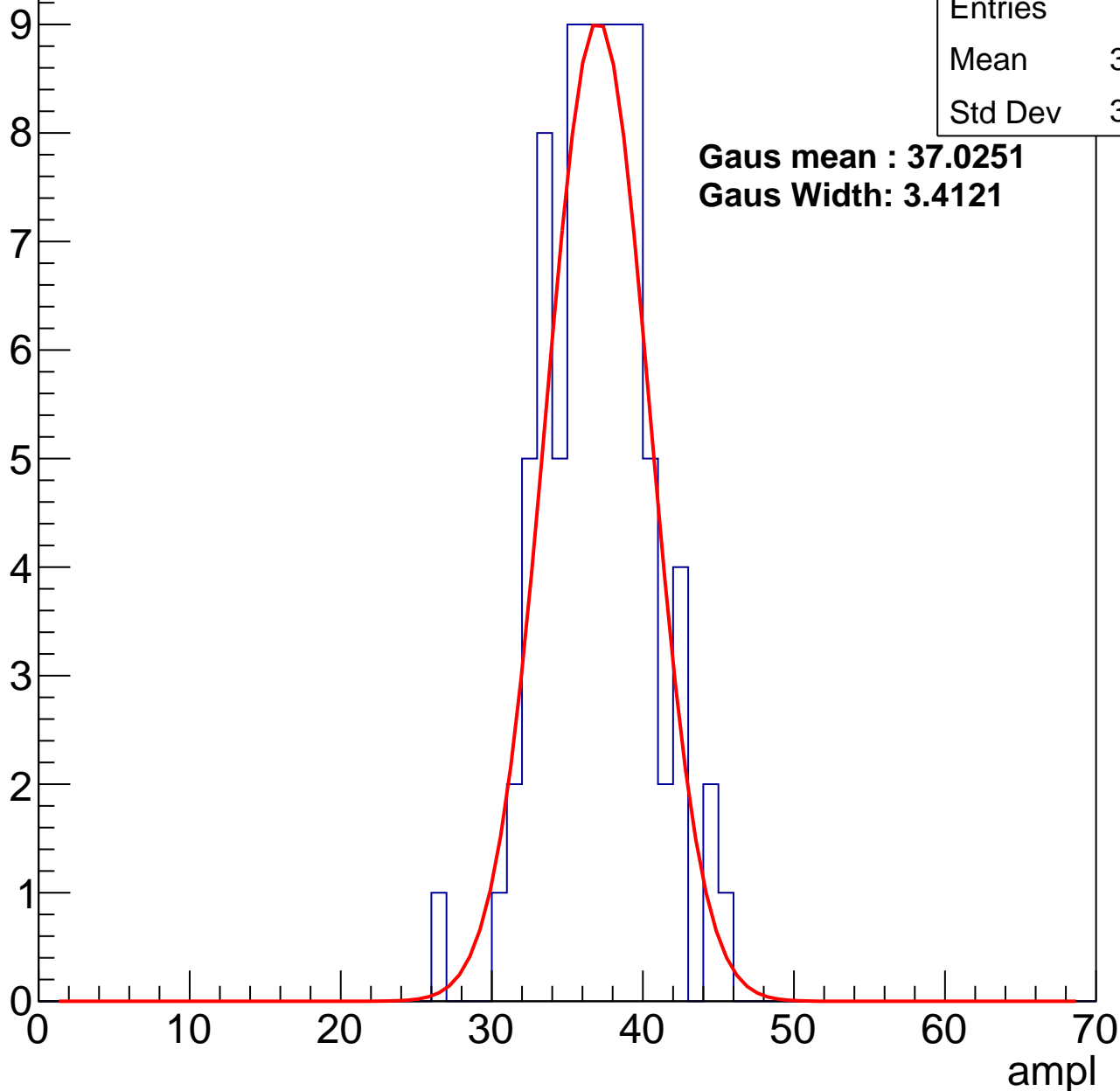
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.54
Std Dev	3.428

Gaus mean : 37.0251

Gaus Width: 3.4121



B1L100S, U10-ch20, adc2

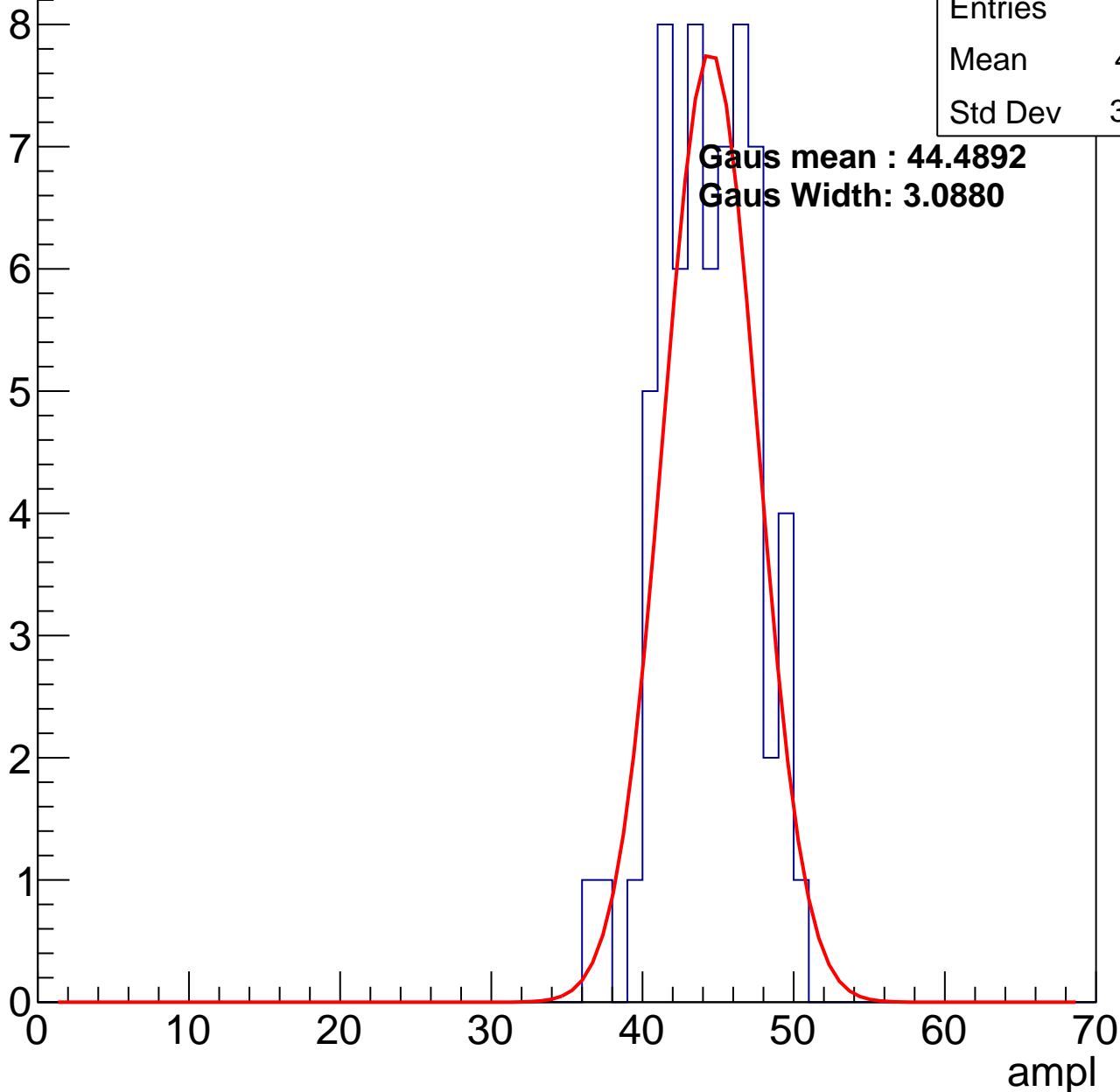
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.91
Std Dev	3.016

Gaus mean : 44.4892

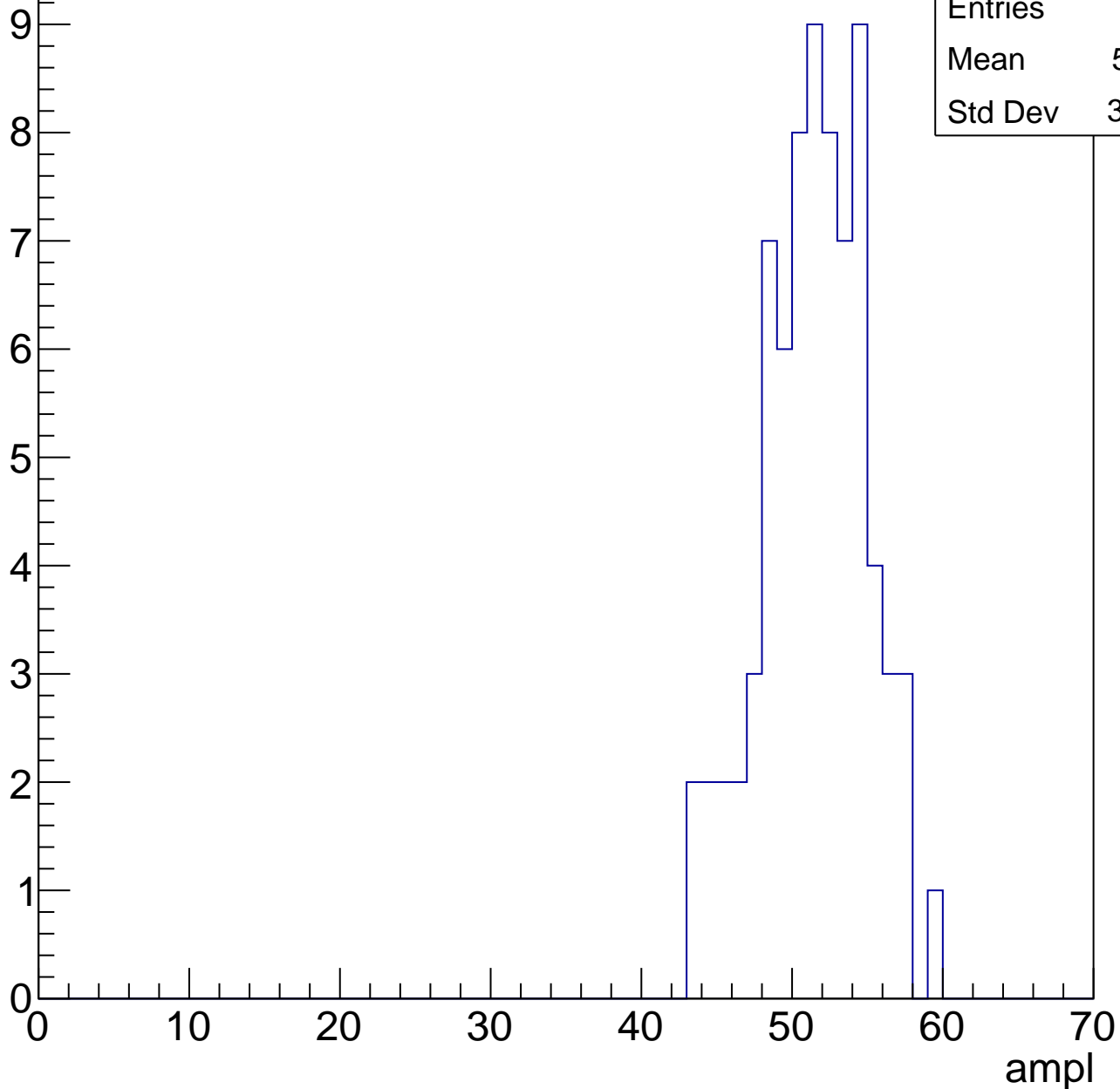
Gaus Width: 3.0880



B1L100S, U10-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

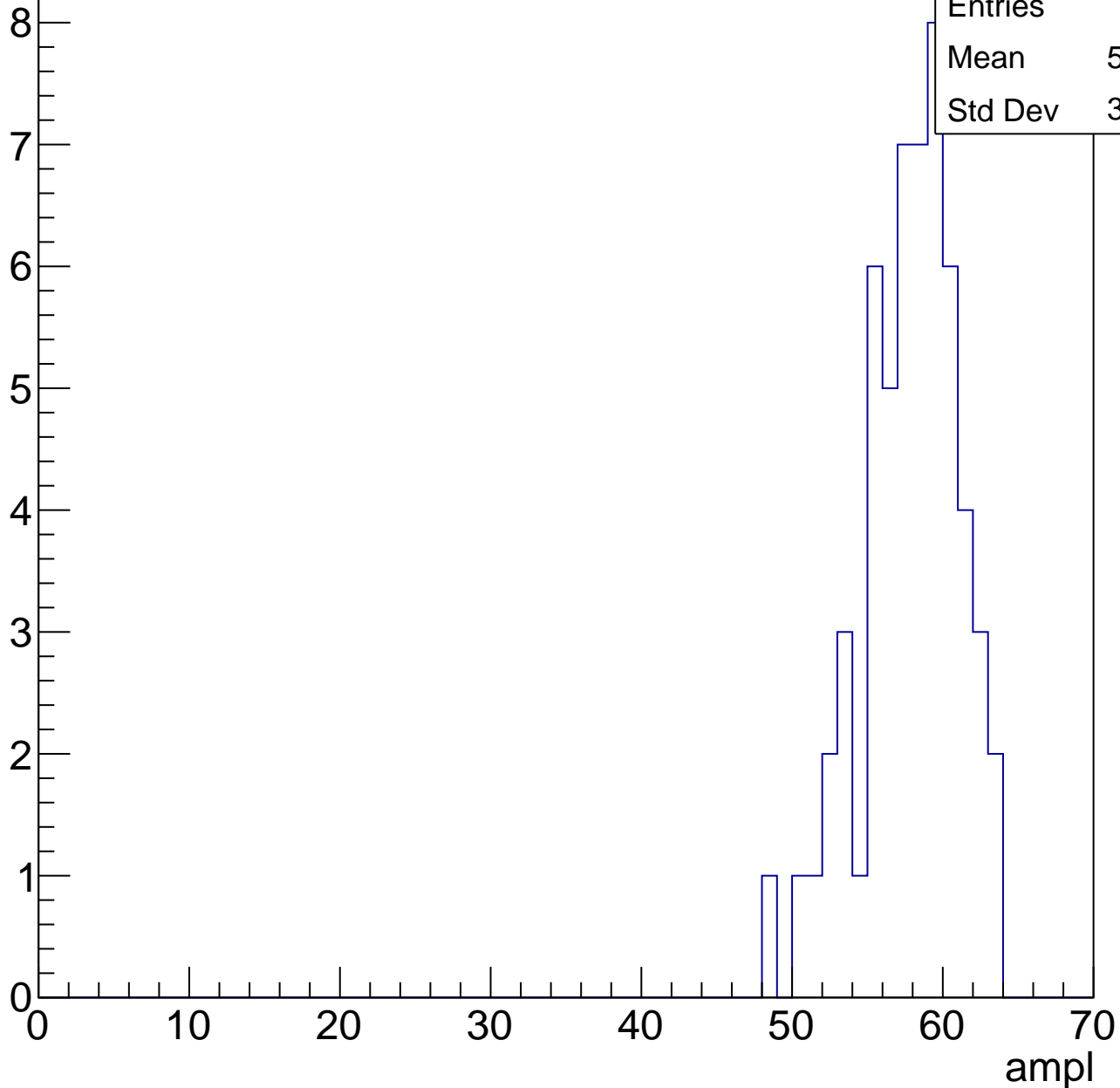


Entries	76
Mean	51.01
Std Dev	3.477

B1L100S, U10-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

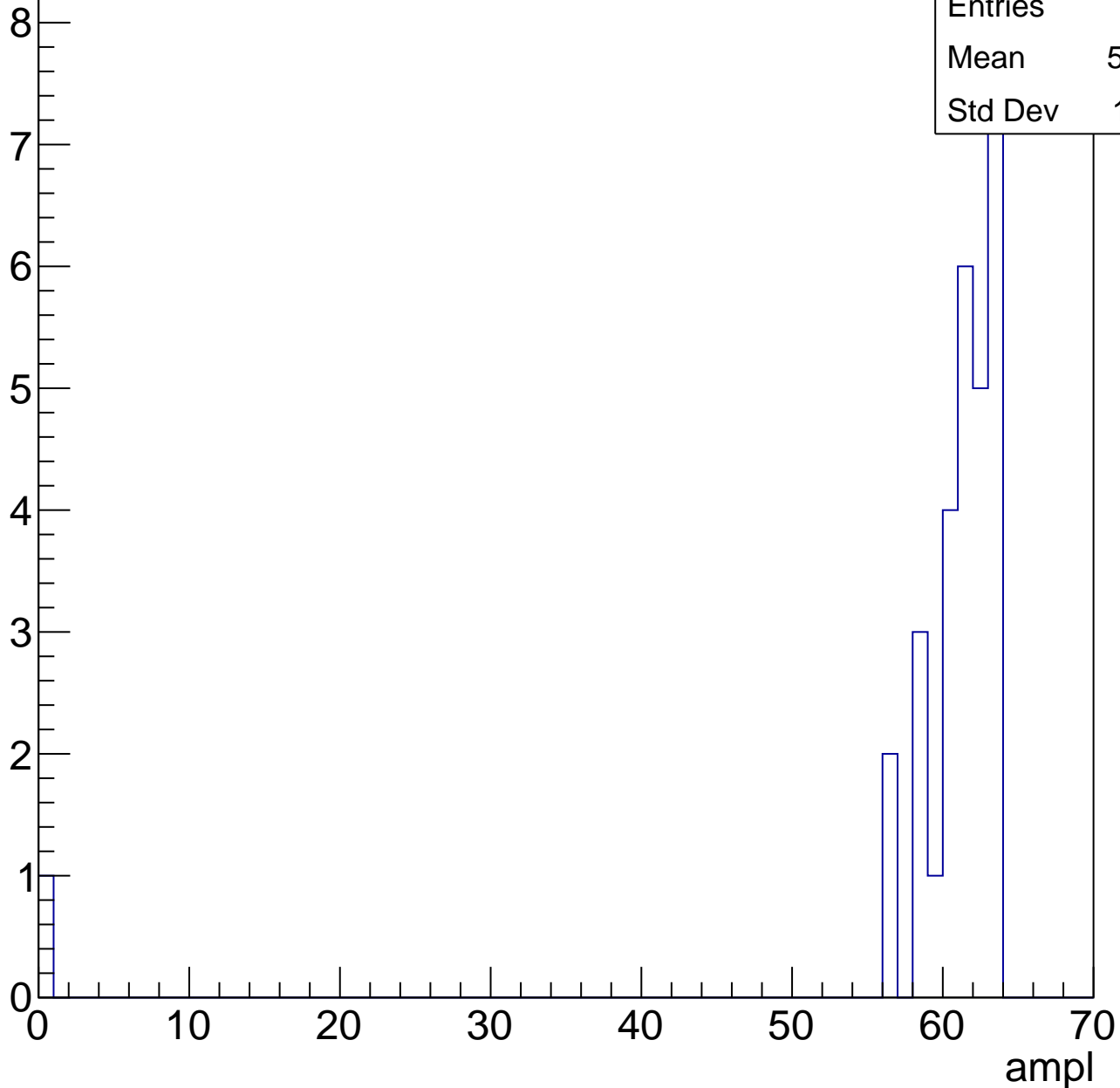


B1L100S, U10-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.83
Std Dev	11.11



B1L100S, U10-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

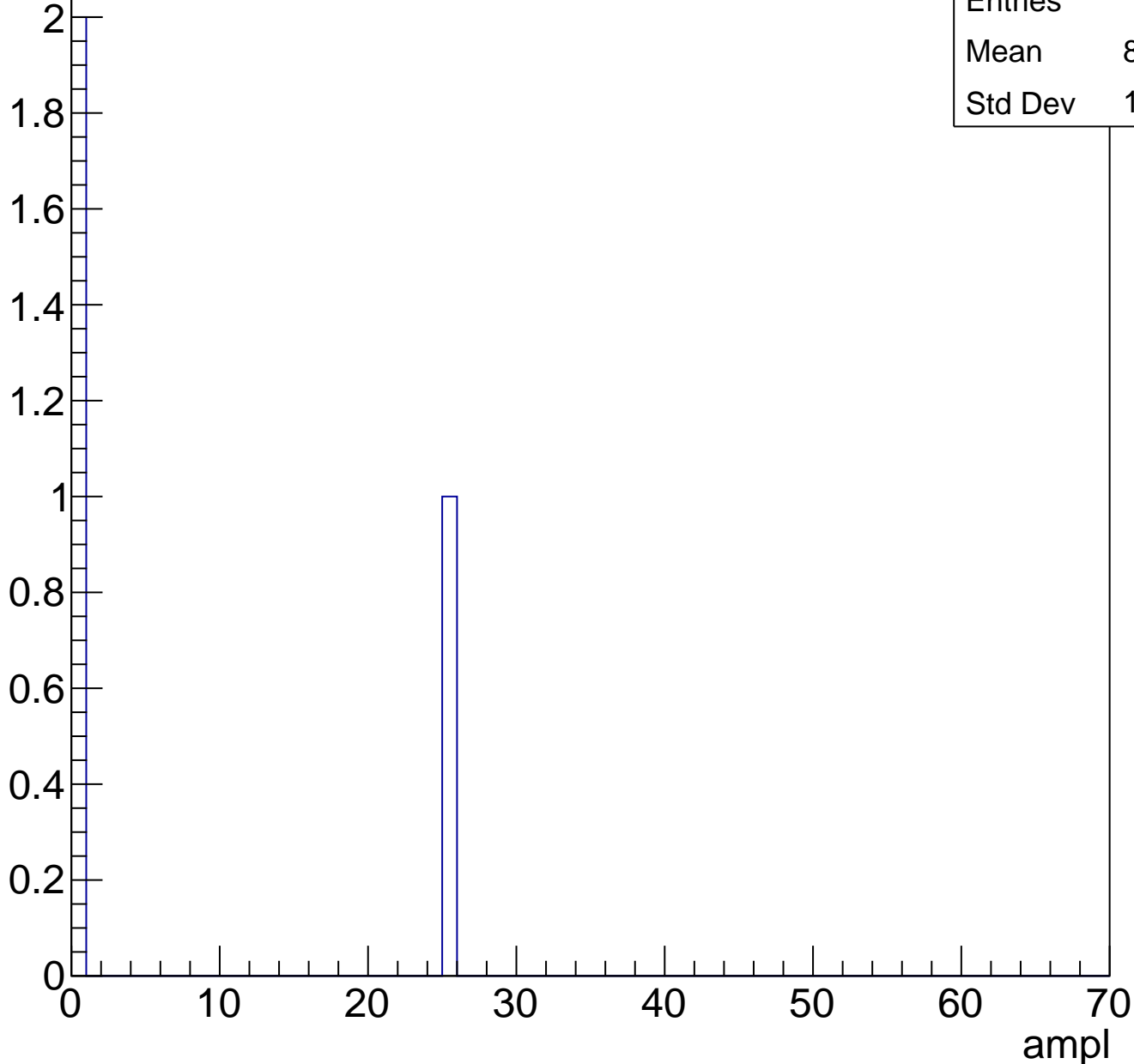
Entry



B1L100S, U10-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L100S, U10-ch21, adc0

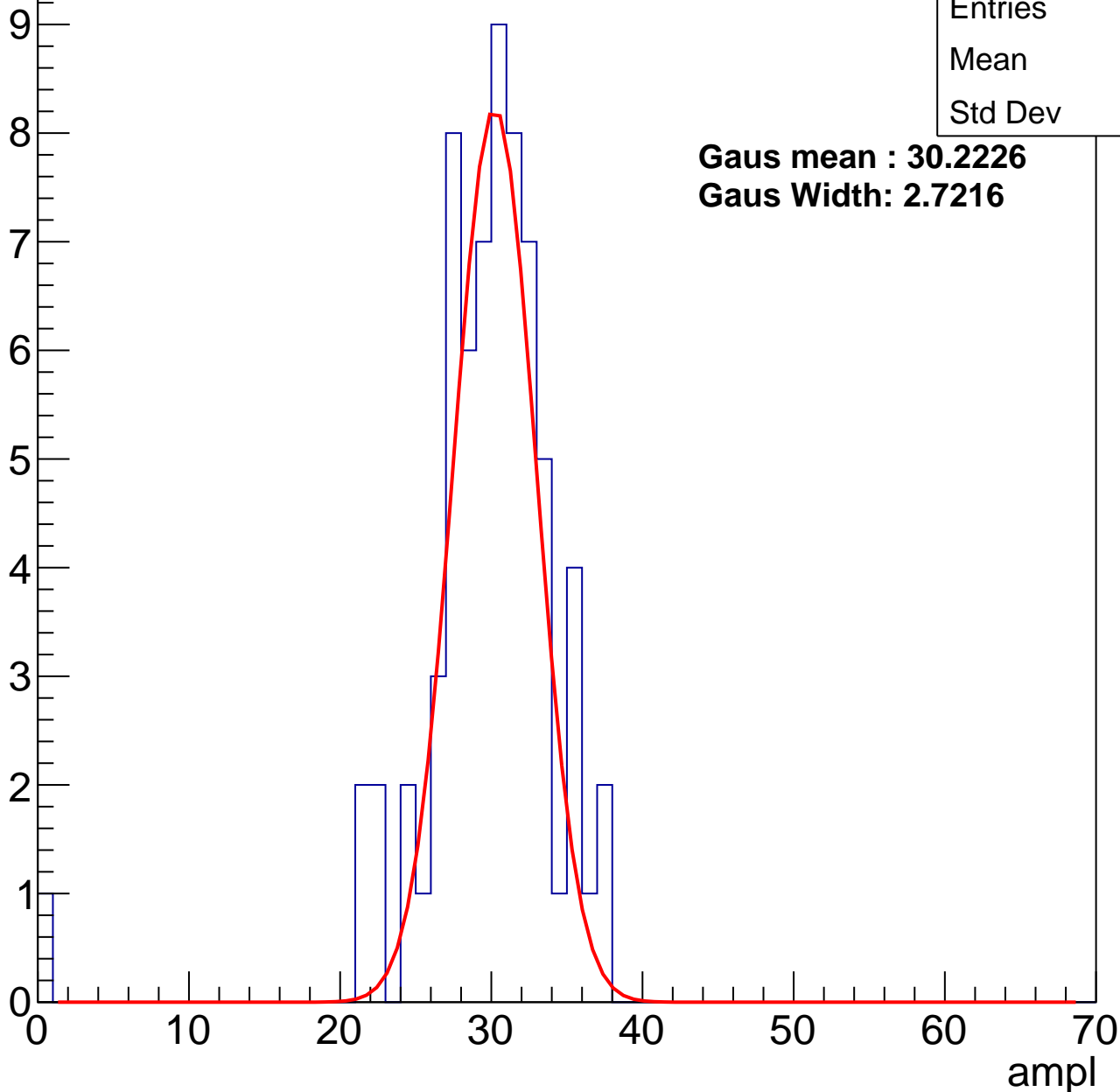
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.2
Std Dev	5

Gaus mean : 30.2226

Gaus Width: 2.7216



B1L100S, U10-ch21, adc1

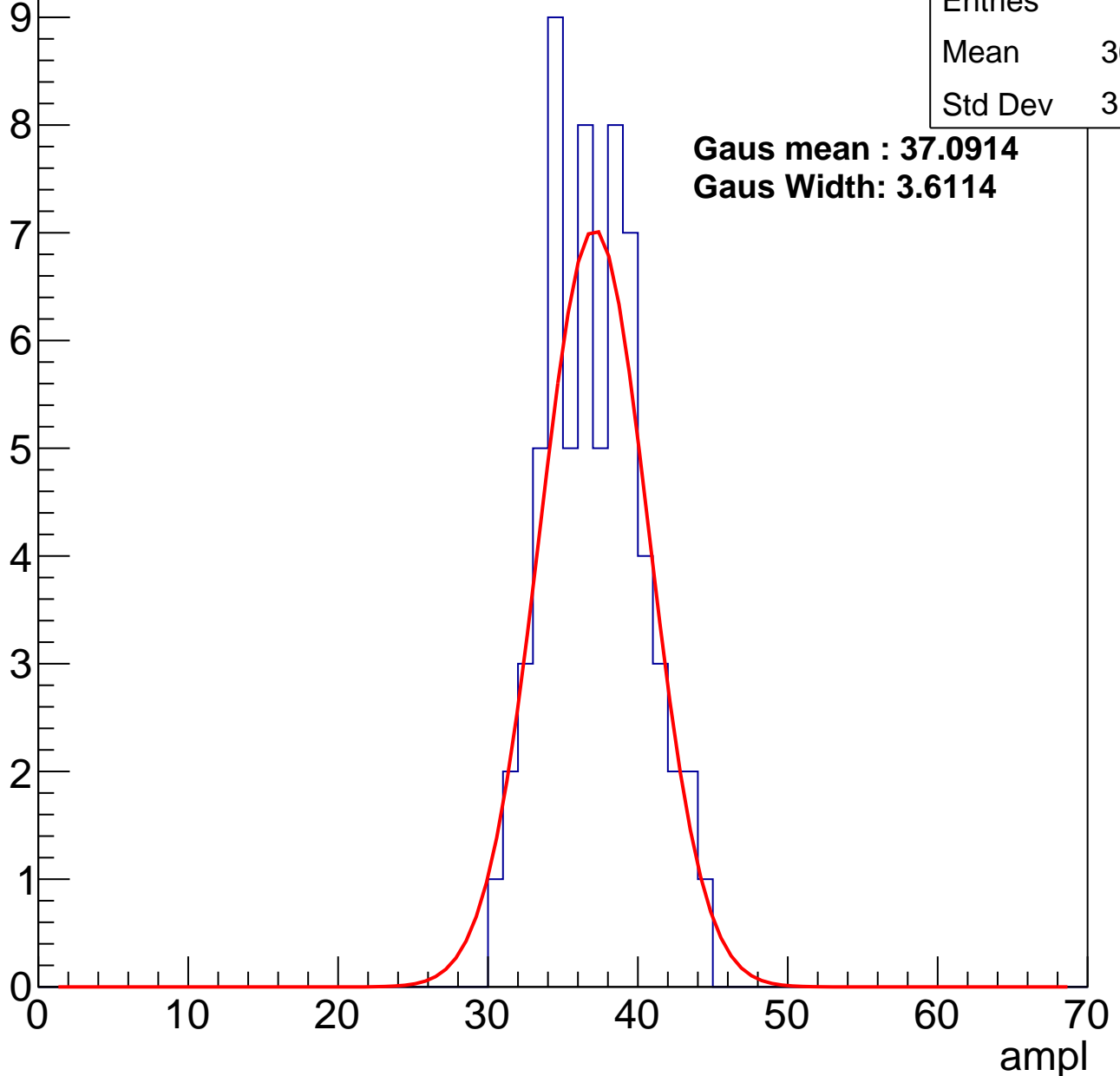
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	36.63
Std Dev	3.204

Gaus mean : 37.0914

Gaus Width: 3.6114



B1L100S, U10-ch21, adc2

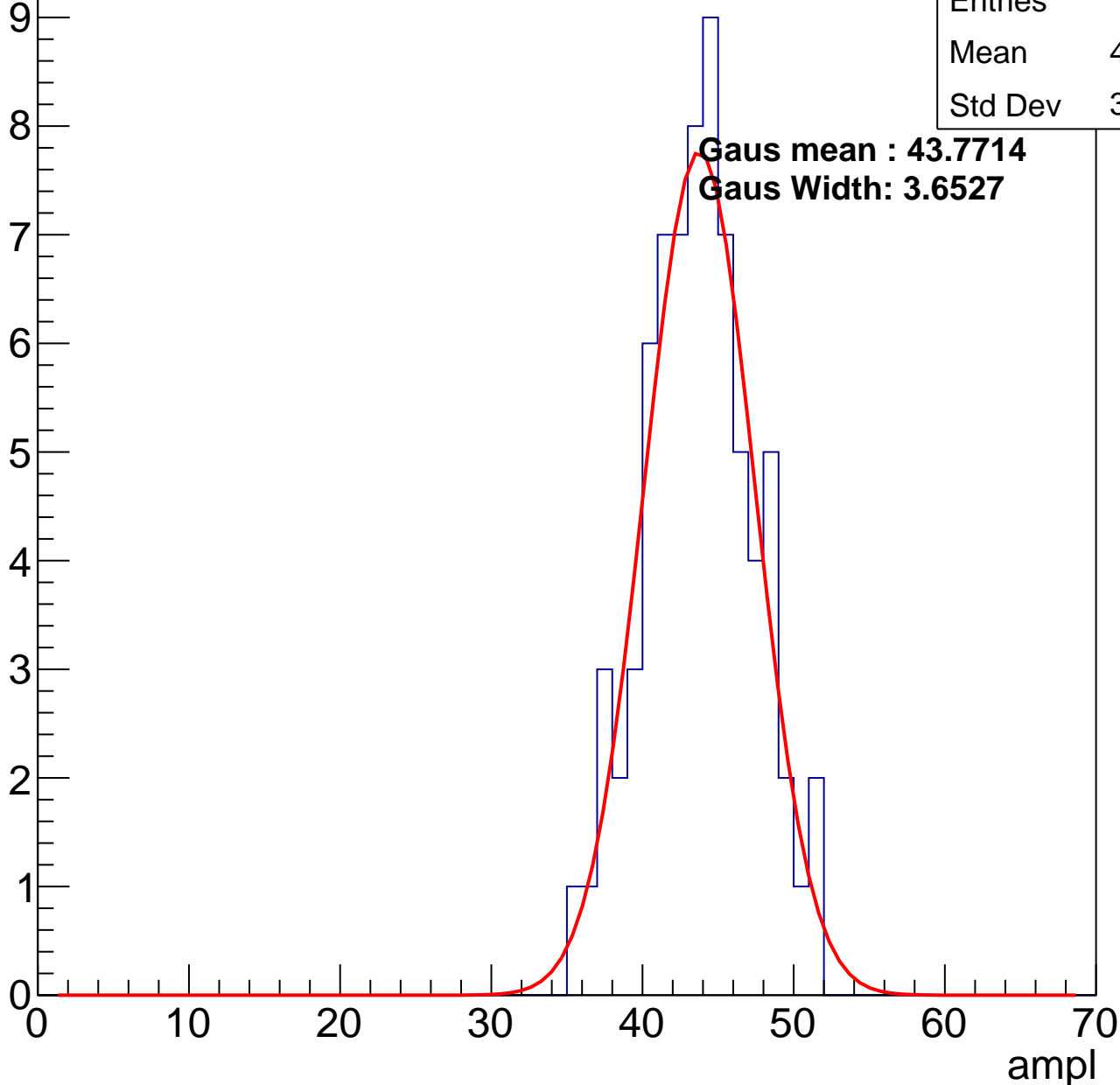
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	43.27
Std Dev	3.555

Gaus mean : 43.7714

Gaus Width: 3.6527

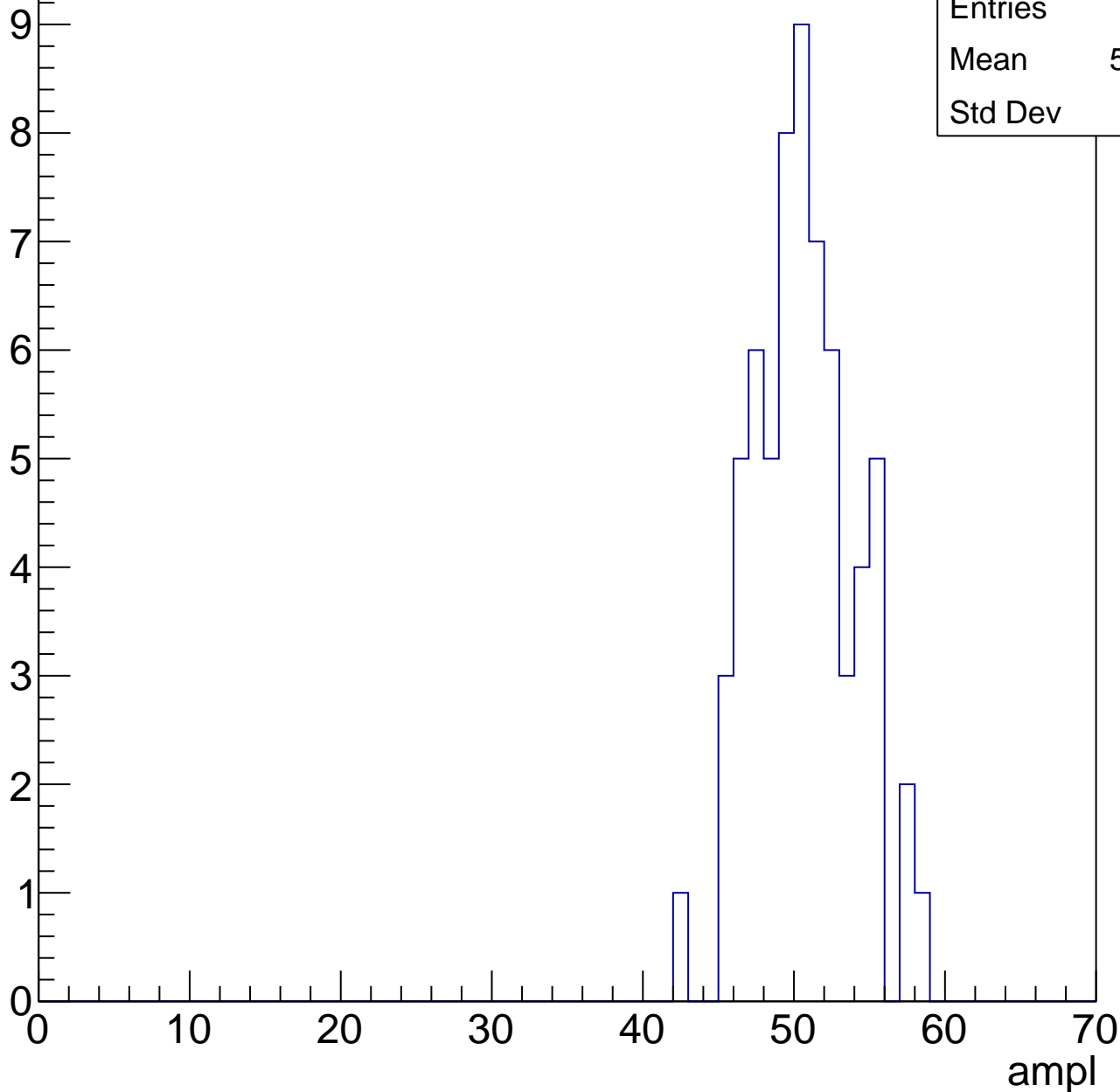


B1L100S, U10-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.18
Std Dev	3.3

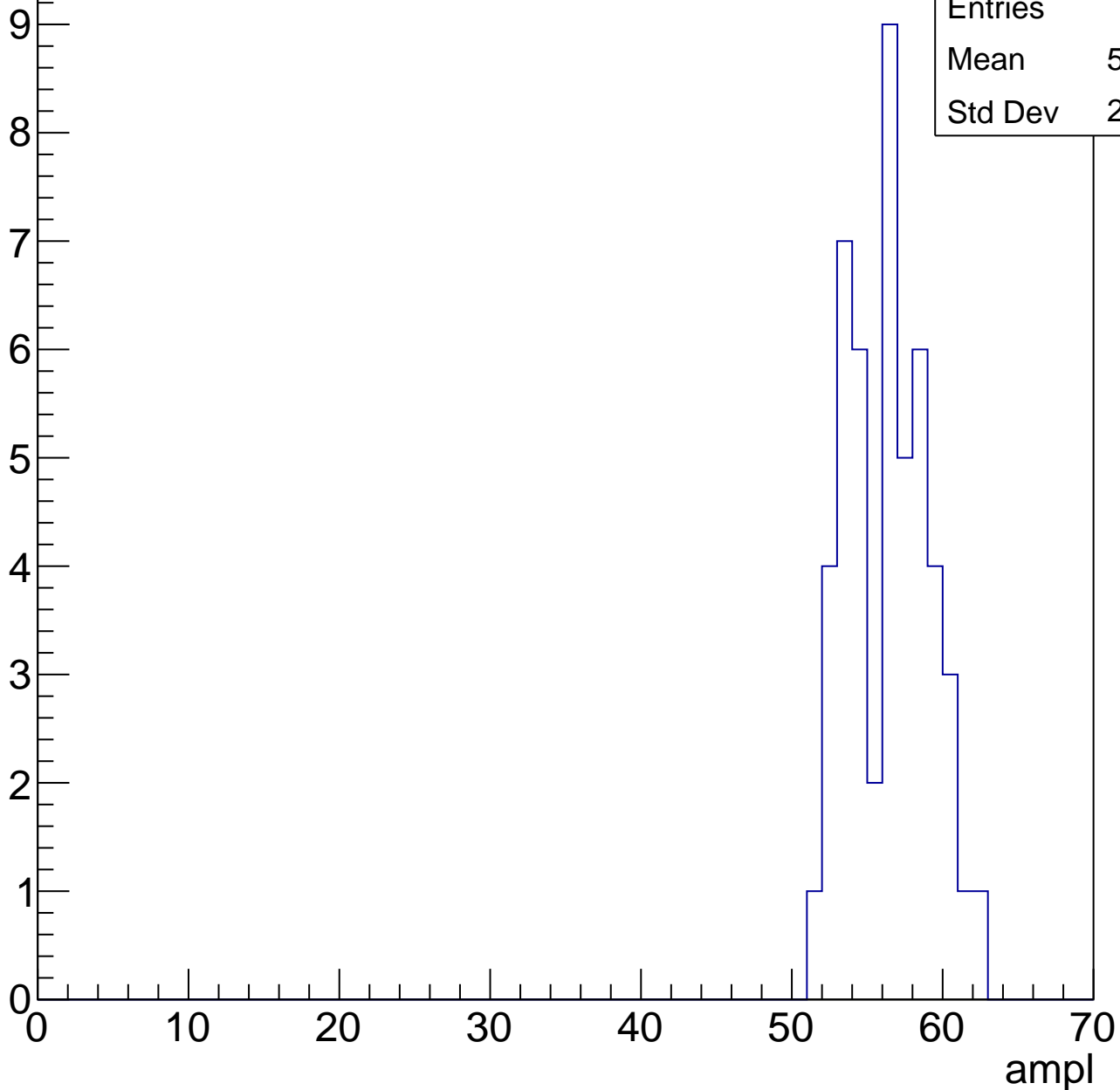


B1L100S, U10-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	55.92
Std Dev	2.679



B1L100S, U10-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 51

Mean 59.12

Std Dev 8.663

8

6

4

2

0

0

10

20

30

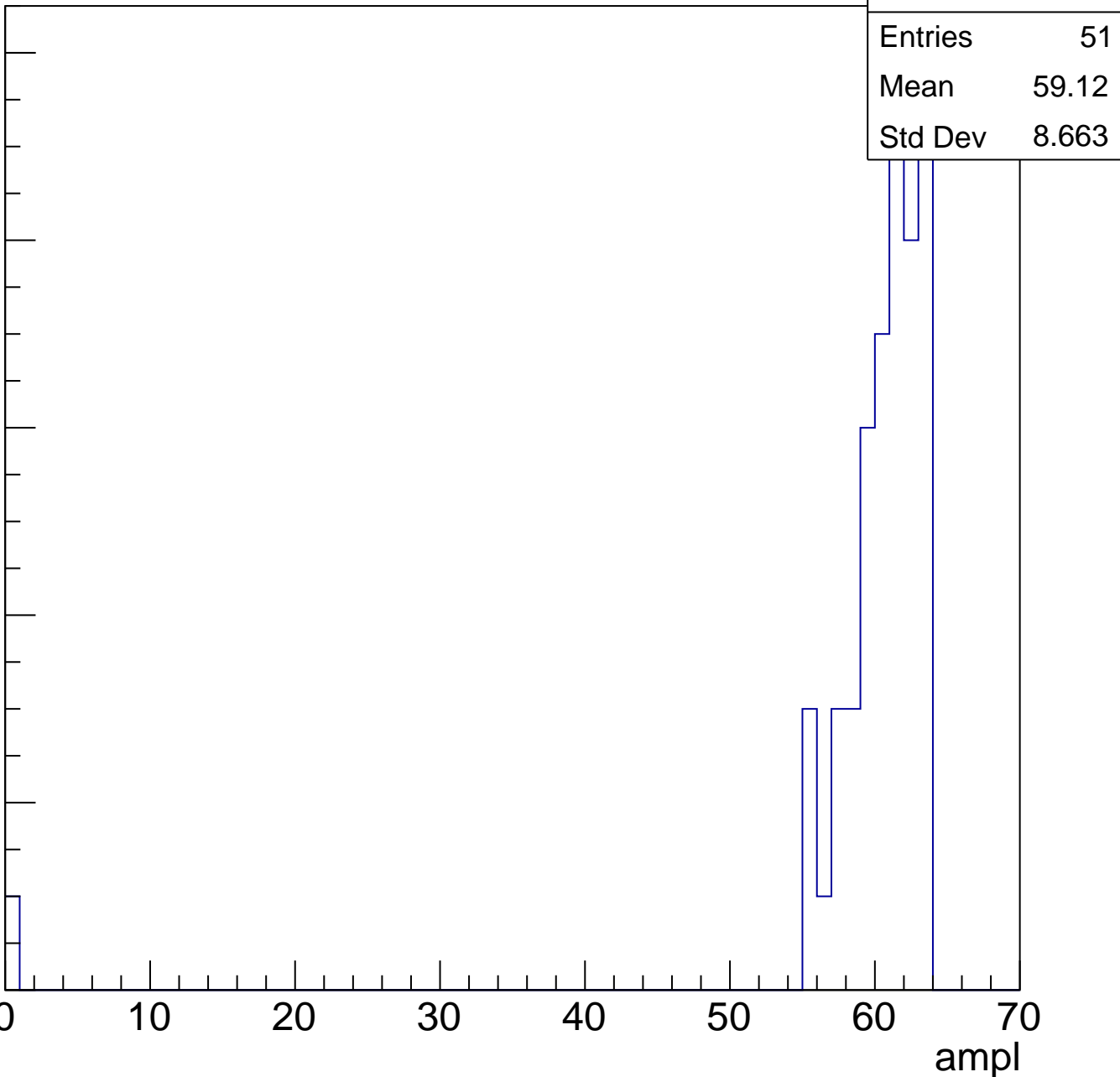
40

50

60

70

ampl



B1L100S, U10-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

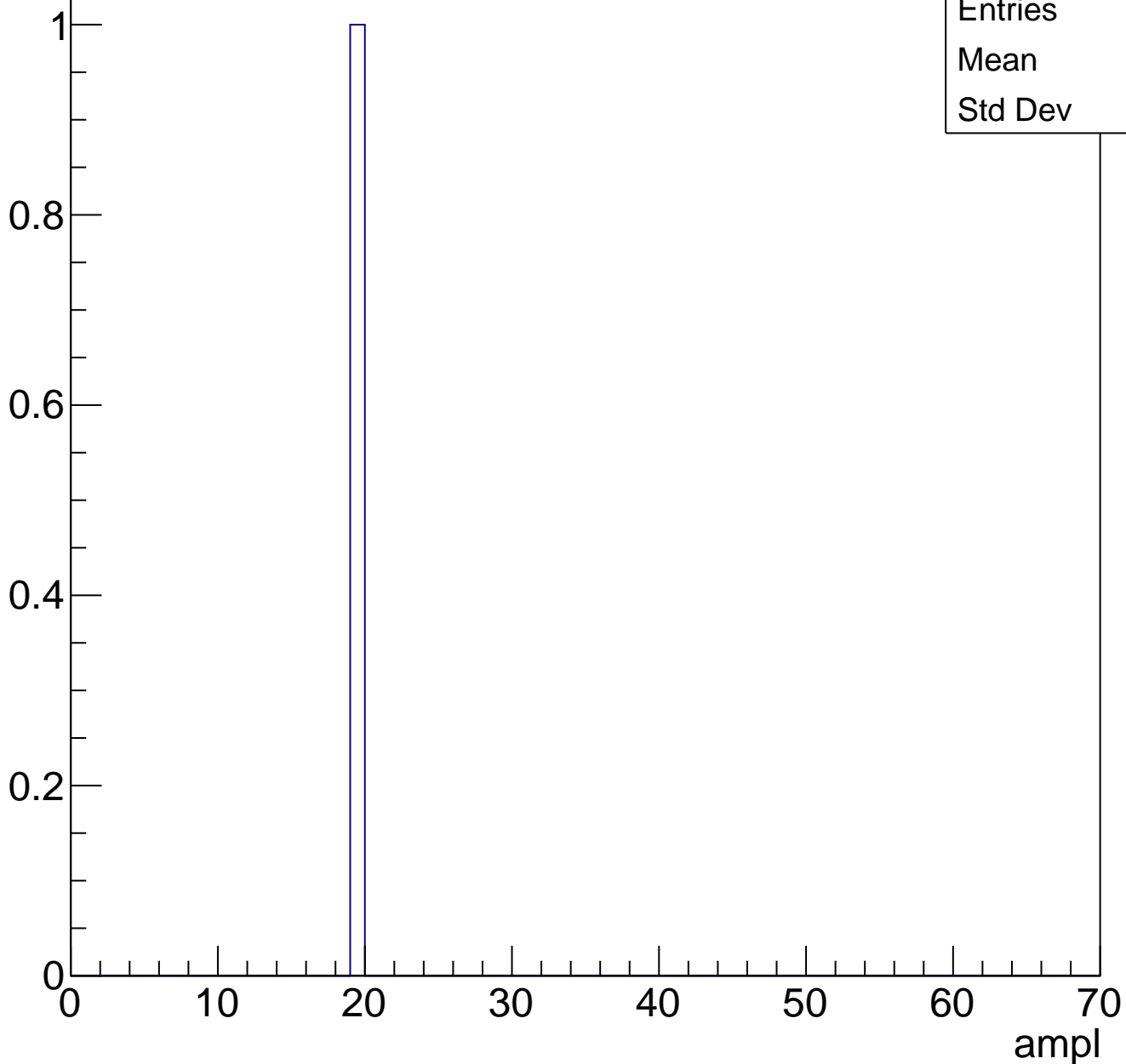


Entries	1
Mean	62
Std Dev	0

B1L100S, U10-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	19
Std Dev	0

B1L100S, U10-ch22, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	30.51
Std Dev	3.167

Gaus mean : 31.2952

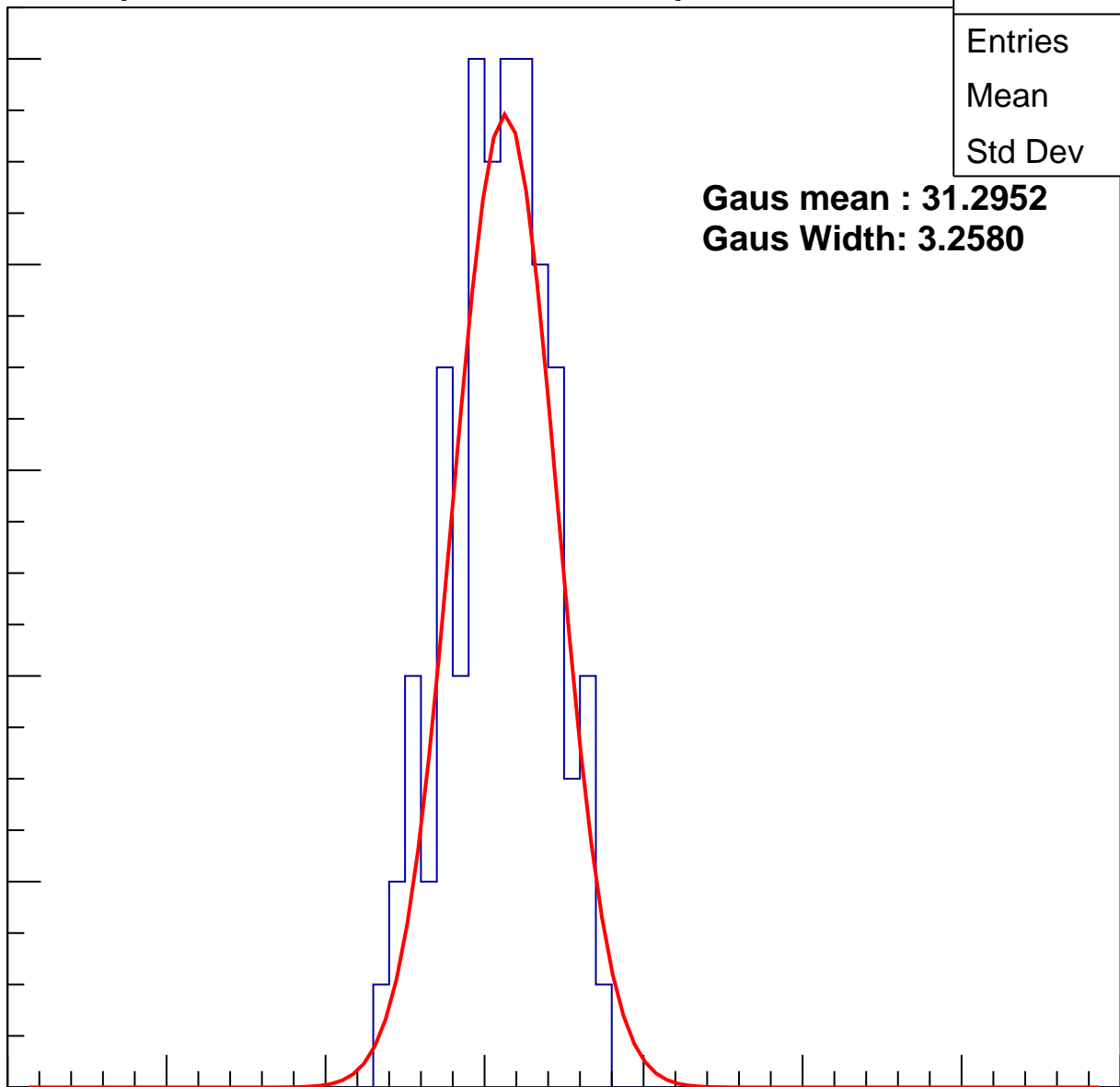
Gaus Width: 3.2580

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch22, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	55
Mean	37.8
Std Dev	2.895

Gaus mean : 38.0879

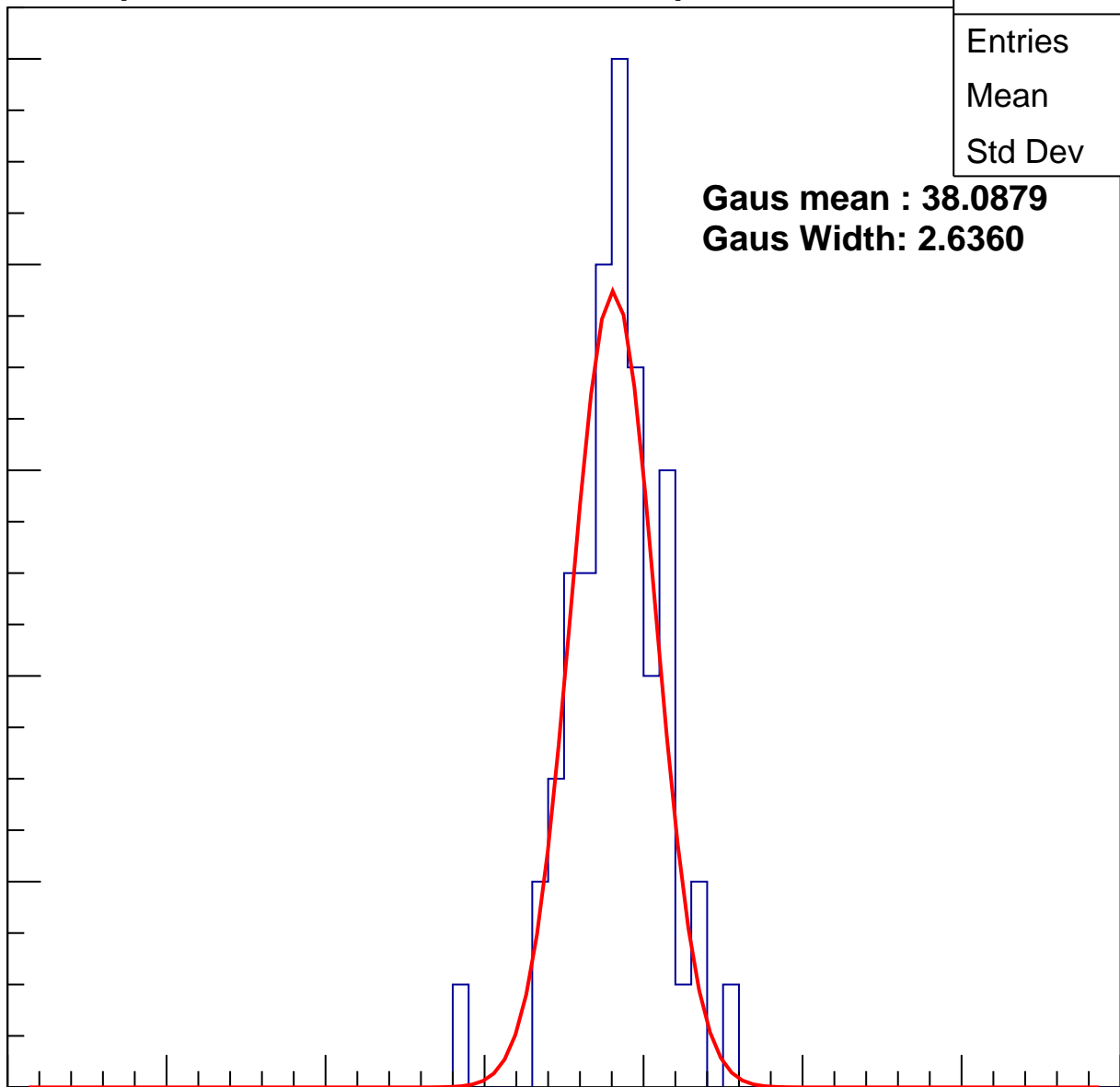
Gaus Width: 2.6360

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch22, adc2

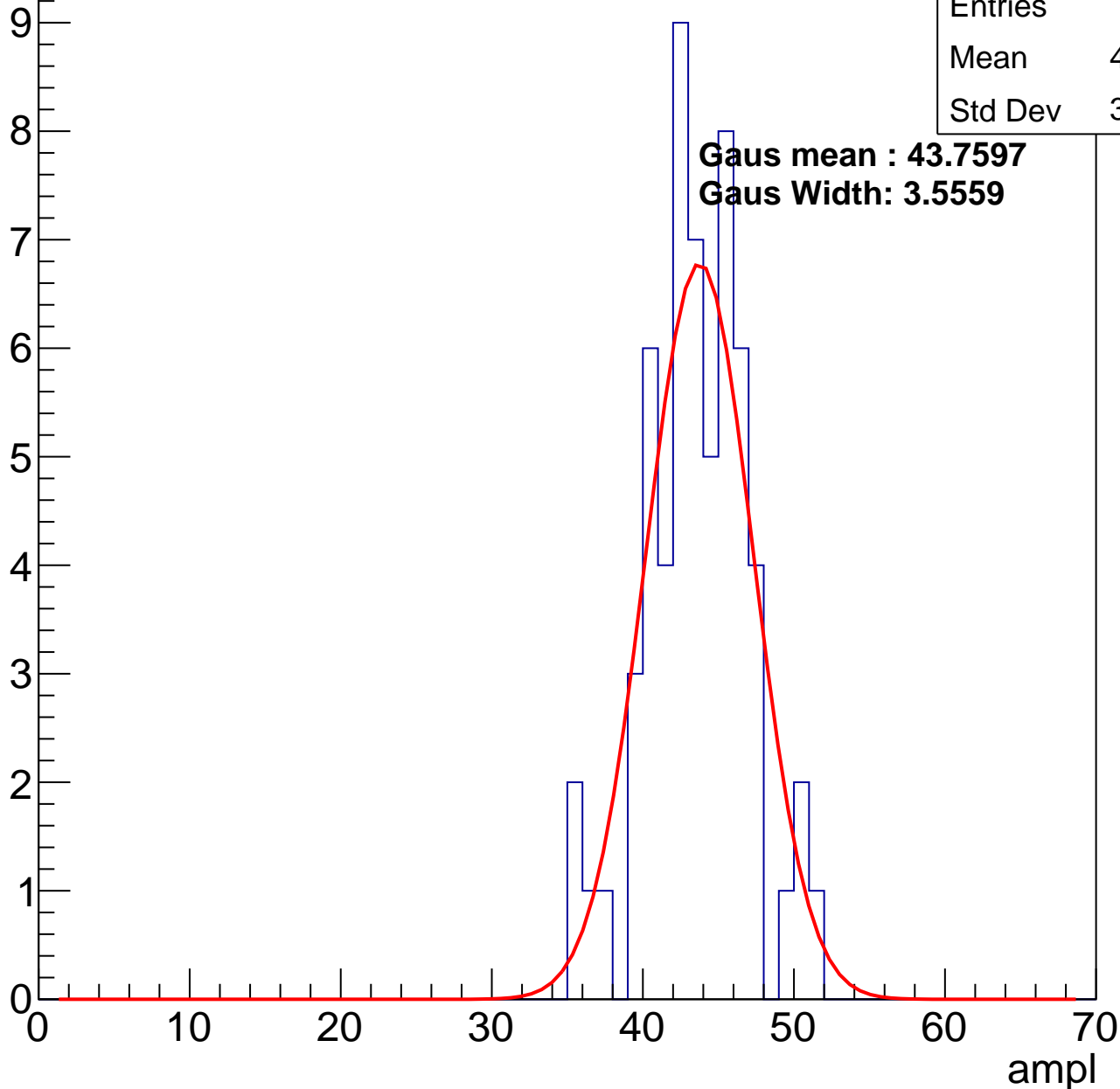
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.12
Std Dev	3.397

Gaus mean : 43.7597

Gaus Width: 3.5559



B1L100S, U10-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	50.35
Std Dev	3.563

Entry

10

8

6

4

2

0

0

10

20

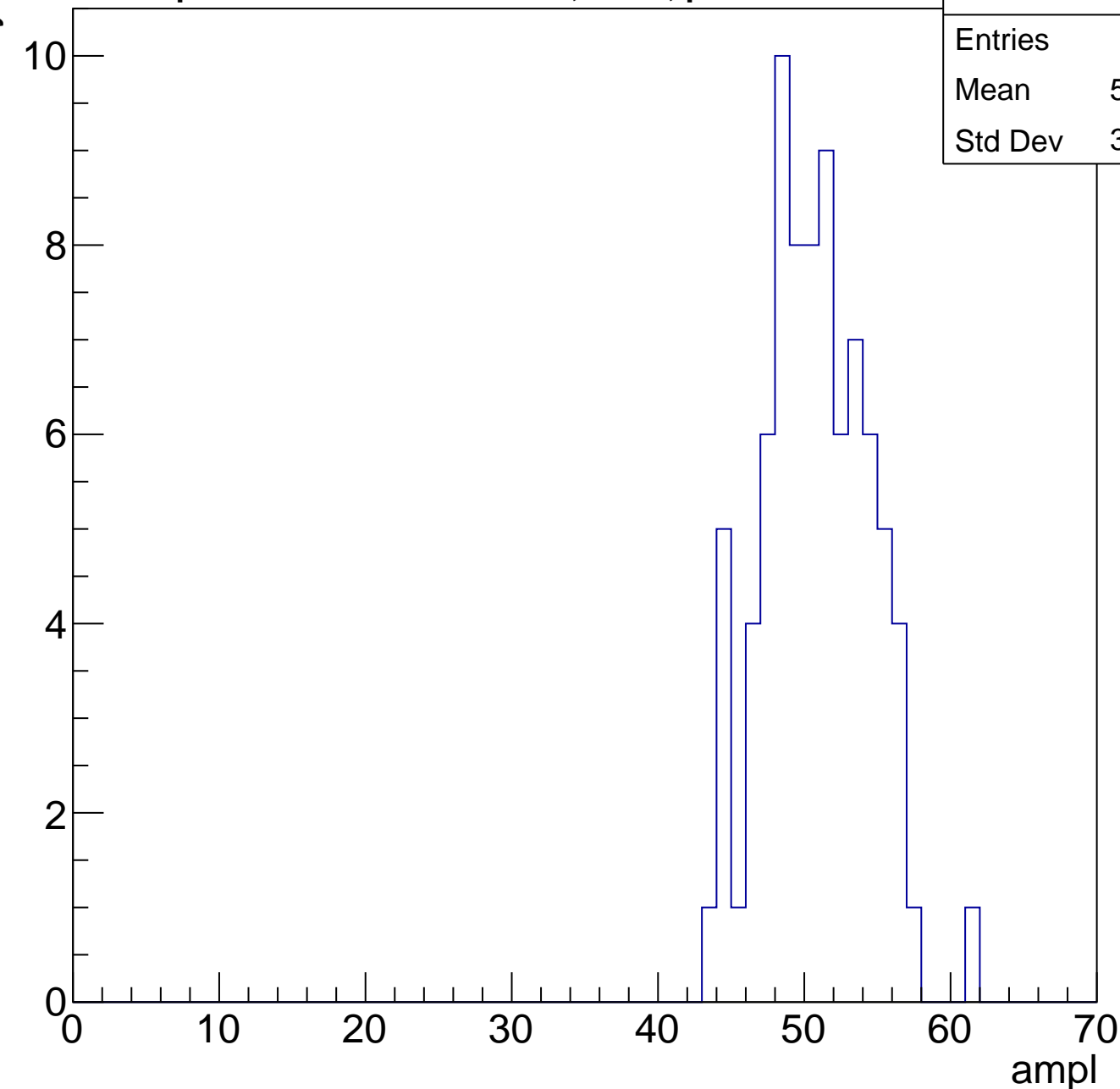
30

40

50

60

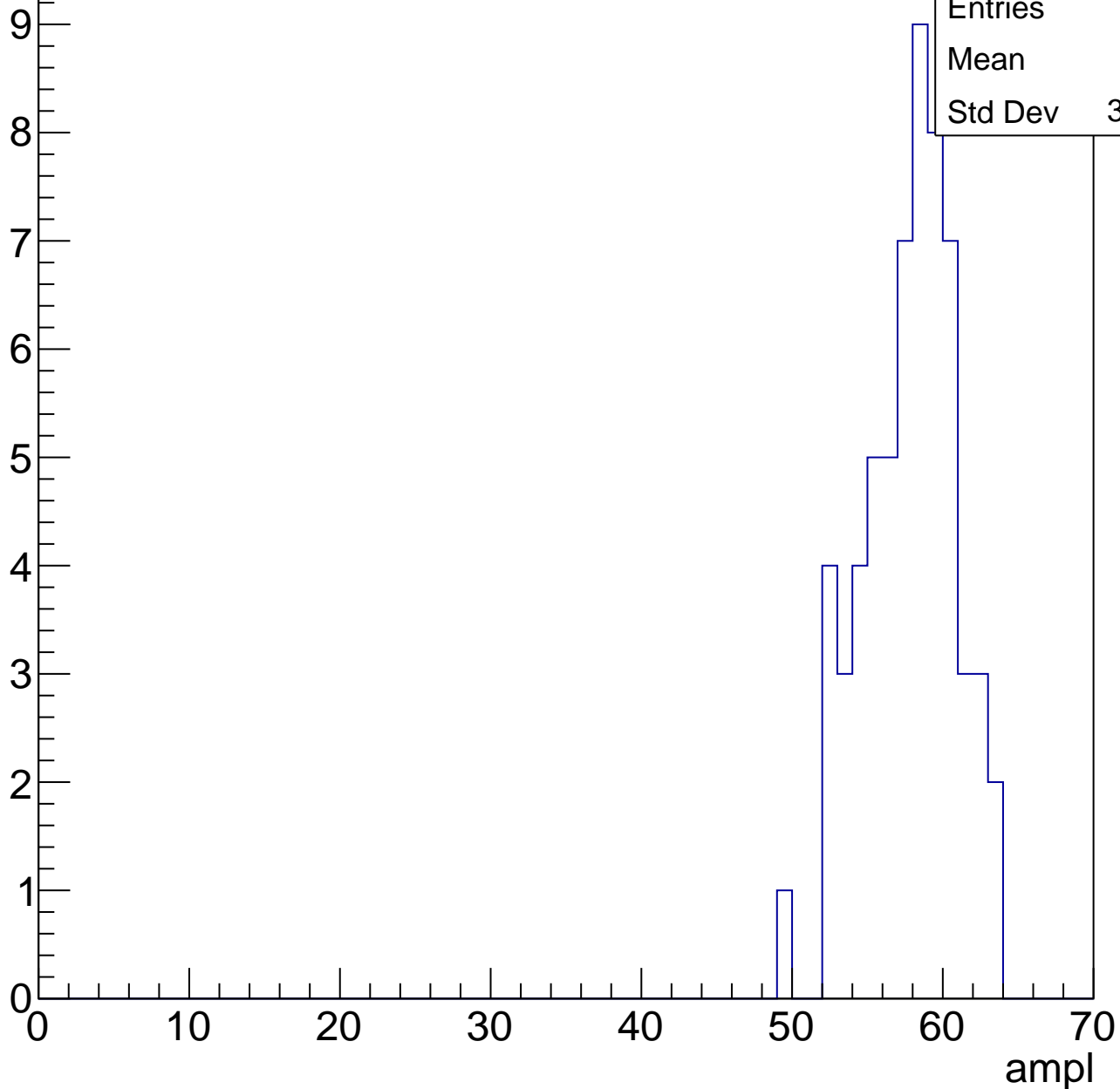
ampl



B1L100S, U10-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

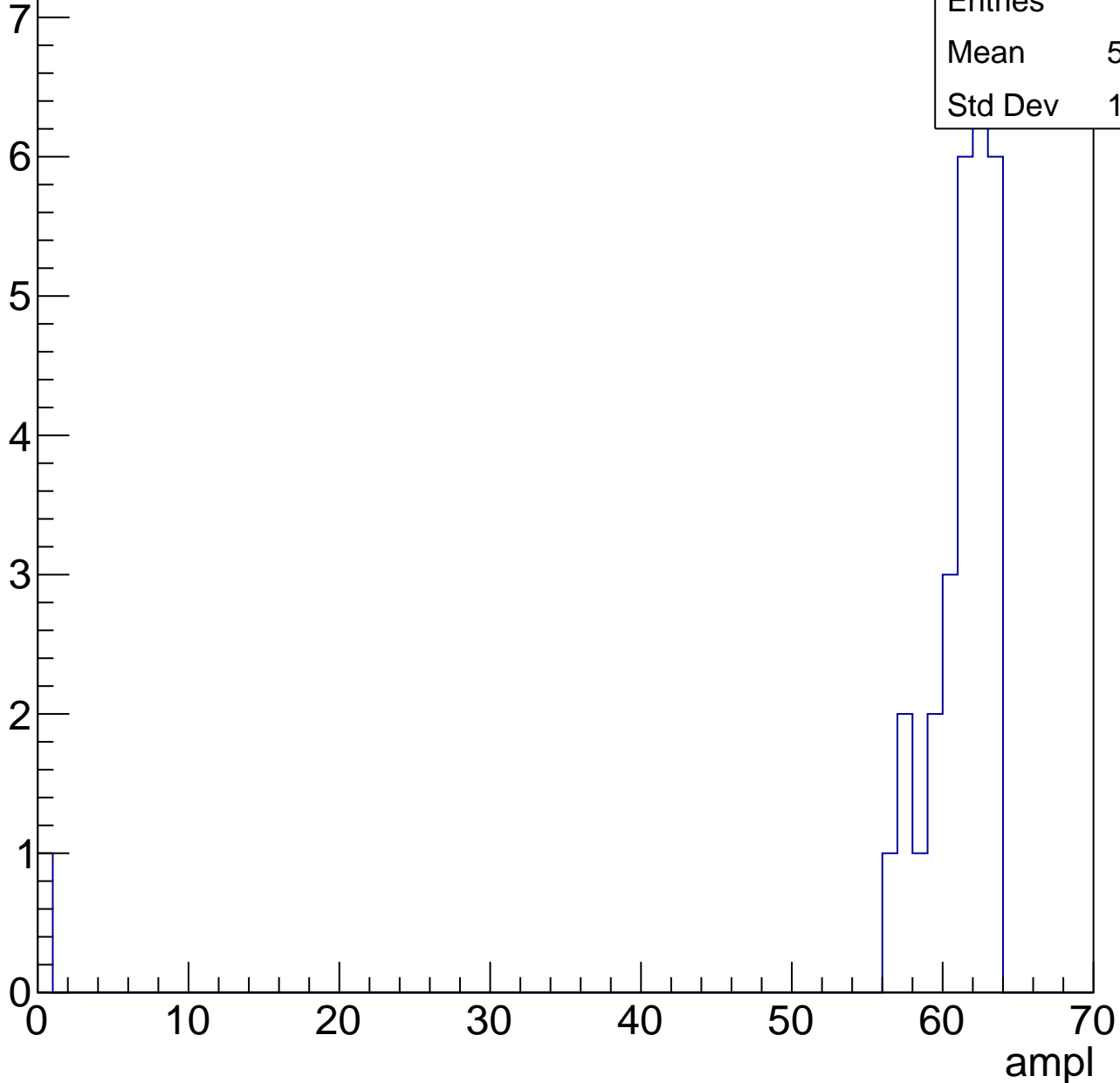


B1L100S, U10-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.76
Std Dev	11.27



B1L100S, U10-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch23, adc0

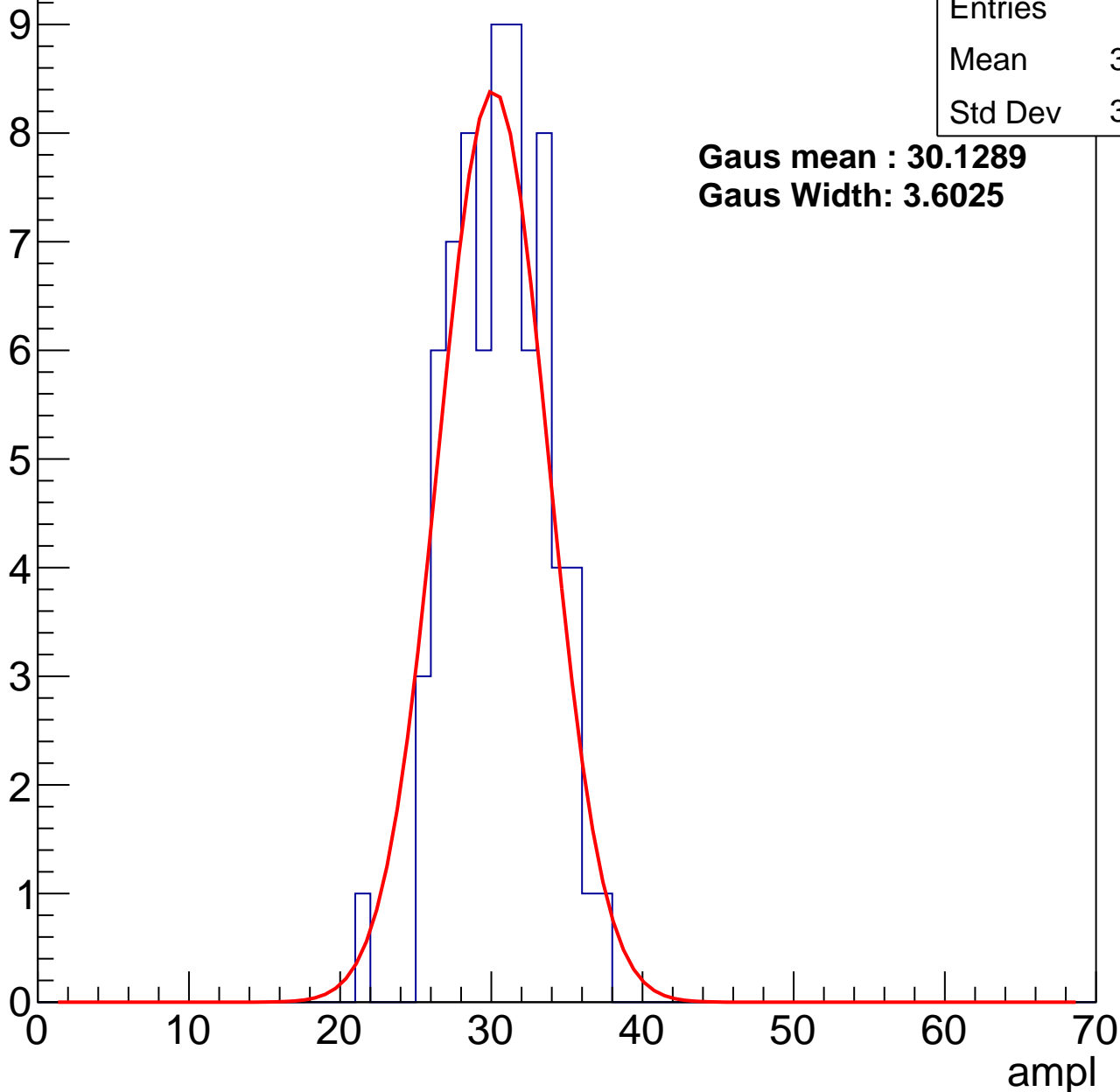
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.04
Std Dev	3.112

Gaus mean : 30.1289

Gaus Width: 3.6025



B1L100S, U10-ch23, adc1

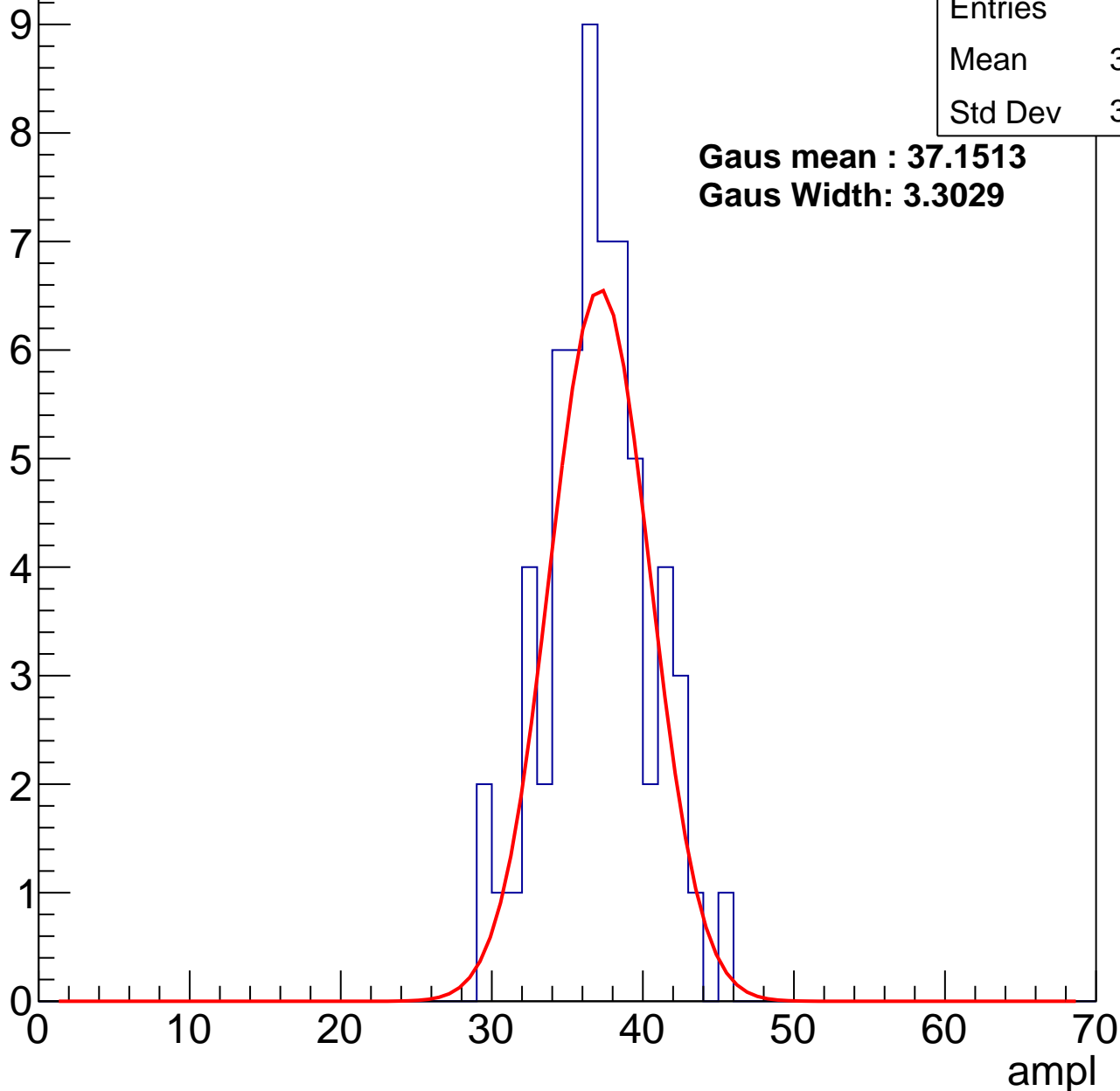
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.54
Std Dev	3.395

Gaus mean : 37.1513

Gaus Width: 3.3029



B1L100S, U10-ch23, adc2

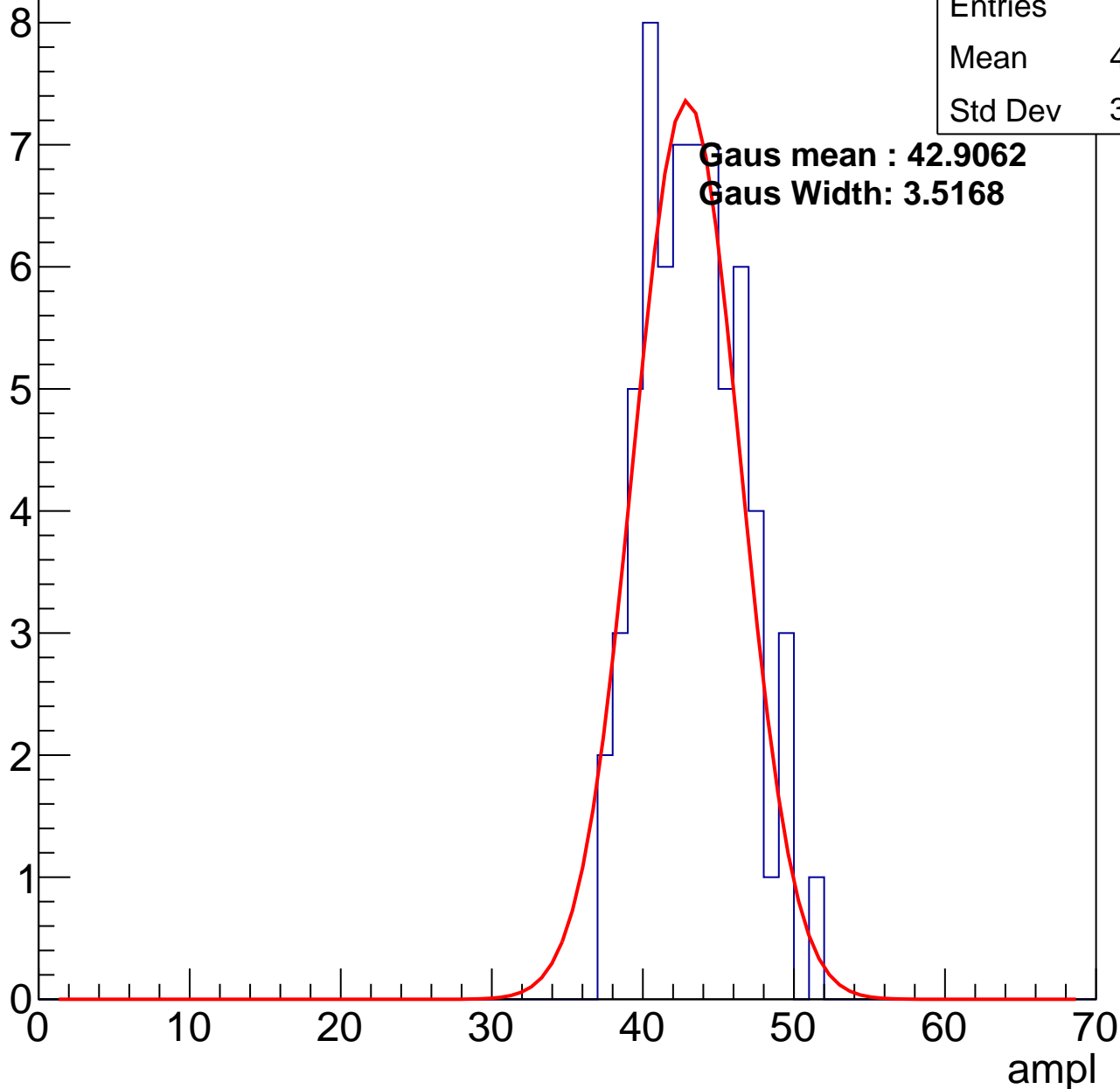
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.88
Std Dev	3.213

Gaus mean : 42.9062

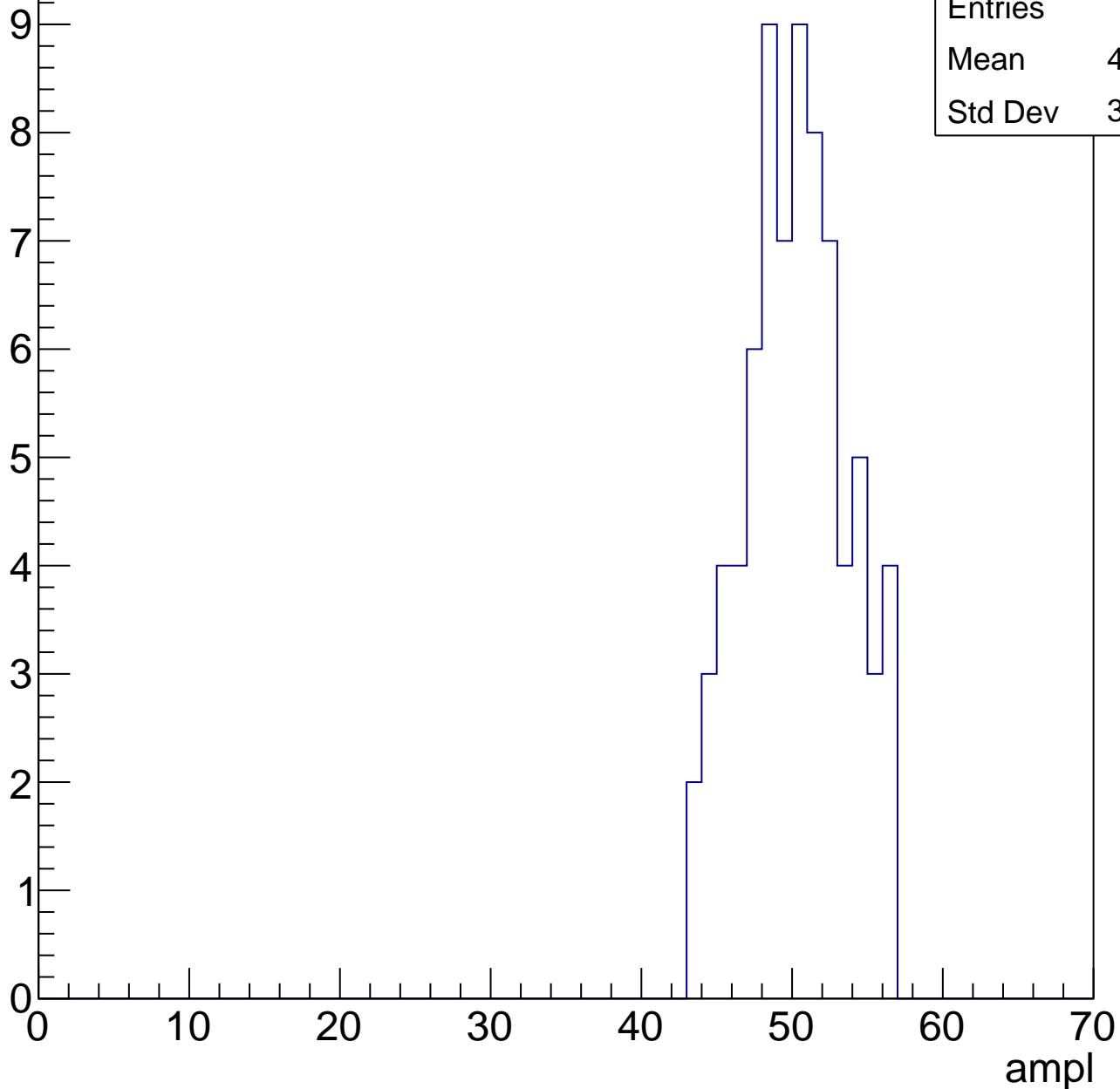
Gaus Width: 3.5168



B1L100S, U10-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

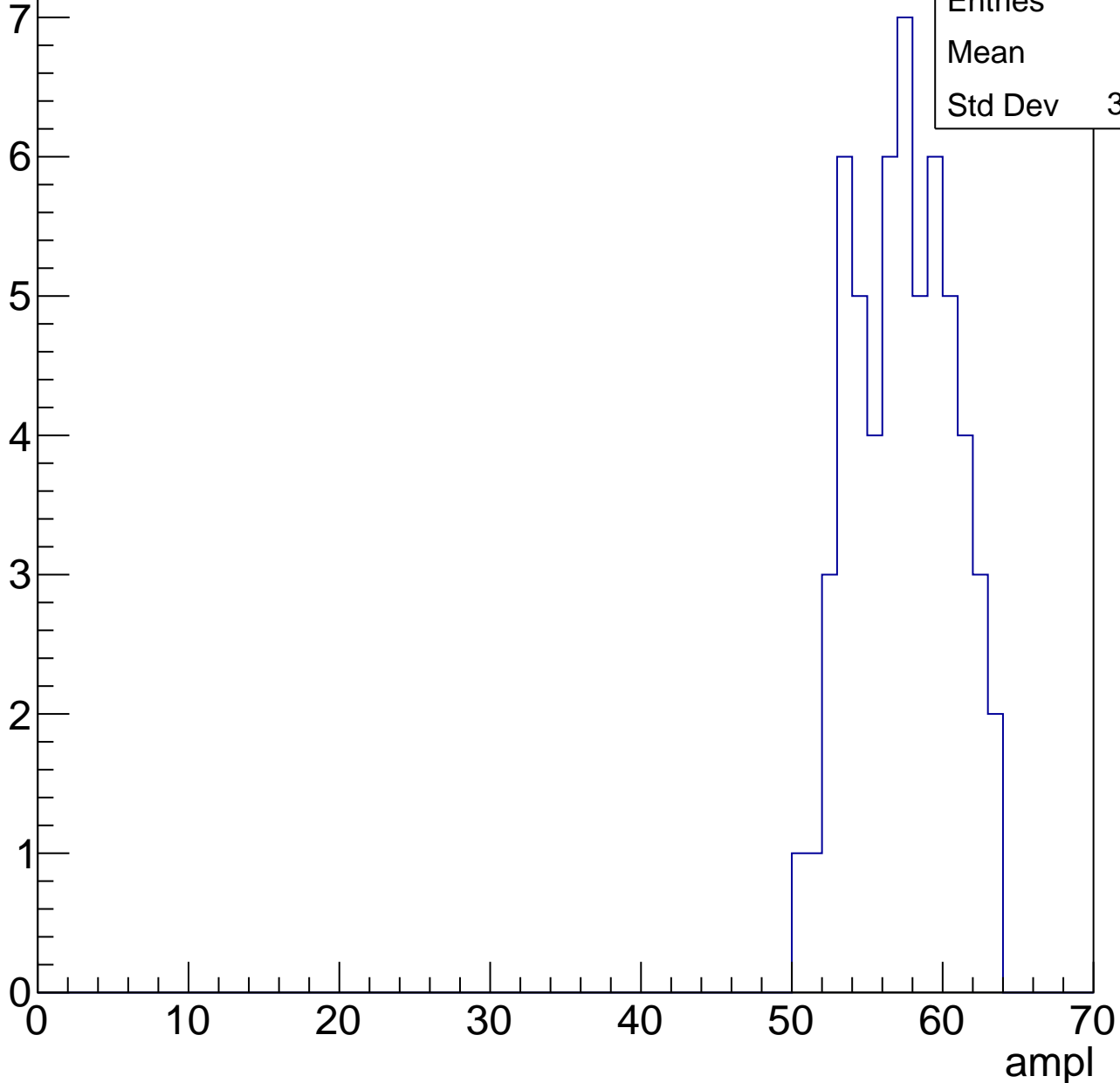


B1L100S, U10-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.9
Std Dev	3.236

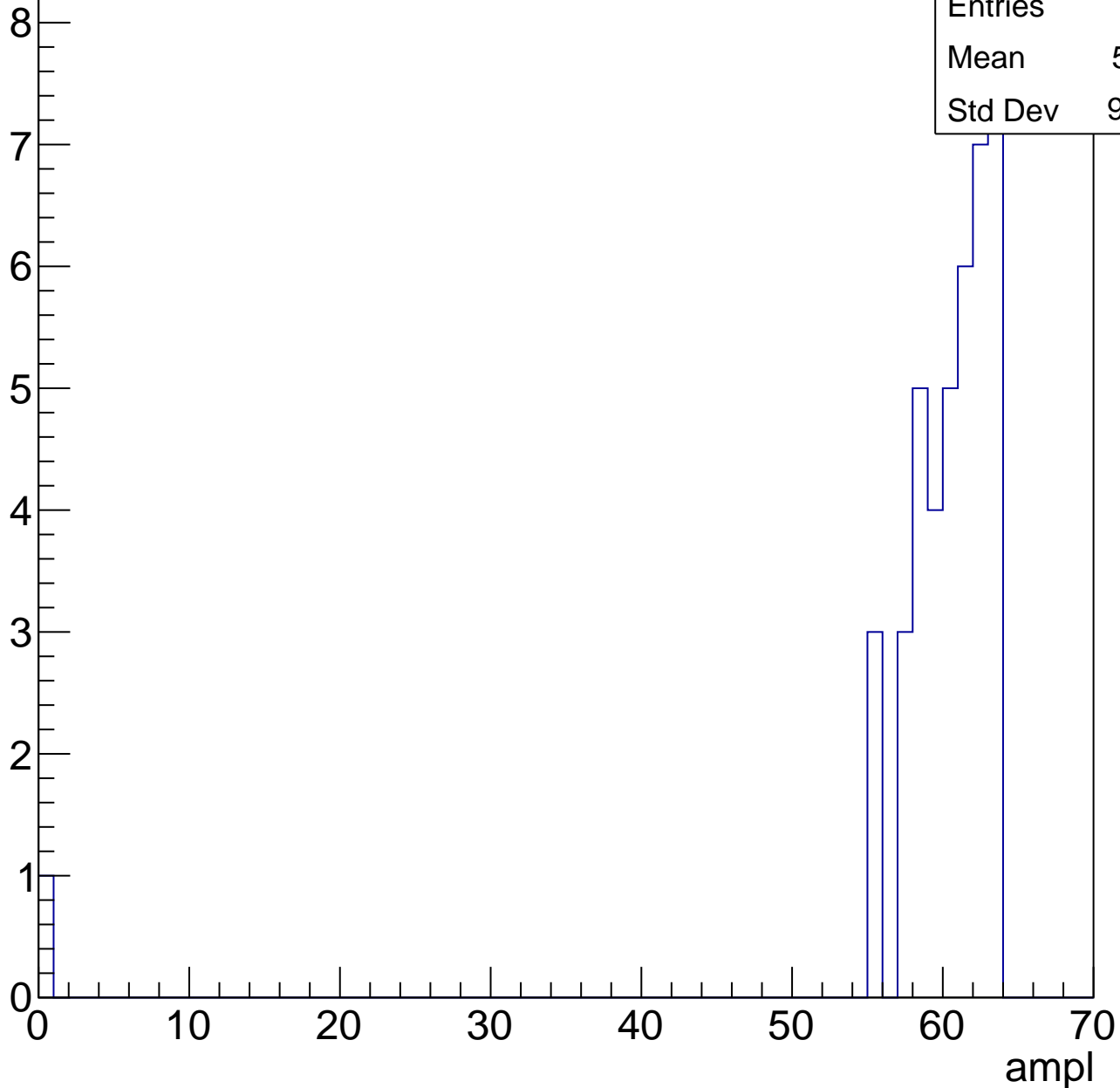


B1L100S, U10-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	58.71
Std Dev	9.465



B1L100S, U10-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch24, adc0

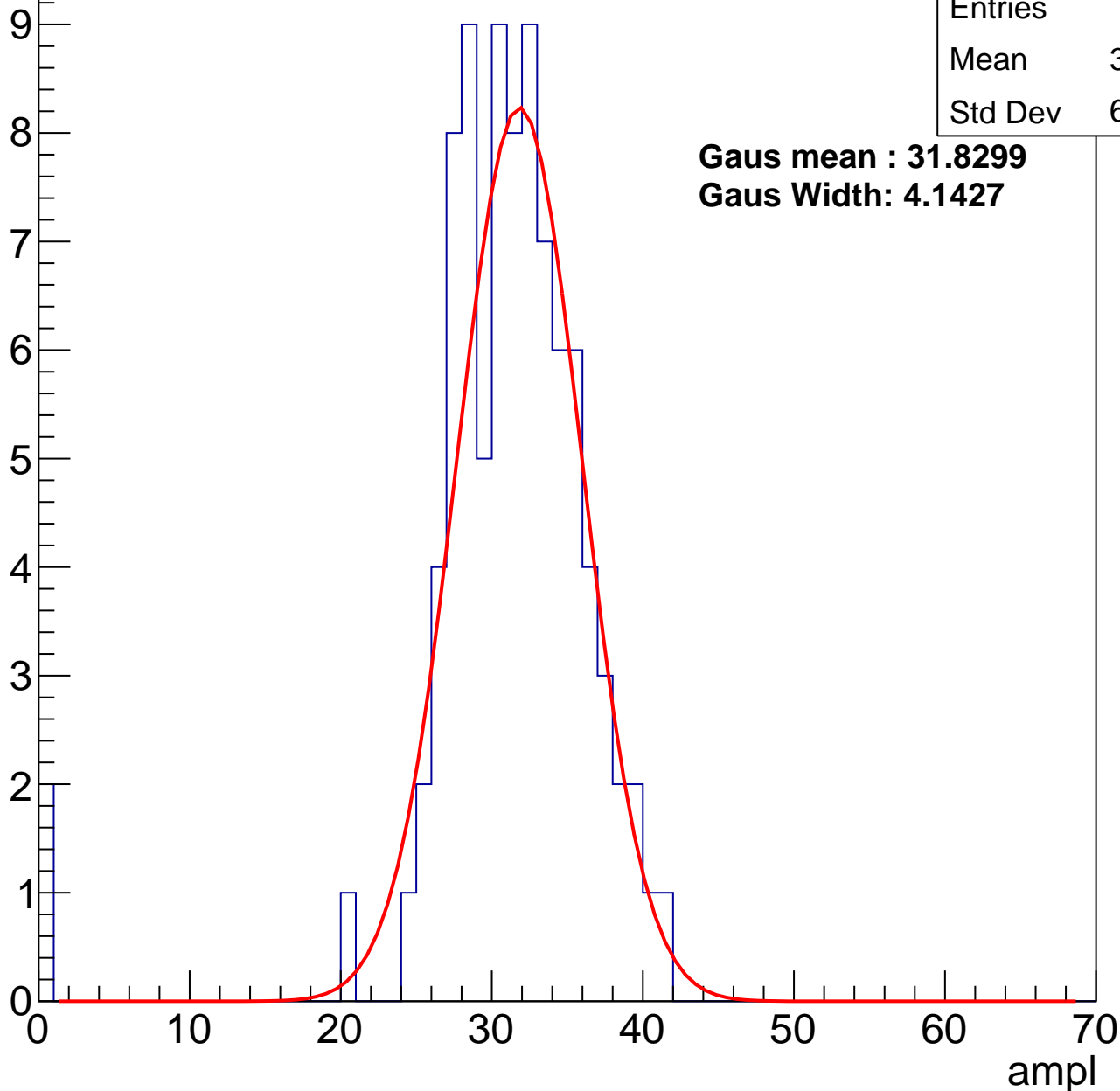
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	30.58
Std Dev	6.044

Gaus mean : 31.8299

Gaus Width: 4.1427



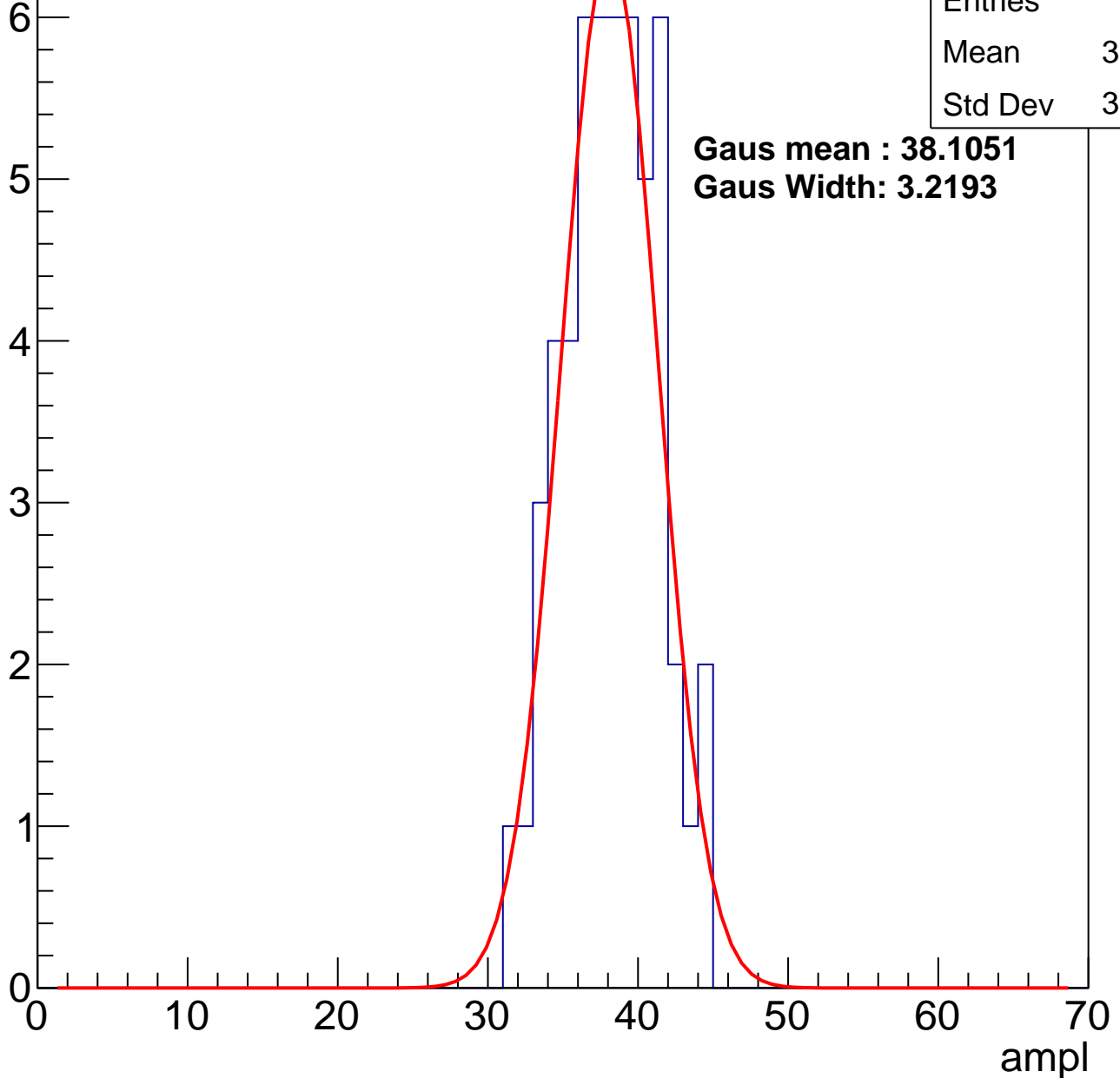
B1L100S, U10-ch24, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	37.72
Std Dev	3.055

Gaus mean : 38.1051
Gaus Width: 3.2193



B1L100S, U10-ch24, adc2

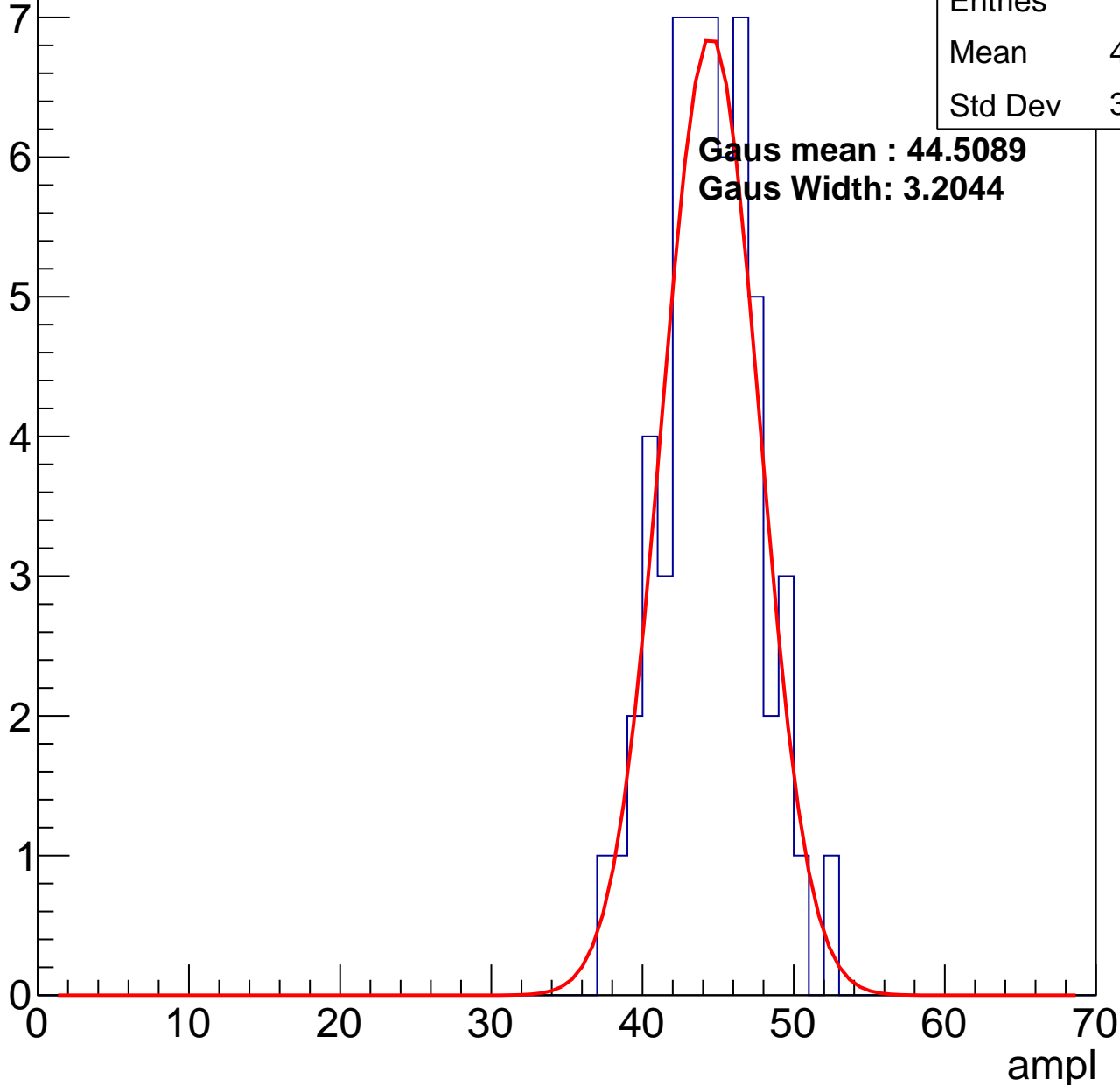
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.05
Std Dev	3.097

Gaus mean : 44.5089

Gaus Width: 3.2044

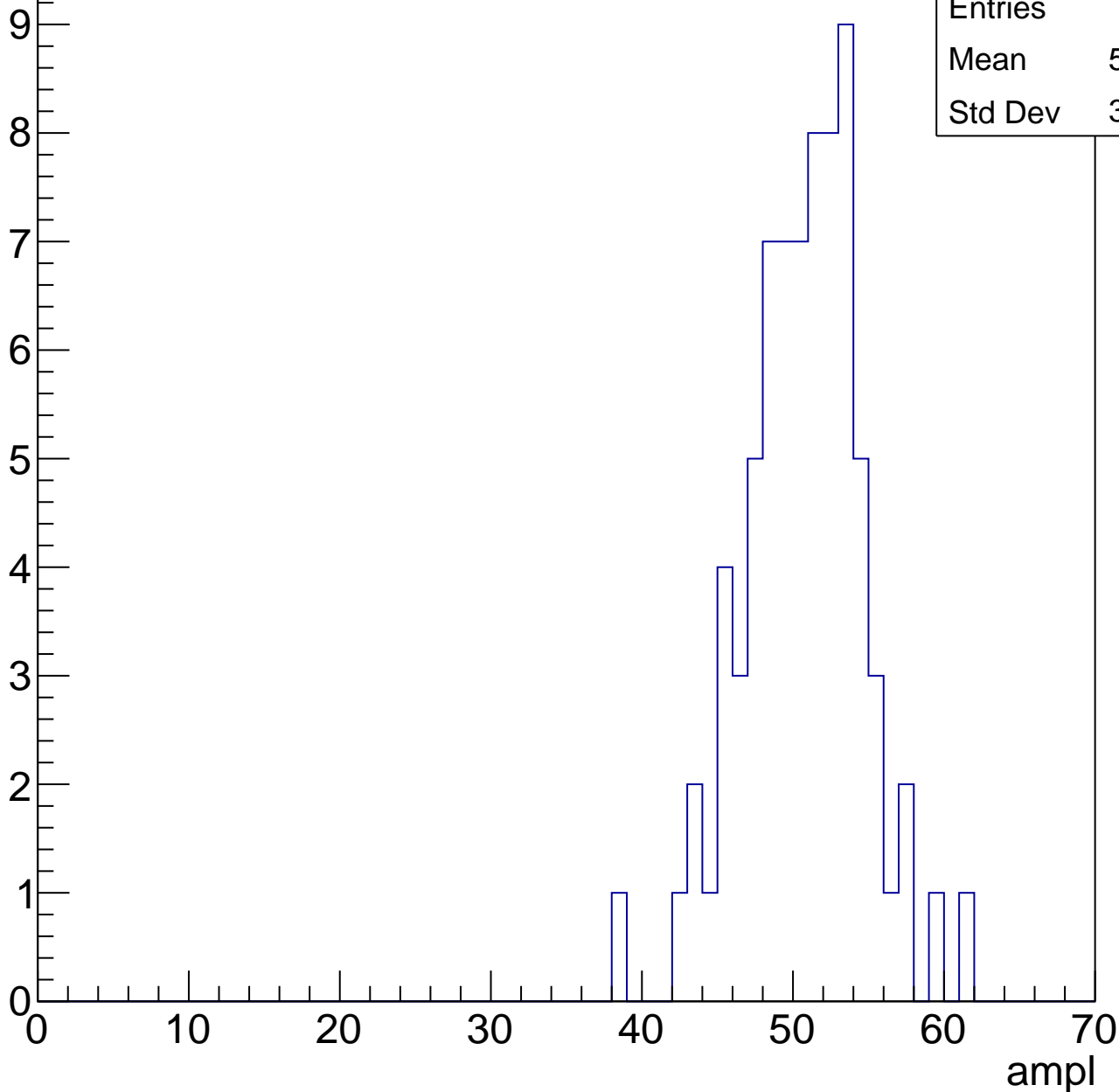


B1L100S, U10-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	50.24
Std Dev	3.937

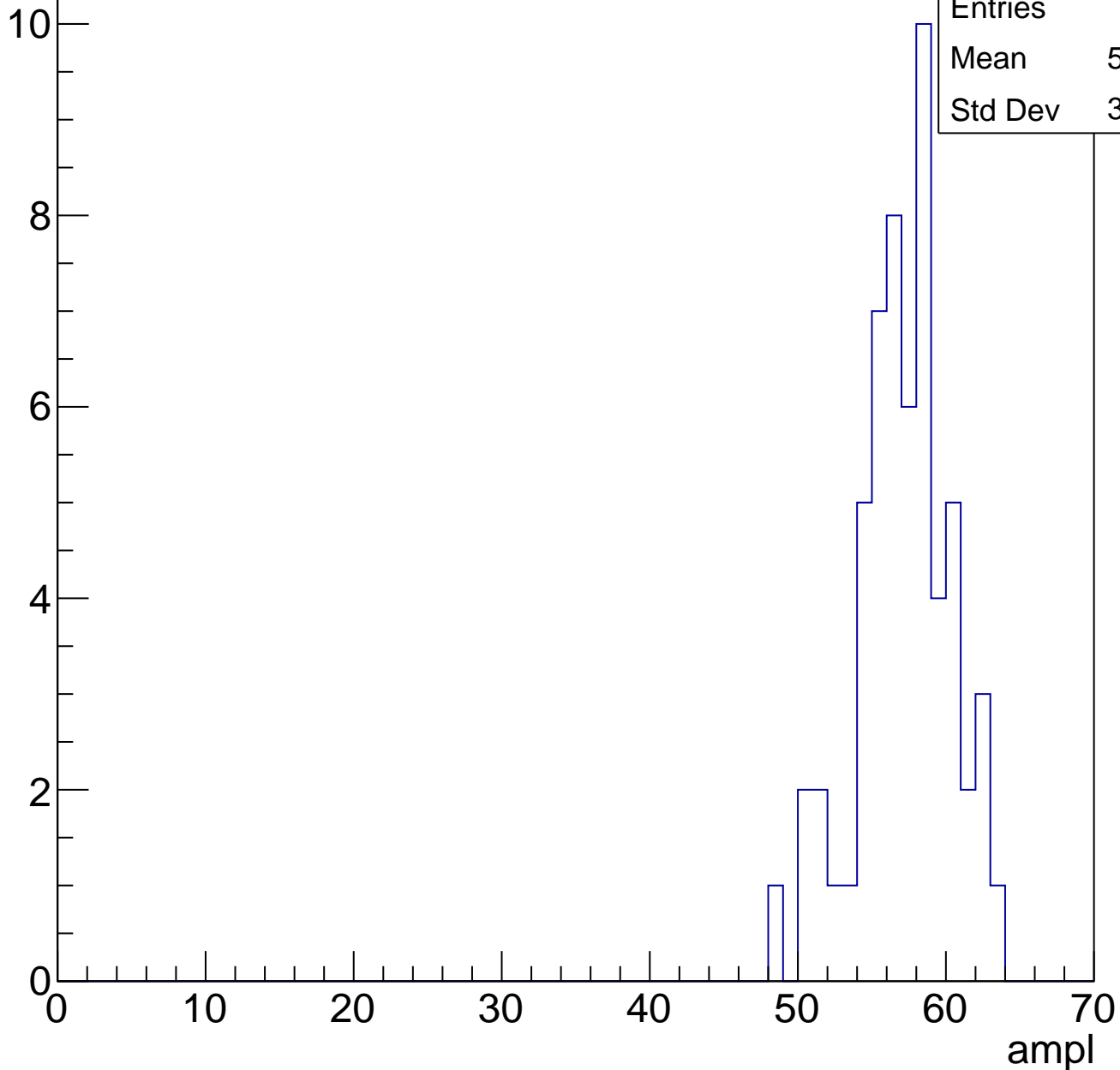


B1L100S, U10-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	56.67
Std Dev	3.164

Entry

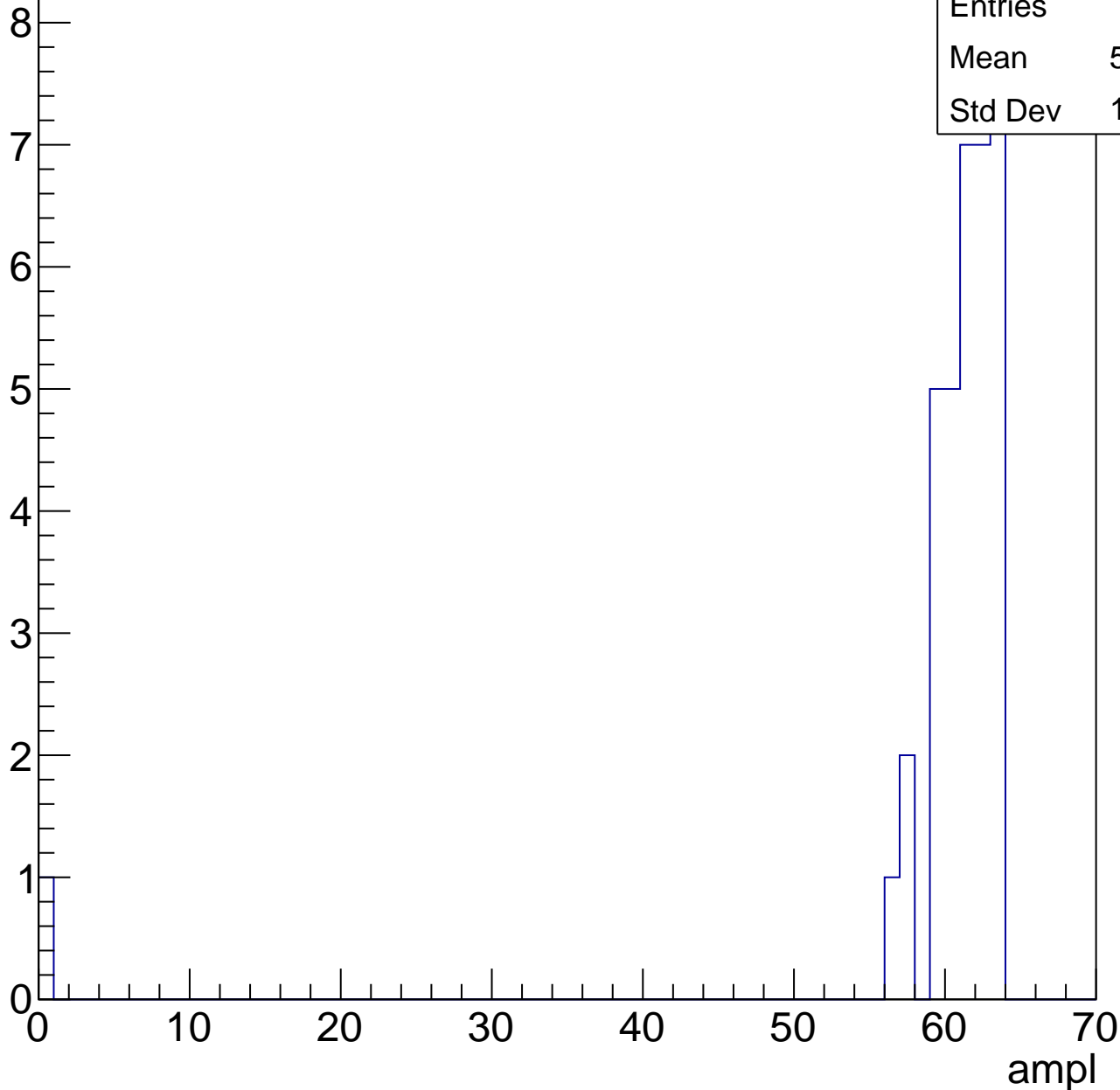


B1L100S, U10-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	59.17
Std Dev	10.17



B1L100S, U10-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch25, adc0

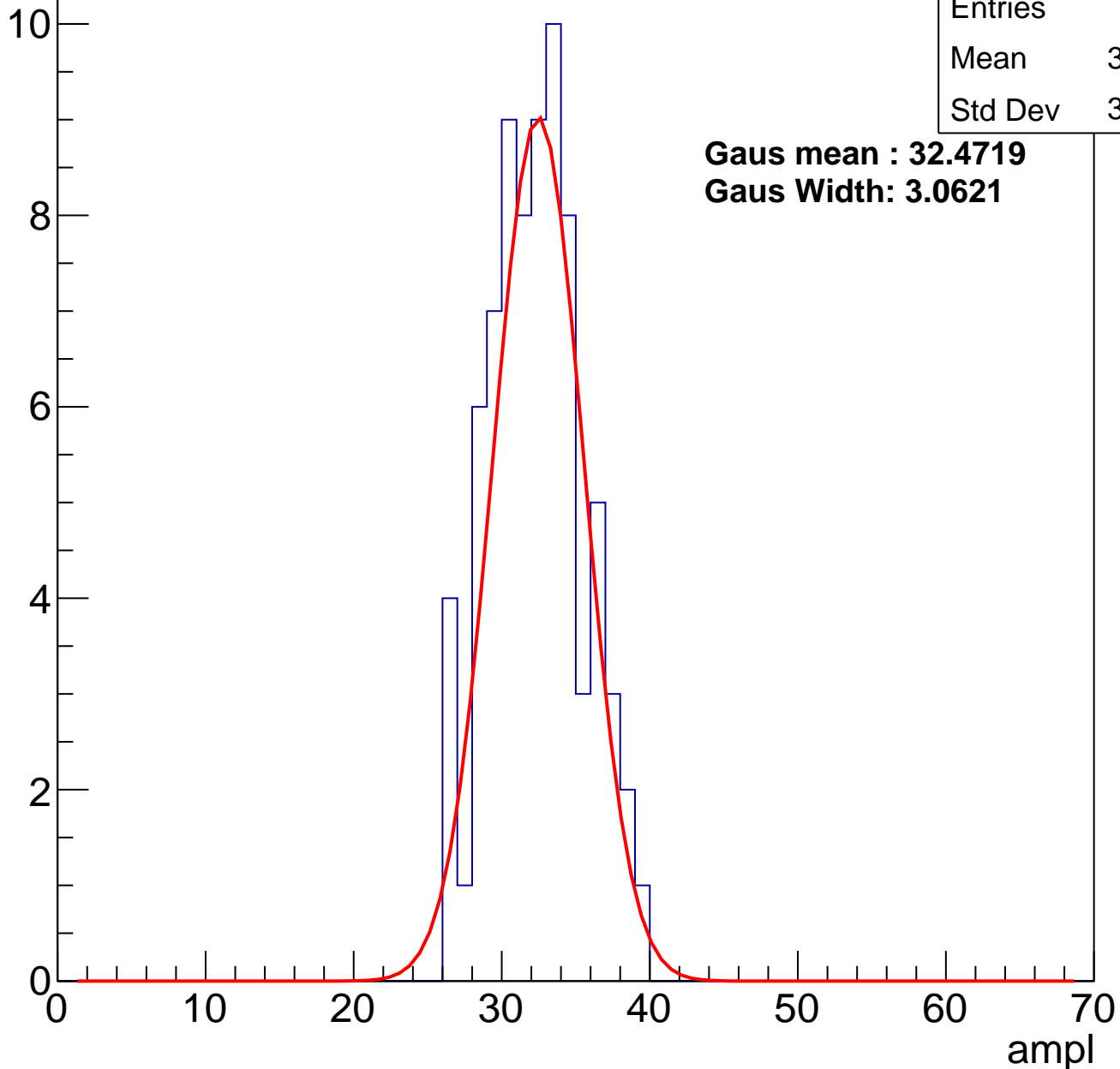
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	31.86
Std Dev	3.068

Gaus mean : 32.4719

Gaus Width: 3.0621

Entry



B1L100S, U10-ch25, adc1

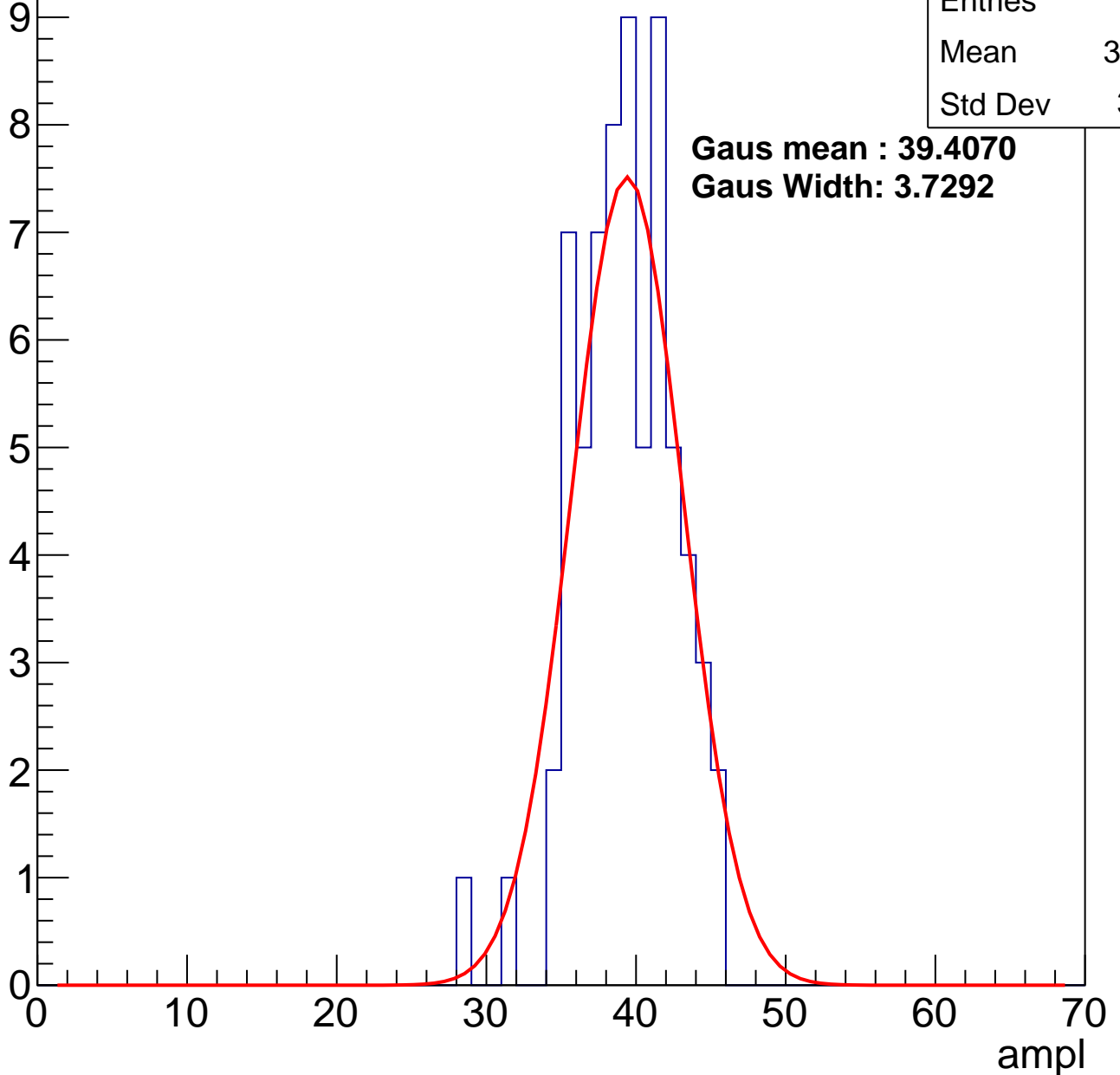
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.81
Std Dev	3.26

Gaus mean : 39.4070

Gaus Width: 3.7292



B1L100S, U10-ch25, adc2

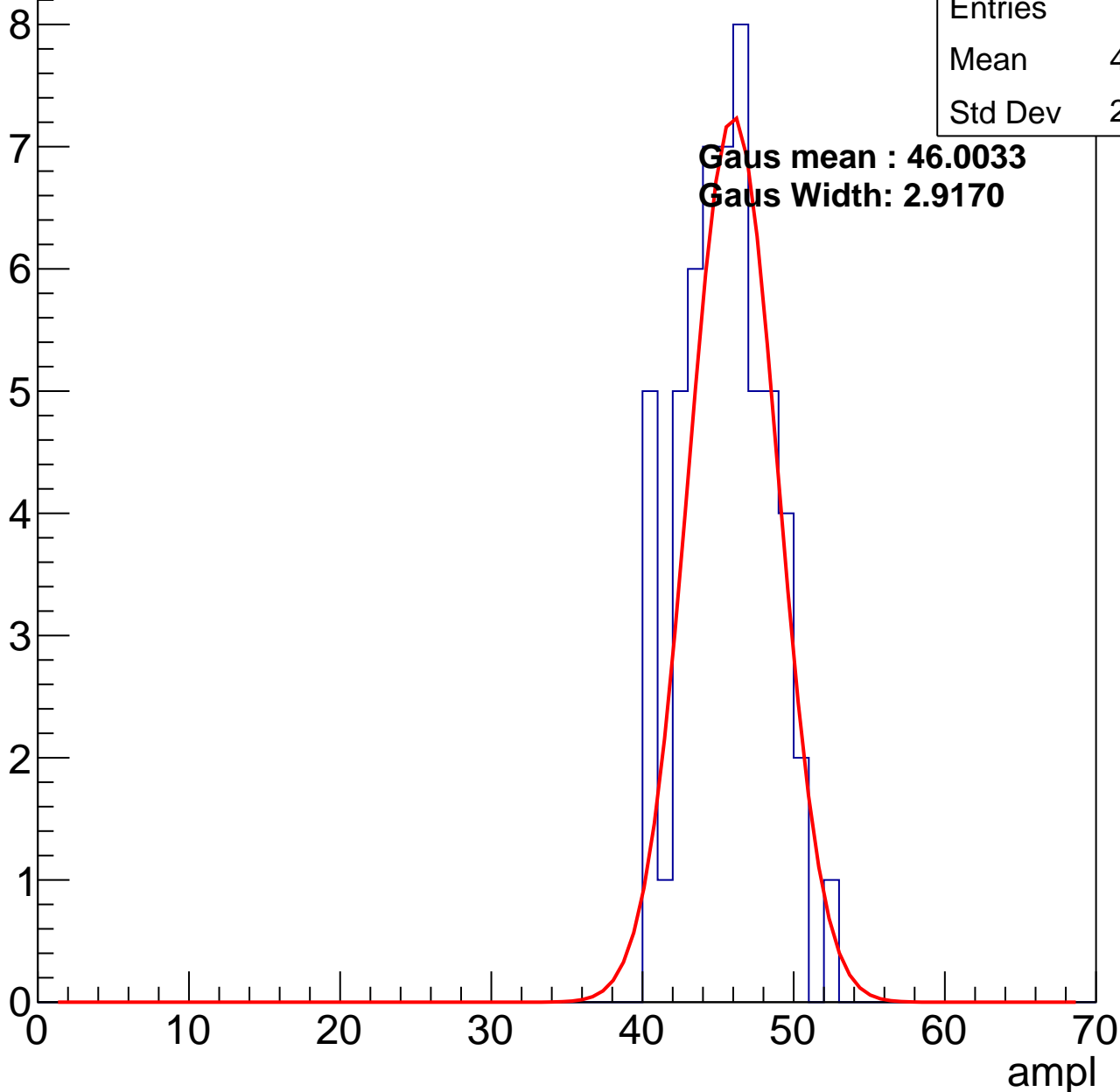
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	45.05
Std Dev	2.844

Gaus mean : 46.0033

Gaus Width: 2.9170

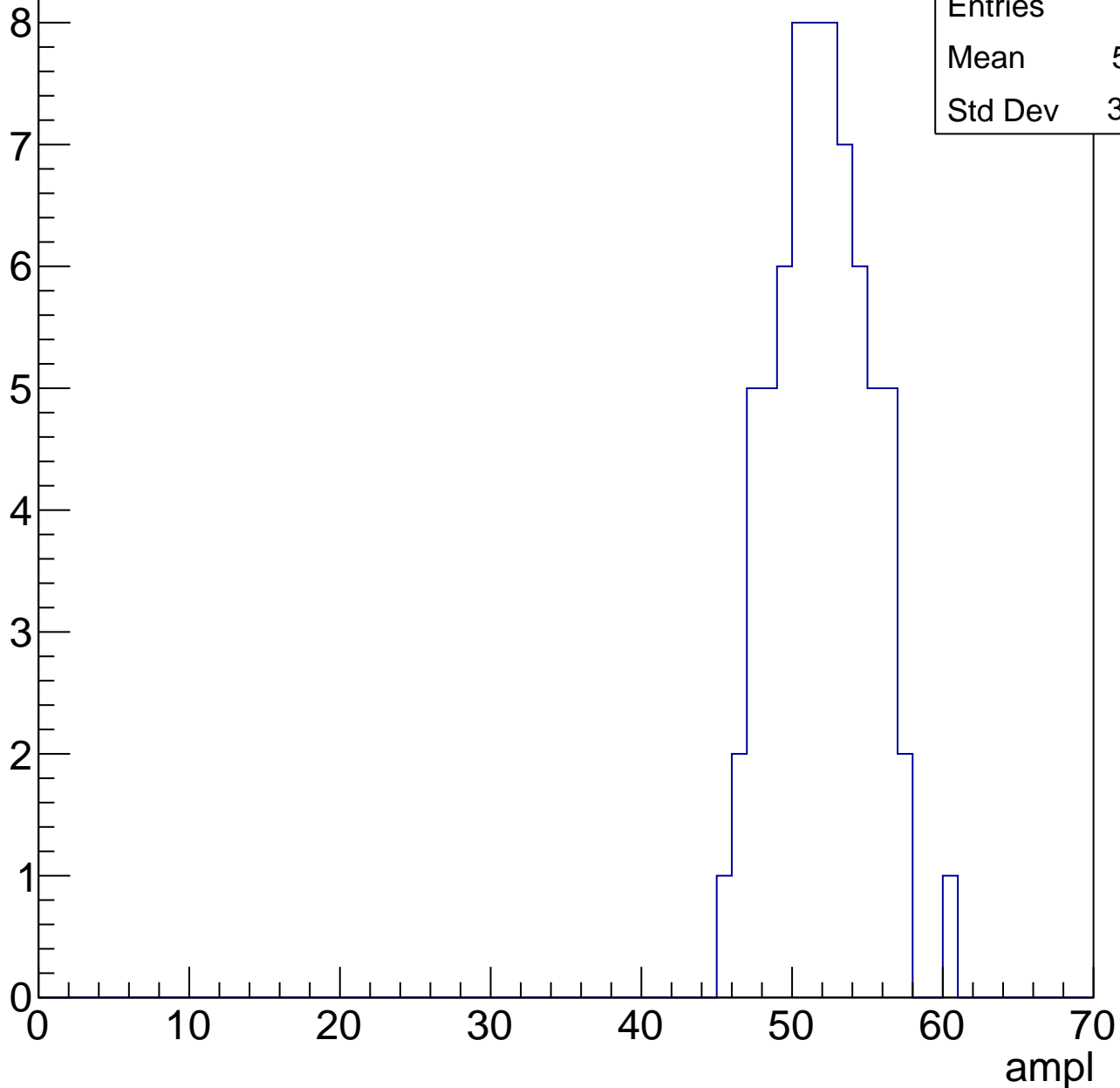


B1L100S, U10-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

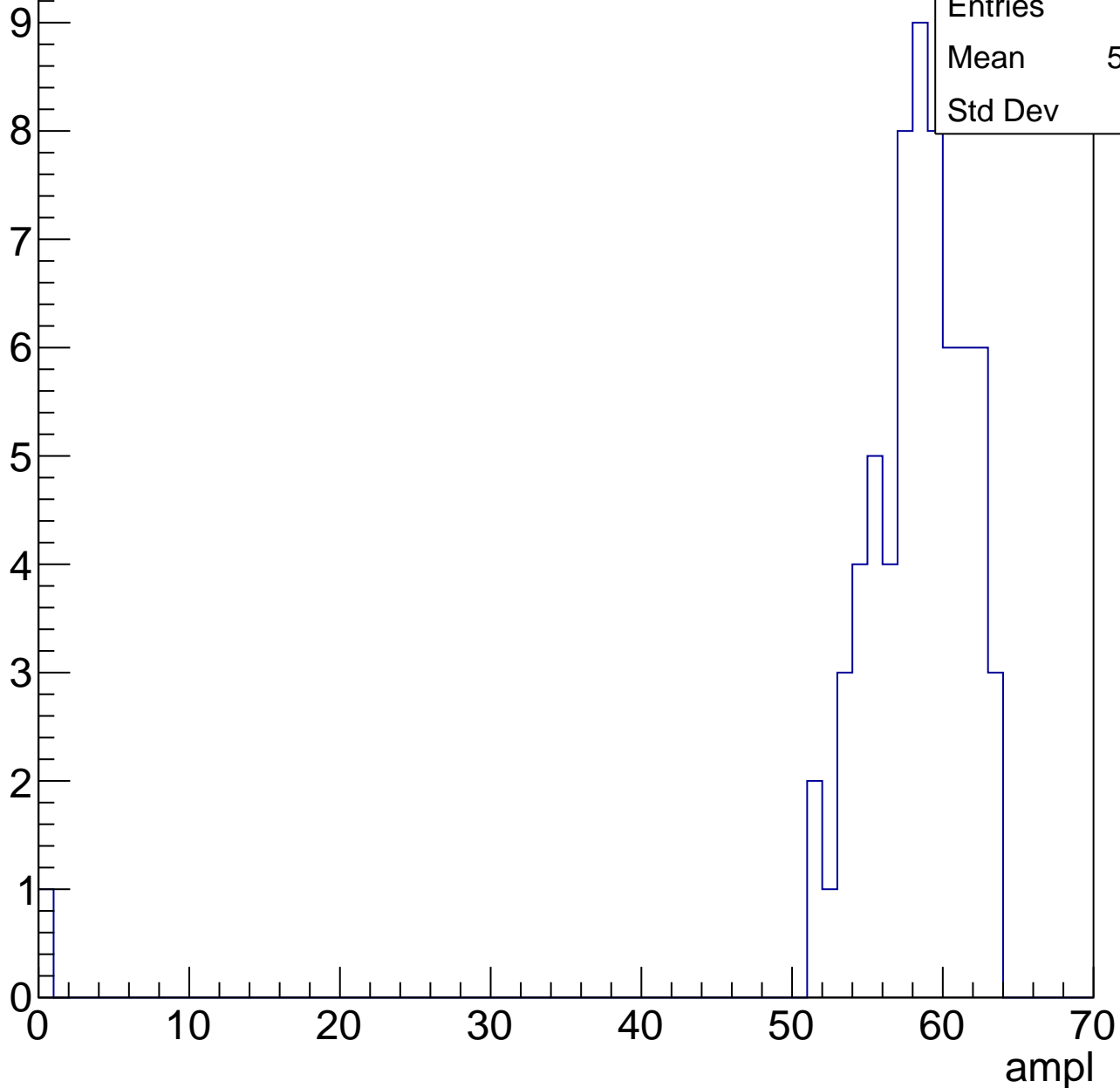
Entries	69
Mean	51.51
Std Dev	3.124



B1L100S, U10-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

22

Mean

61.14

Std Dev

1.866

ampl

0

10

20

30

40

50

60

70

B1L100S, U10-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch26, adc0

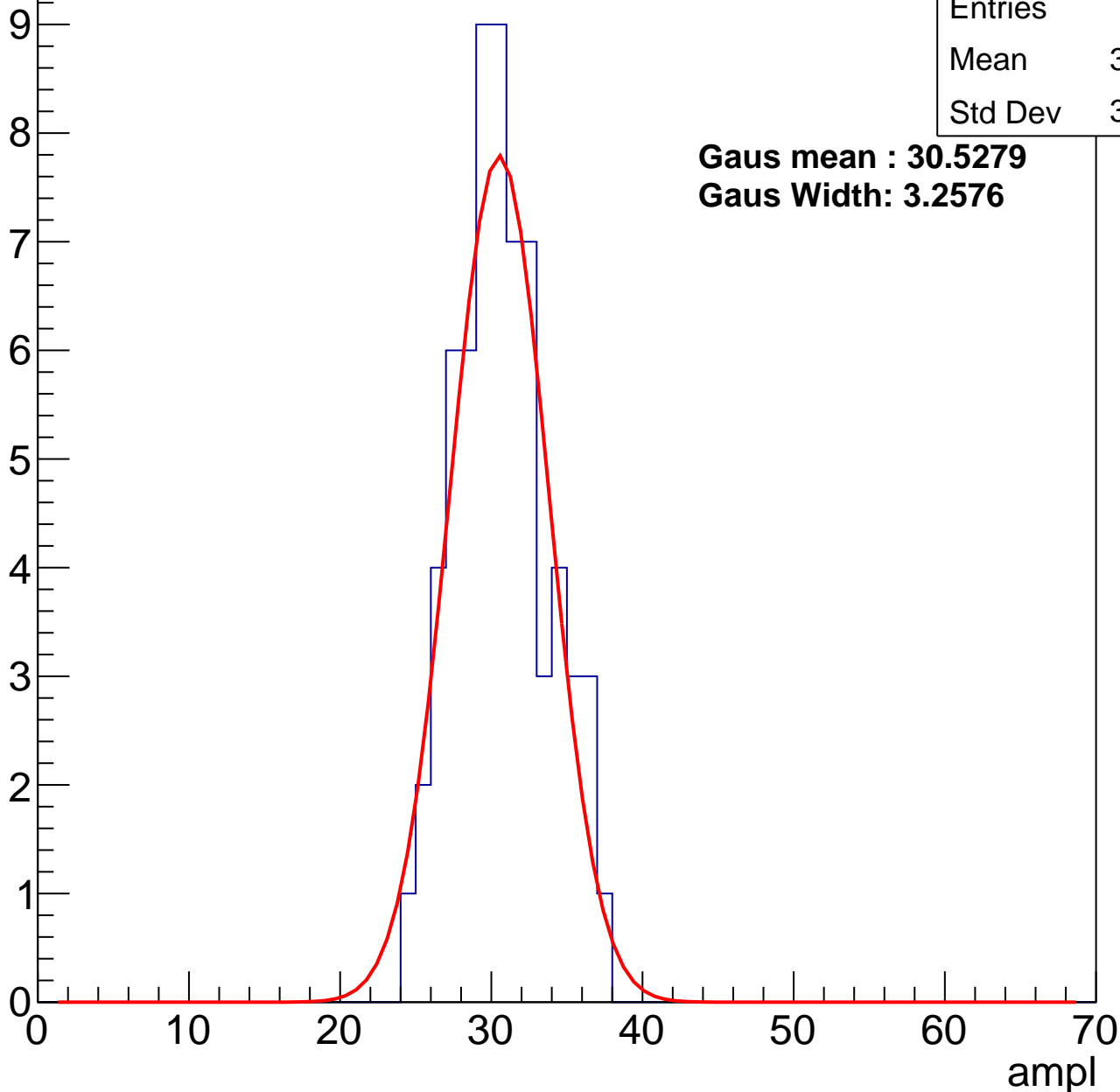
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	30.23
Std Dev	3.017

Gaus mean : 30.5279

Gaus Width: 3.2576



B1L100S, U10-ch26, adc1

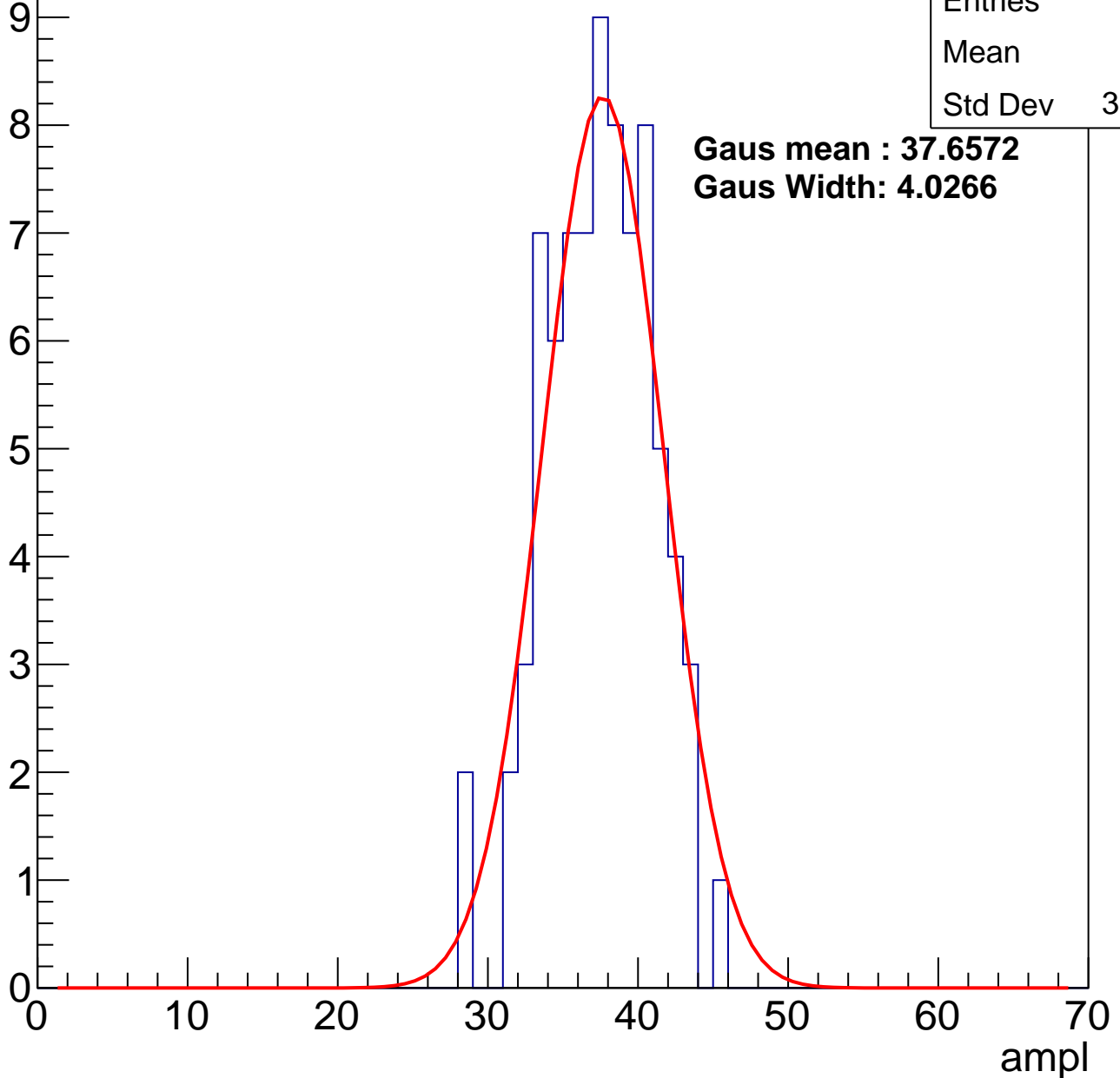
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37
Std Dev	3.504

Gaus mean : 37.6572

Gaus Width: 4.0266



B1L100S, U10-ch26, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	44.25
Std Dev	3.122

Gaus mean : 44.5550

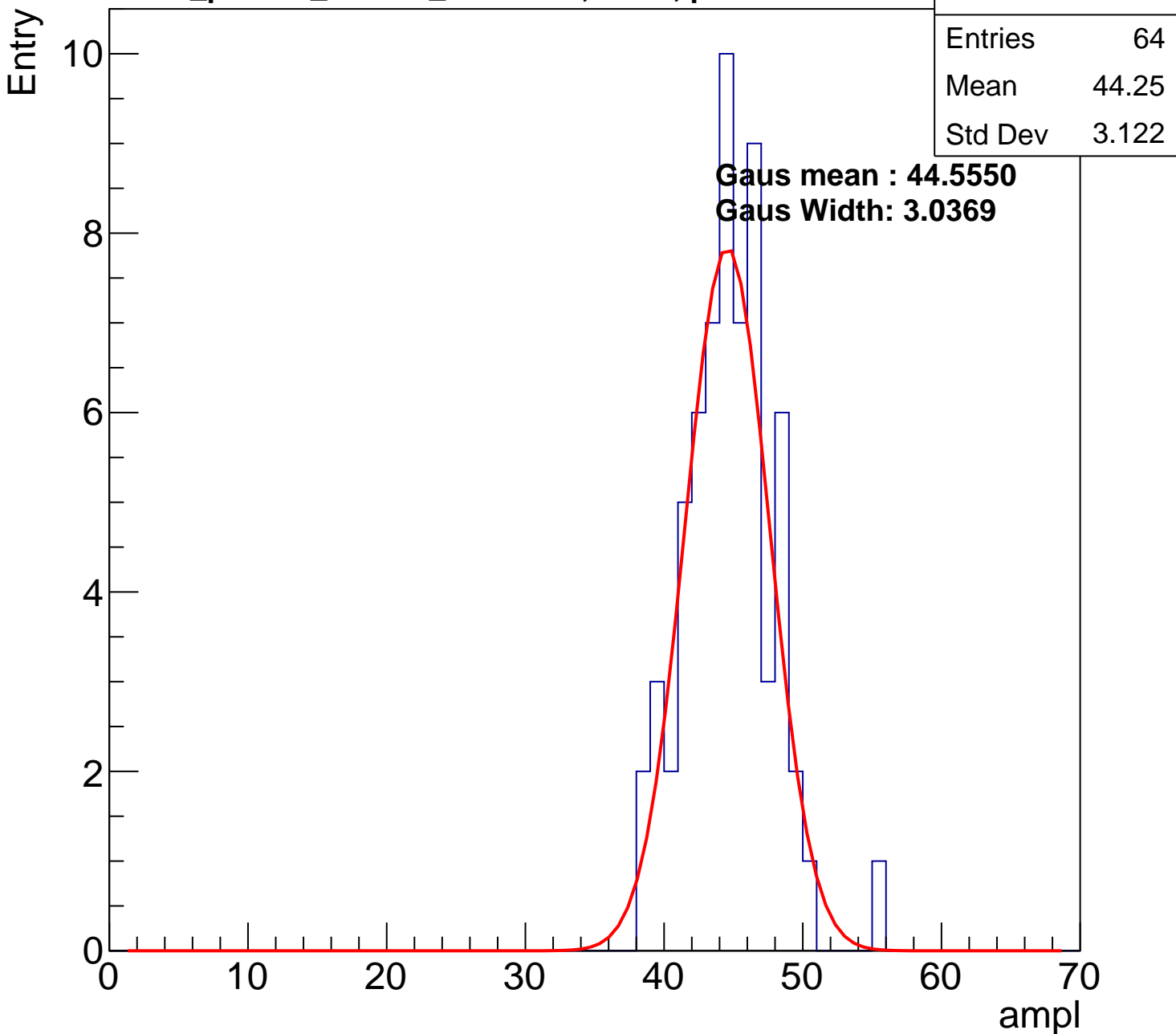
Gaus Width: 3.0369

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

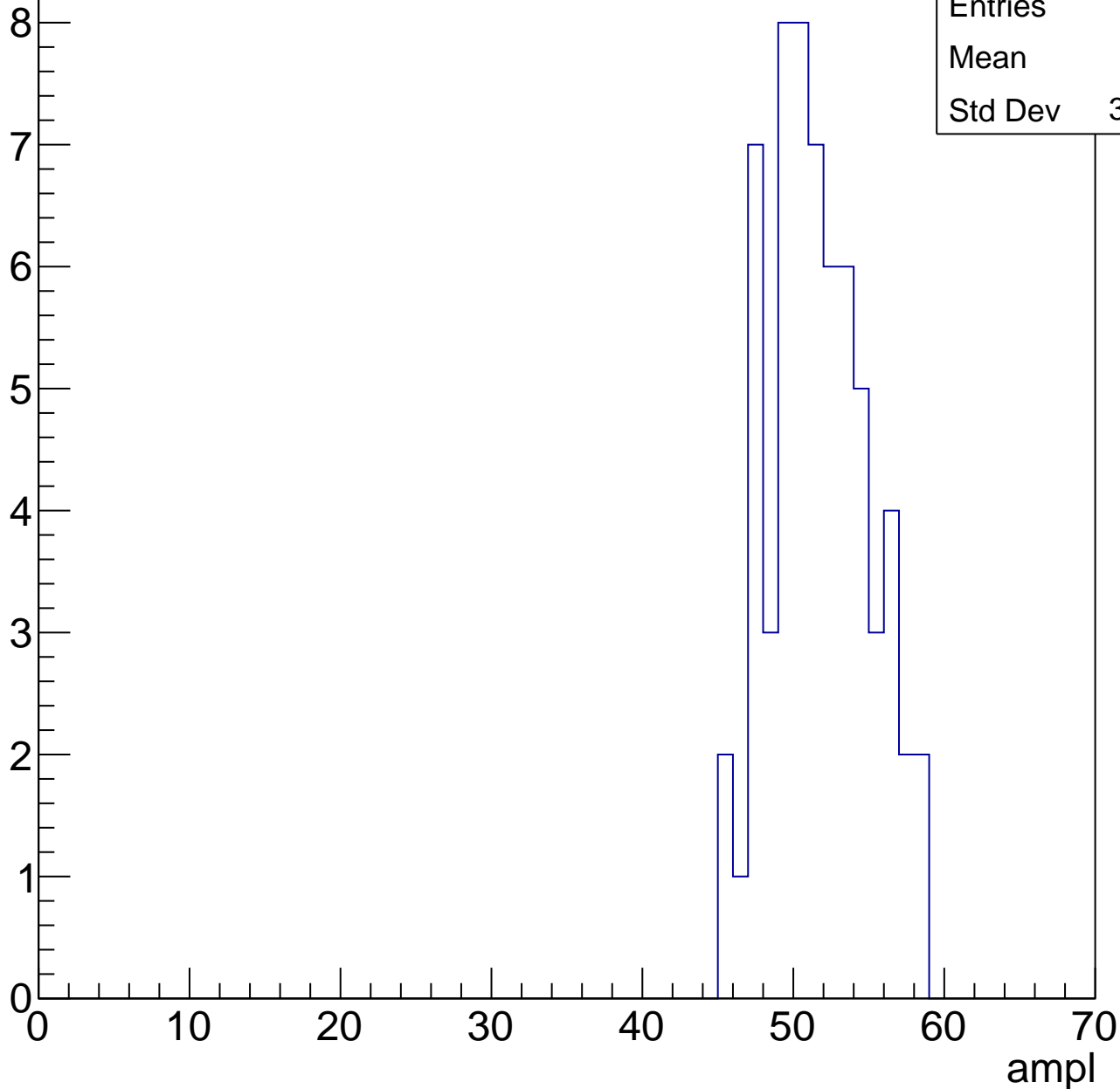


B1L100S, U10-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	51.2
Std Dev	3.227

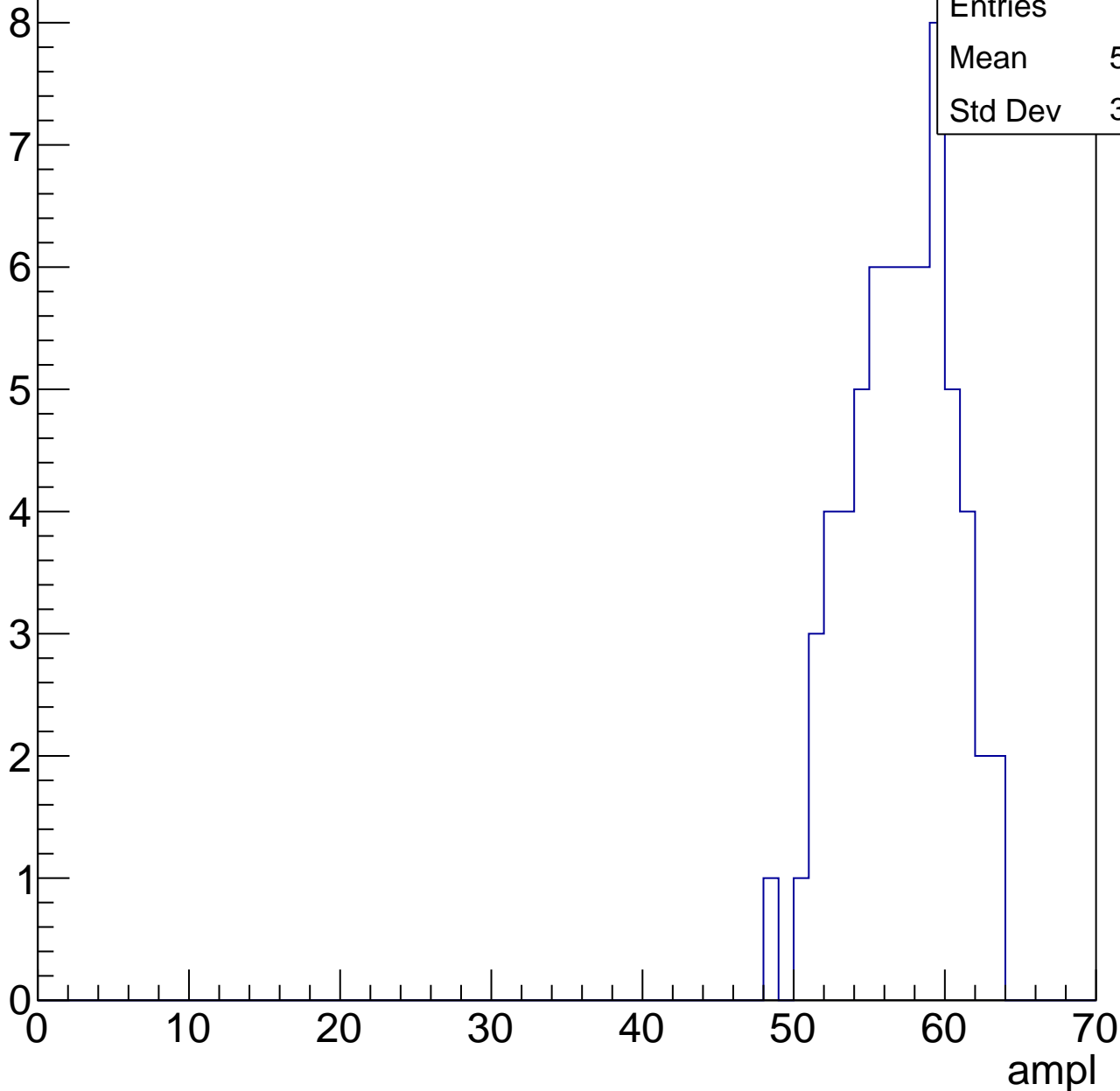


B1L100S, U10-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

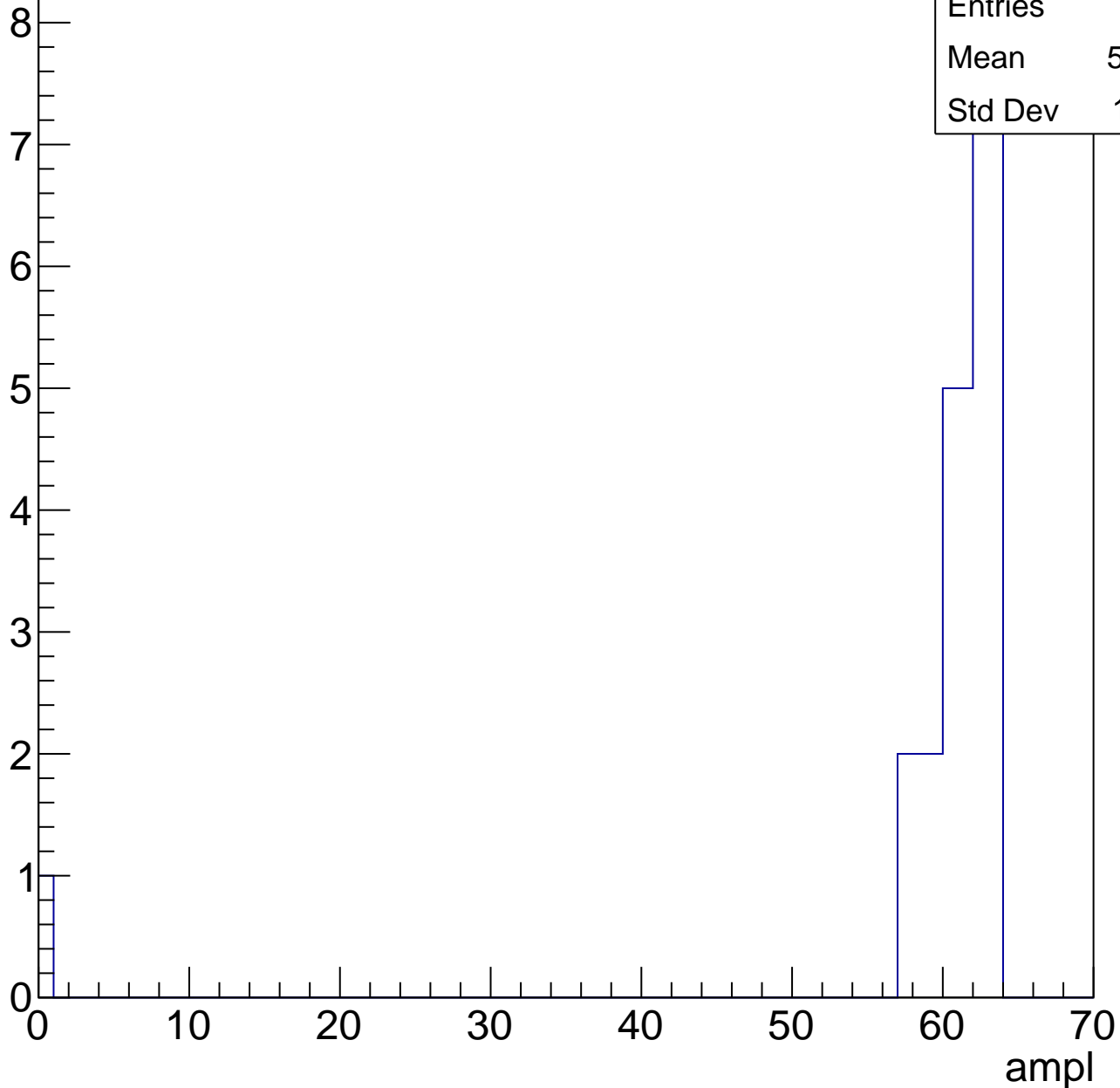
Entries	63
Mean	56.56
Std Dev	3.412



B1L100S, U10-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch27, adc0

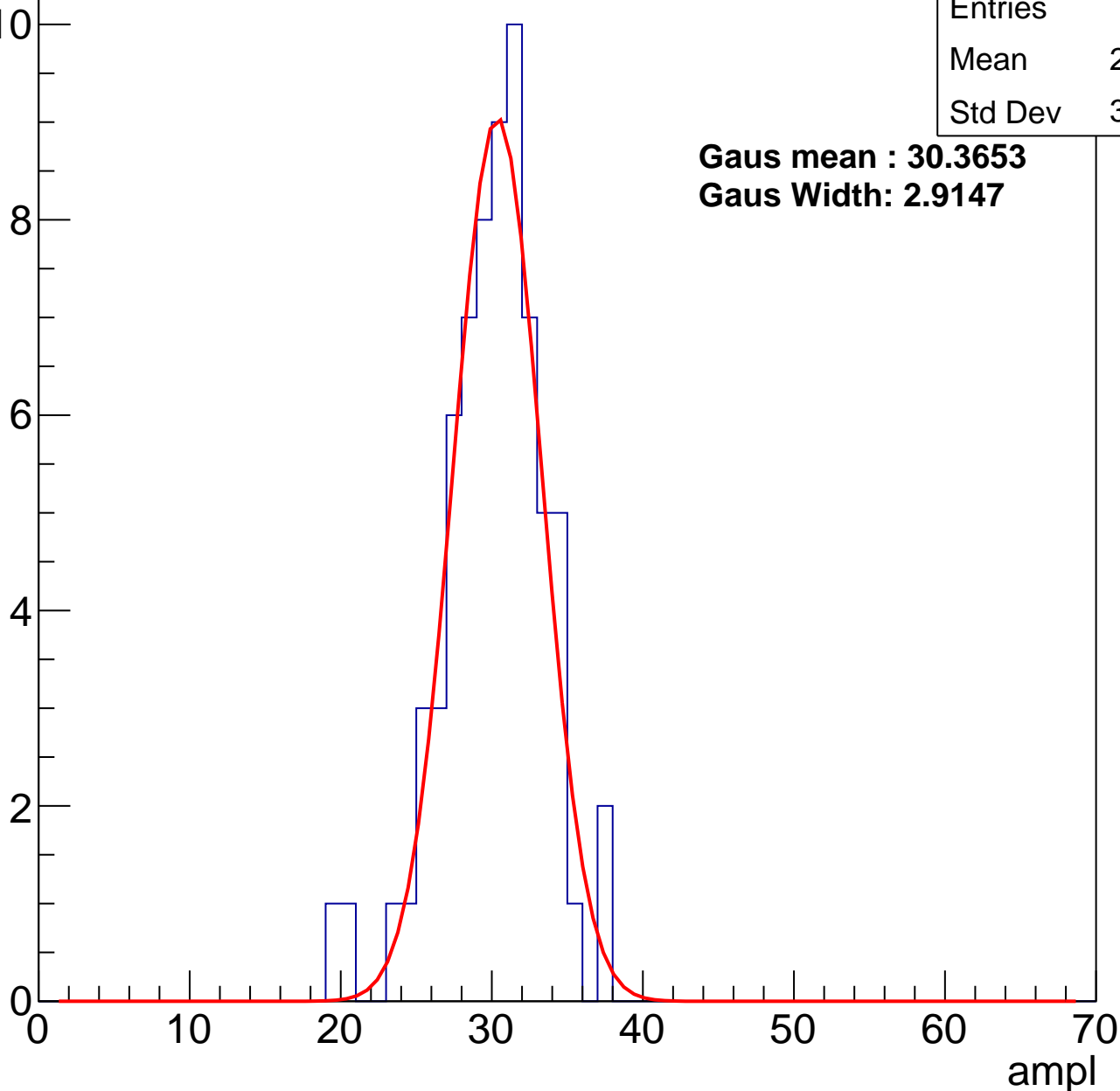
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.67
Std Dev	3.375

Gaus mean : 30.3653

Gaus Width: 2.9147



B1L100S, U10-ch27, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	36.73
Std Dev	3.375

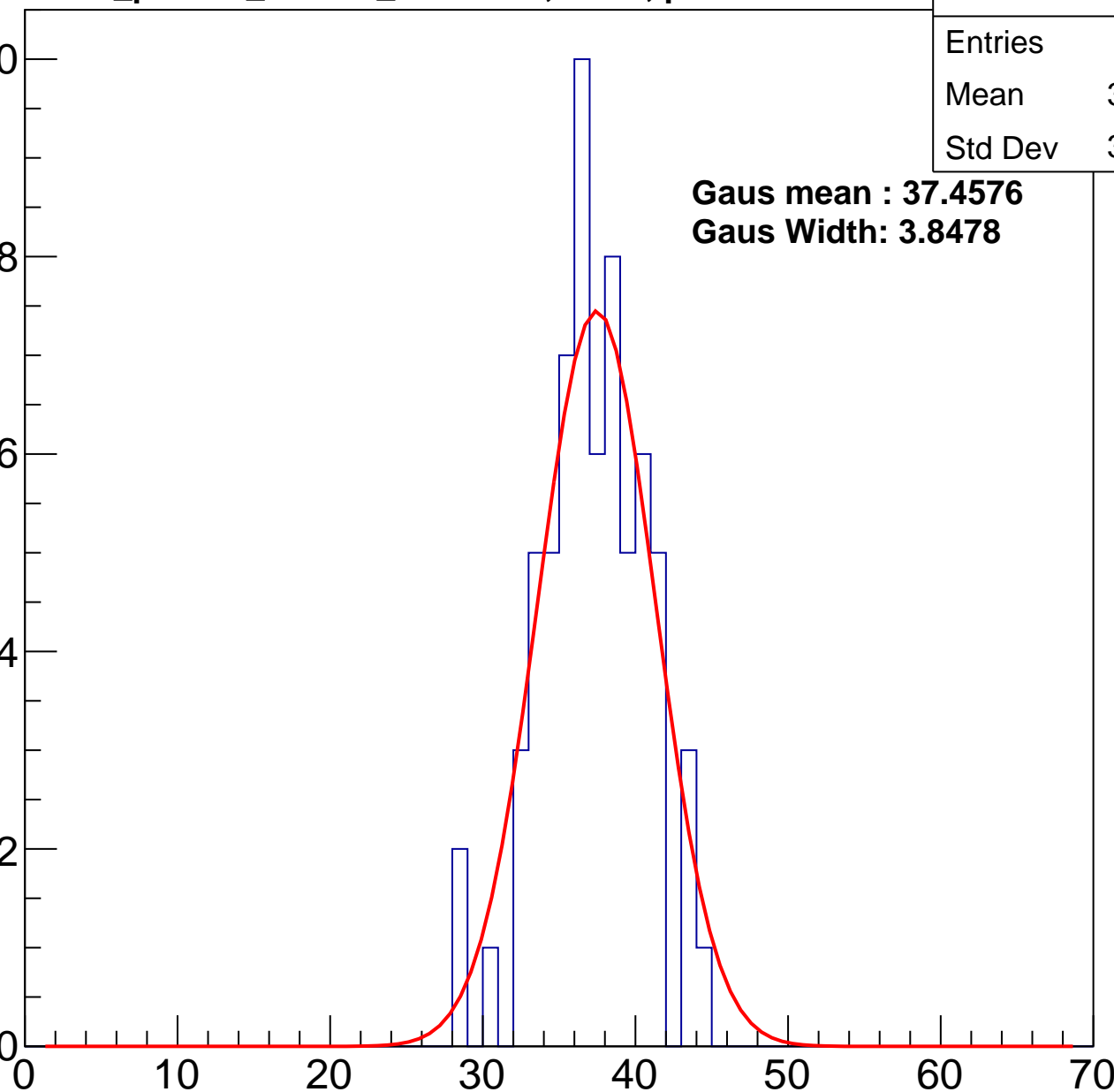
Gaus mean : 37.4576

Gaus Width: 3.8478

Entry

10
8
6
4
2
0

ampl



B1L100S, U10-ch27, adc2

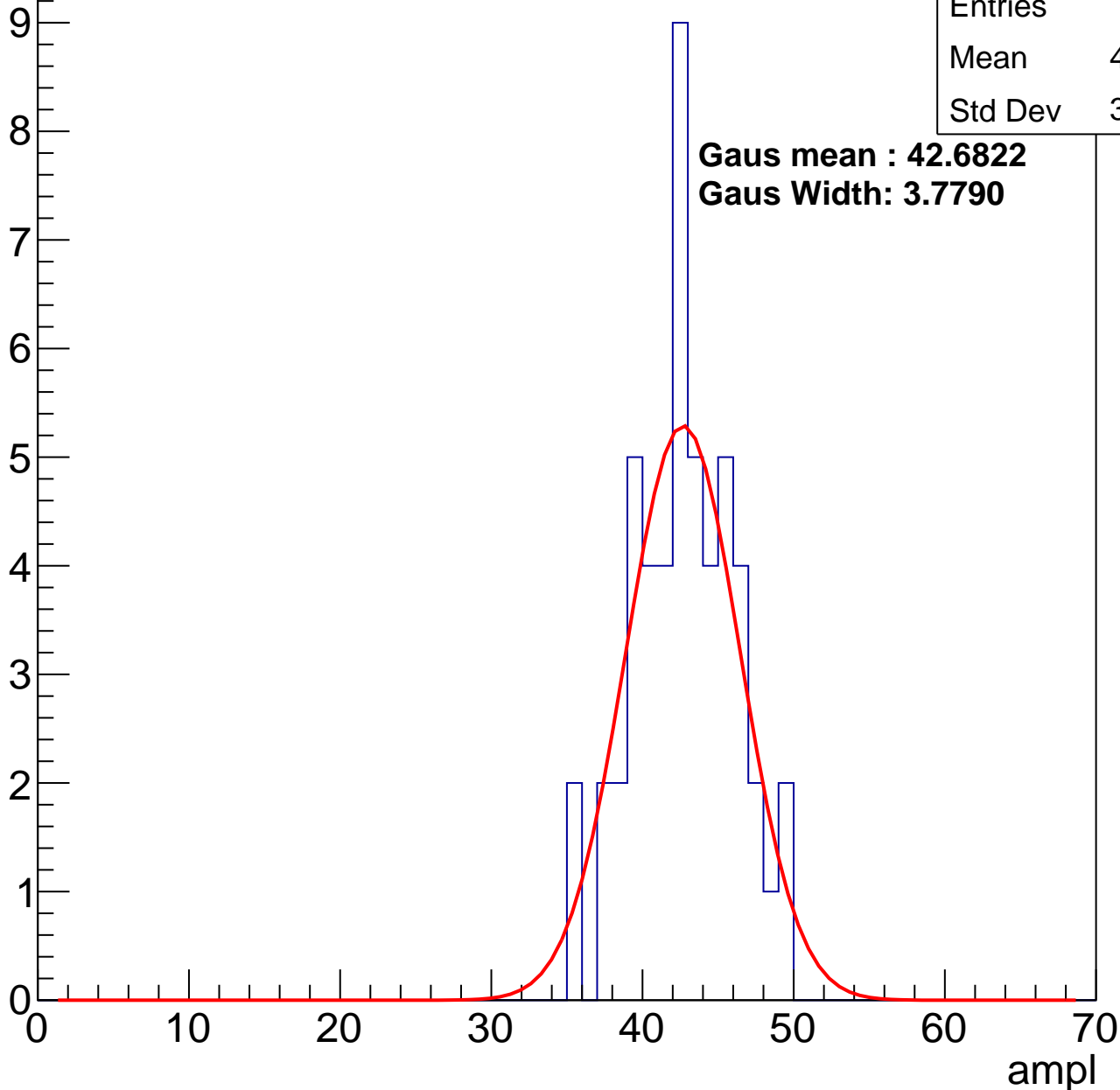
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.29
Std Dev	3.298

Gaus mean : 42.6822

Gaus Width: 3.7790

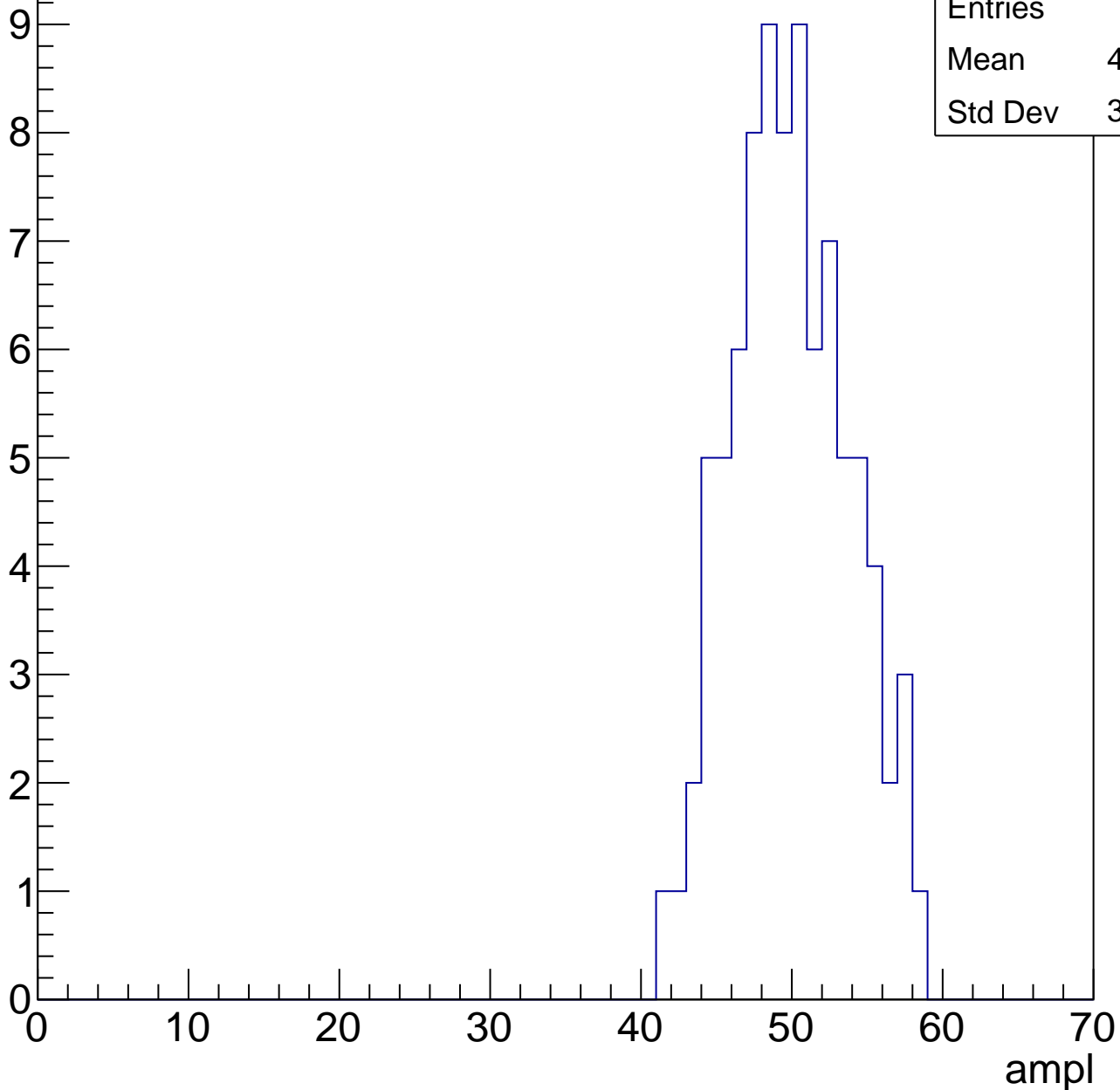


B1L100S, U10-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

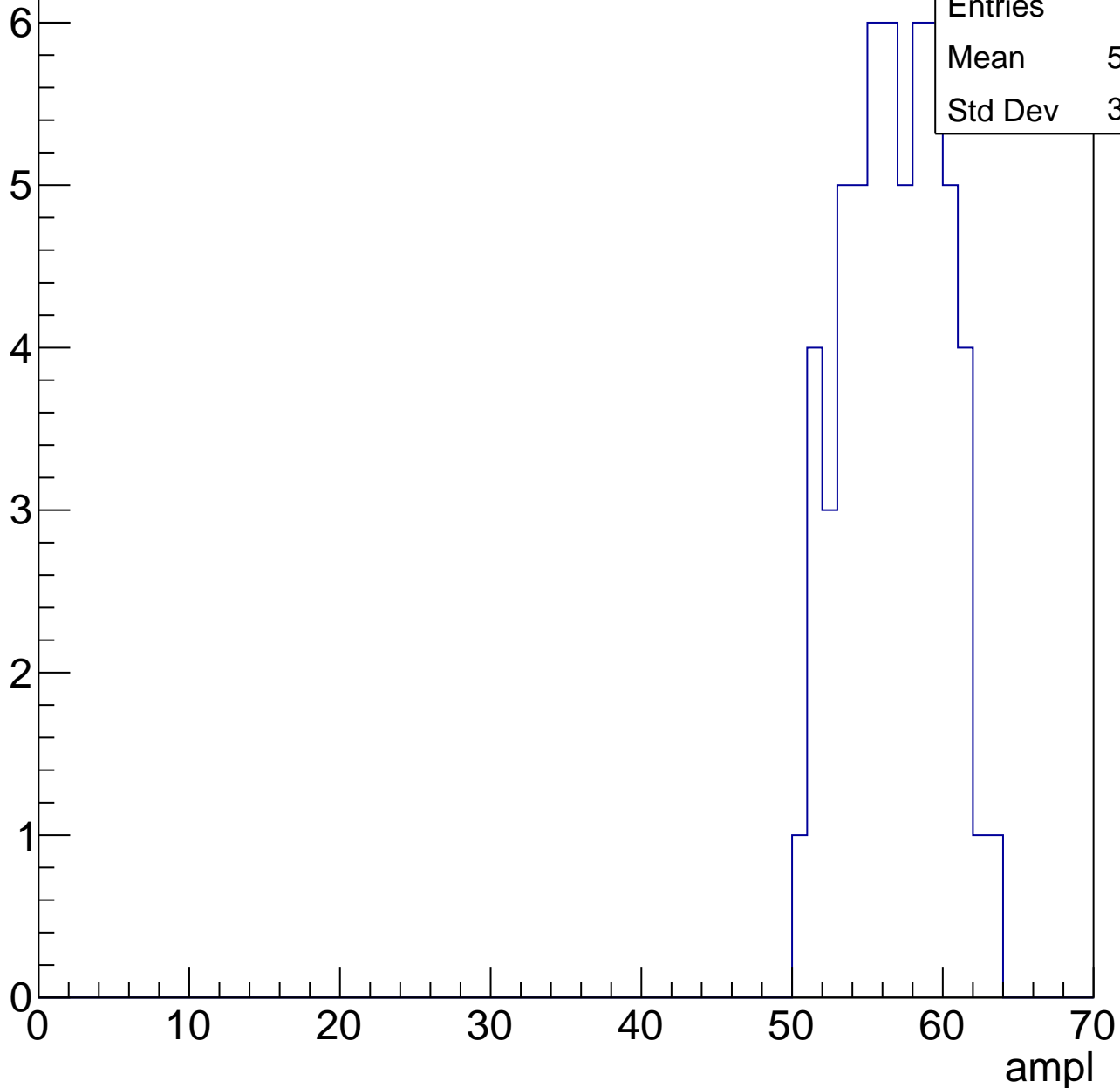
Entries	87
Mean	49.49
Std Dev	3.835



B1L100S, U10-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

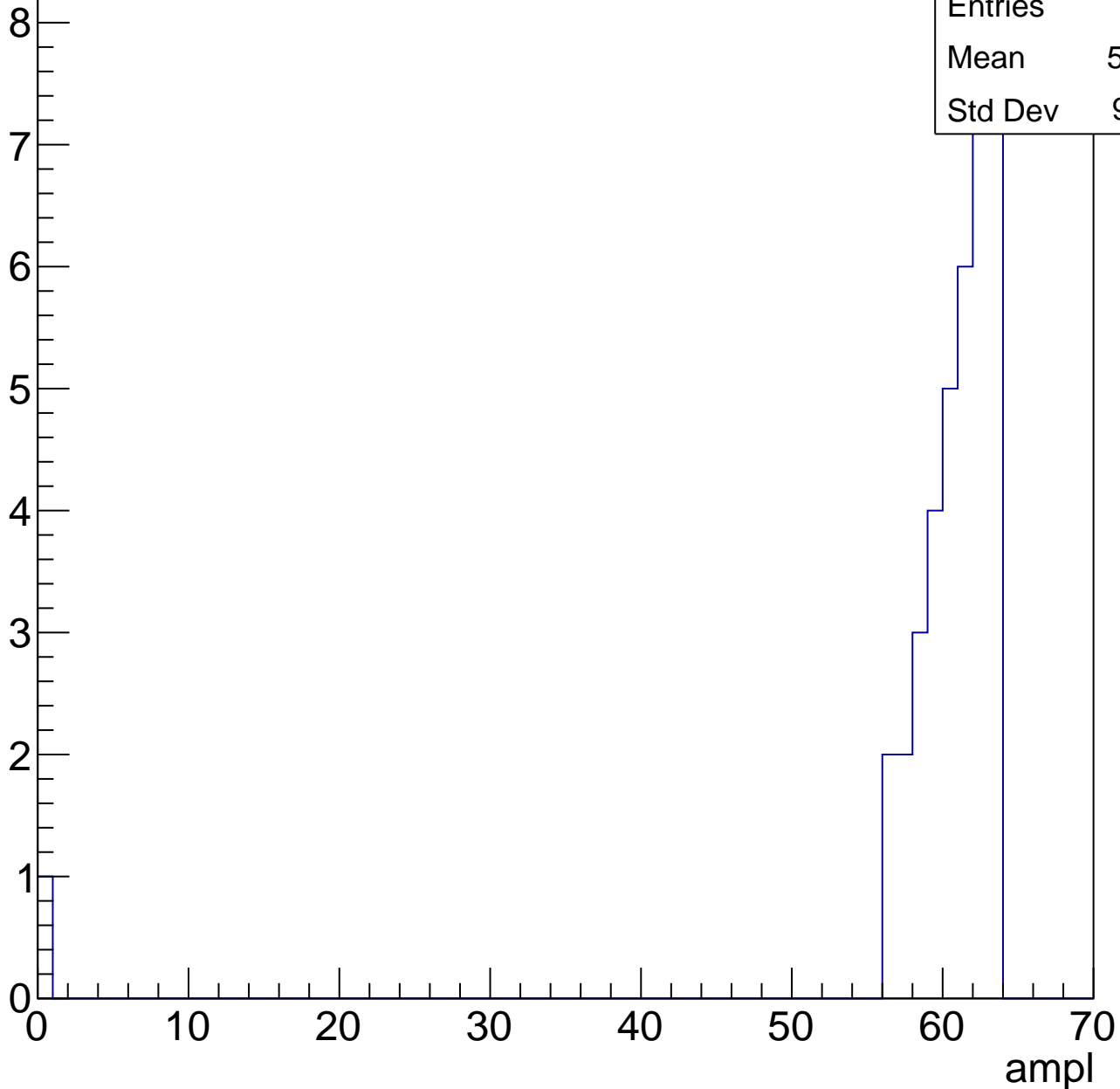


B1L100S, U10-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.03
Std Dev	9.791



B1L100S, U10-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch28, adc0

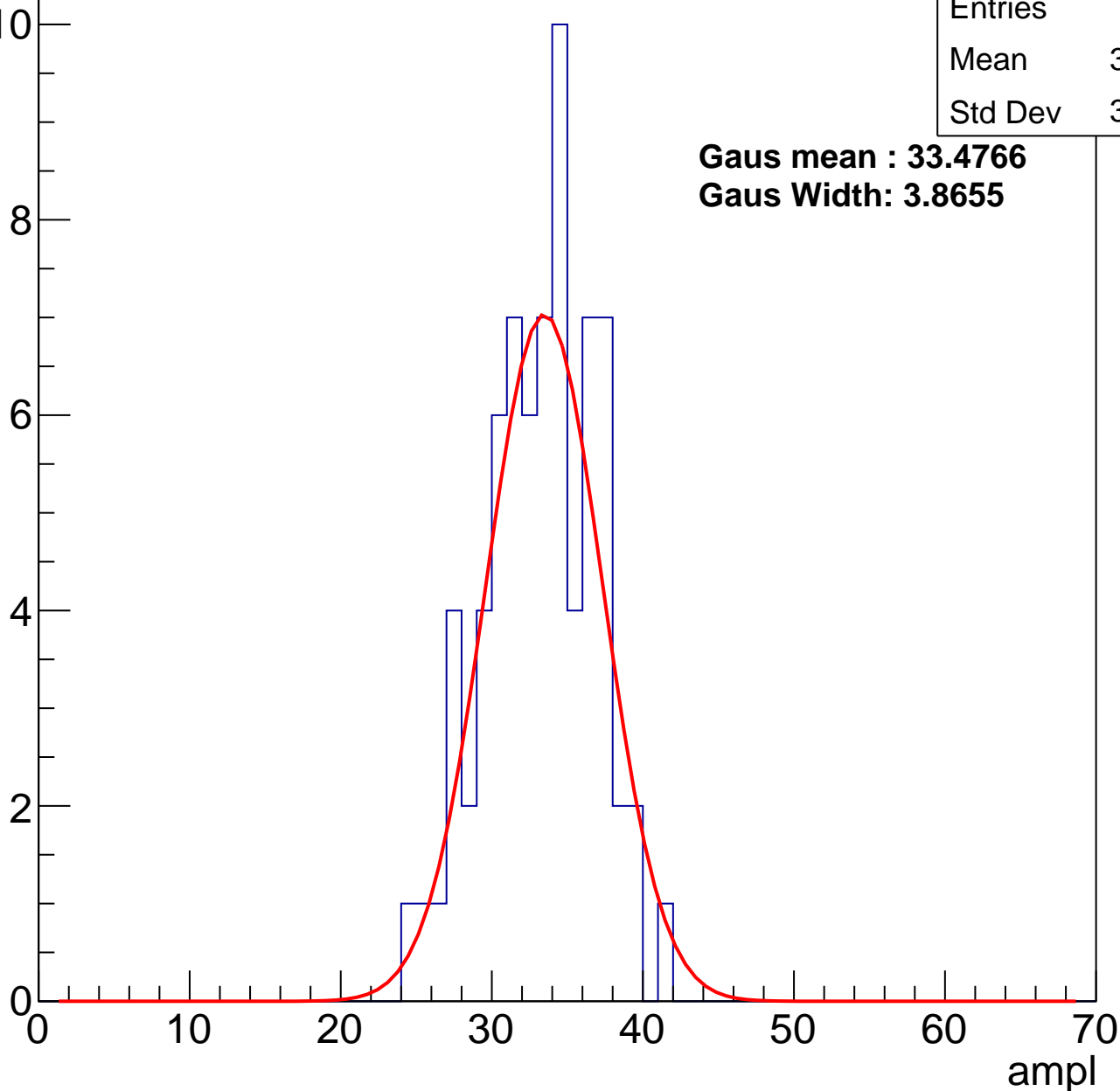
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	32.79
Std Dev	3.582

Gaus mean : 33.4766

Gaus Width: 3.8655



B1L100S, U10-ch28, adc1

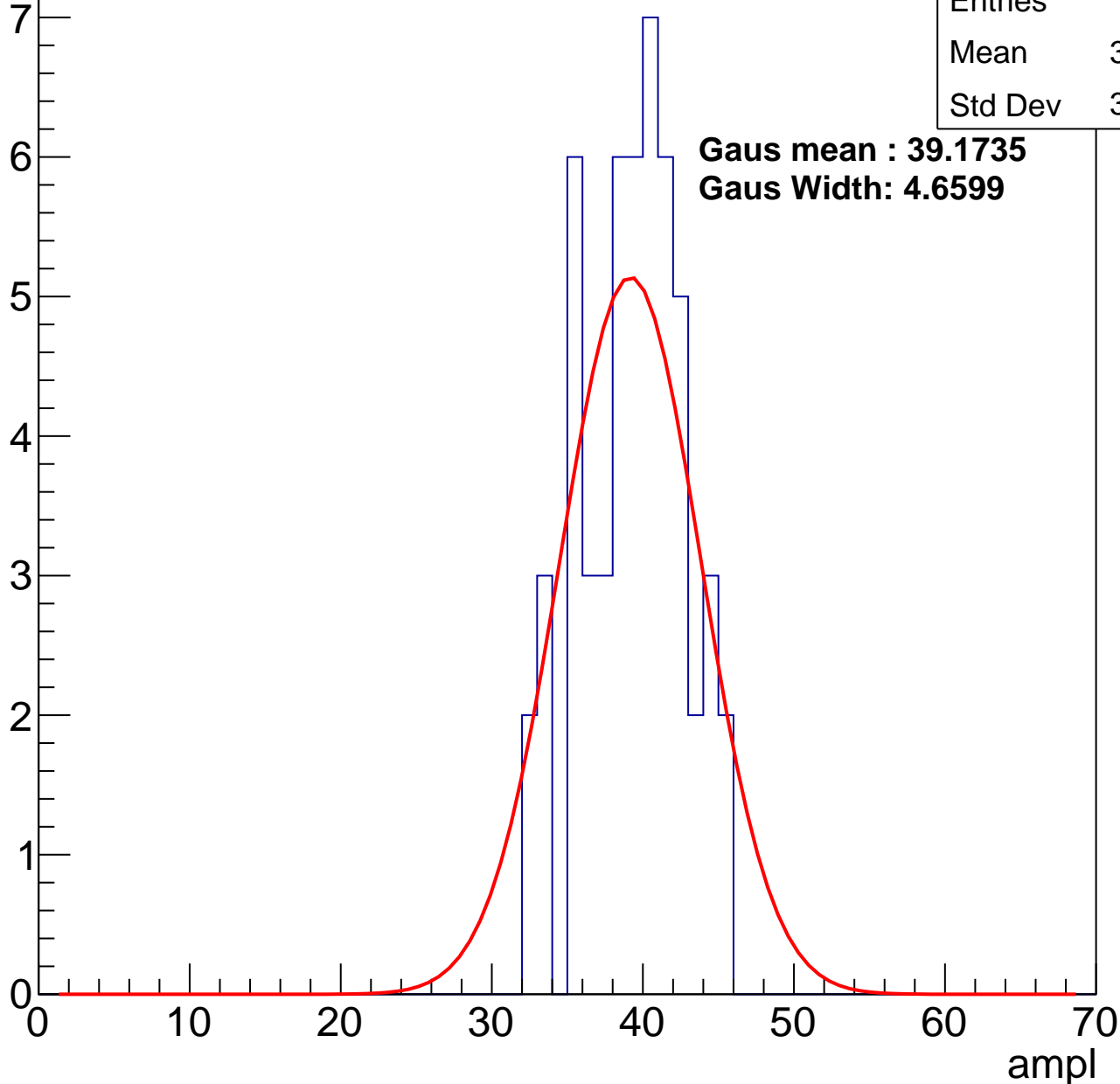
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	38.85
Std Dev	3.336

Gaus mean : 39.1735

Gaus Width: 4.6599



B1L100S, U10-ch28, adc2

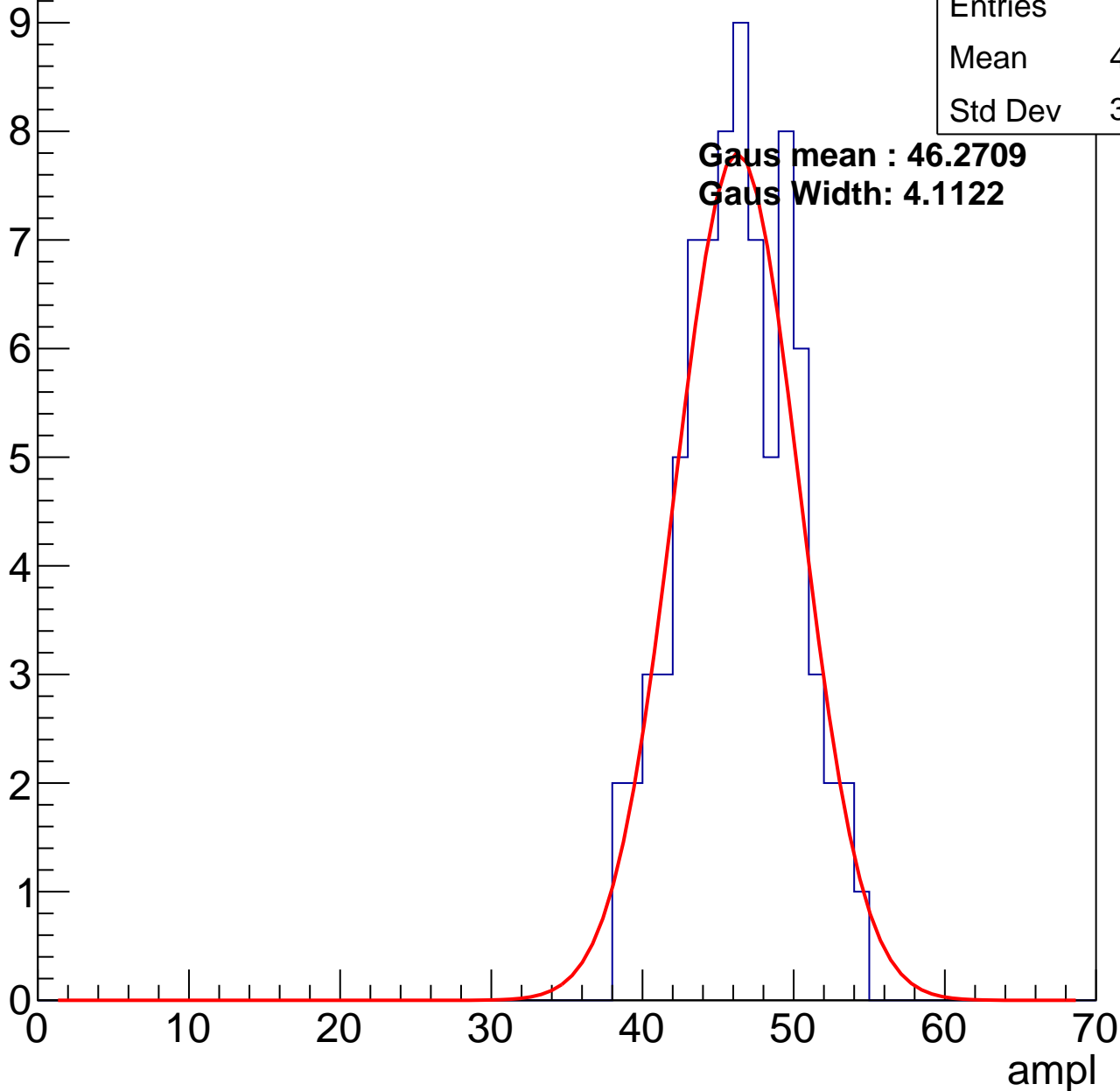
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	45.85
Std Dev	3.692

Gaus mean : 46.2709

Gaus Width: 4.1122

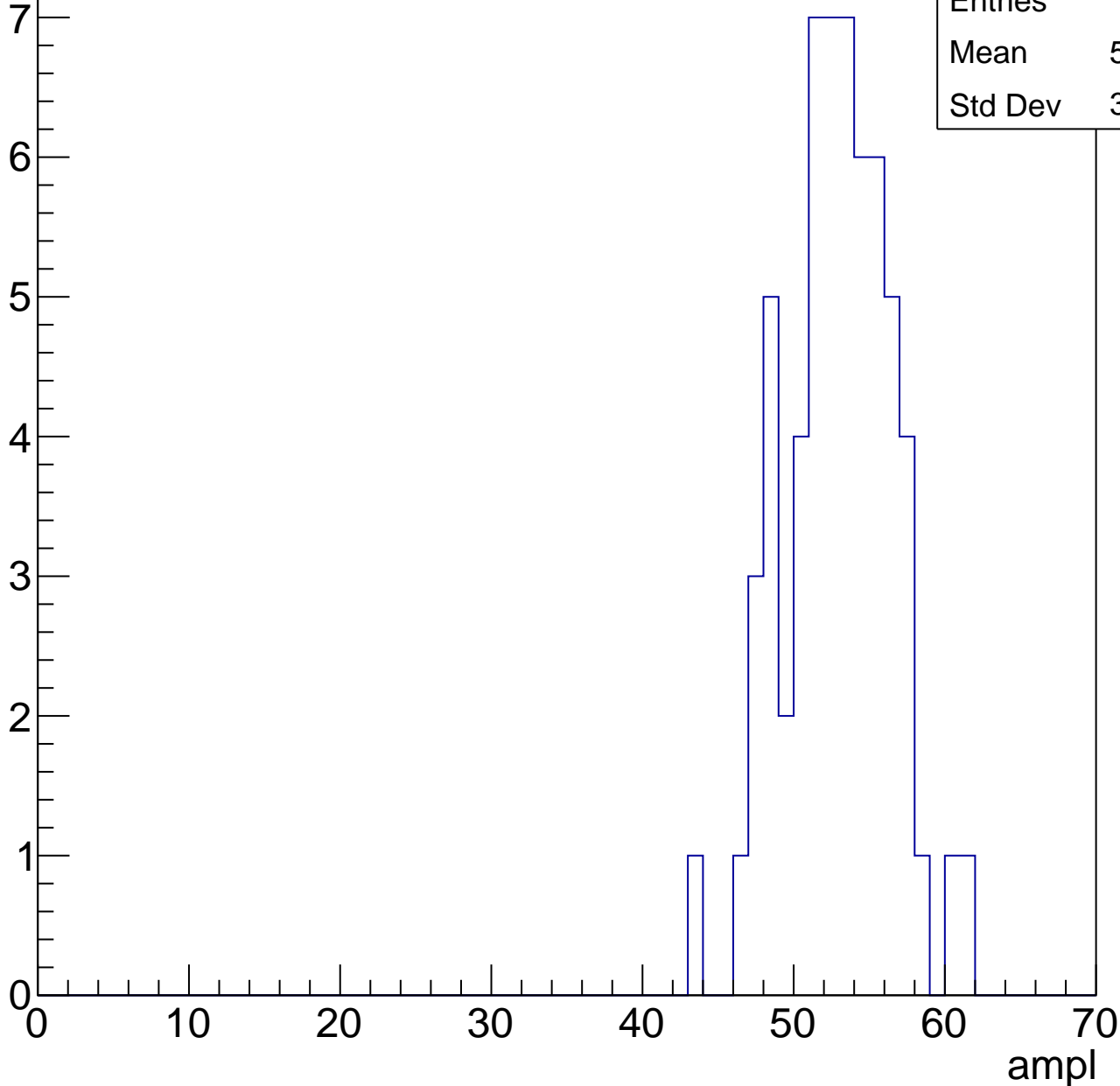


B1L100S, U10-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	52.48
Std Dev	3.495



B1L100S, U10-ch28, adc4

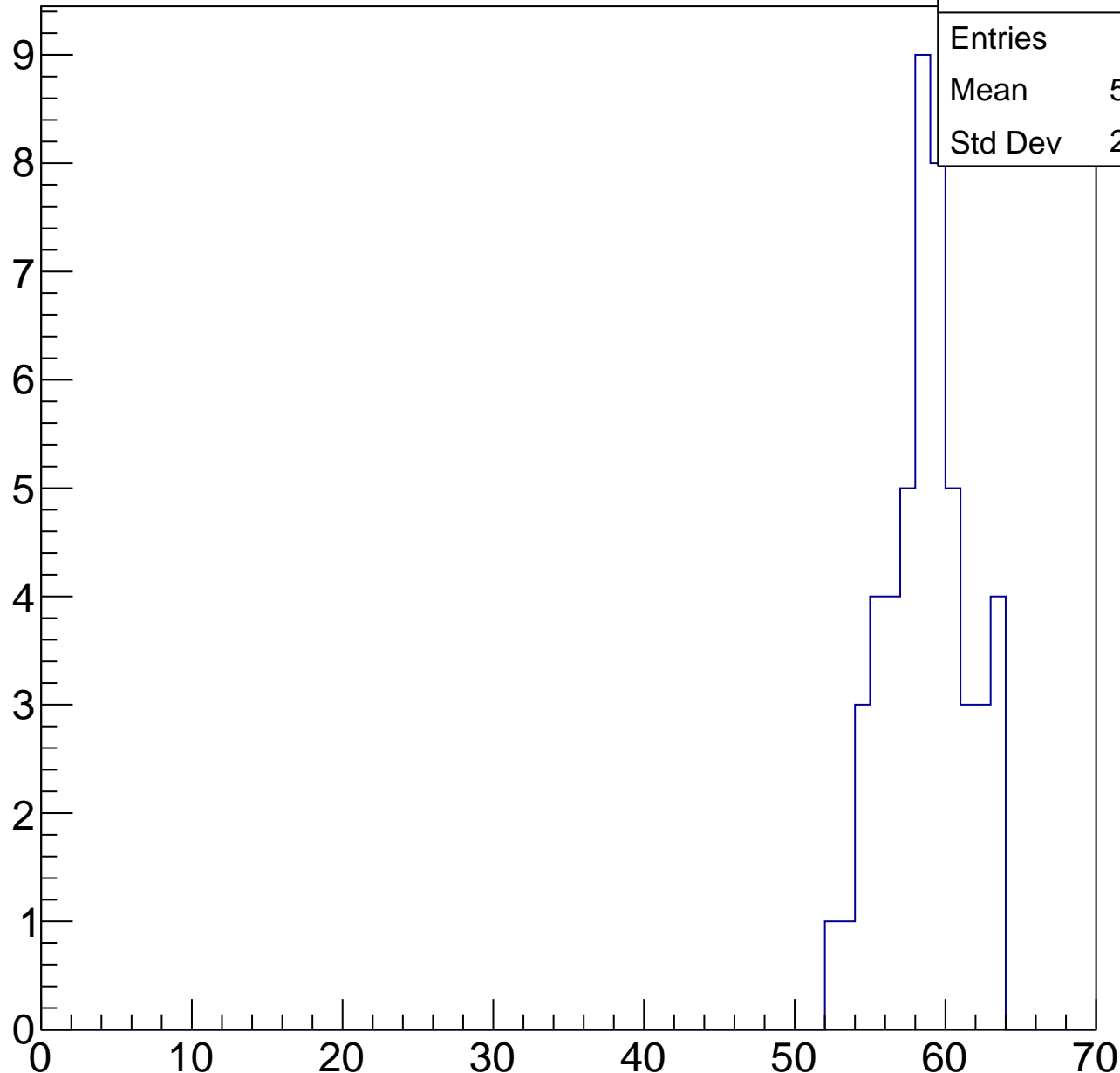
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.22
Std Dev	2.708

ampl

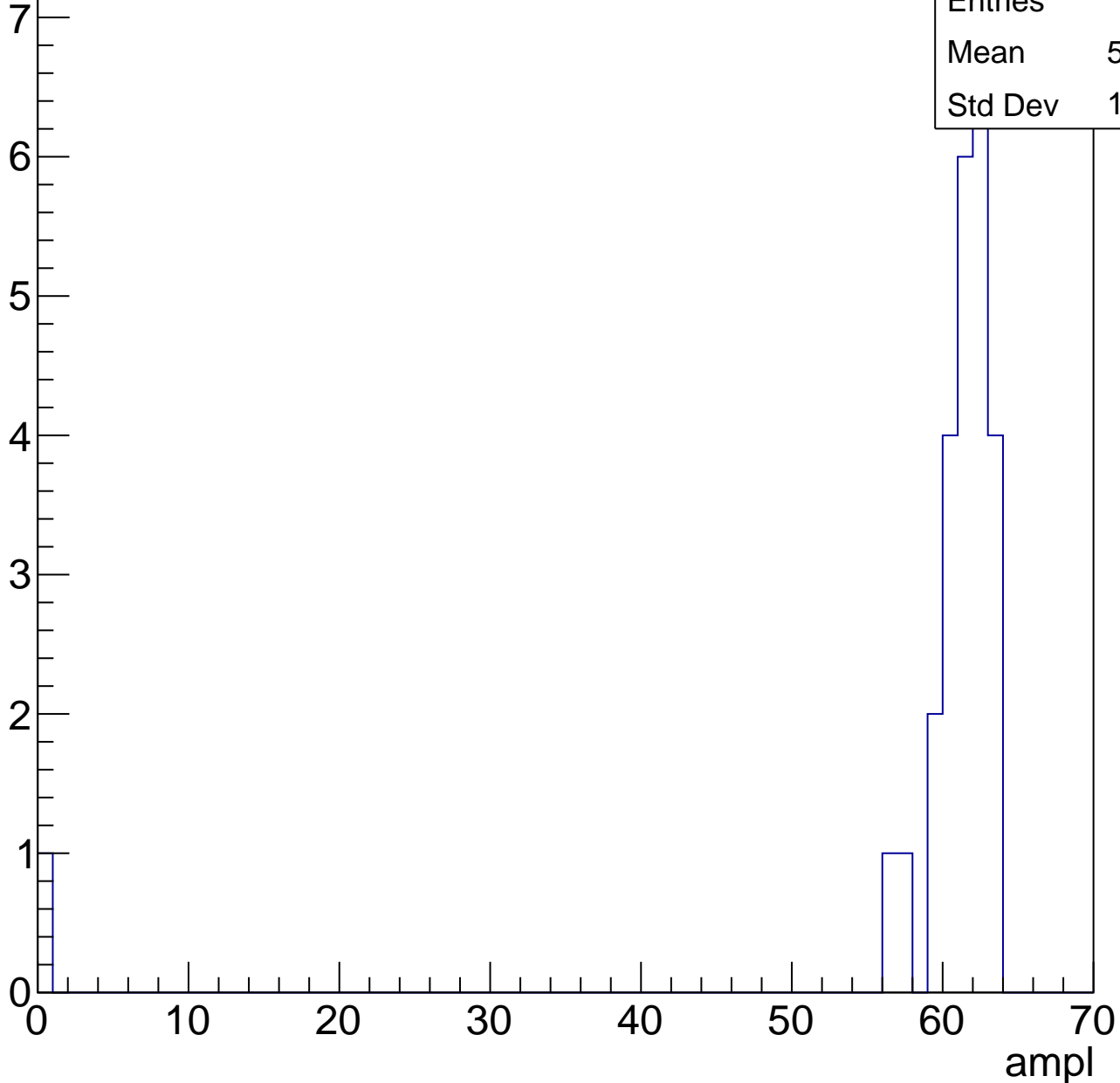


B1L100S, U10-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.58
Std Dev	11.84



B1L100S, U10-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch29, adc0

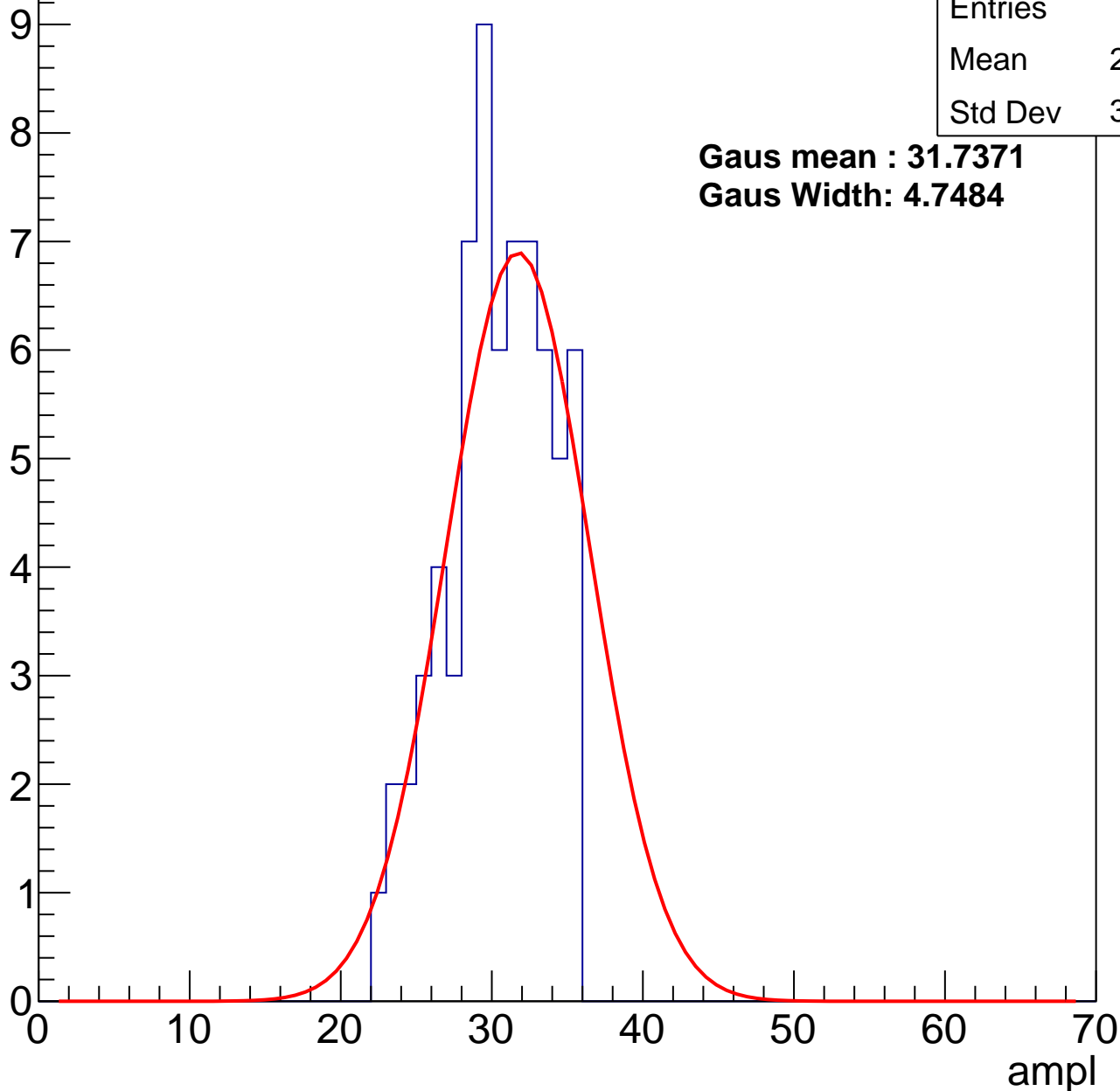
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.88
Std Dev	3.332

Gaus mean : 31.7371

Gaus Width: 4.7484



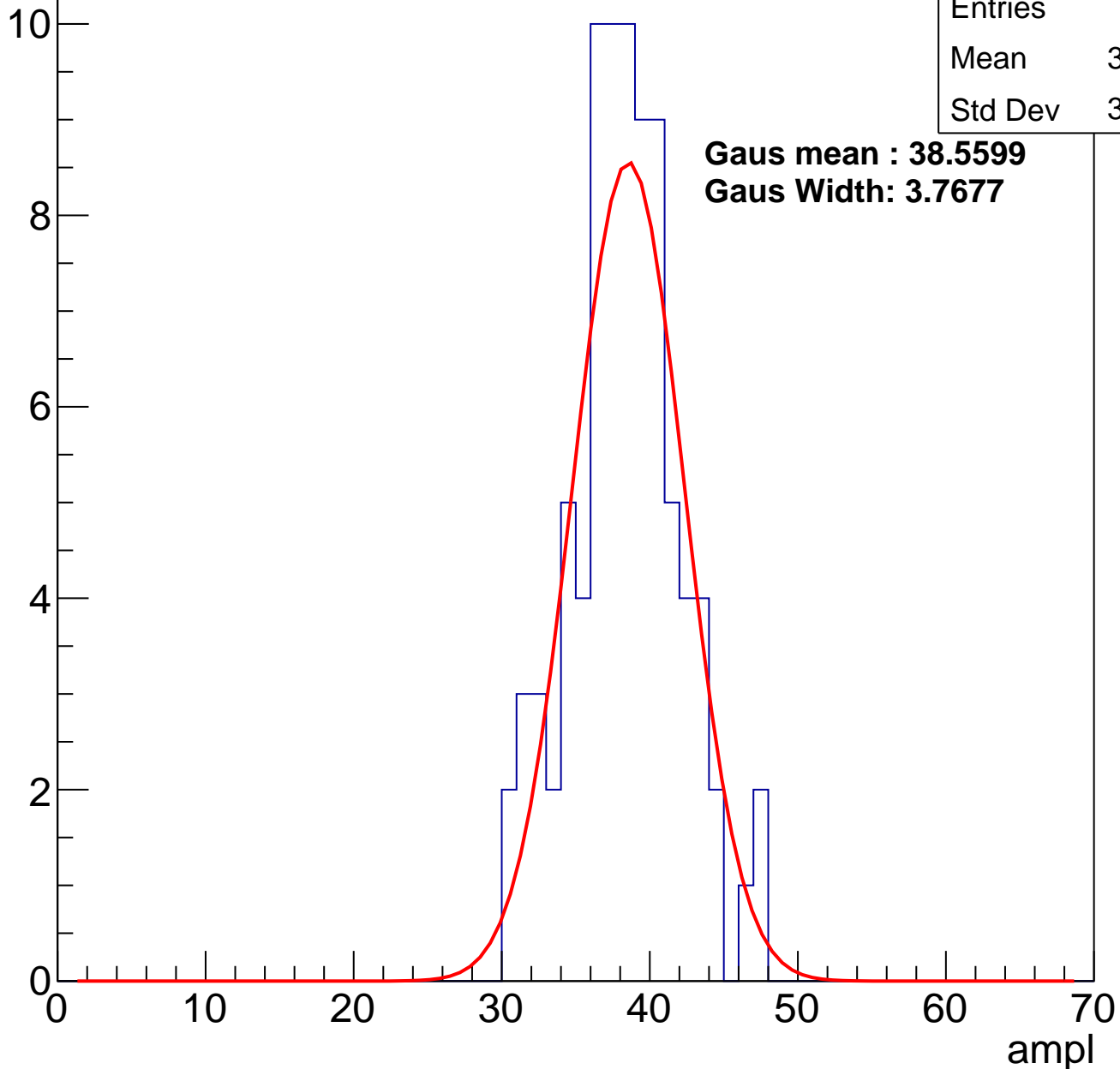
B1L100S, U10-ch29, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	37.87
Std Dev	3.678

Gaus mean : 38.5599
Gaus Width: 3.7677

Entry



B1L100S, U10-ch29, adc2

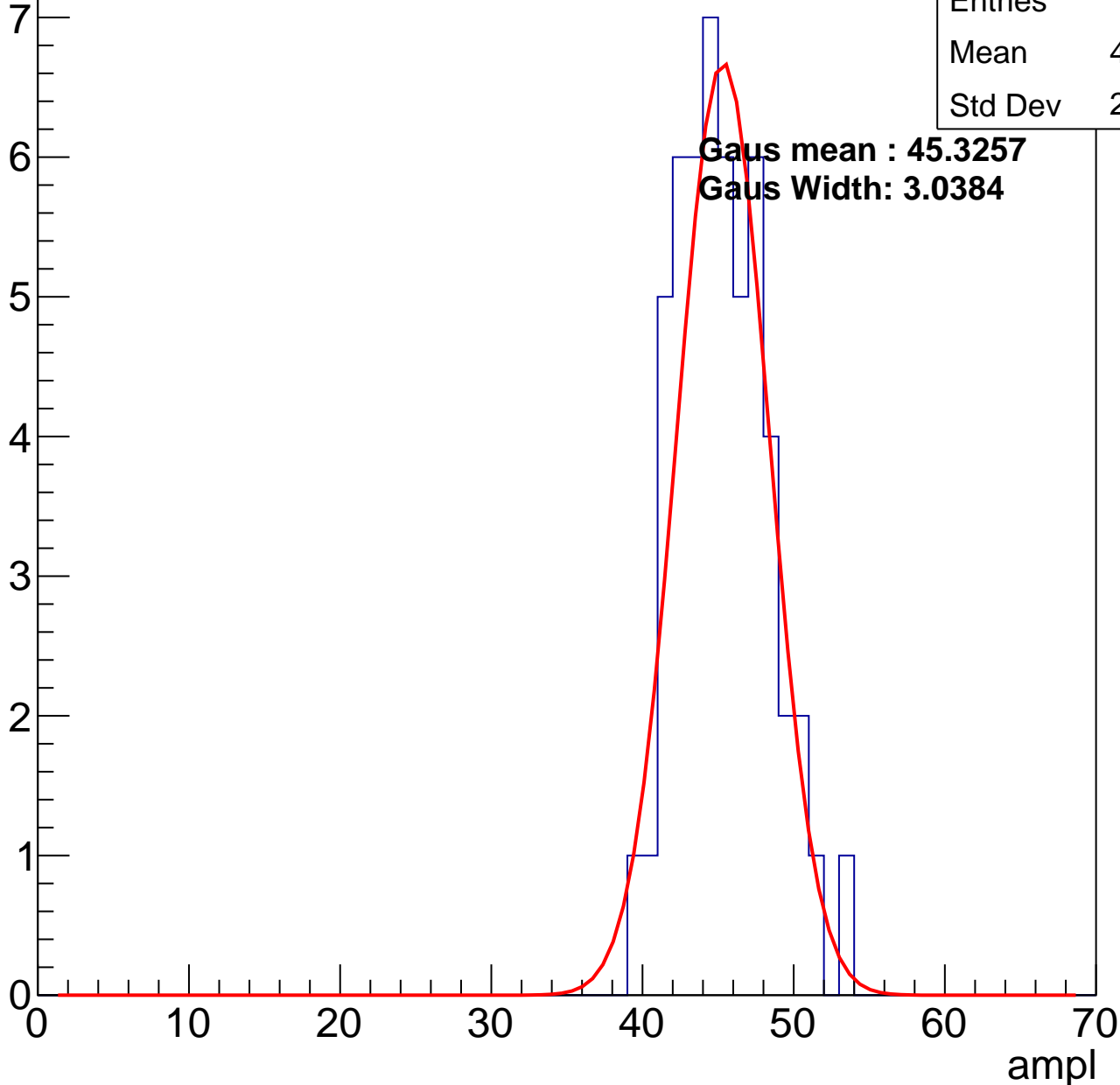
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	44.87
Std Dev	2.984

Gaus mean : 45.3257

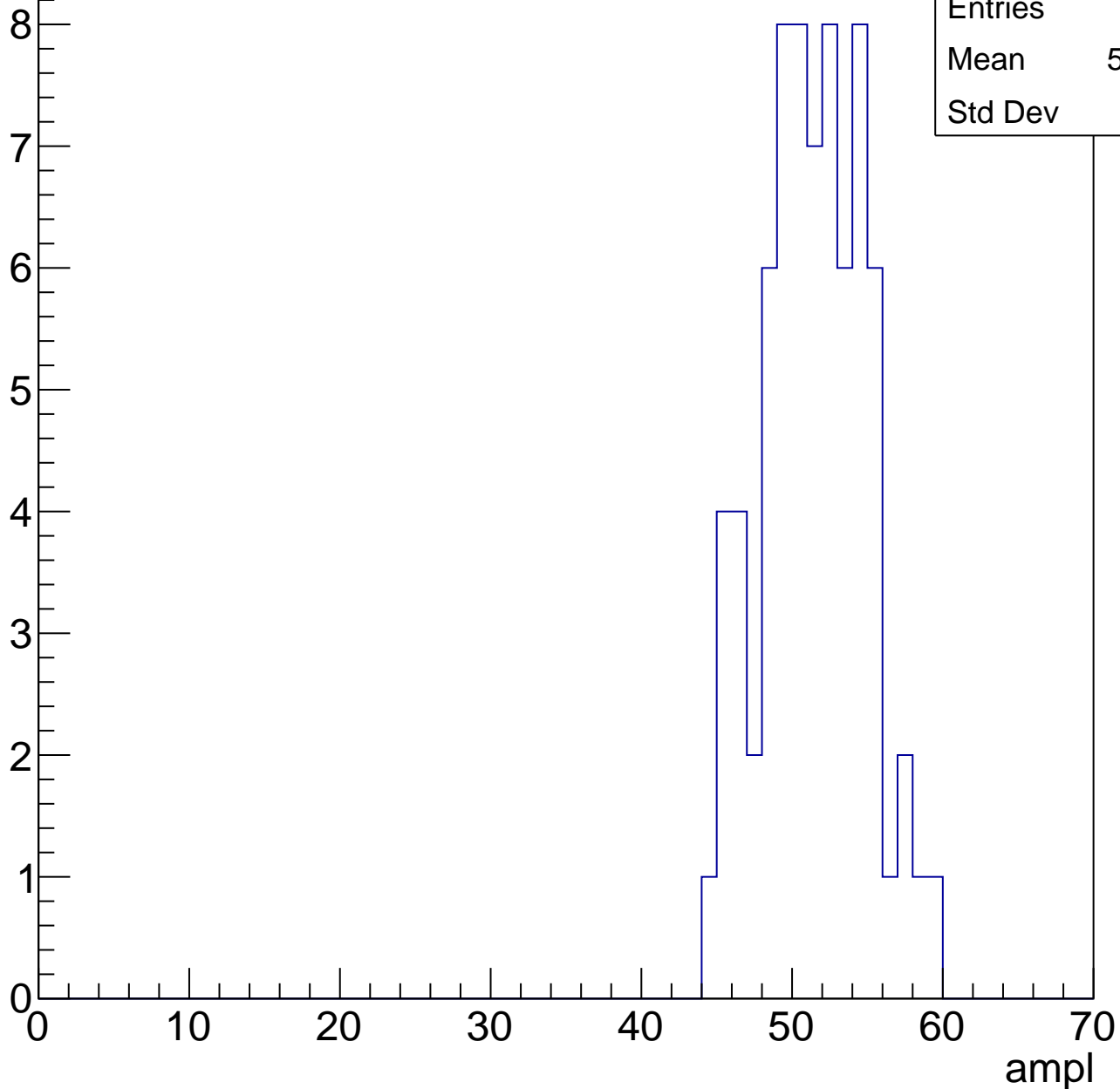
Gaus Width: 3.0384



B1L100S, U10-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

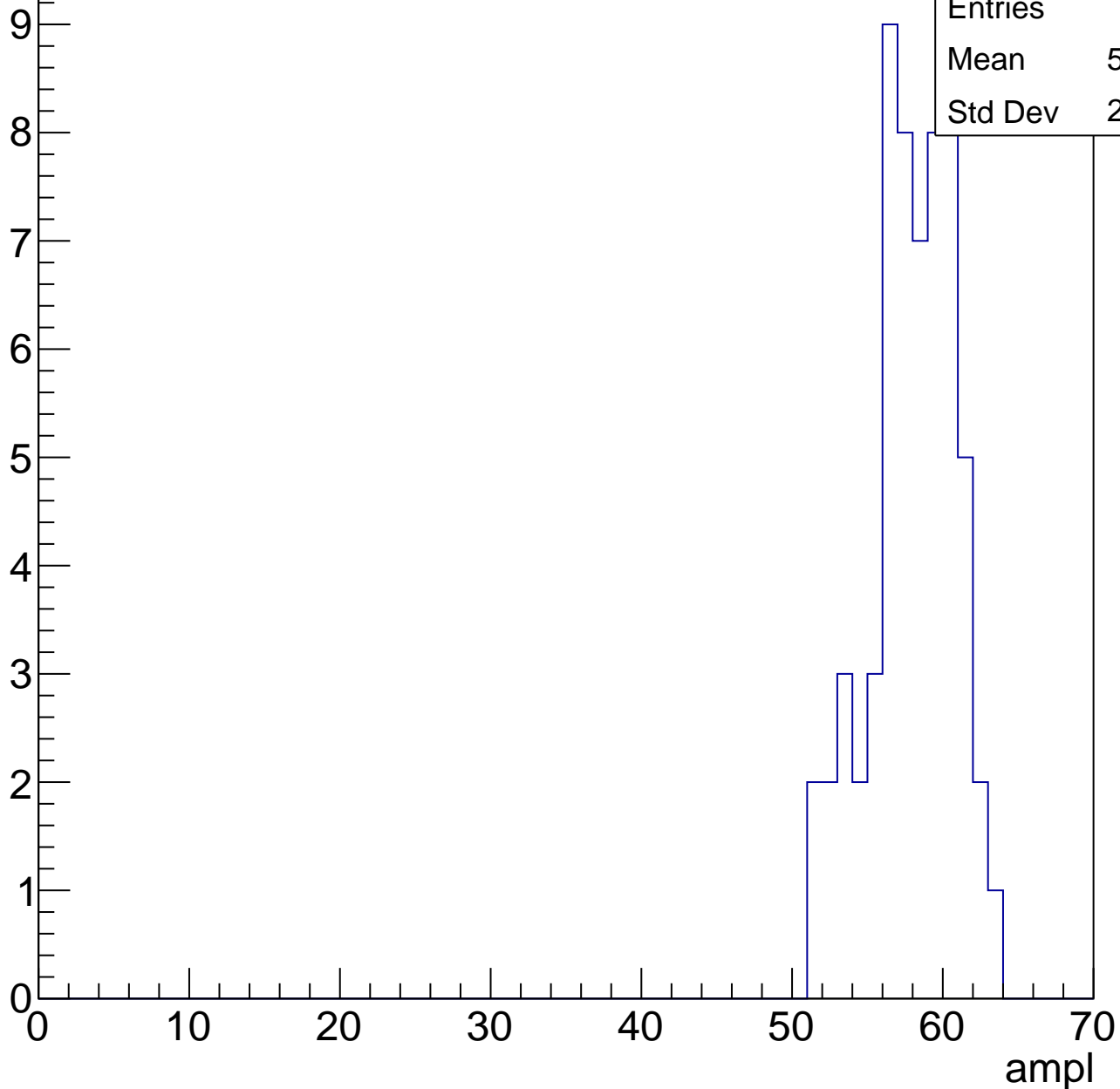


Entries	73
Mean	50.99
Std Dev	3.37

B1L100S, U10-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

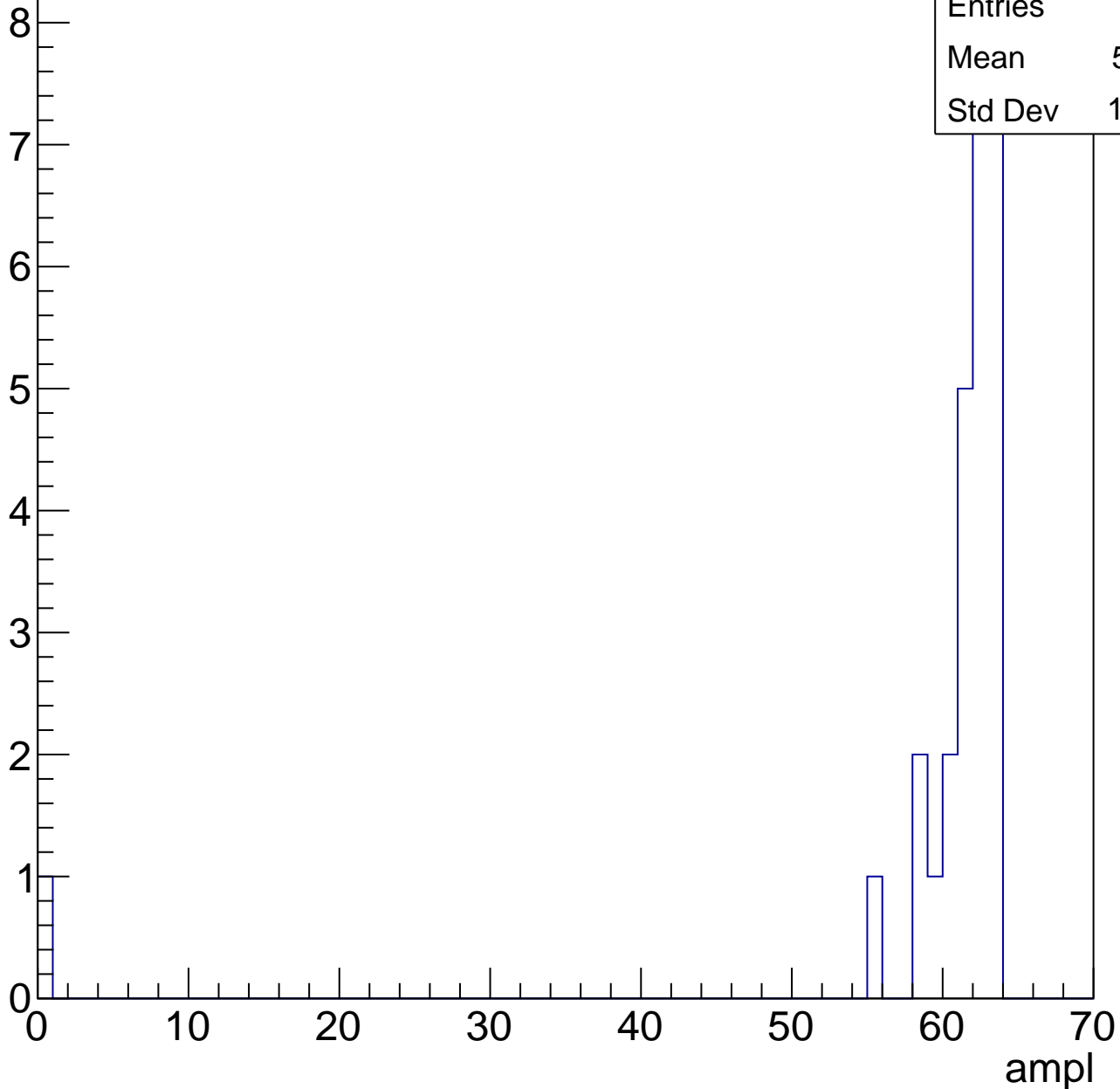


B1L100S, U10-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	59.11
Std Dev	11.53



B1L100S, U10-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U10-ch30, adc0

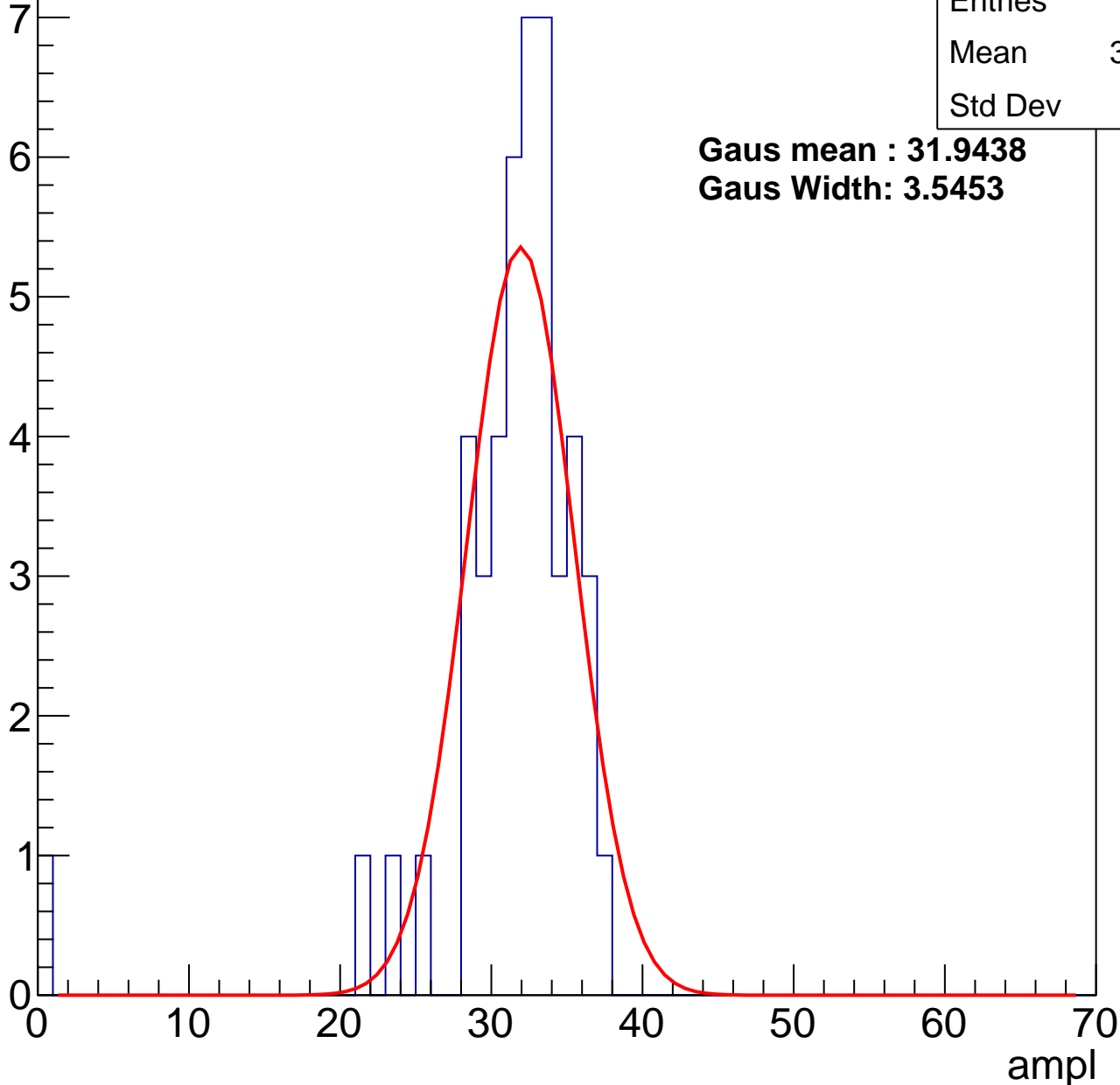
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	30.78
Std Dev	5.61

Gaus mean : 31.9438

Gaus Width: 3.5453



B1L100S, U10-ch30, adc1

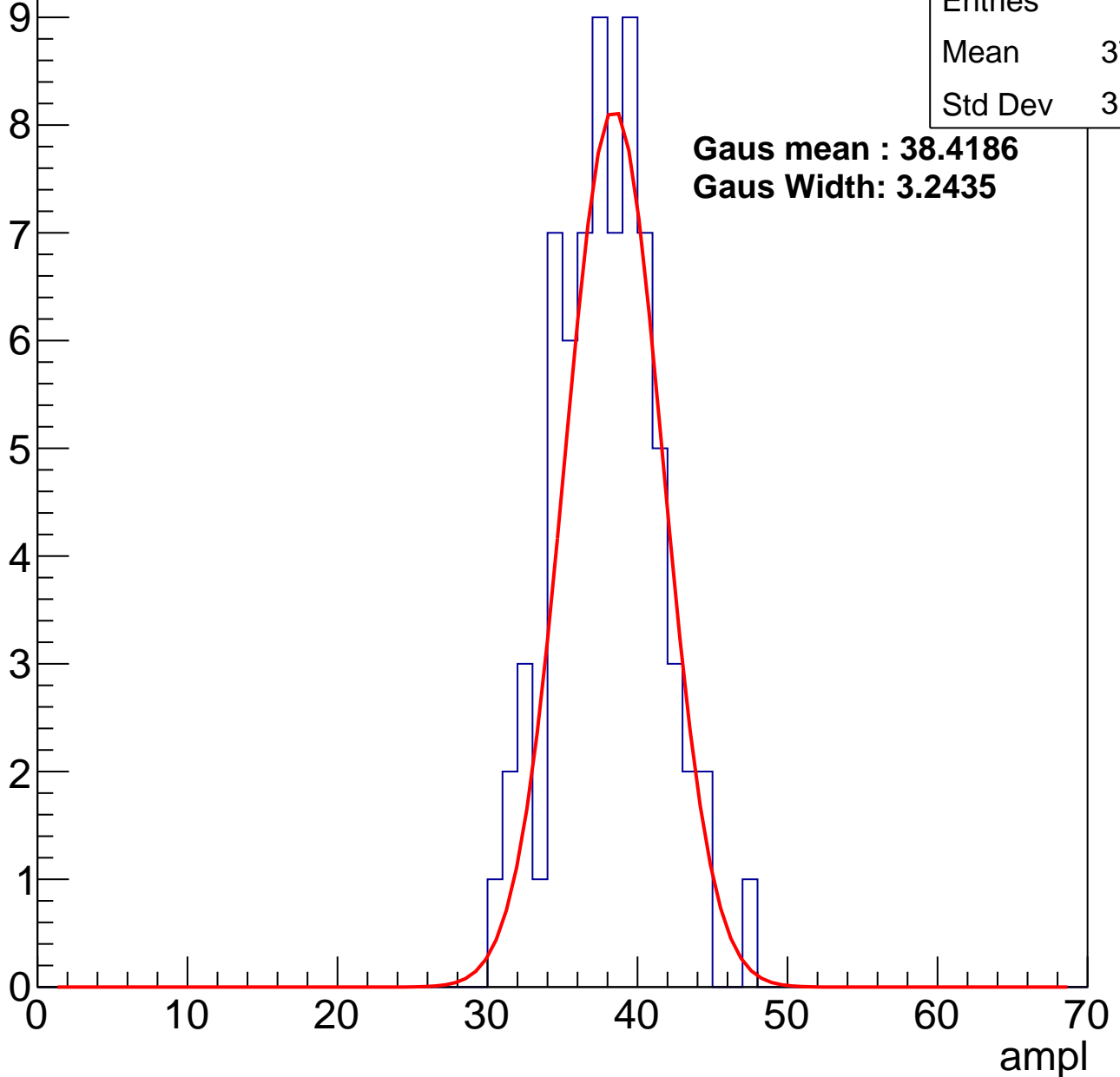
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.54
Std Dev	3.366

Gaus mean : 38.4186

Gaus Width: 3.2435



B1L100S, U10-ch30, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.97
Std Dev	3.517

Gaus mean : 45.6511

Gaus Width: 3.5060

10

8

6

4

2

0

0

10

20

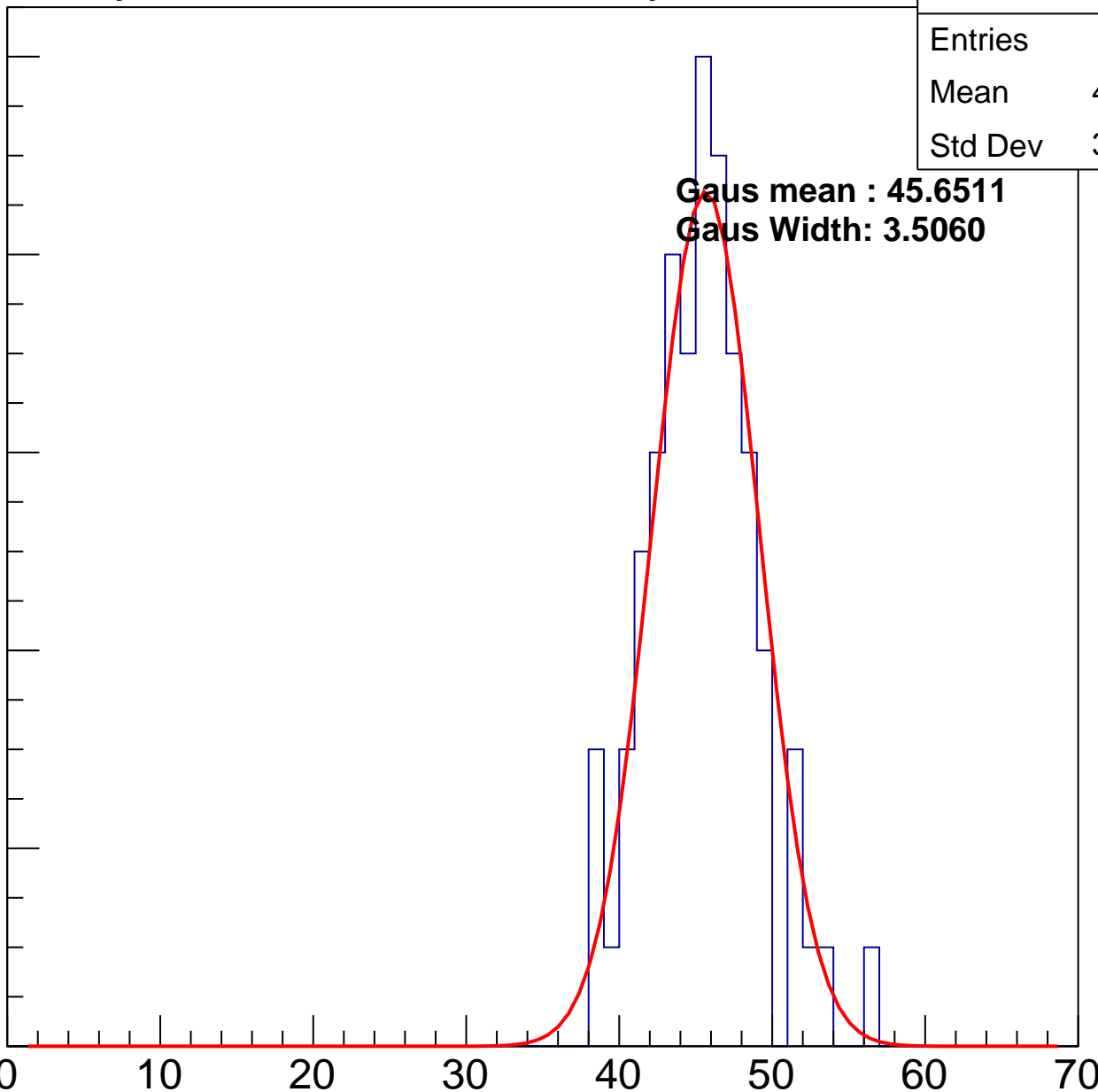
30

40

50

60

ampl

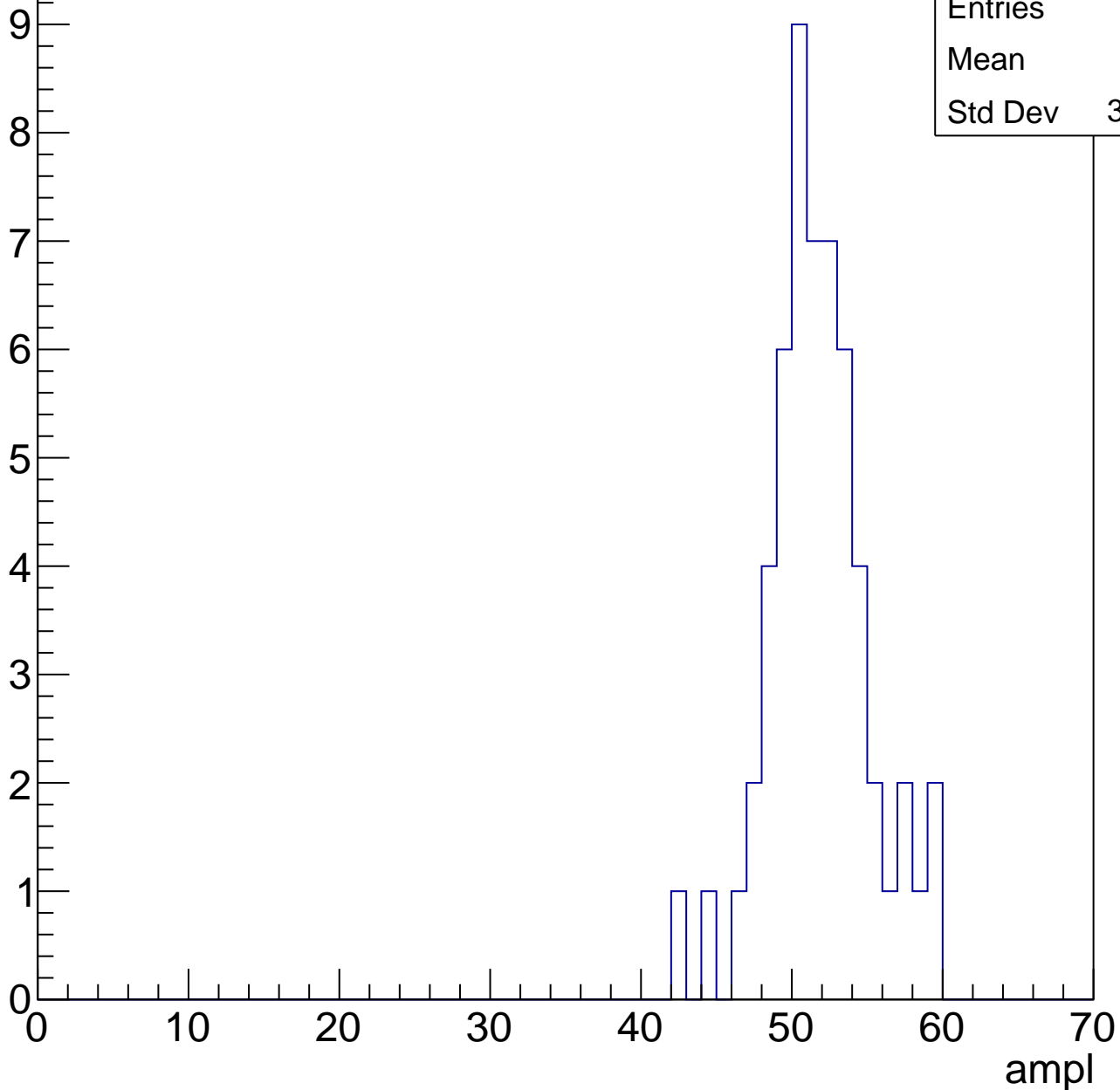


B1L100S, U10-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	51.3
Std Dev	3.338

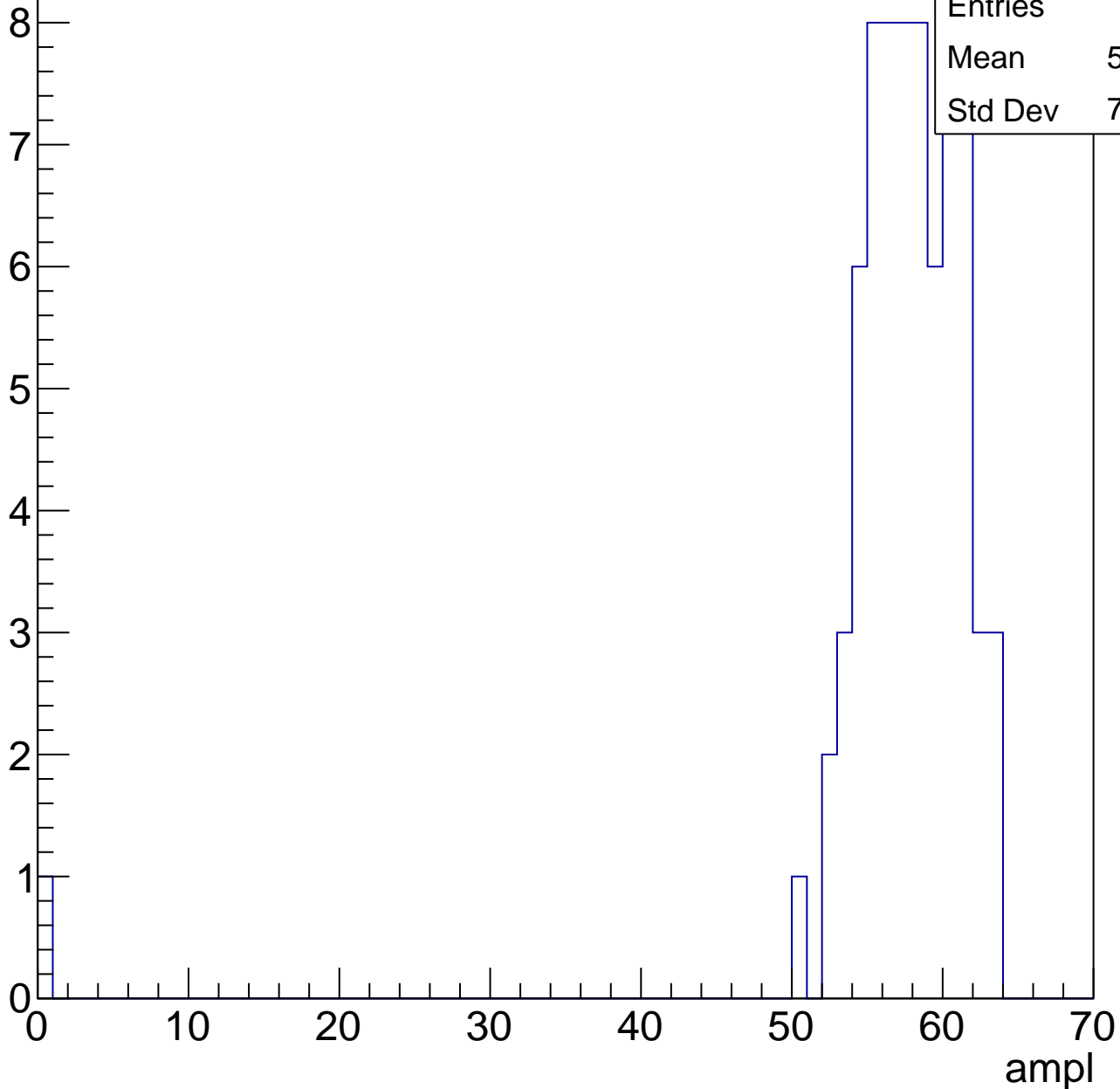


B1L100S, U10-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

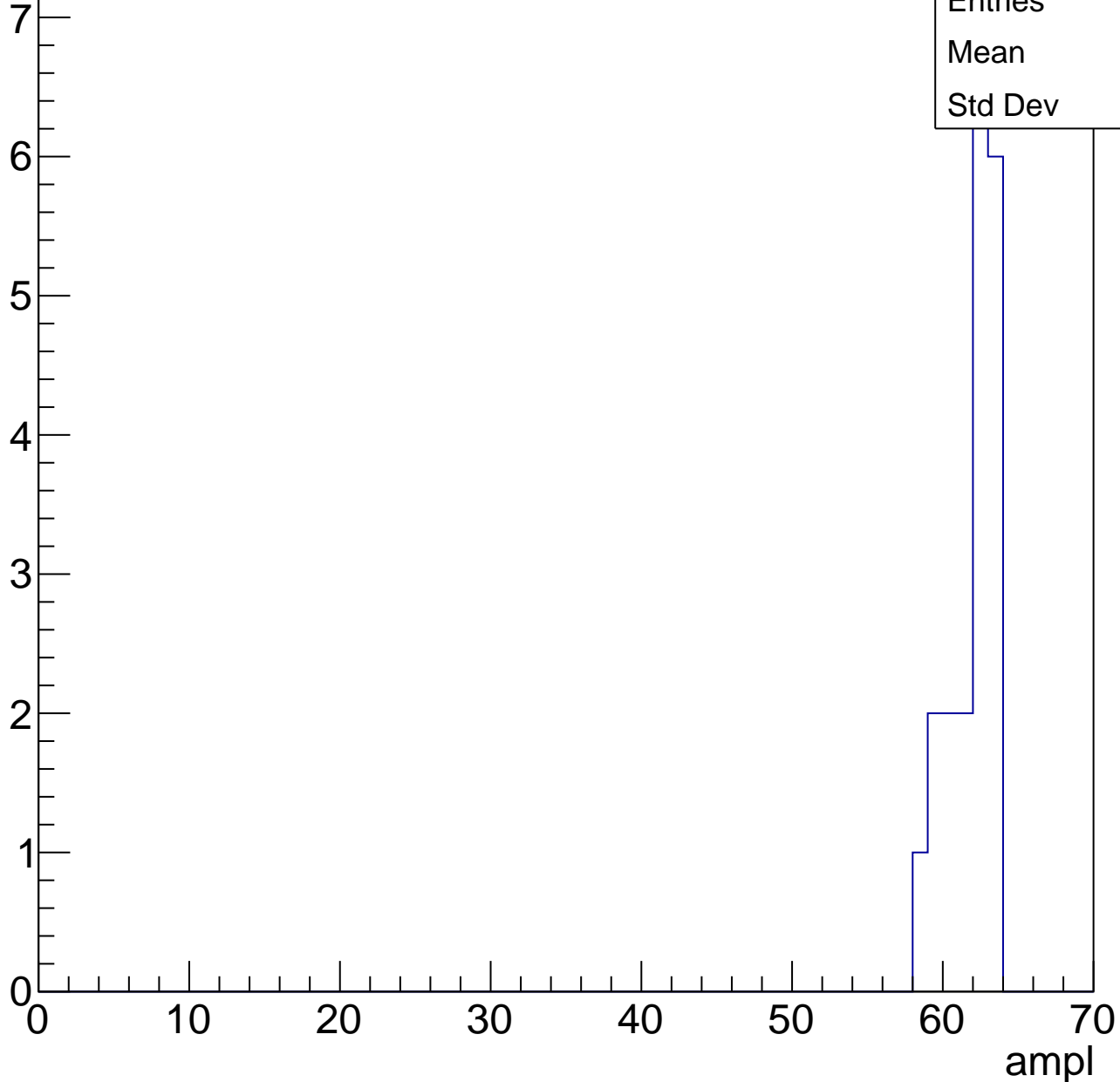
Entries	73
Mean	56.74
Std Dev	7.309



B1L100S, U10-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	20
Mean	61.5
Std Dev	1.5

B1L100S, U10-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch31, adc0

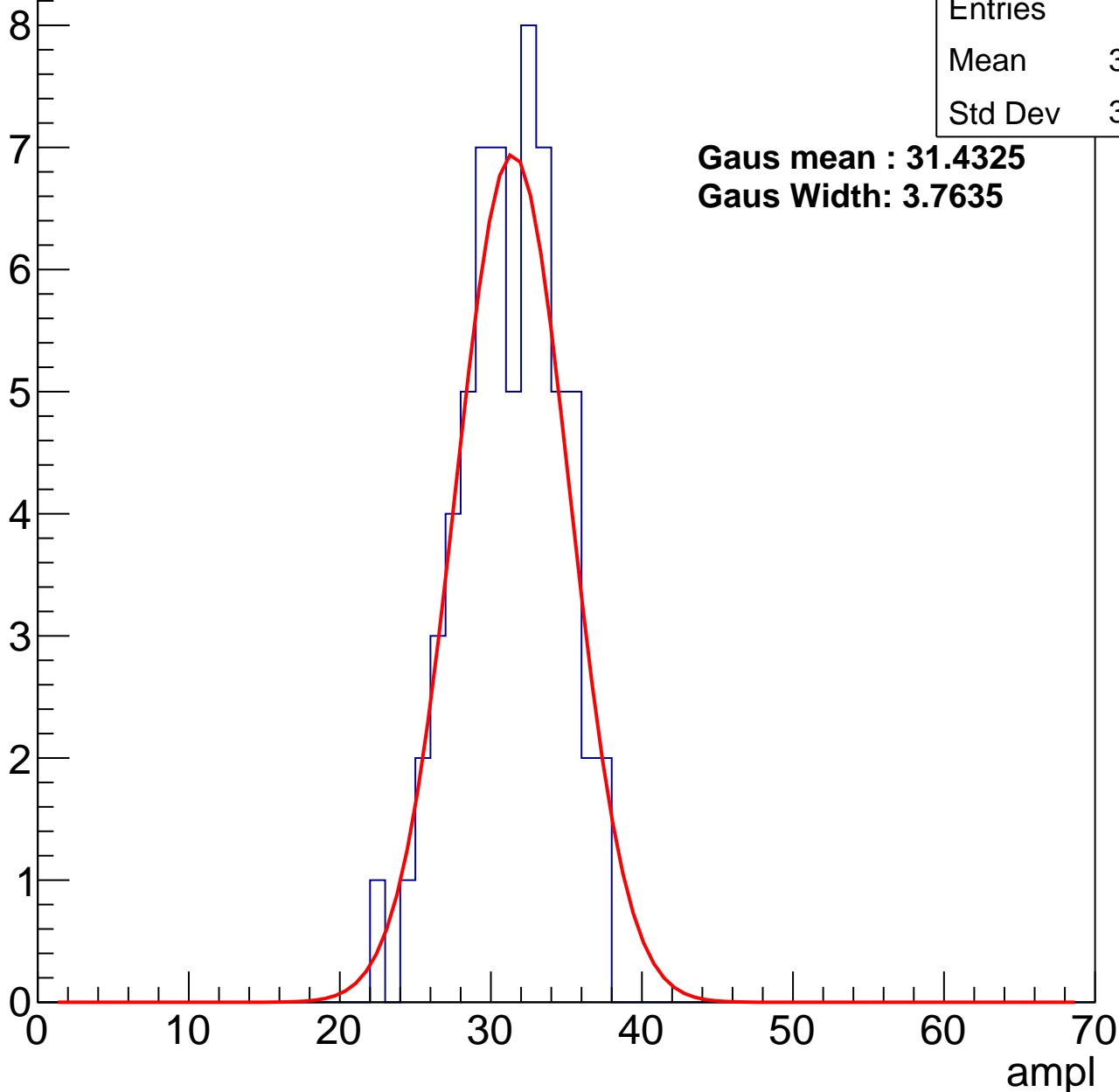
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.75
Std Dev	3.307

Gaus mean : 31.4325

Gaus Width: 3.7635



B1L100S, U10-ch31, adc1

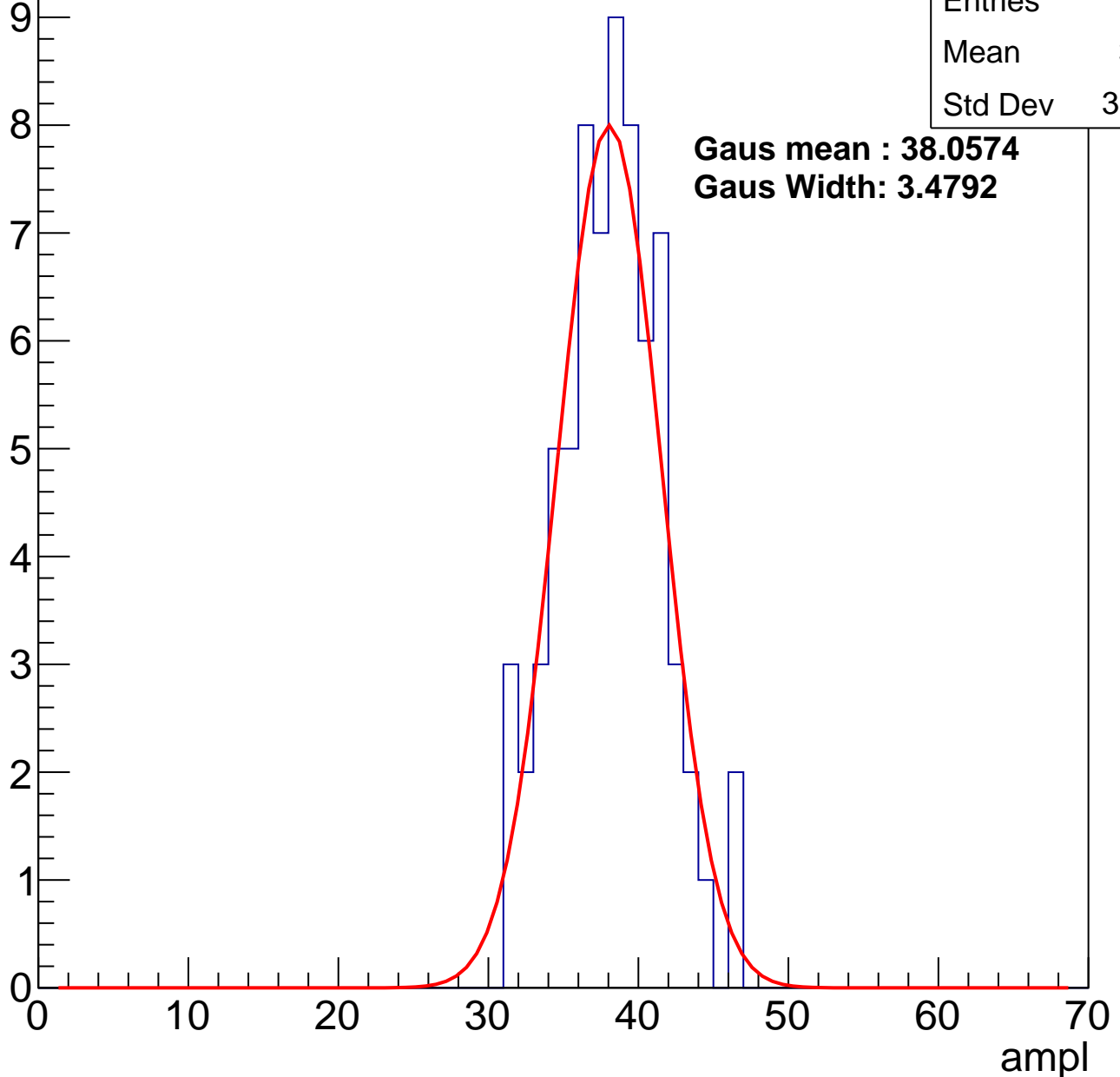
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.7
Std Dev	3.375

Gaus mean : 38.0574

Gaus Width: 3.4792



B1L100S, U10-ch31, adc2

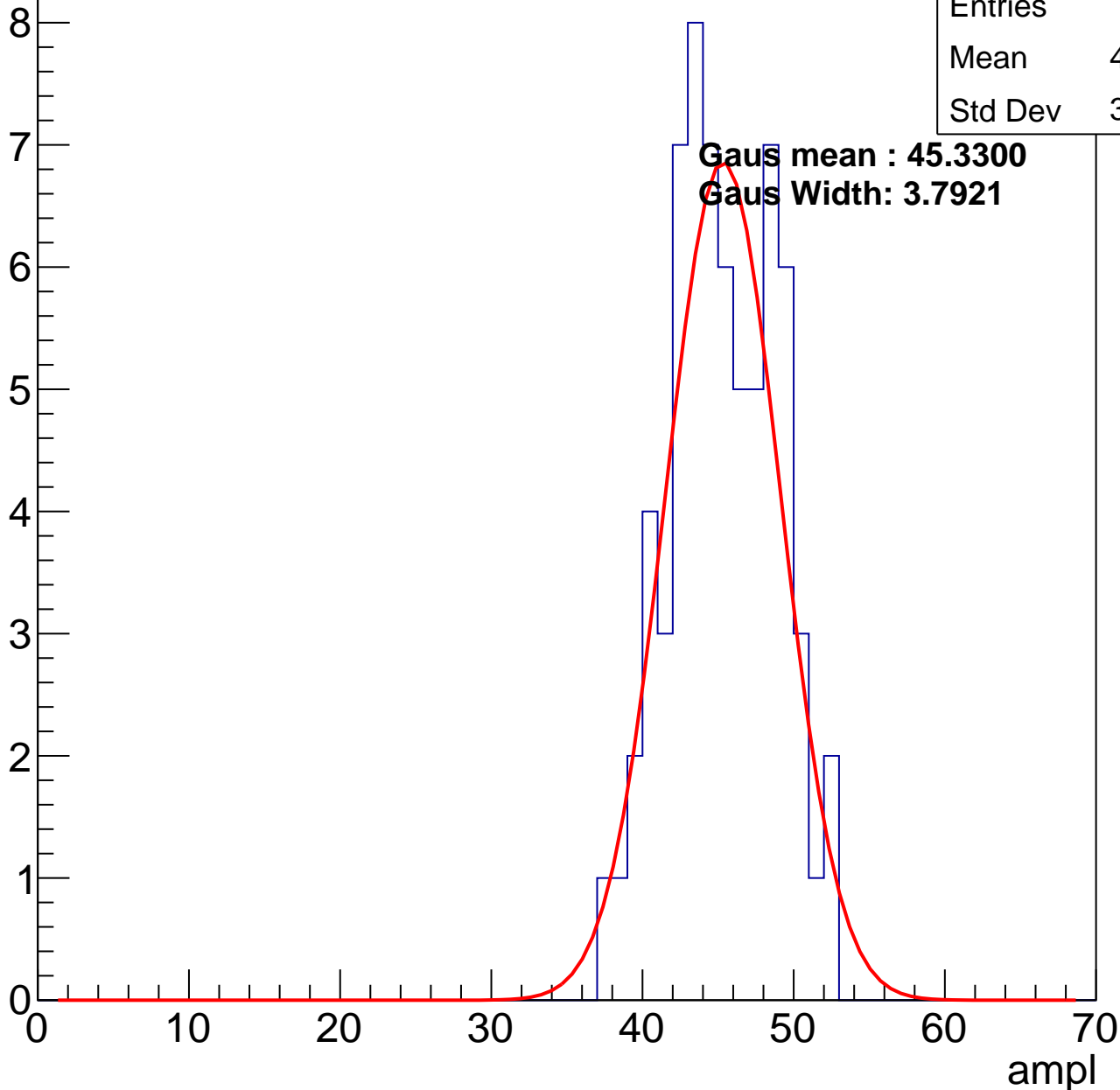
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.88
Std Dev	3.487

Gaus mean : 45.3300

Gaus Width: 3.7921

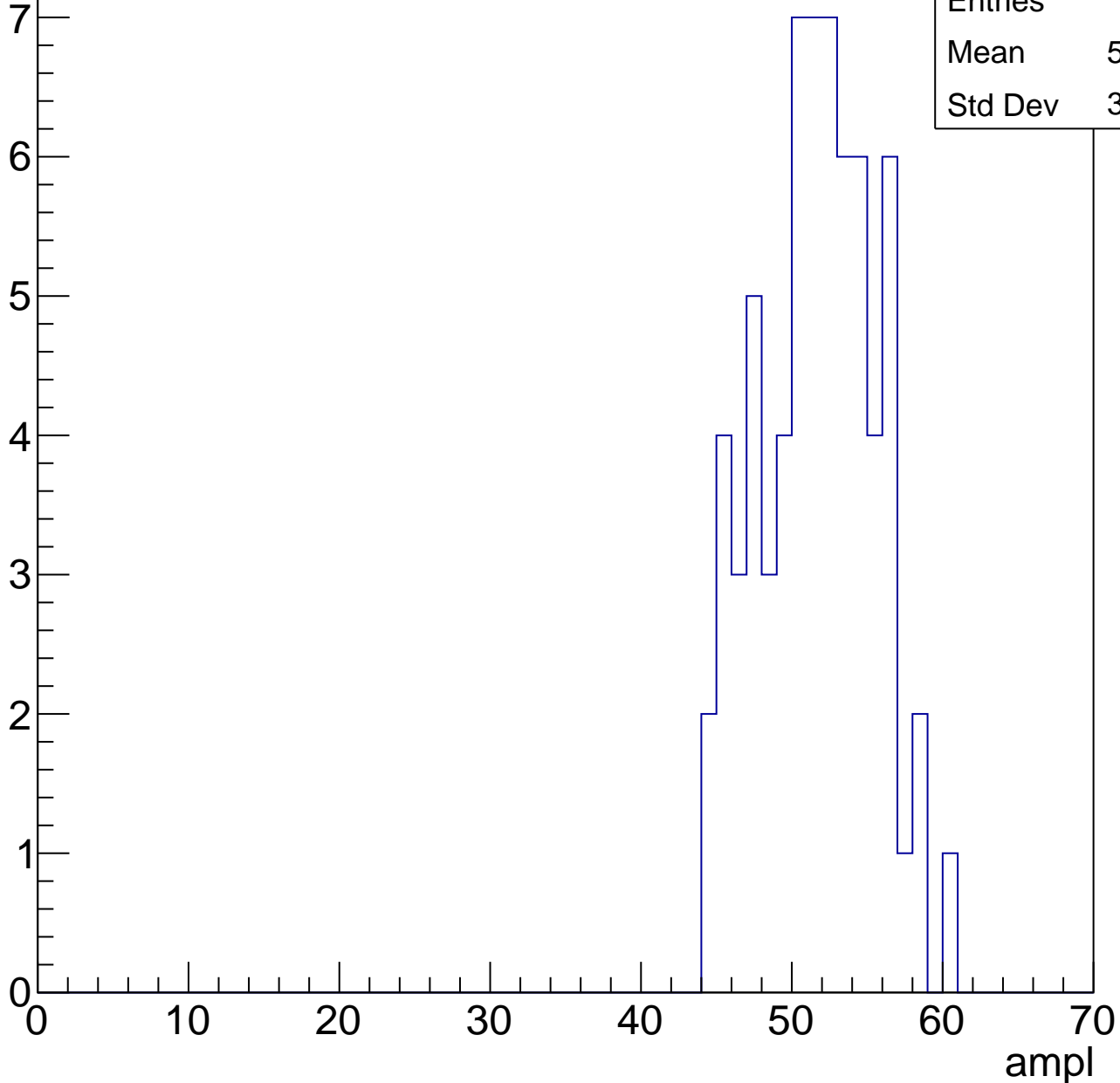


B1L100S, U10-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

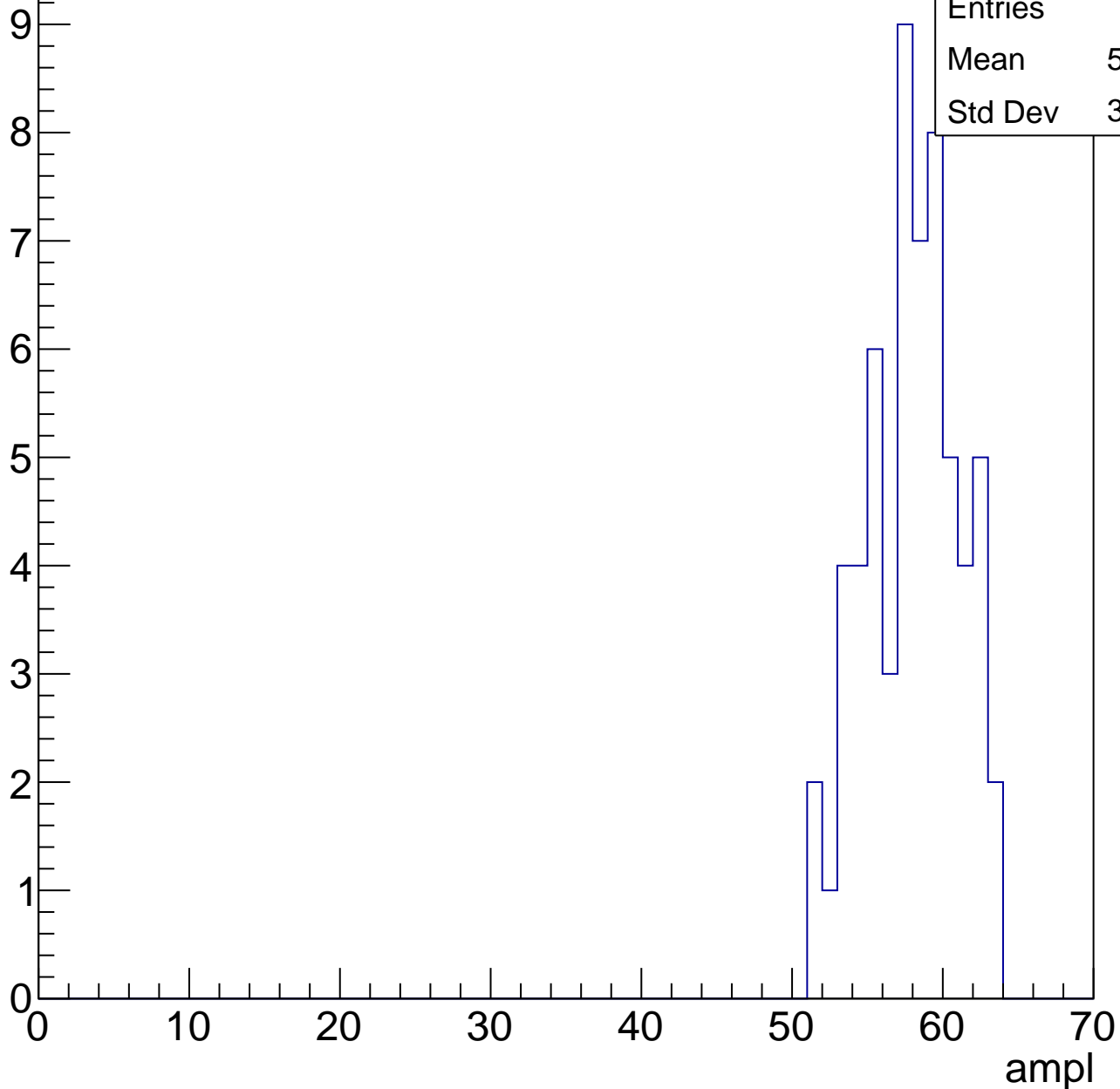
Entries	68
Mean	51.22
Std Dev	3.753



B1L100S, U10-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



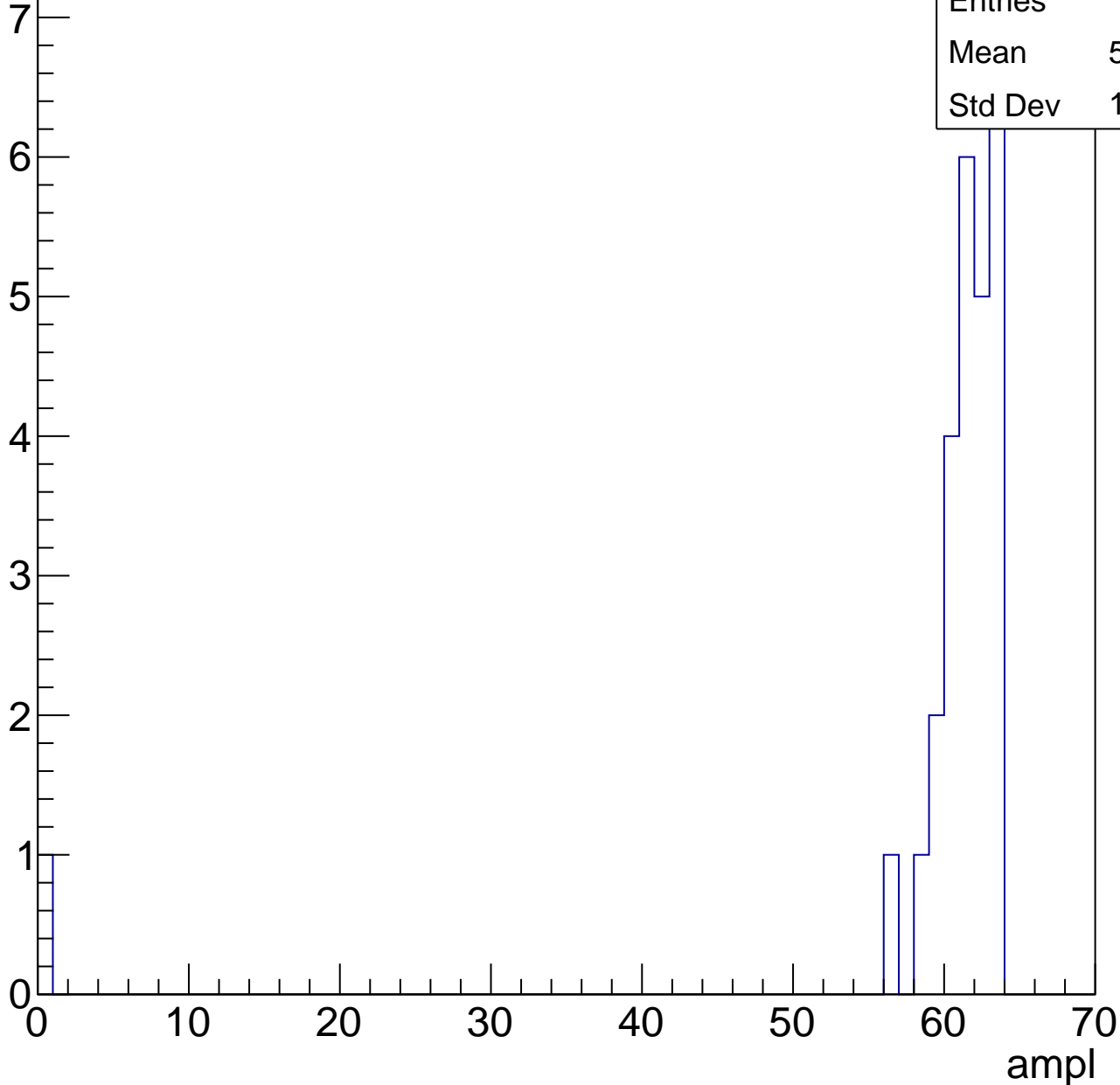
Entries	60
Mean	57.52
Std Dev	3.036

B1L100S, U10-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.85
Std Dev	11.67



B1L100S, U10-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U10-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch32, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	29.31
Std Dev	4.85

Gaus mean : 30.0698

Gaus Width: 3.7781

Entry

10
8
6
4
2
0

ampl

0

10

20

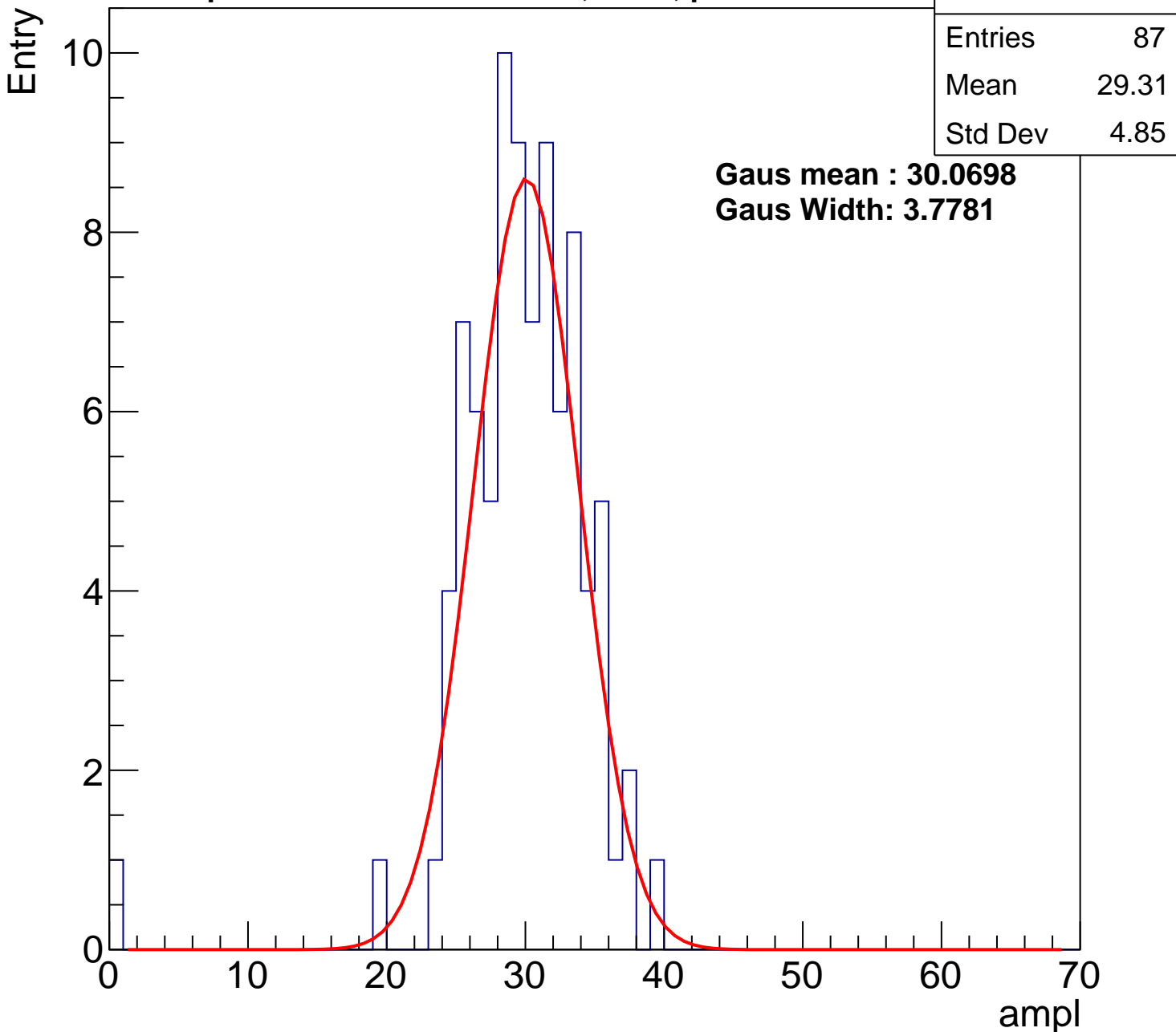
30

40

50

60

70



B1L100S, U10-ch32, adc1

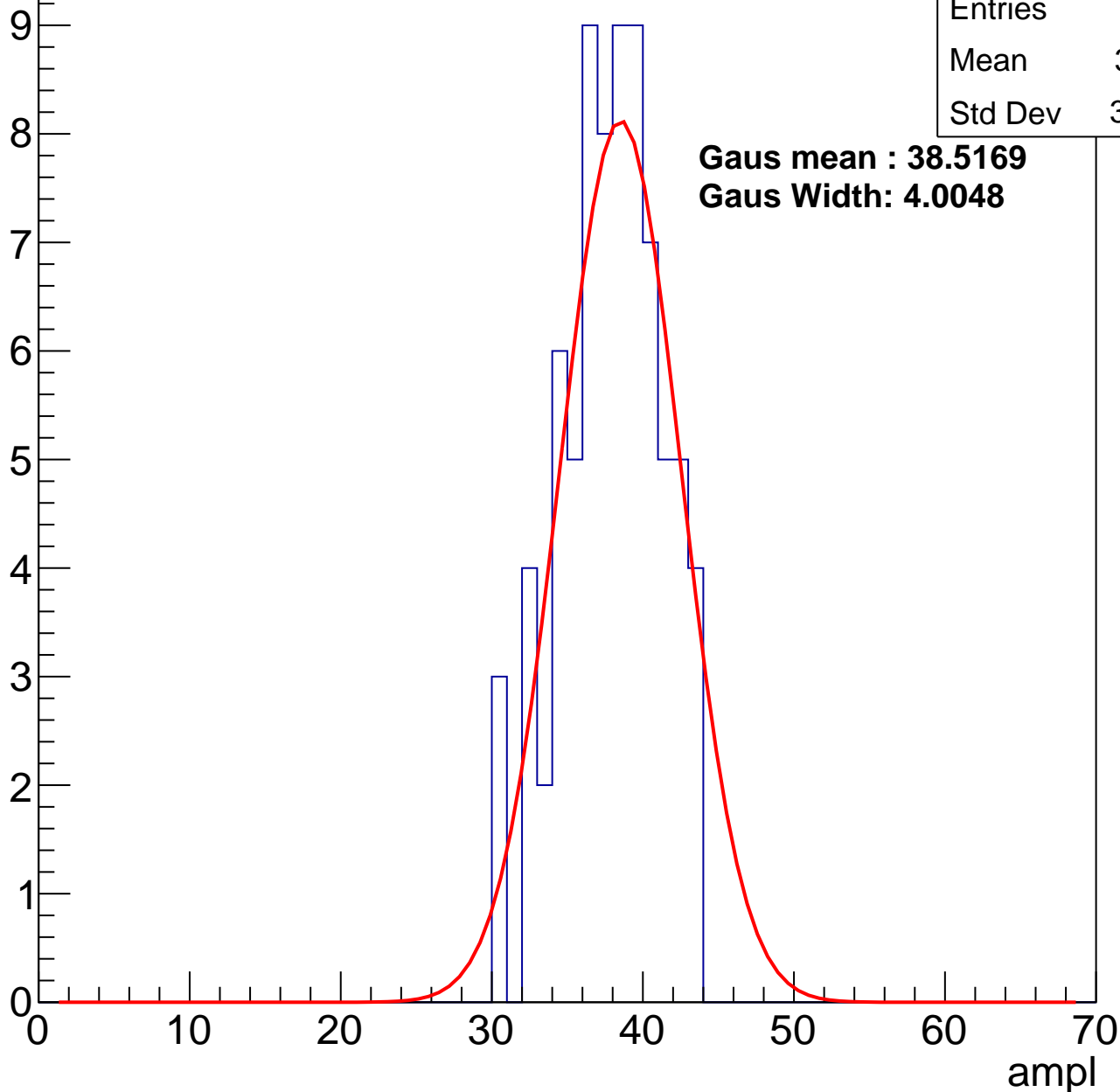
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	37.41
Std Dev	3.257

Gaus mean : 38.5169

Gaus Width: 4.0048



B1L100S, U10-ch32, adc2

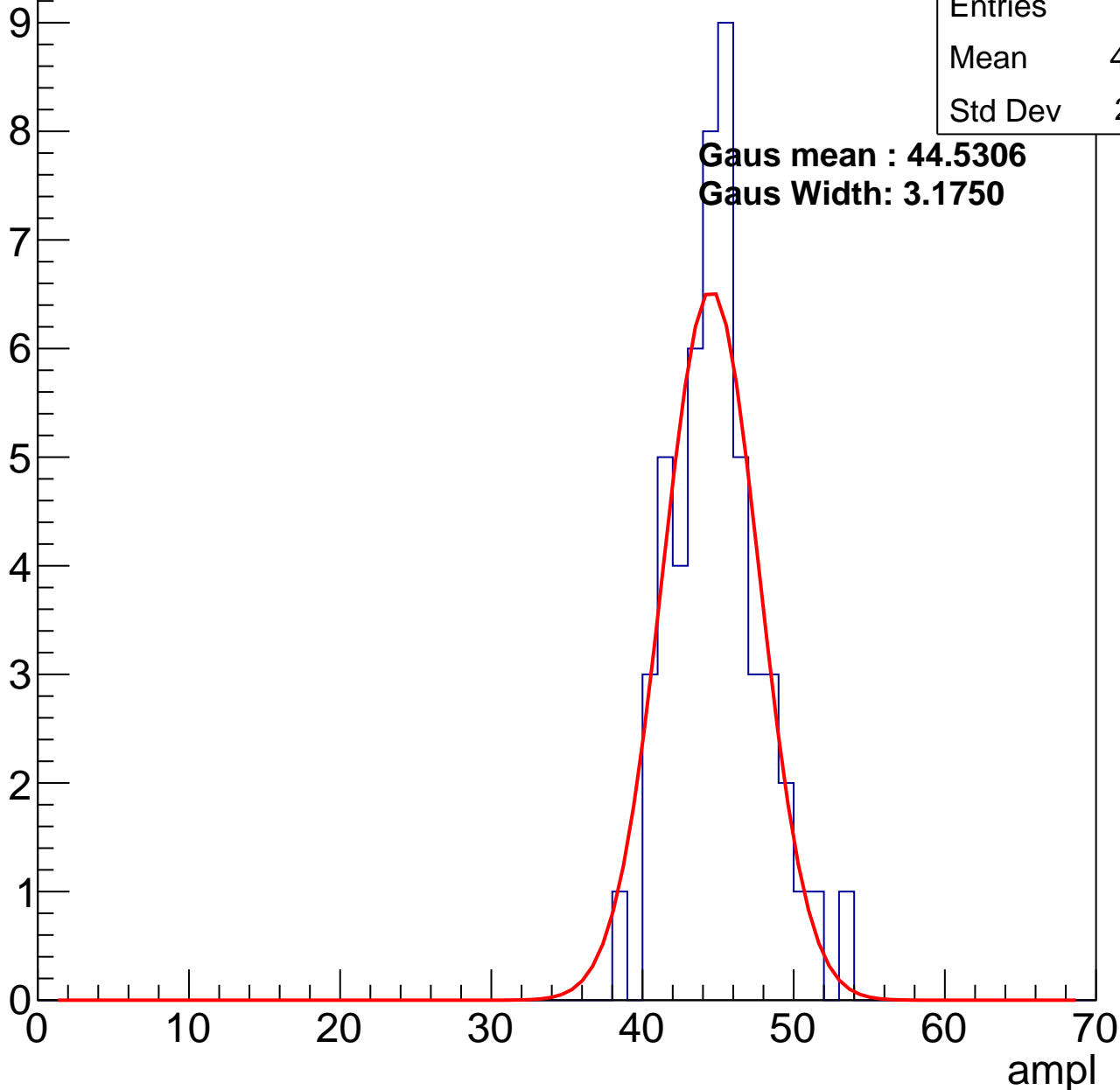
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	44.48
Std Dev	2.971

Gaus mean : 44.5306

Gaus Width: 3.1750

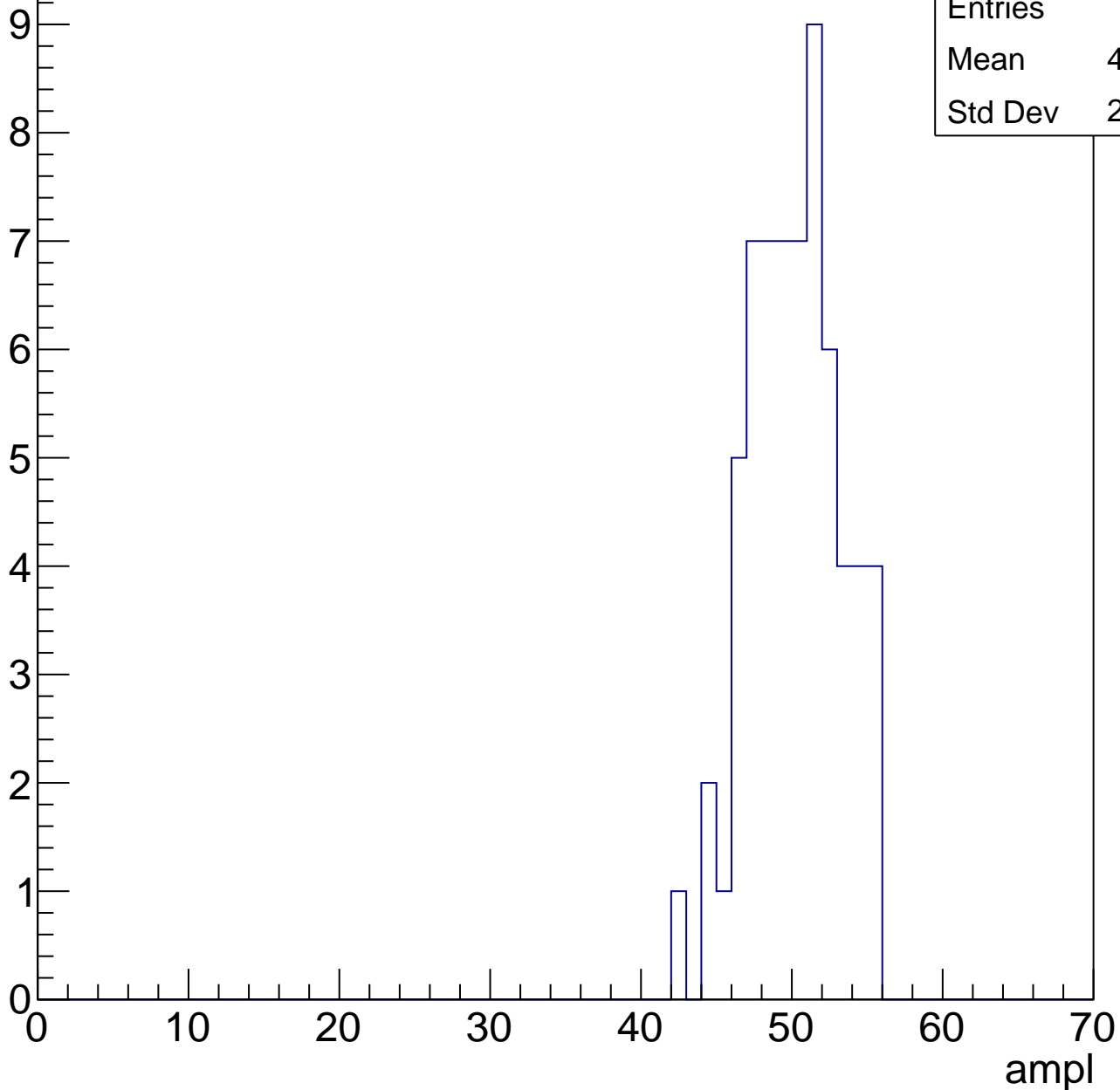


B1L100S, U10-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.72
Std Dev	2.976

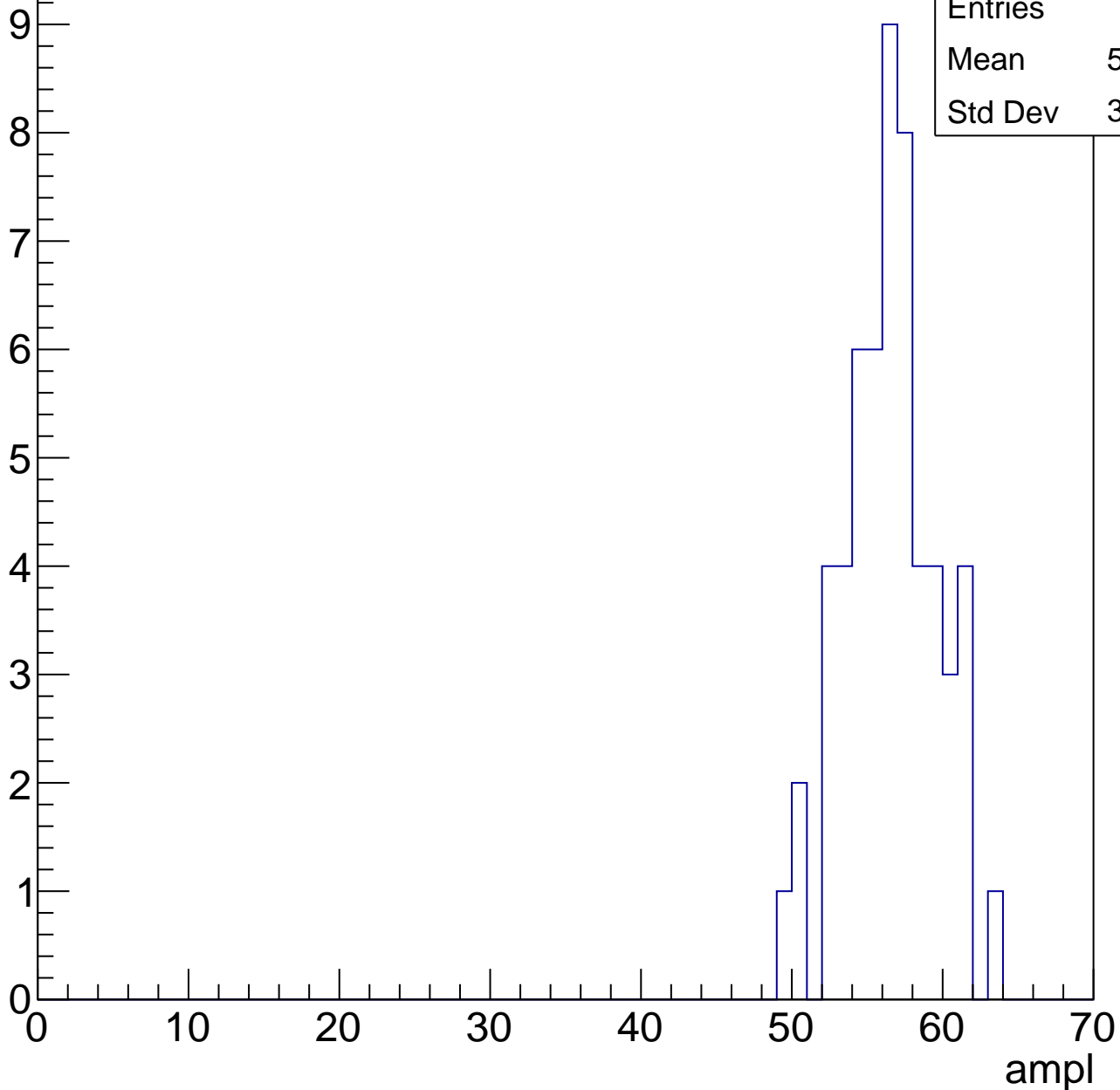


B1L100S, U10-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.04
Std Dev	3.012

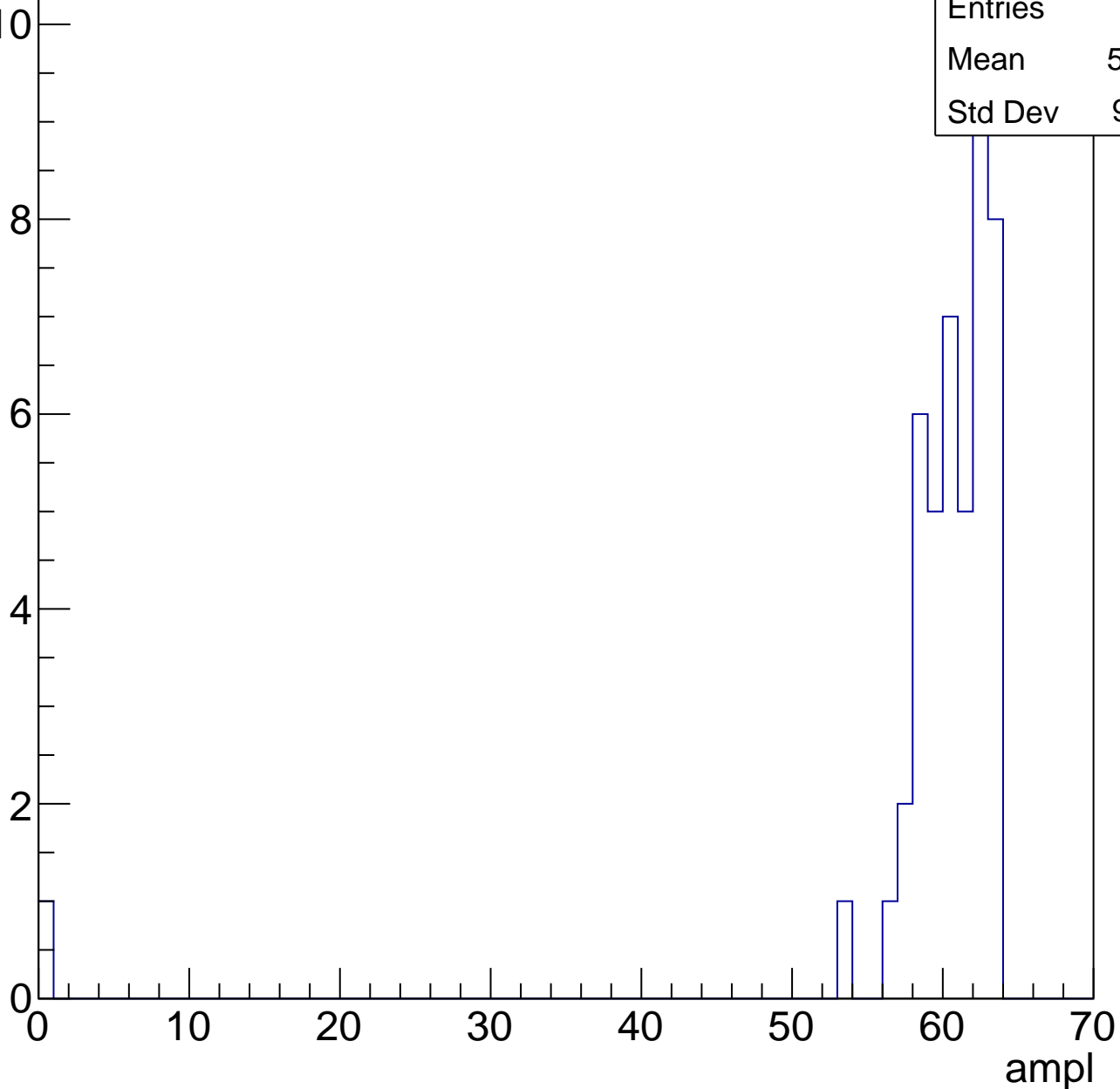


B1L100S, U10-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

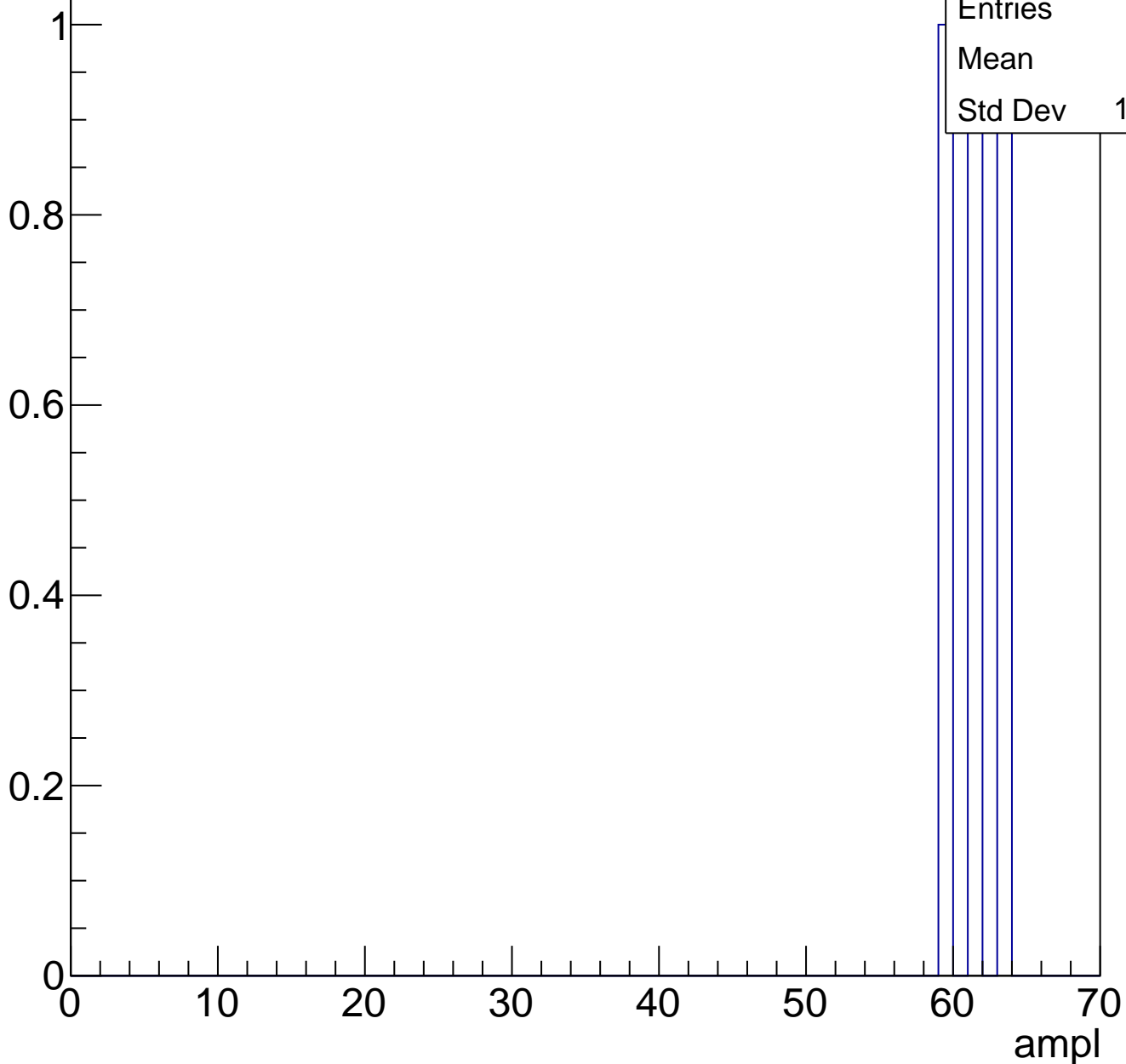
Entries	46
Mean	59.02
Std Dev	9.071



B1L100S, U10-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch33, adc0

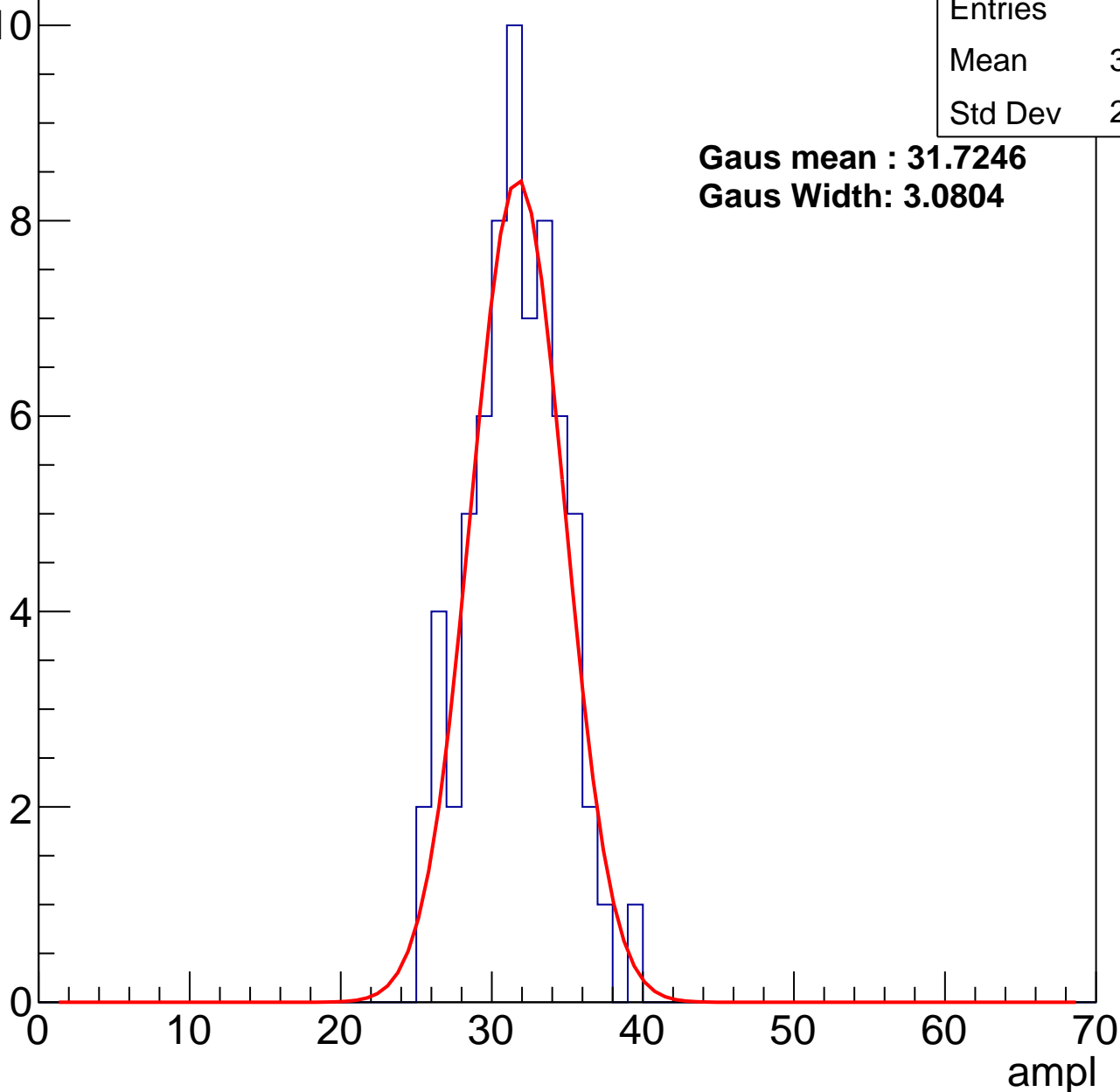
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	31.15
Std Dev	2.999

Gaus mean : 31.7246

Gaus Width: 3.0804



B1L100S, U10-ch33, adc1

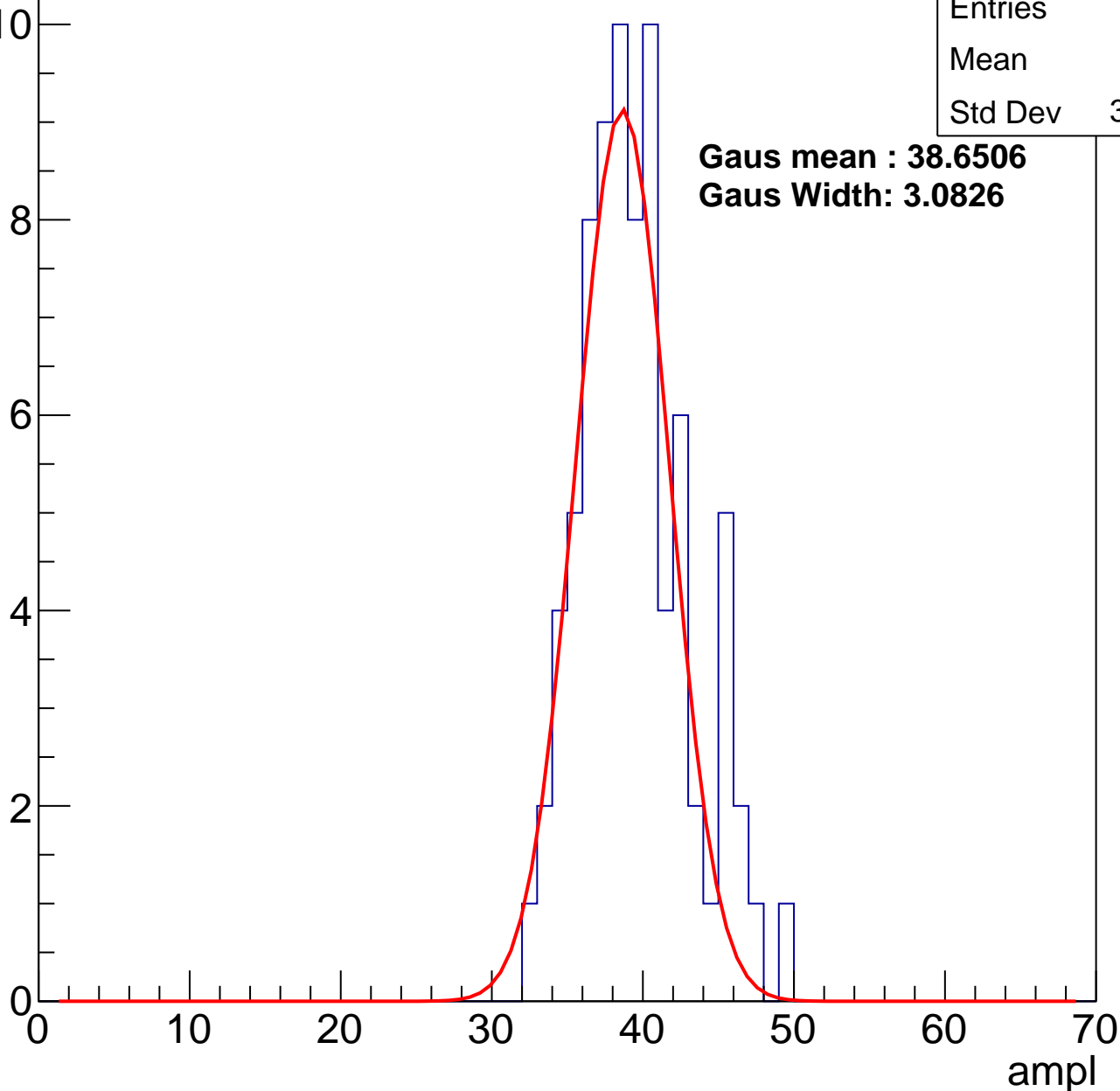
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	39
Std Dev	3.554

Gaus mean : 38.6506

Gaus Width: 3.0826

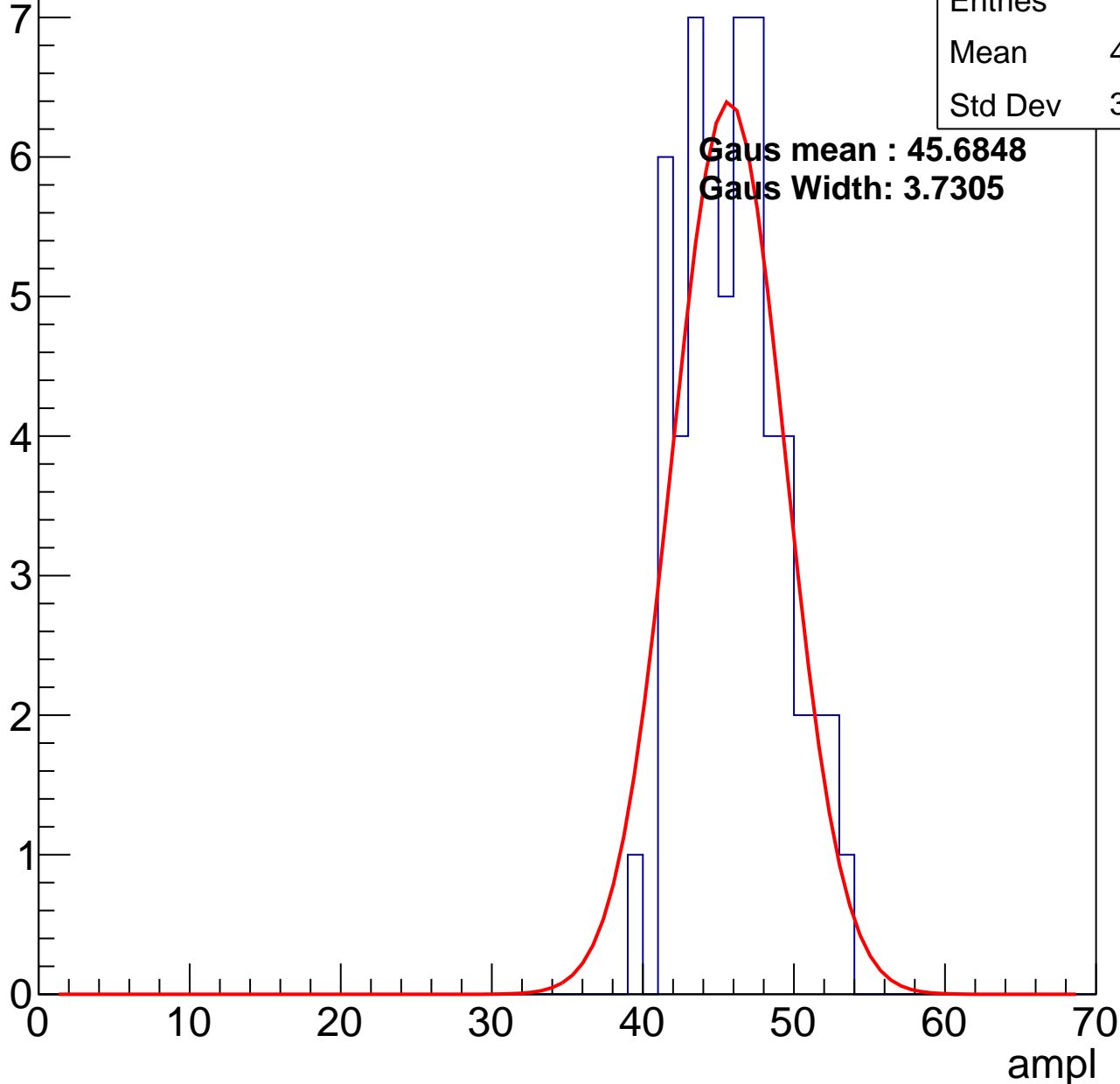


B1L100S, U10-ch33, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	45.53
Std Dev	3.228

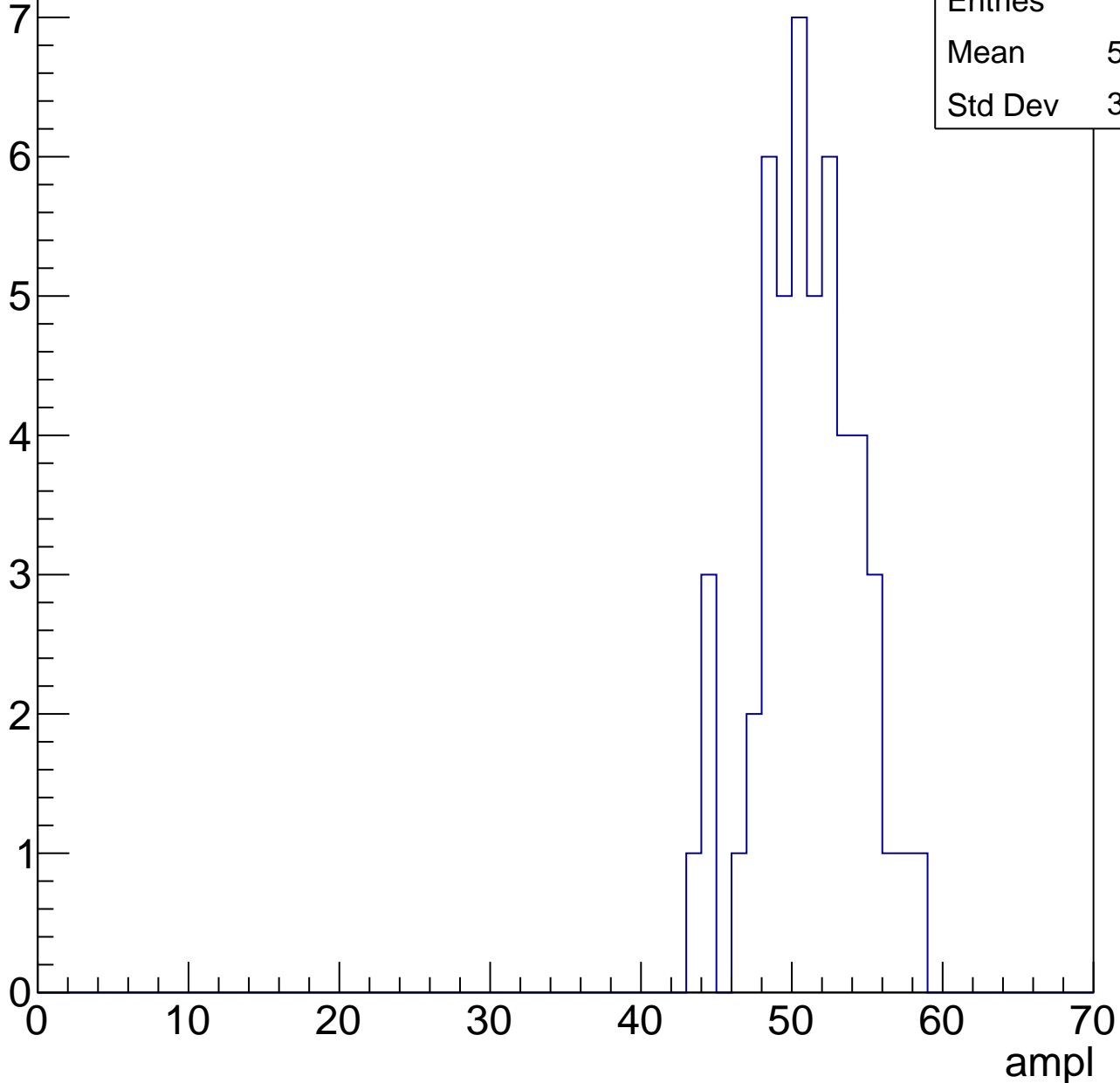


B1L100S, U10-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

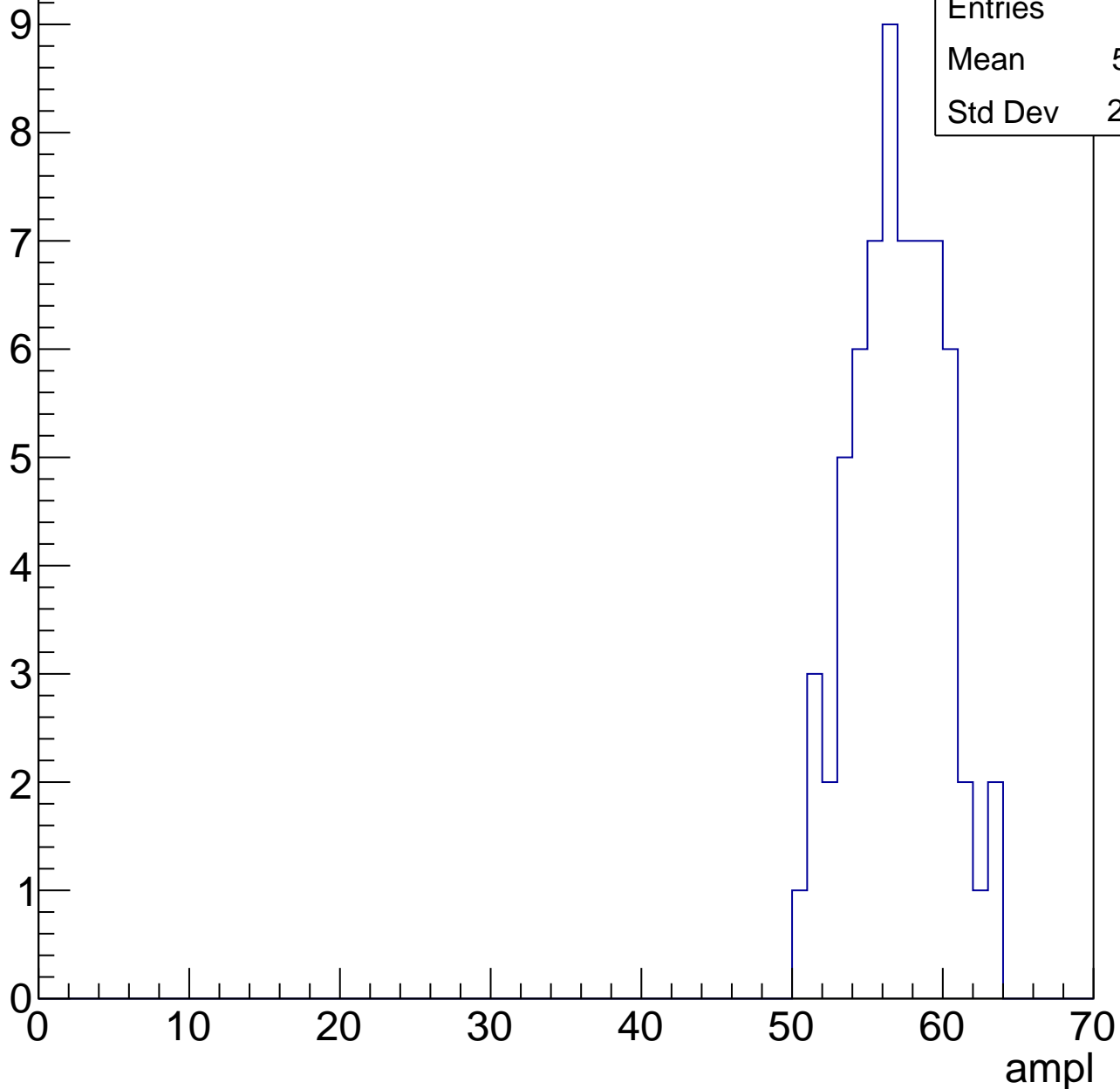
Entries	50
Mean	50.58
Std Dev	3.335



B1L100S, U10-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

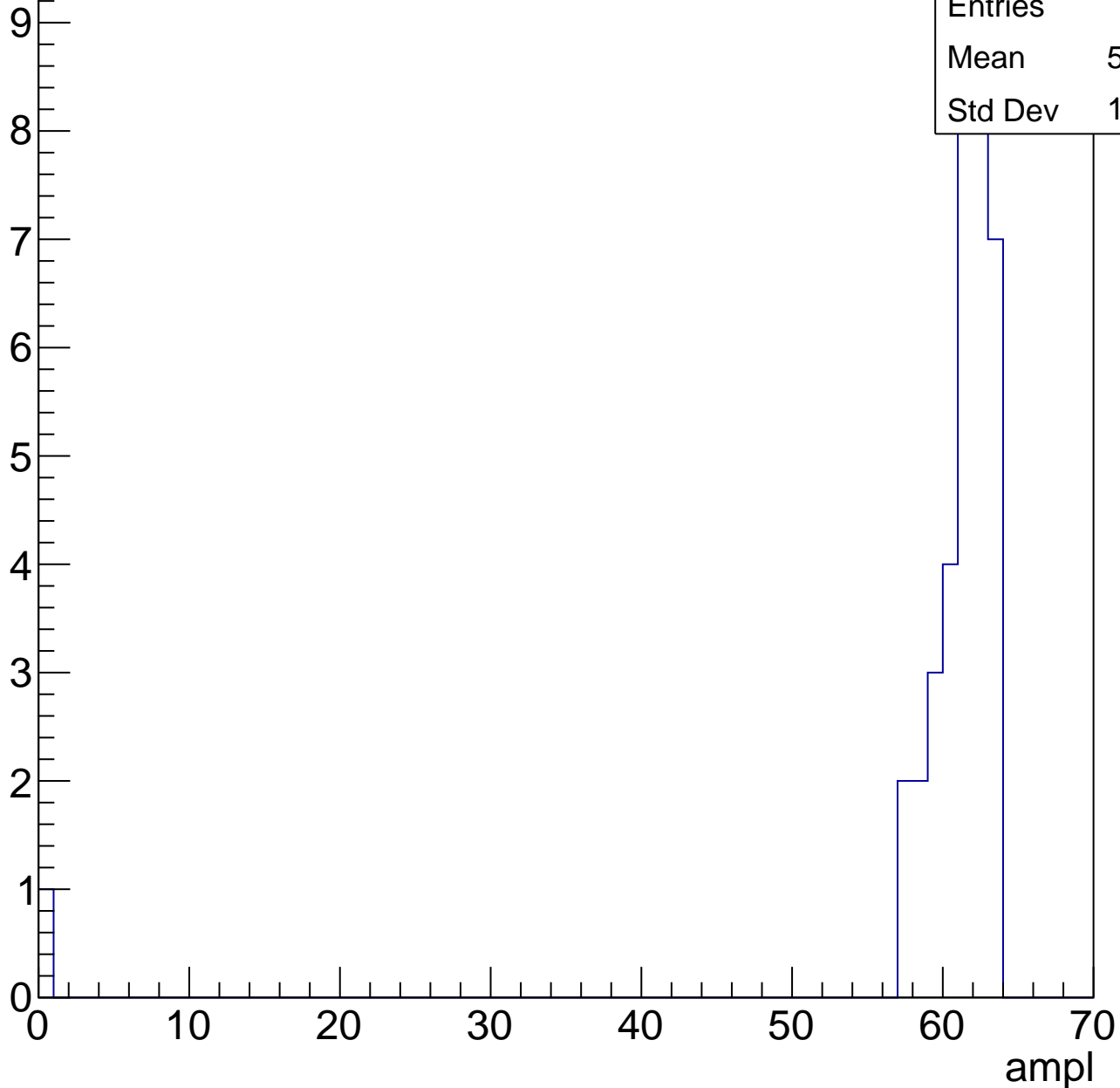


Entries	65
Mean	56.51
Std Dev	2.988

B1L100S, U10-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch34, adc0

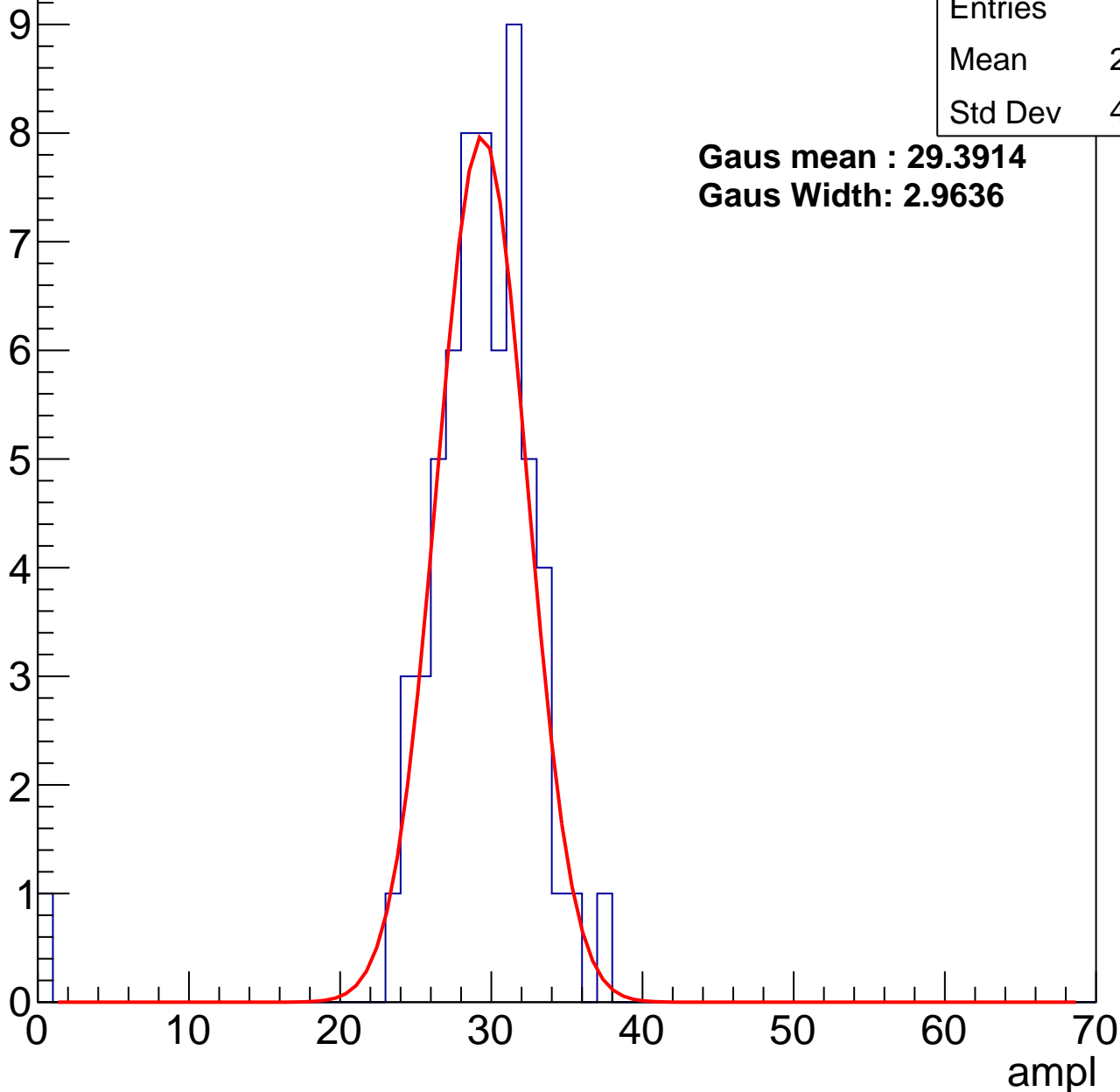
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.63
Std Dev	4.657

Gaus mean : 29.3914

Gaus Width: 2.9636



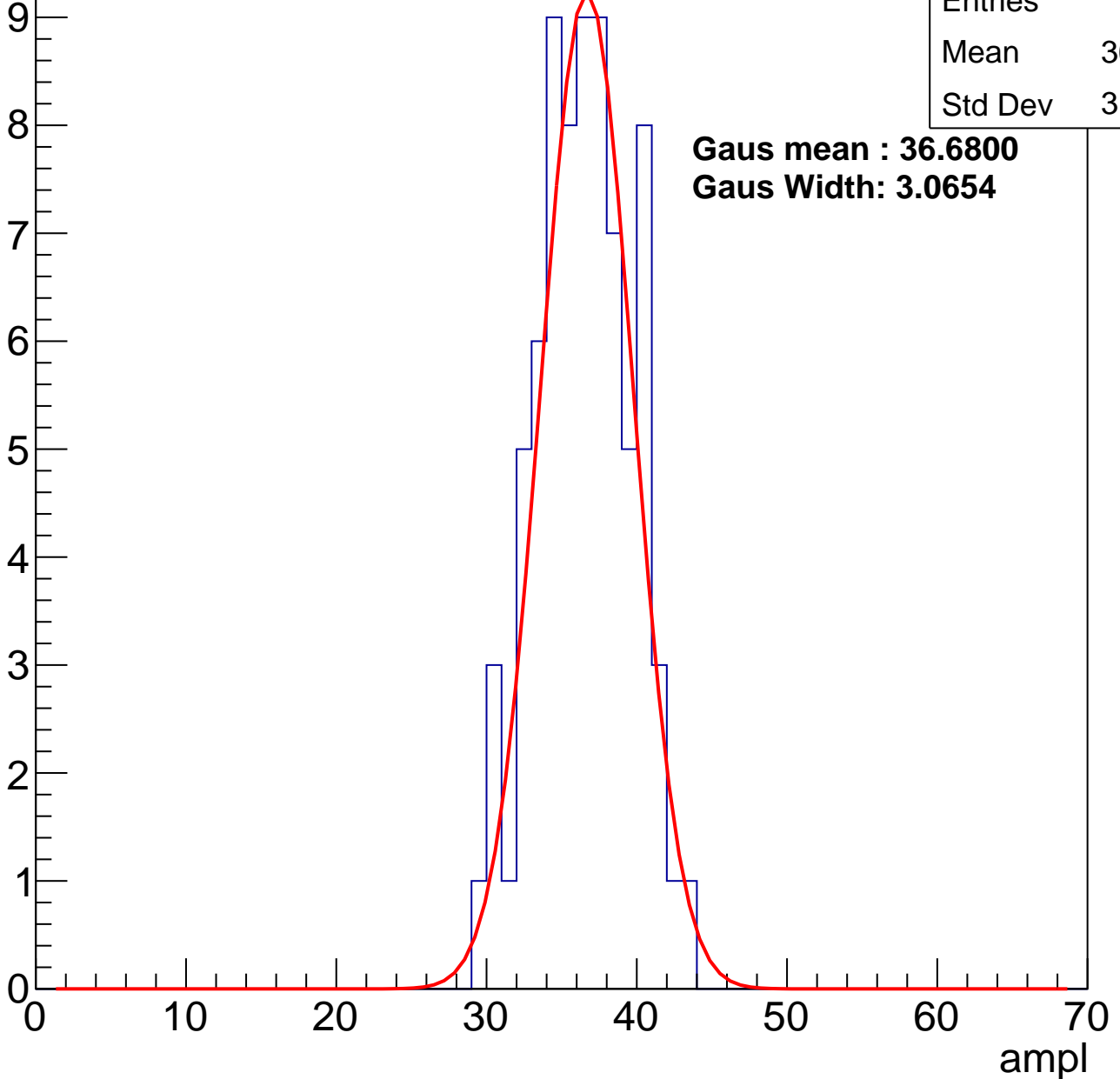
B1L100S, U10-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.05
Std Dev	3.099

Gaus mean : 36.6800
Gaus Width: 3.0654



B1L100S, U10-ch34, adc2

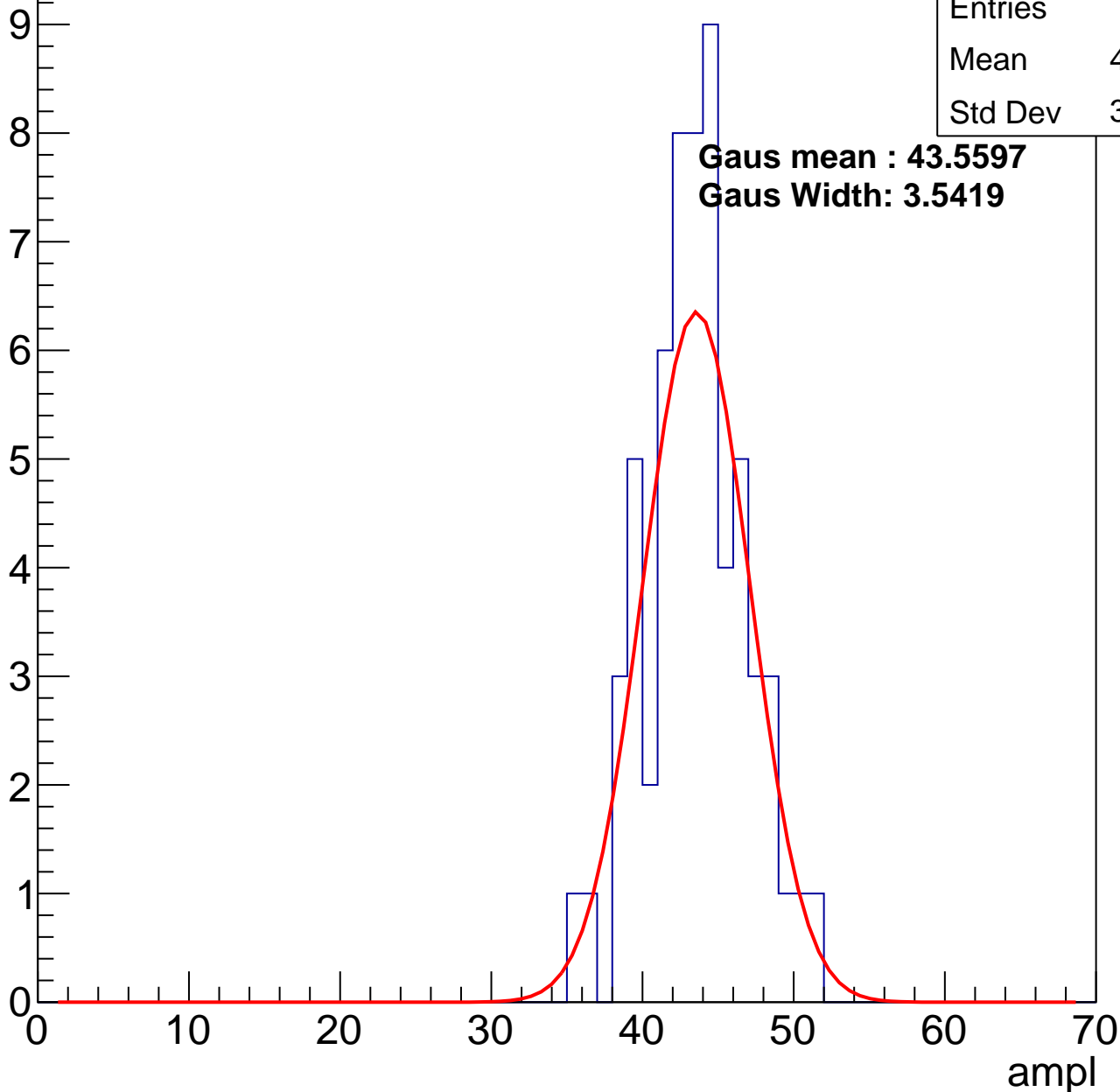
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.07
Std Dev	3.289

Gaus mean : 43.5597

Gaus Width: 3.5419

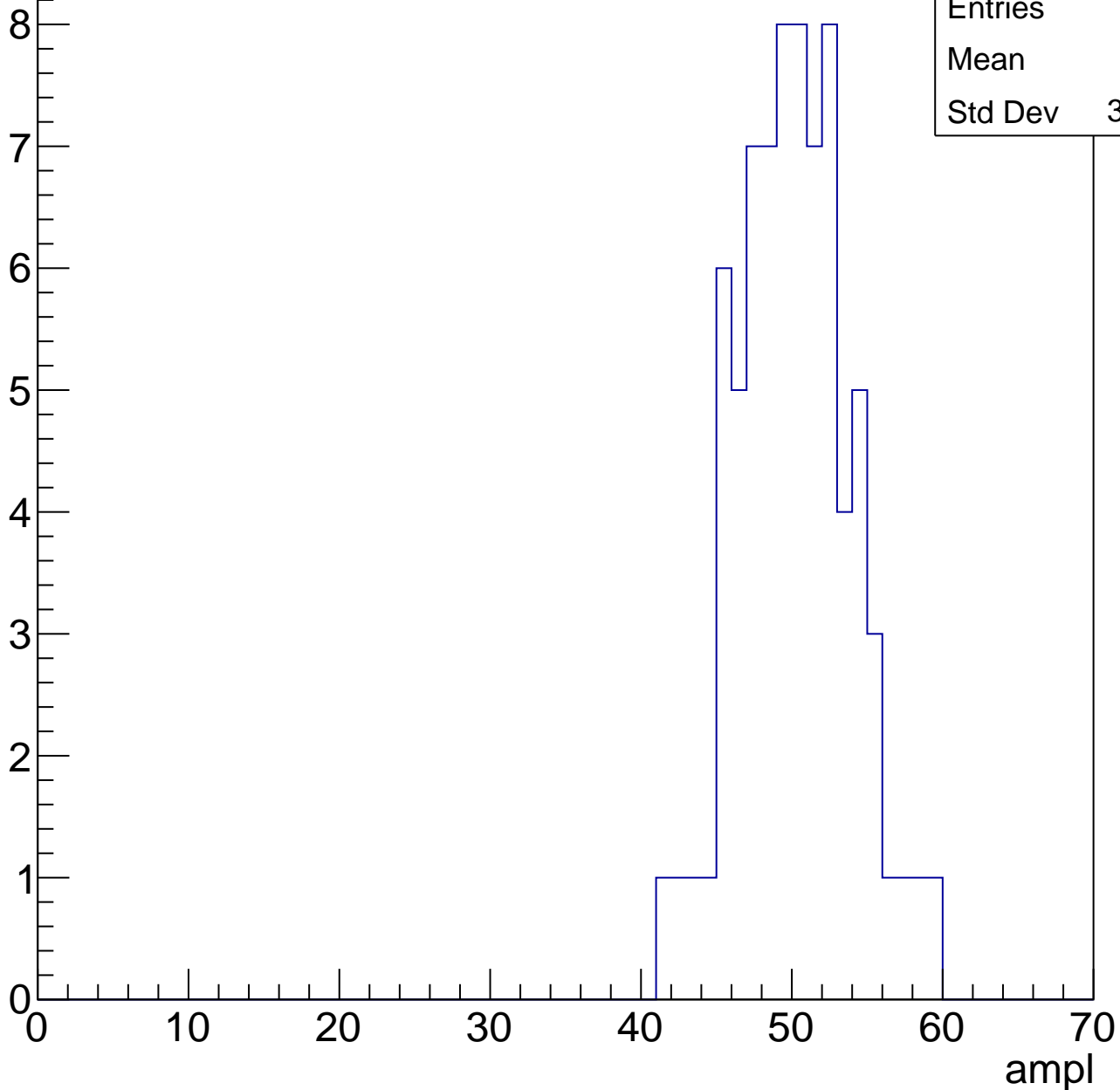


B1L100S, U10-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	49.7
Std Dev	3.649

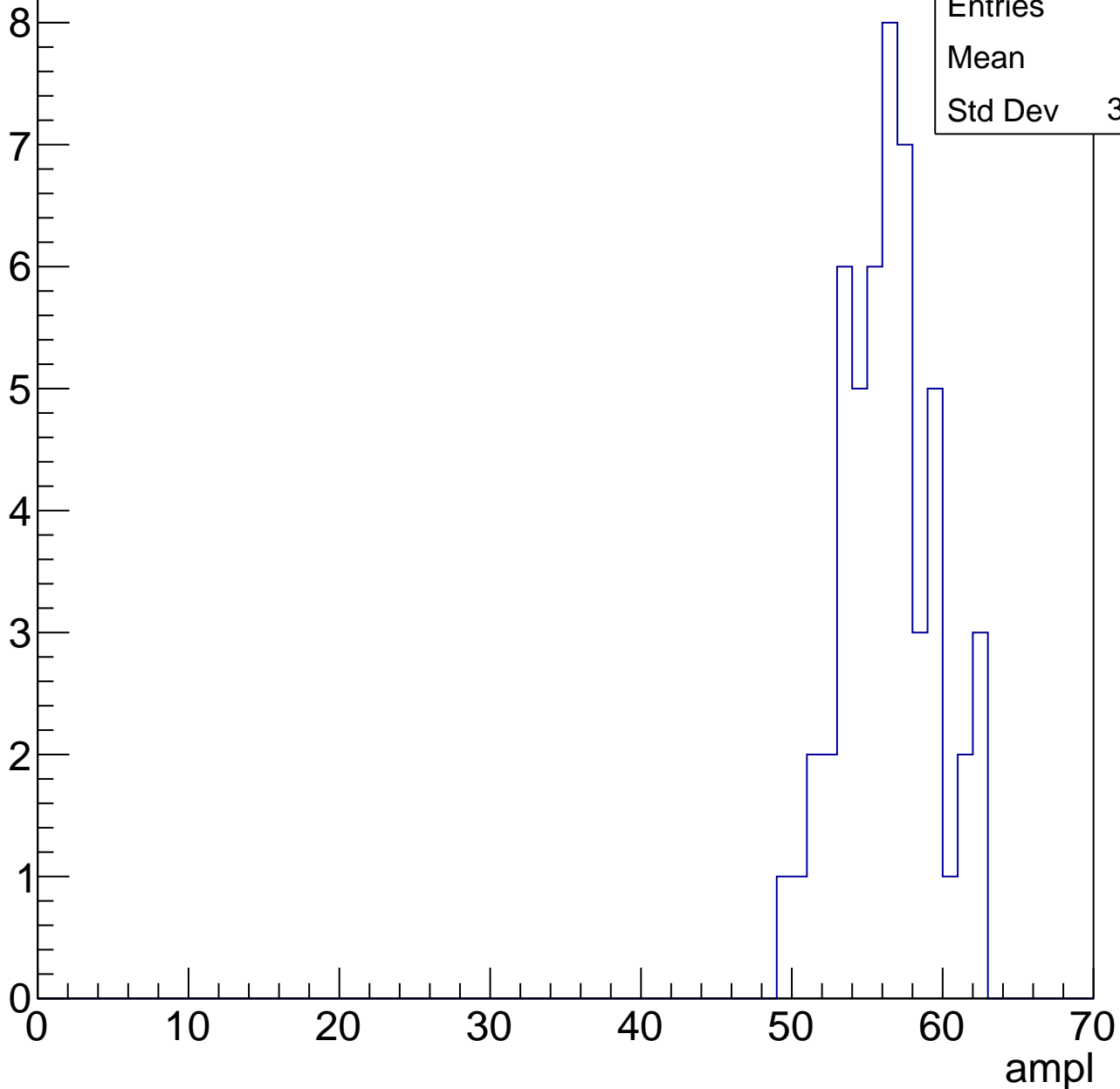


B1L100S, U10-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

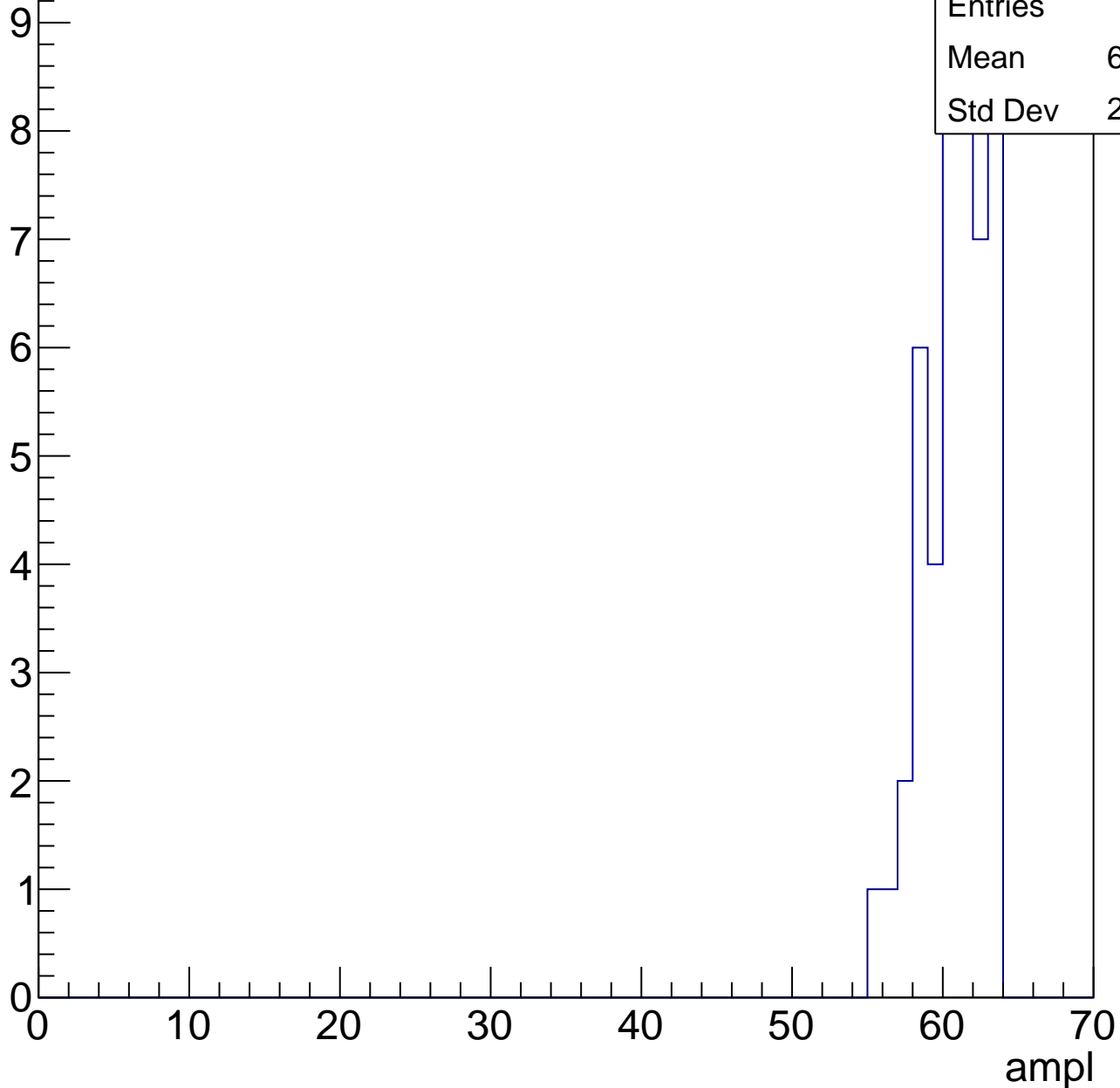
Entries	52
Mean	55.9
Std Dev	3.052



B1L100S, U10-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch35, adc0

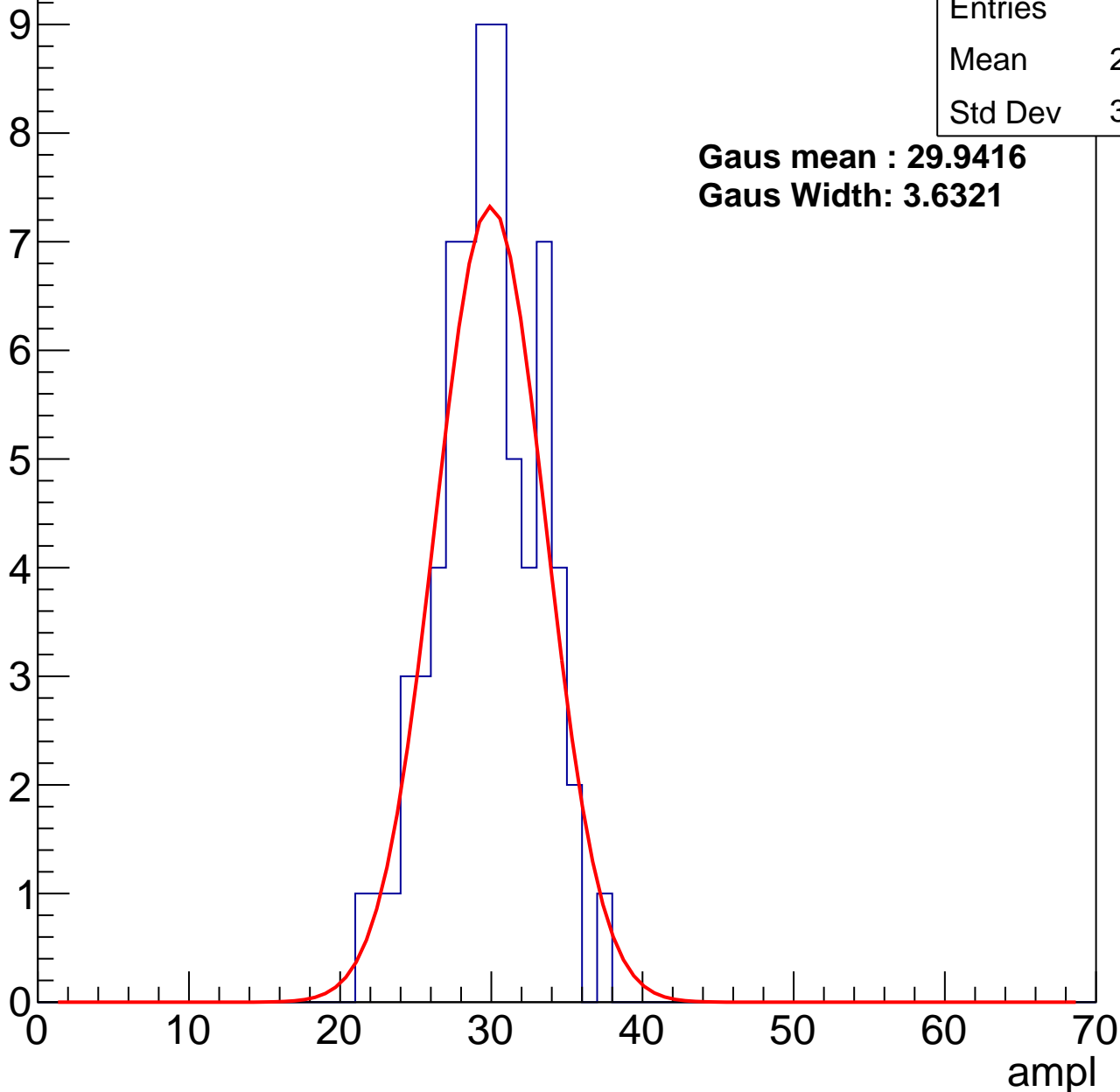
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.26
Std Dev	3.324

Gaus mean : 29.9416

Gaus Width: 3.6321



B1L100S, U10-ch35, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	36.58
Std Dev	3.319

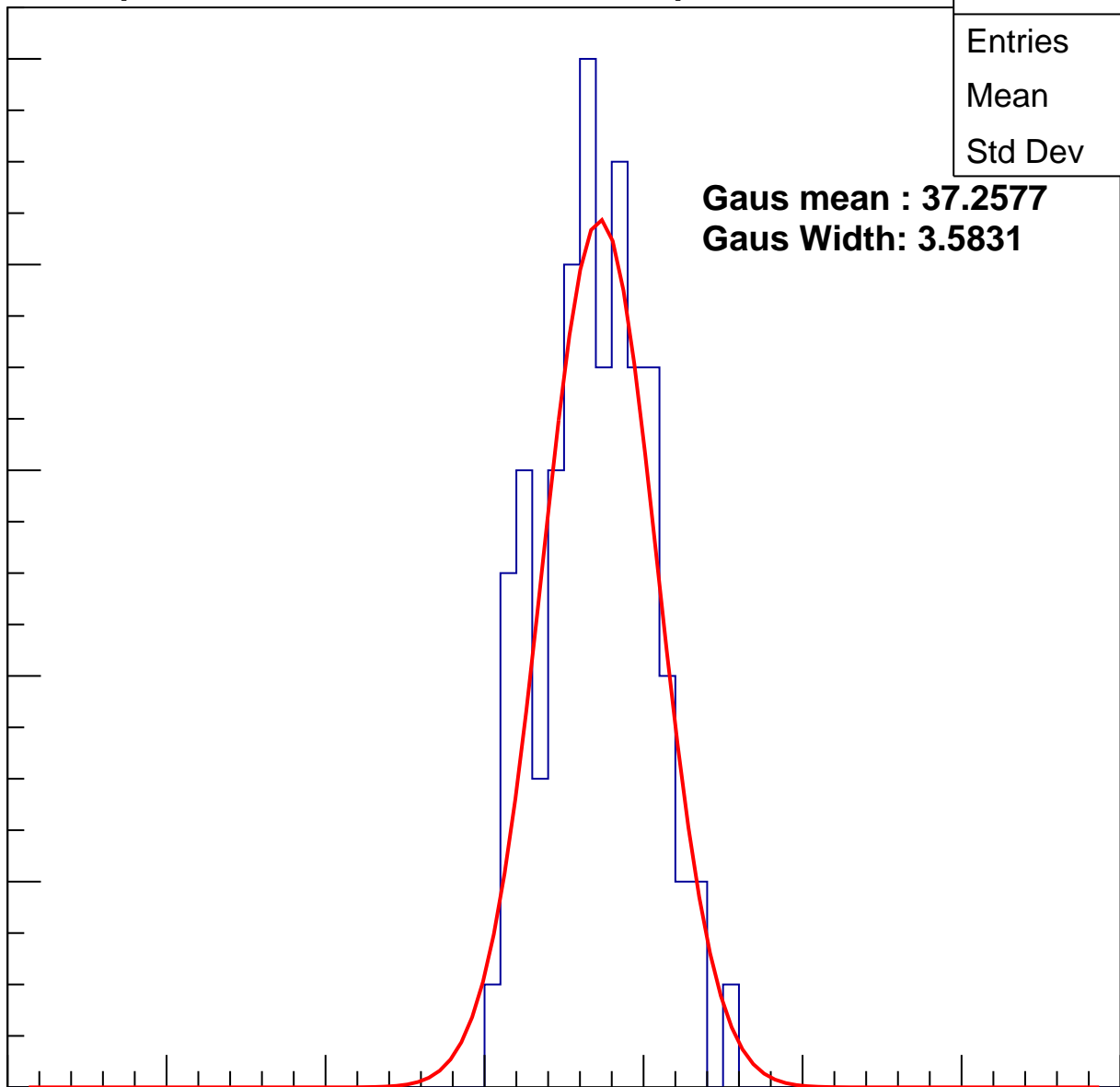
Gaus mean : 37.2577
Gaus Width: 3.5831

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch35, adc2

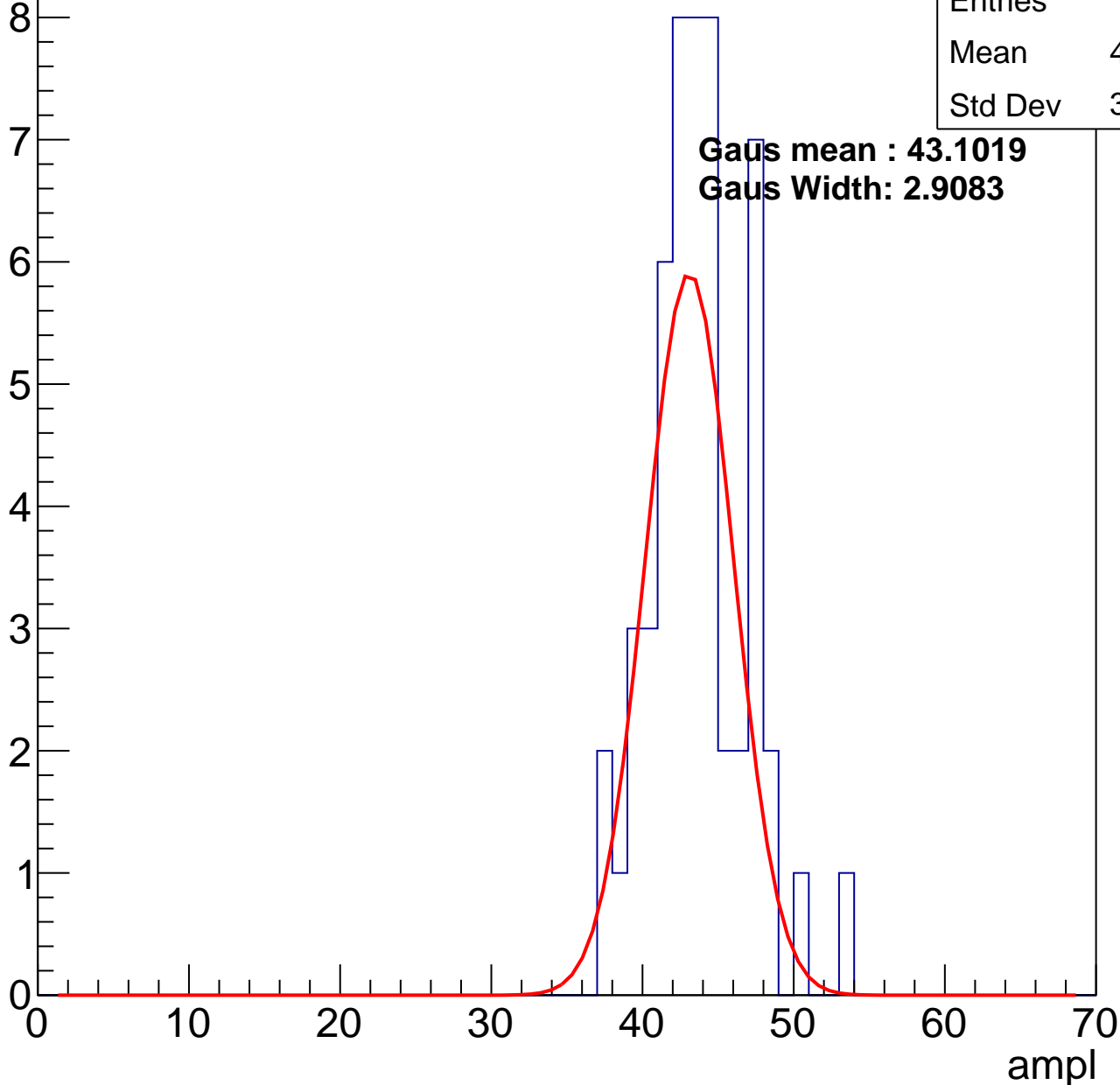
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.28
Std Dev	3.176

Gaus mean : 43.1019

Gaus Width: 2.9083

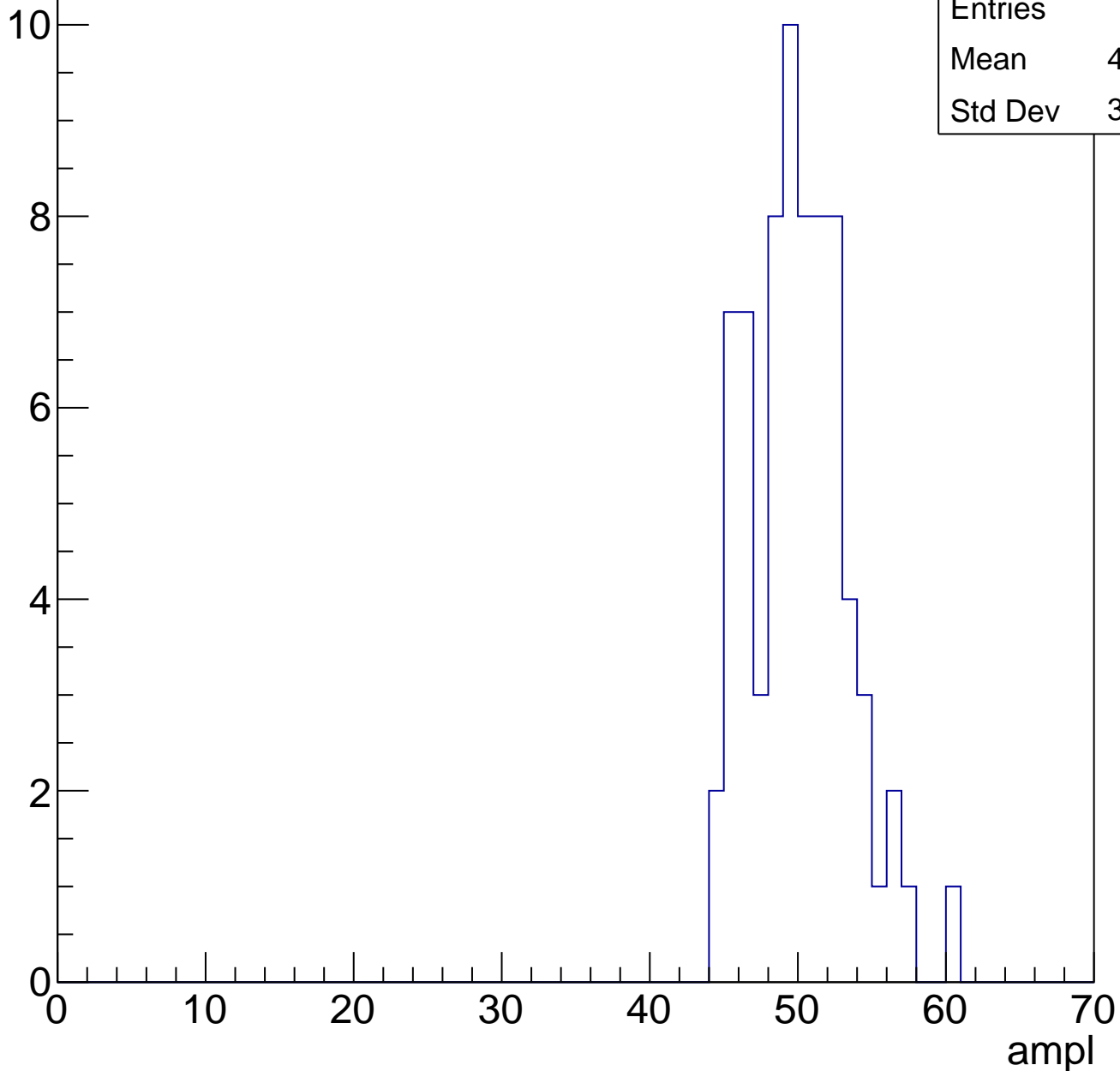


B1L100S, U10-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

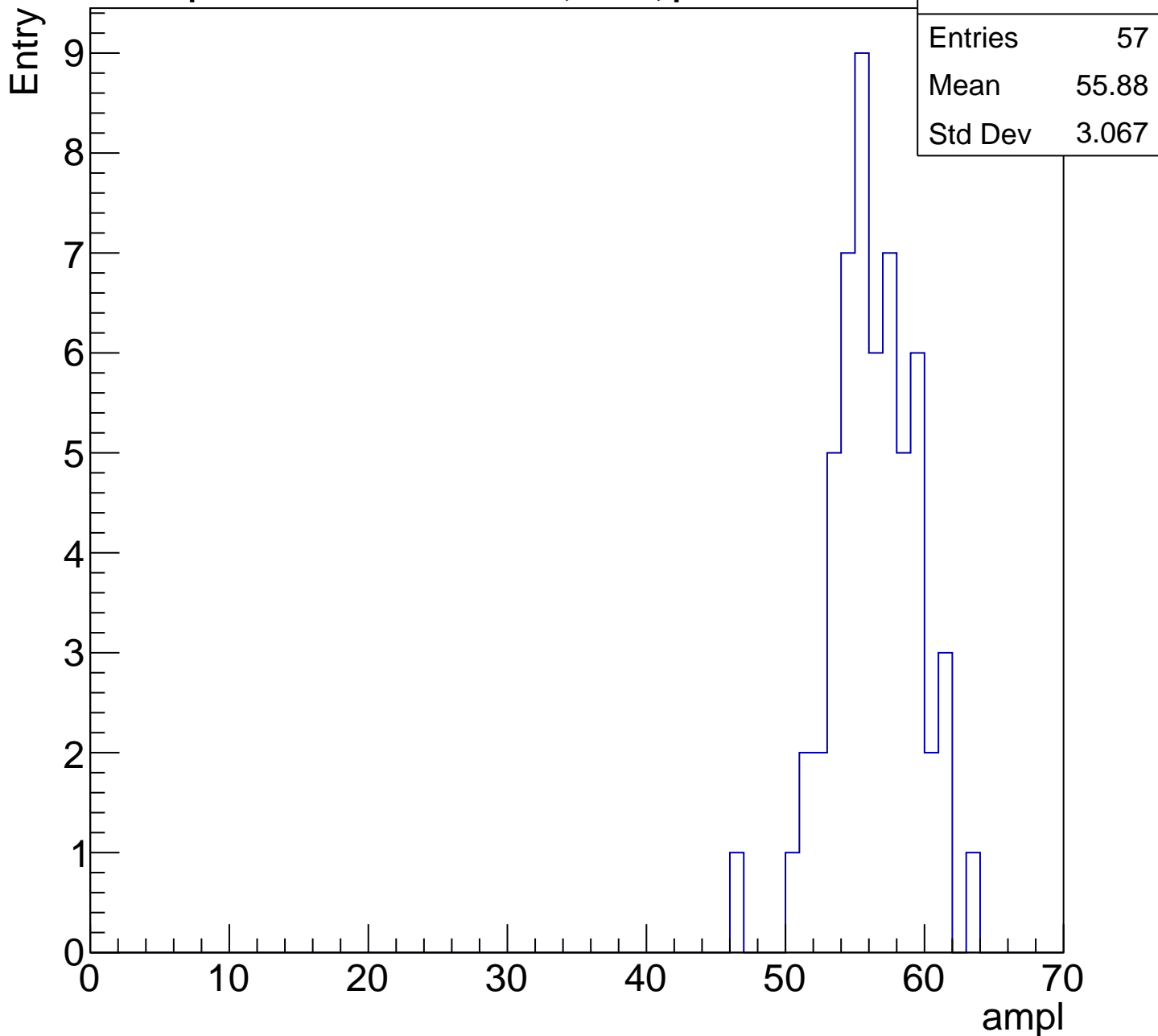
Entries	73
Mean	49.62
Std Dev	3.284

Entry



B1L100S, U10-ch35, adc4

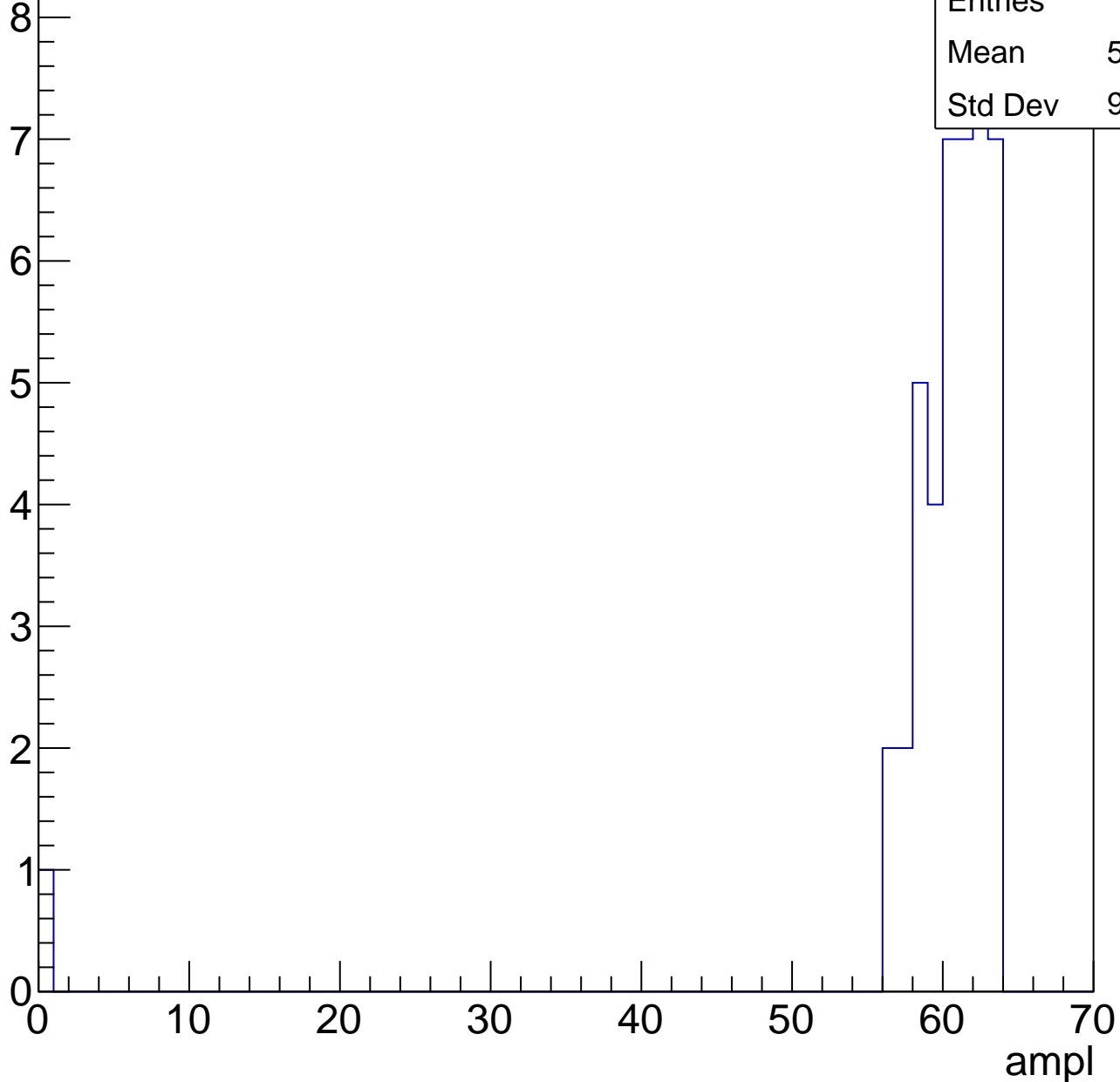
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch36, adc0

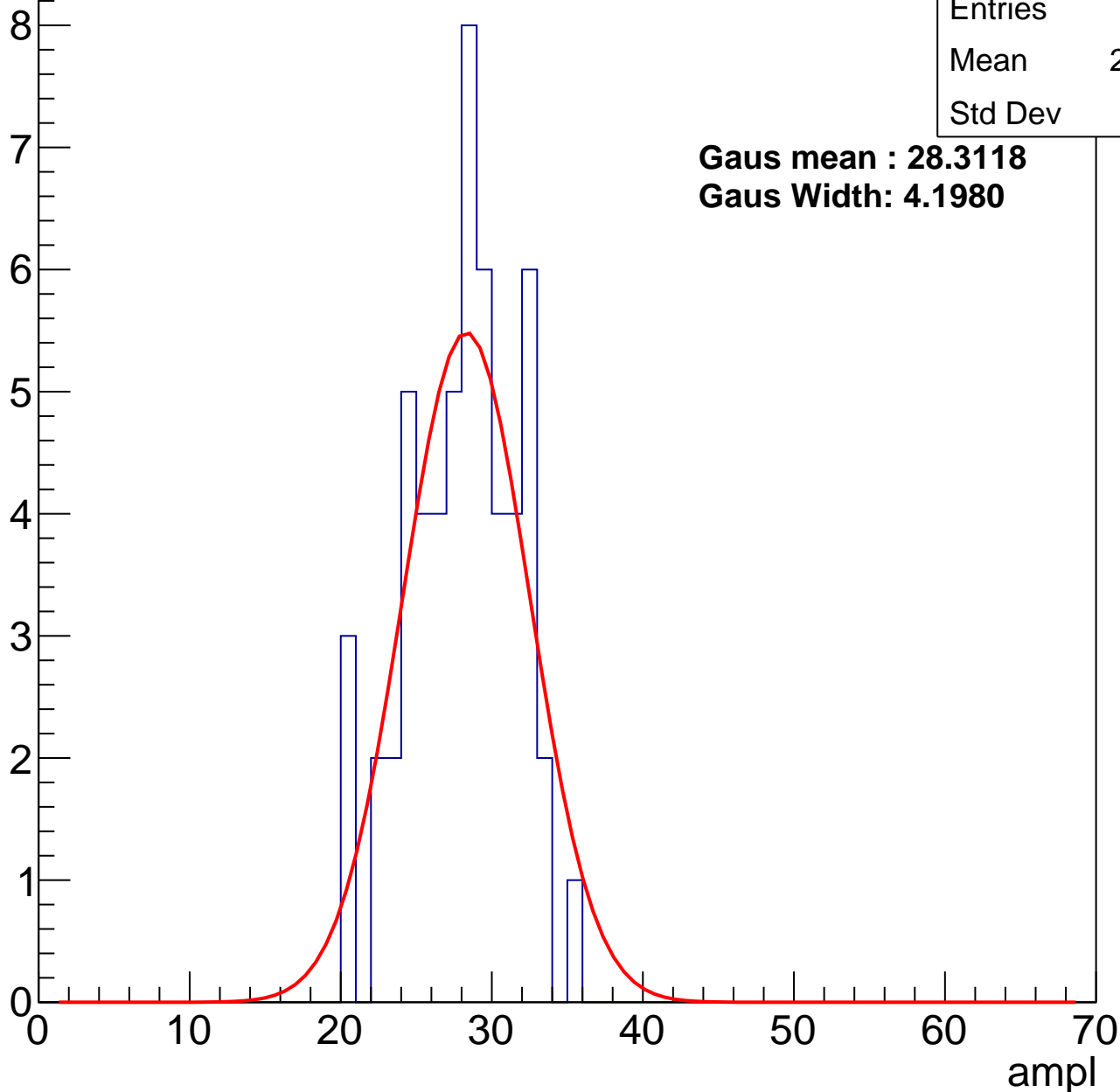
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	27.57
Std Dev	3.52

Gaus mean : 28.3118

Gaus Width: 4.1980



B1L100S, U10-ch36, adc1

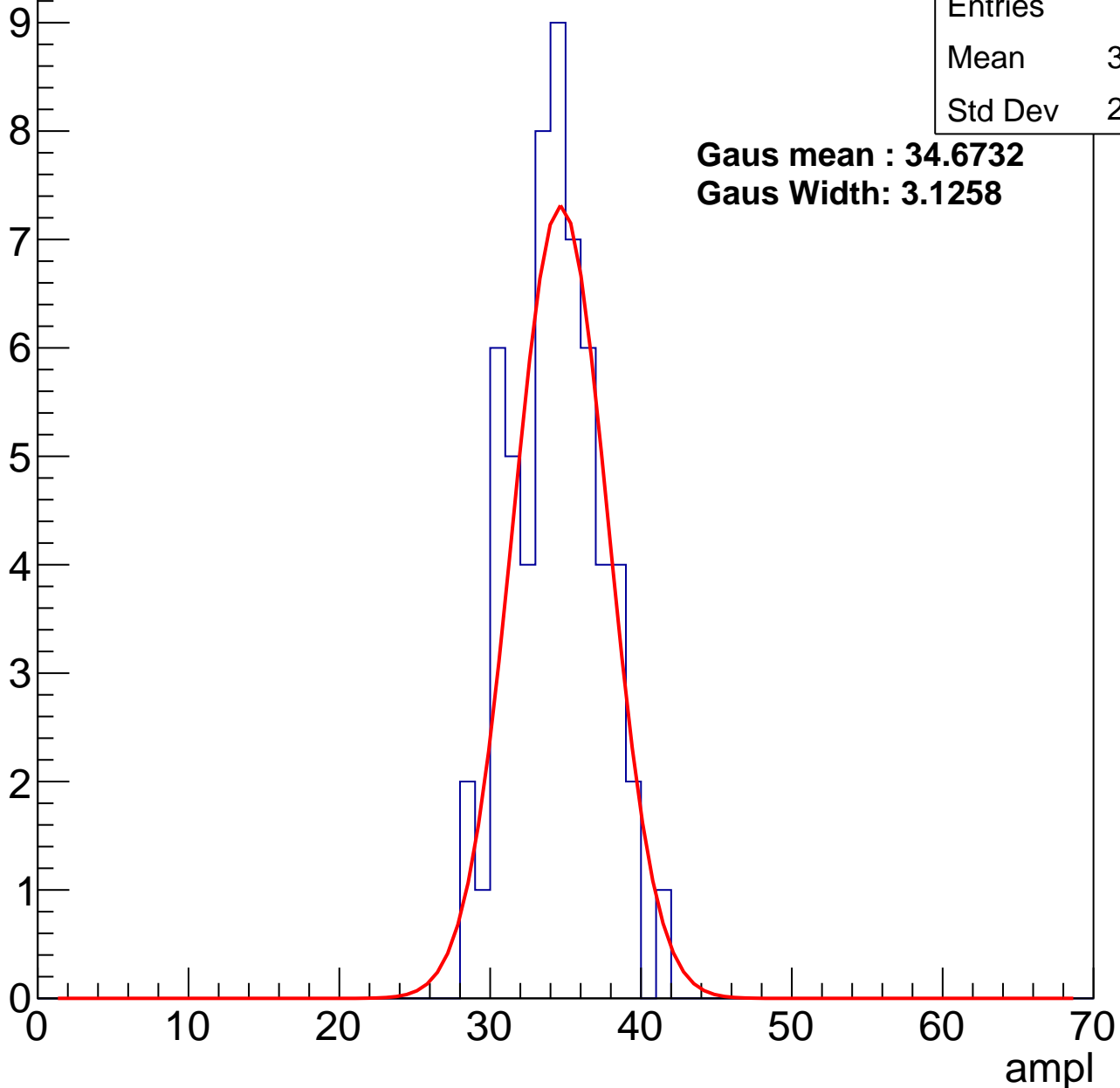
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	33.86
Std Dev	2.885

Gaus mean : 34.6732

Gaus Width: 3.1258



B1L100S, U10-ch36, adc2

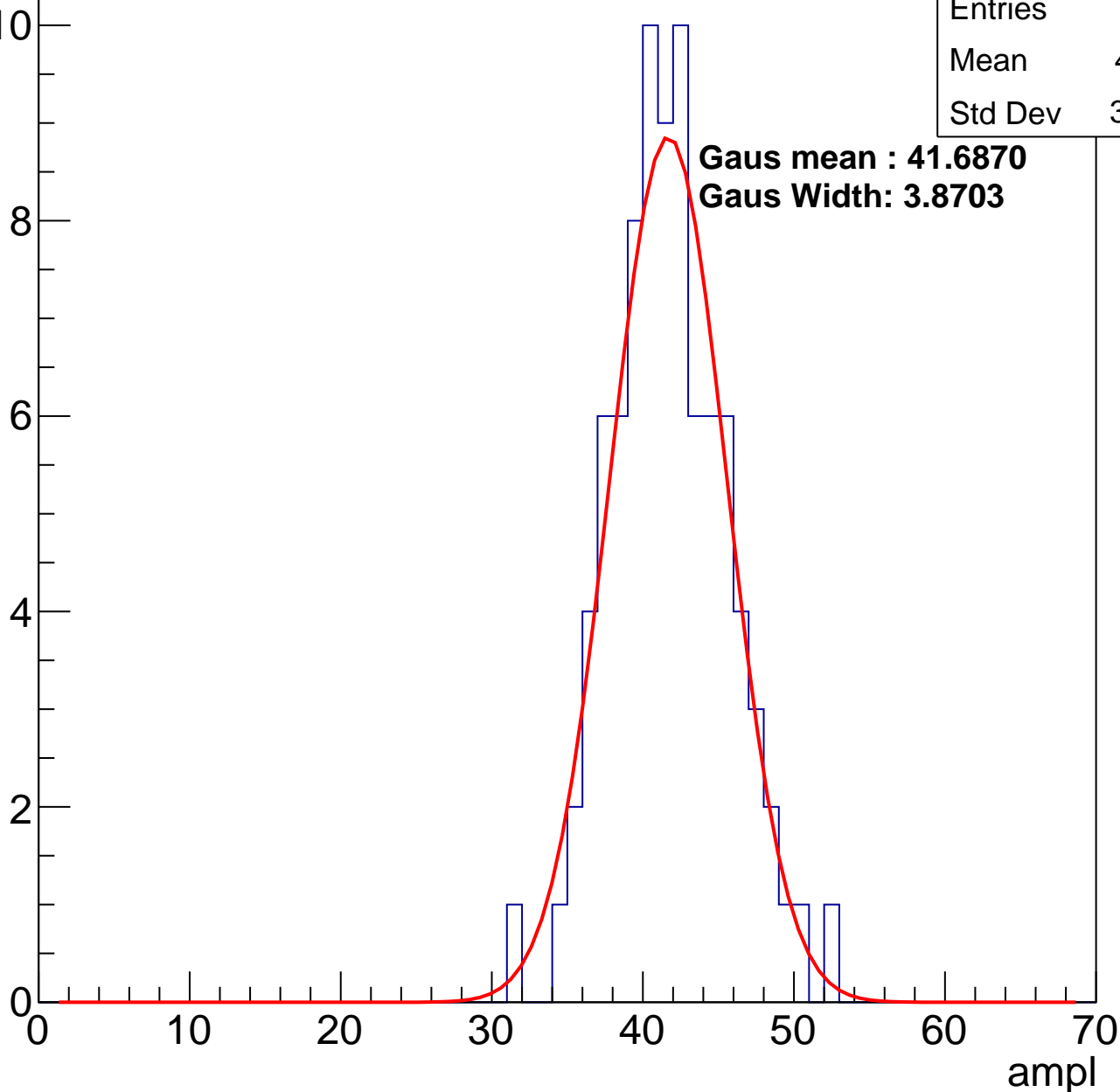
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	41.31
Std Dev	3.807

Gaus mean : 41.6870

Gaus Width: 3.8703

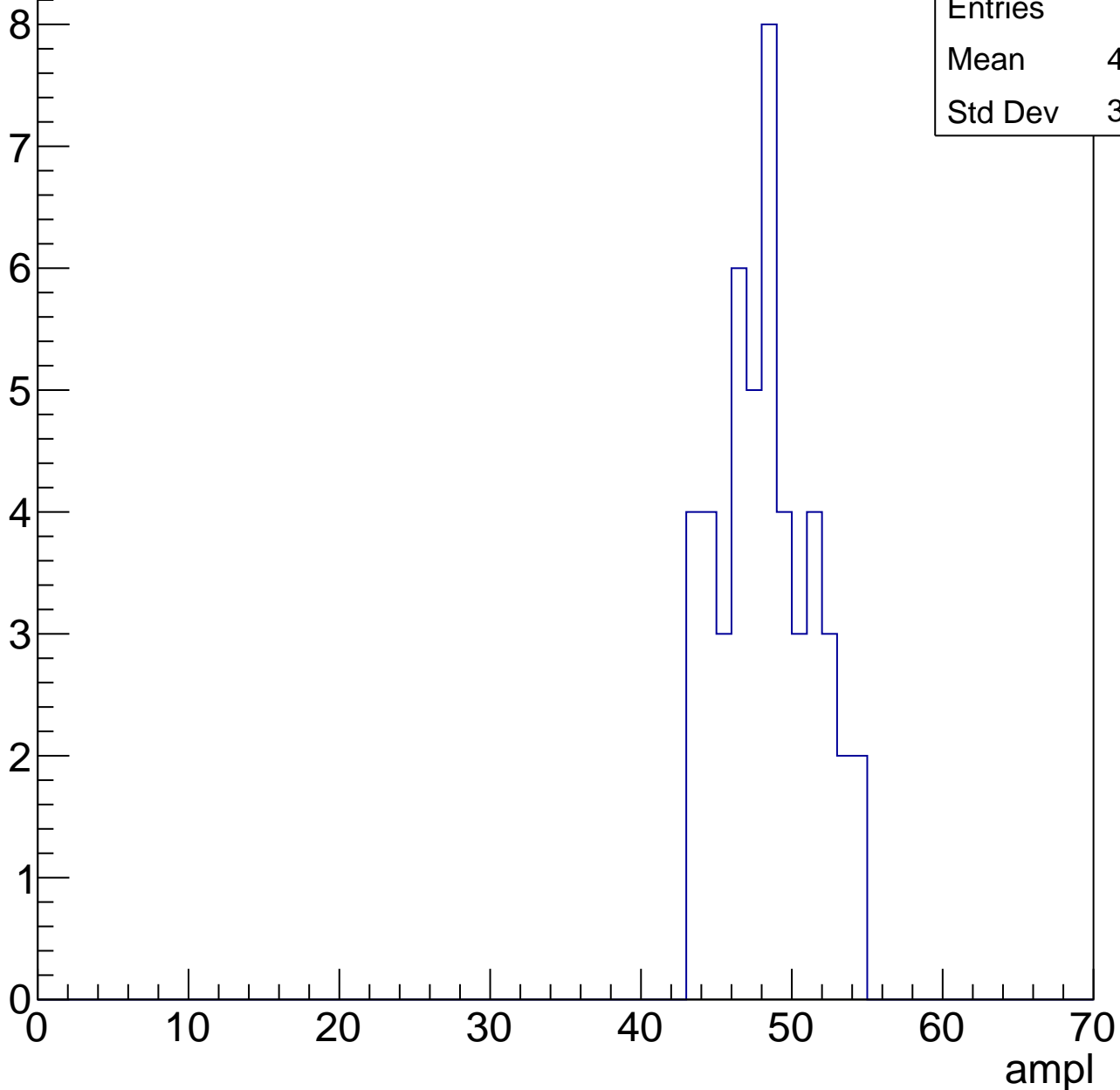


B1L100S, U10-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

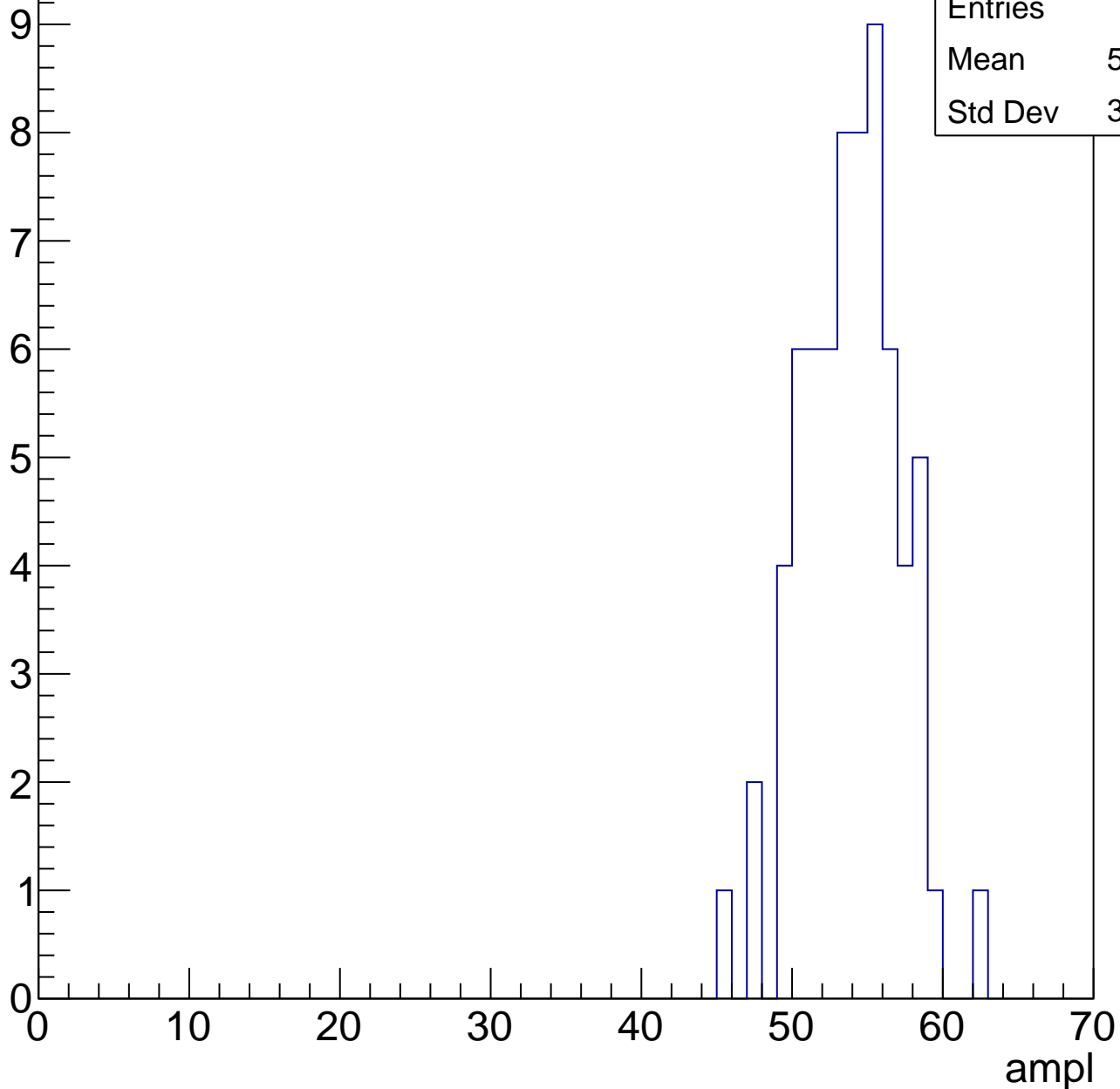
Entries	48
Mean	47.88
Std Dev	3.032



B1L100S, U10-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

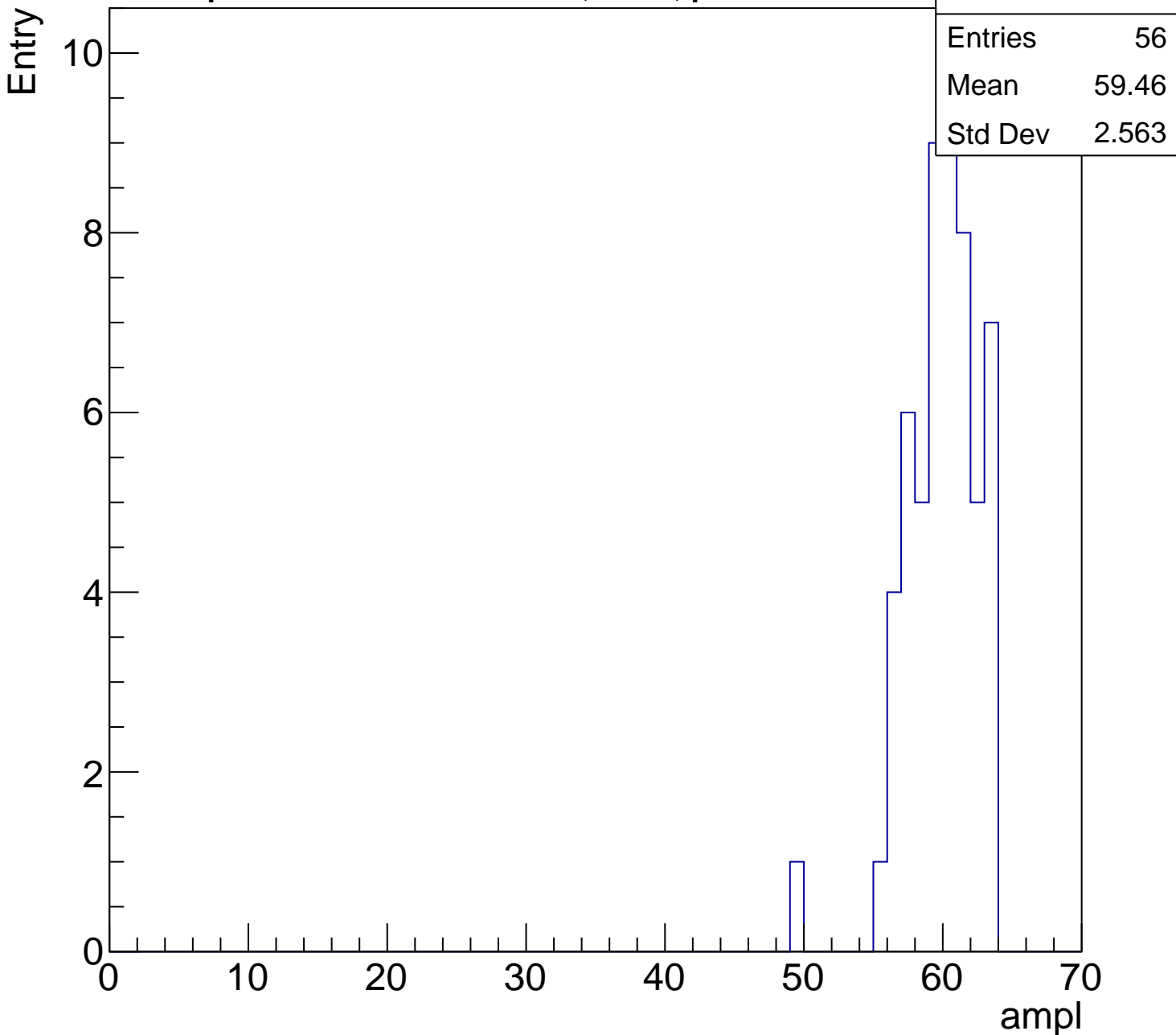
50

60

70

ampl

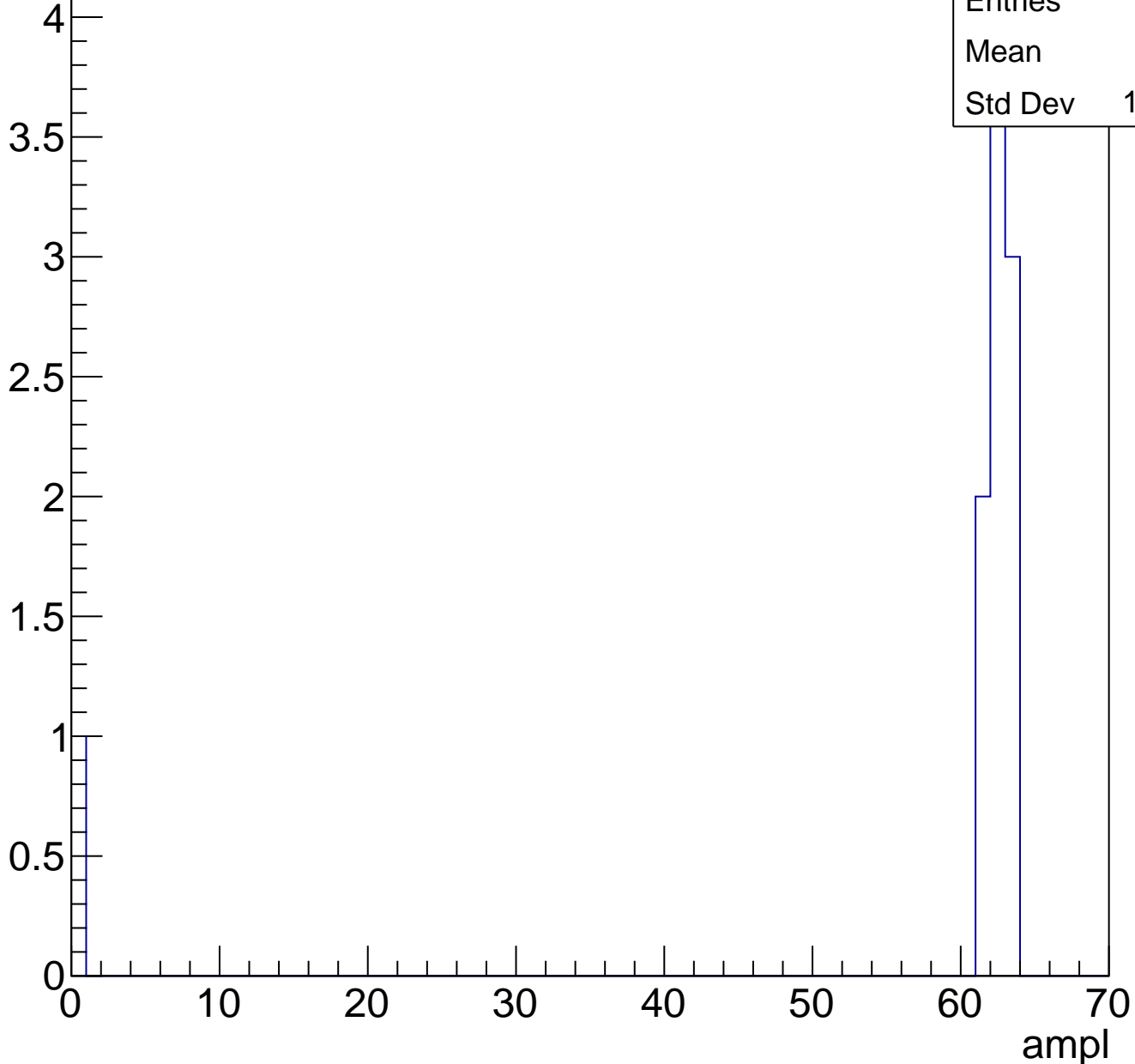
Entries	56
Mean	59.46
Std Dev	2.563



B1L100S, U10-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch37, adc0

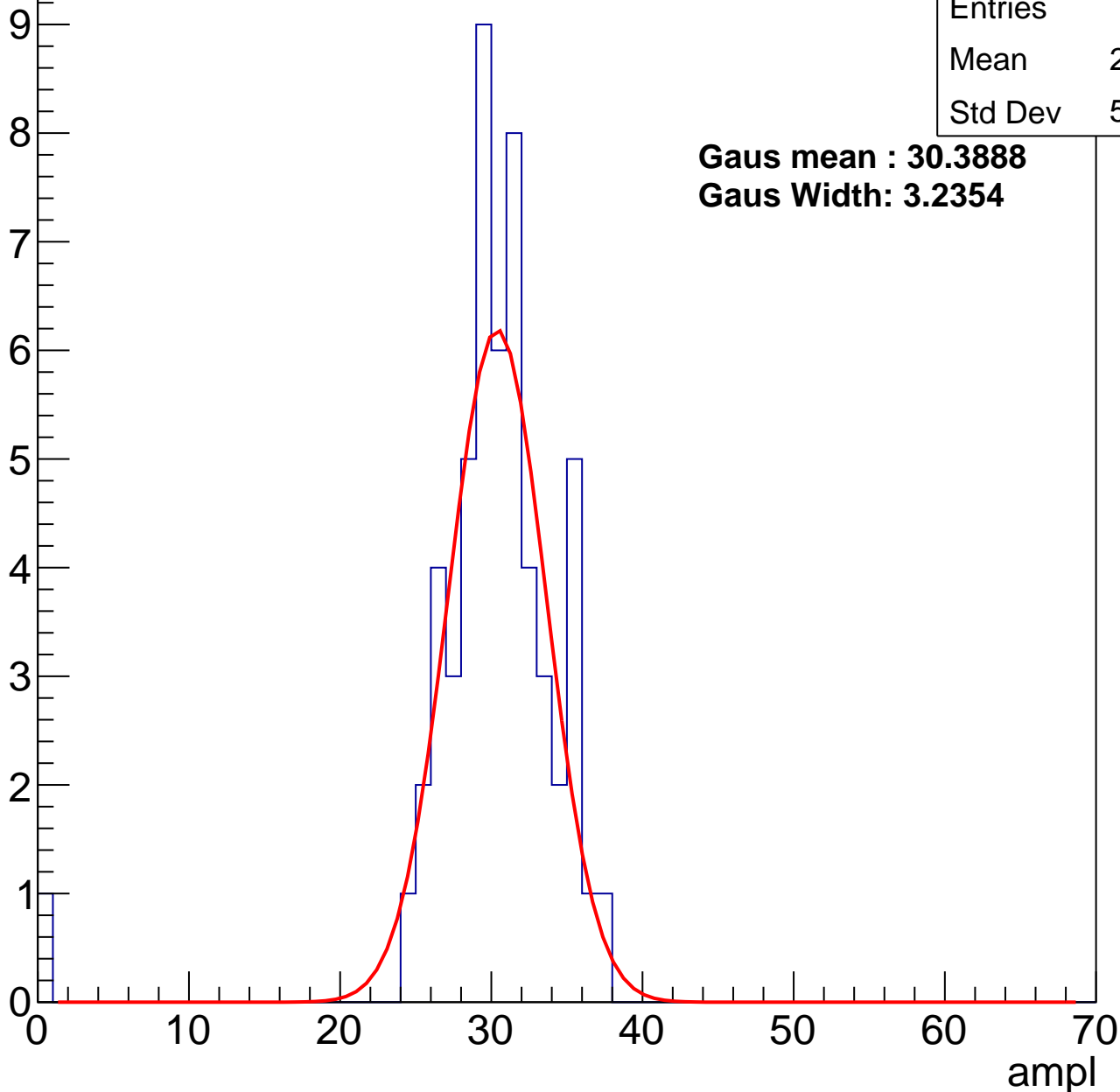
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.65
Std Dev	5.032

Gaus mean : 30.3888

Gaus Width: 3.2354



B1L100S, U10-ch37, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	37.43
Std Dev	3.762

Gaus mean : 37.5575

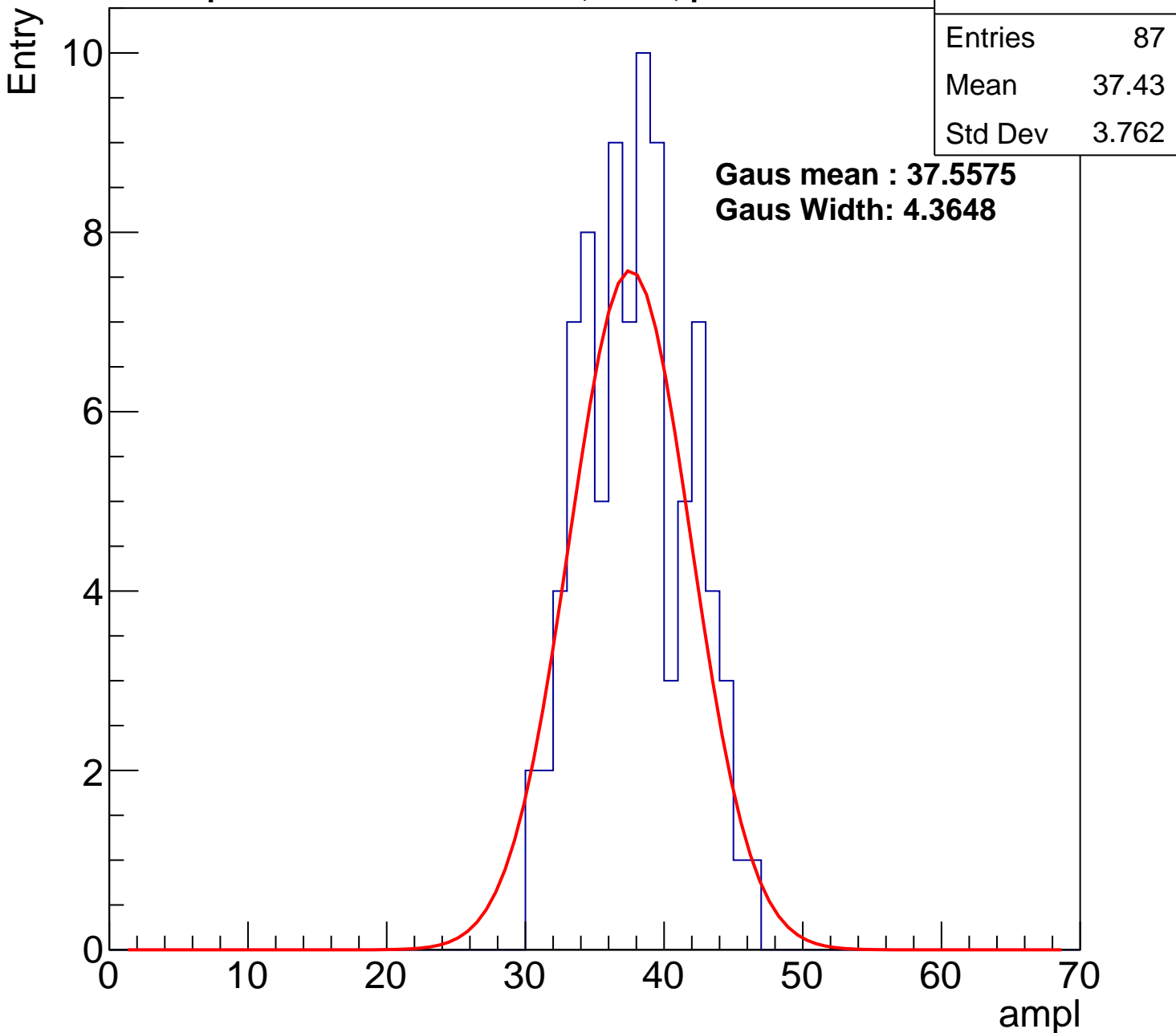
Gaus Width: 4.3648

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch37, adc2

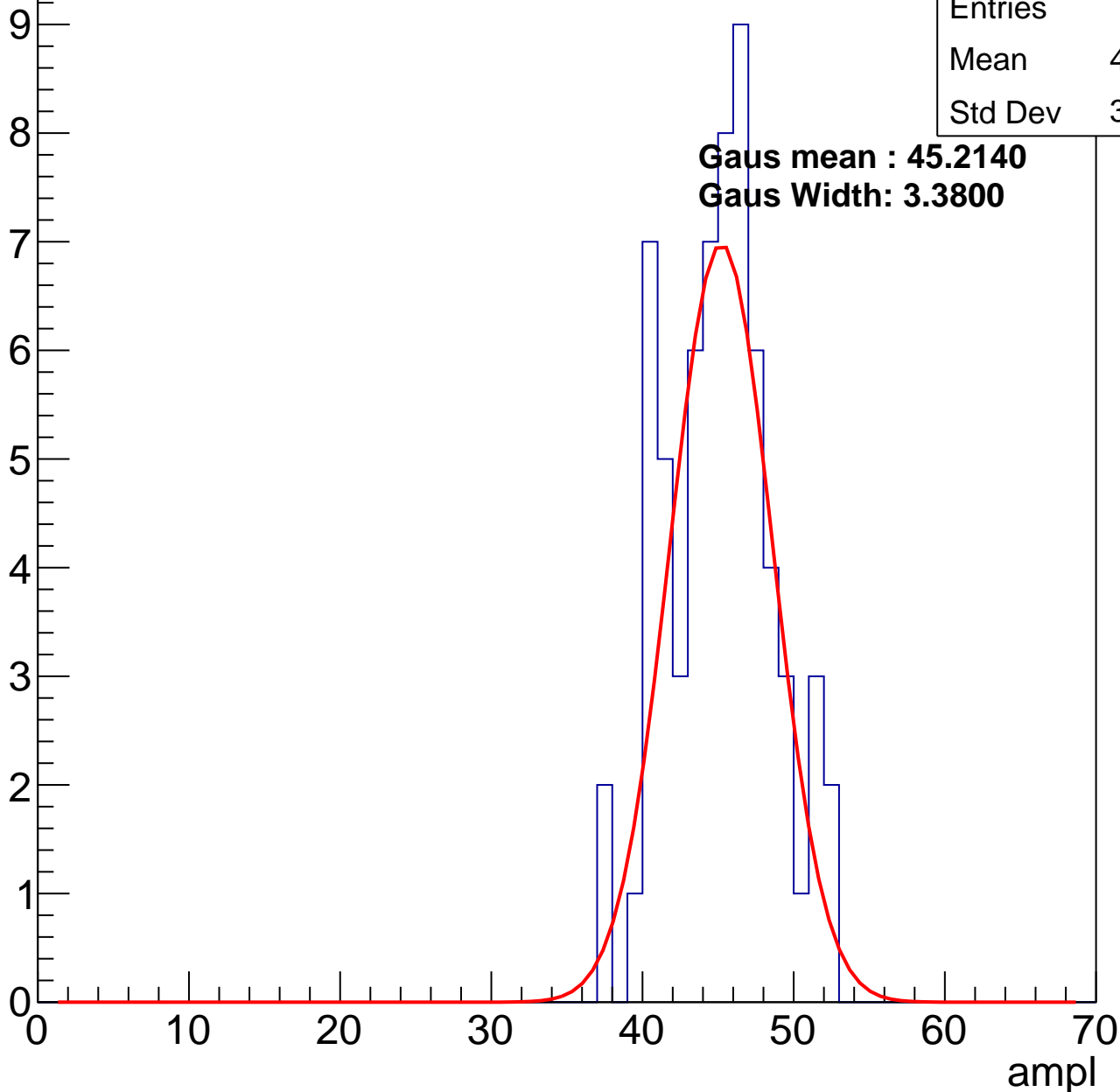
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.66
Std Dev	3.492

Gaus mean : 45.2140

Gaus Width: 3.3800

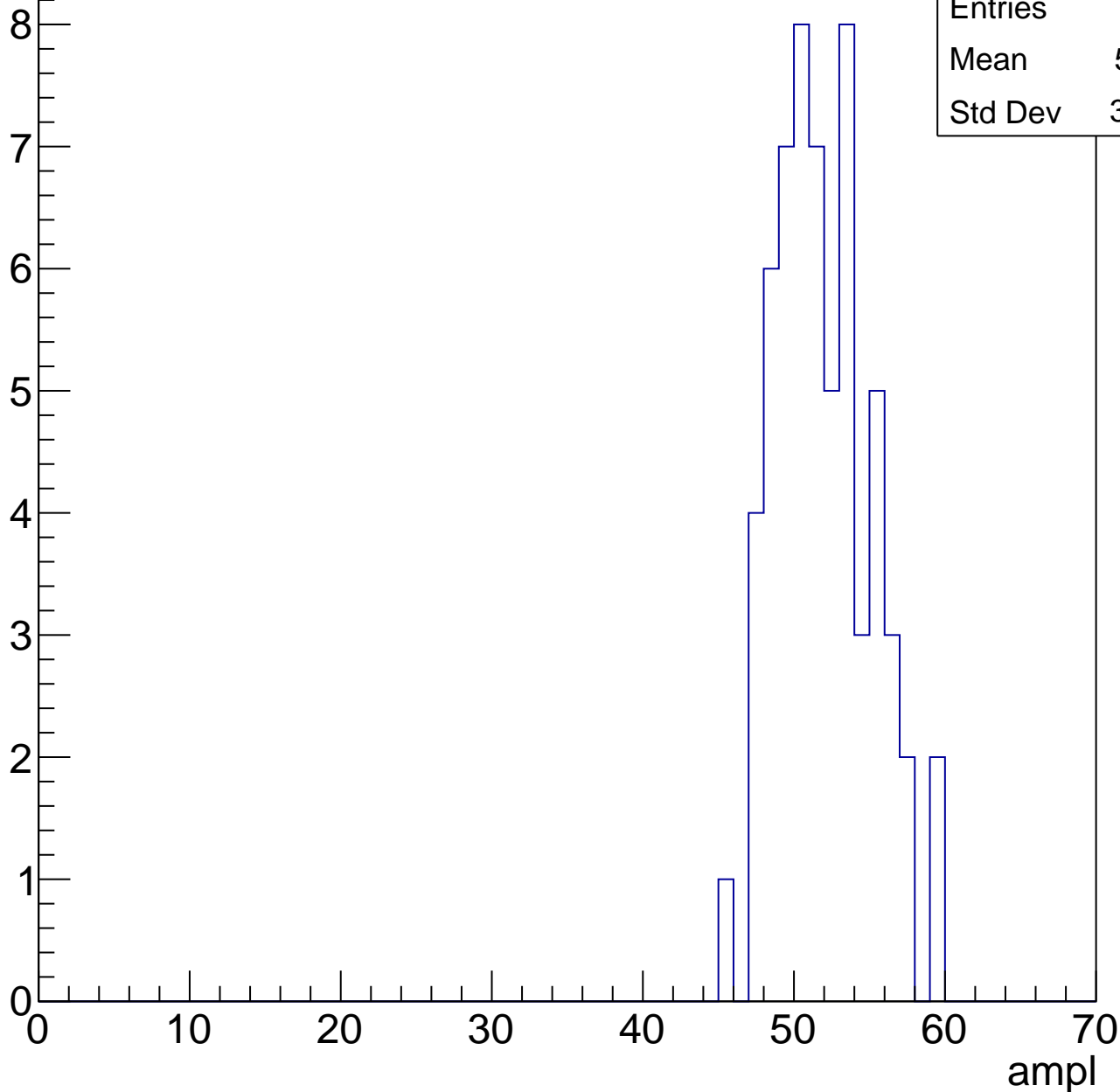


B1L100S, U10-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	51.51
Std Dev	3.119

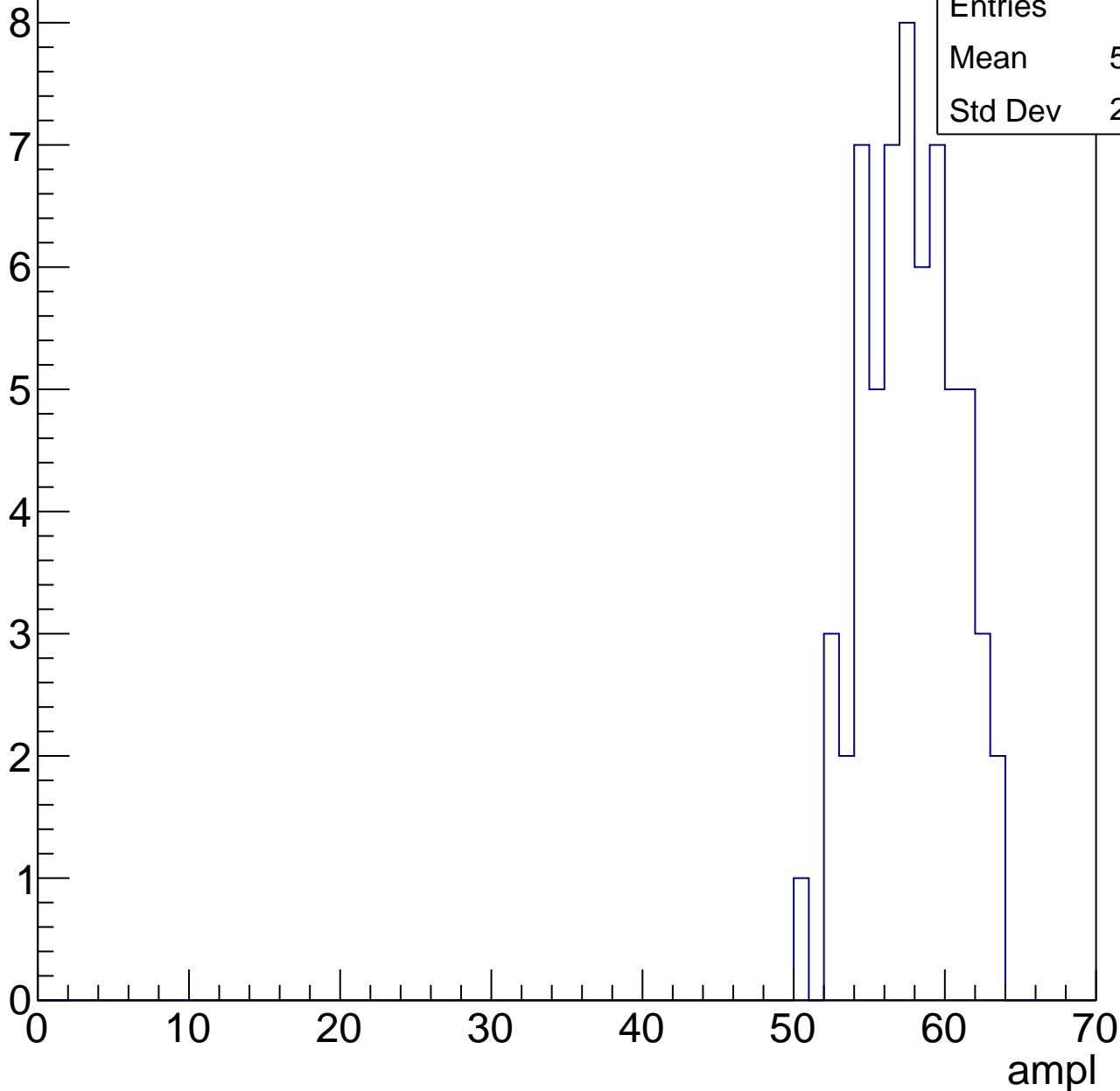


B1L100S, U10-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.23
Std Dev	2.999

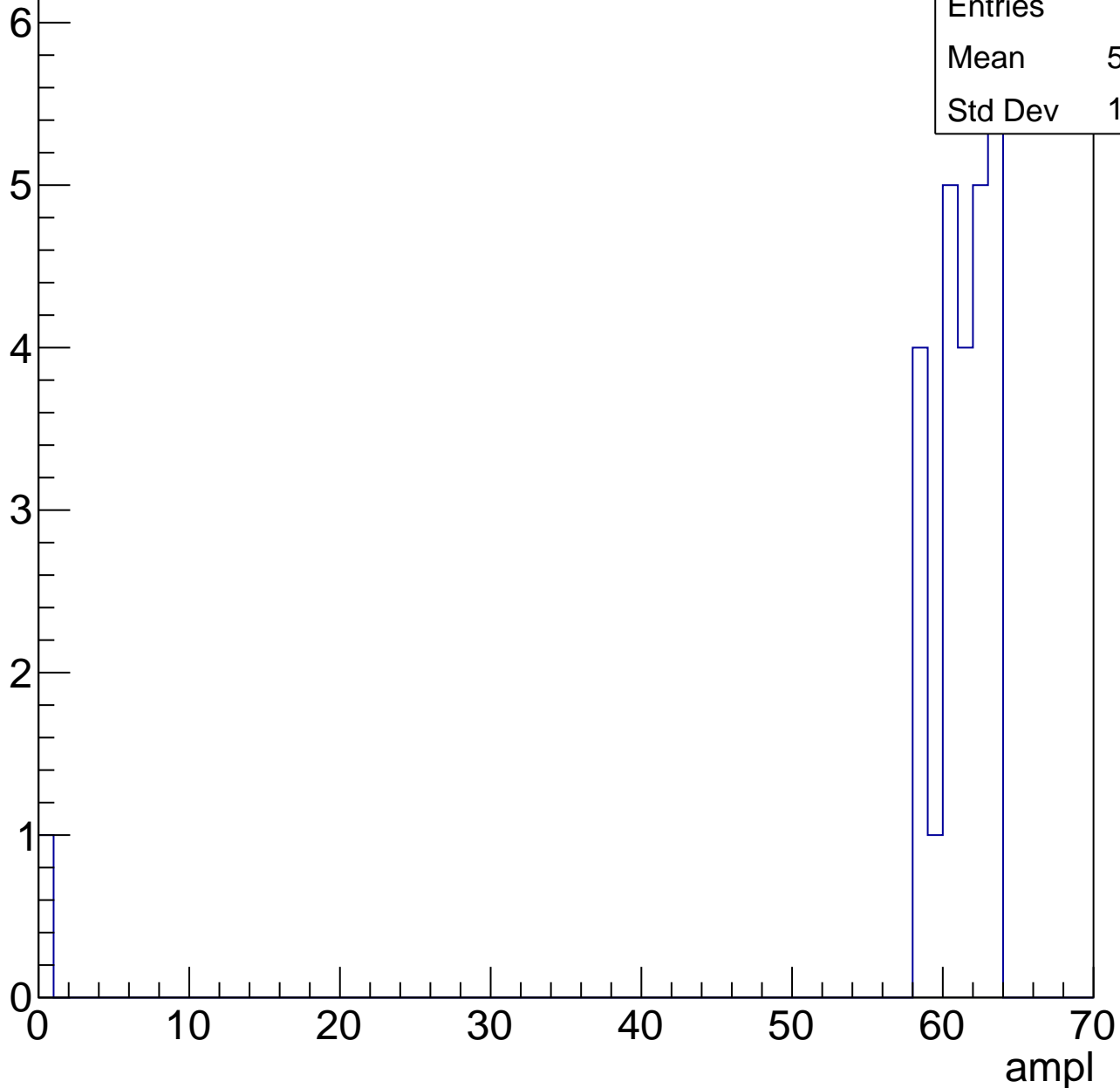


B1L100S, U10-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.58
Std Dev	11.84



B1L100S, U10-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L100S, U10-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U10-ch38, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	54
Mean	28.09
Std Dev	3.406

Gaus mean : 29.3016

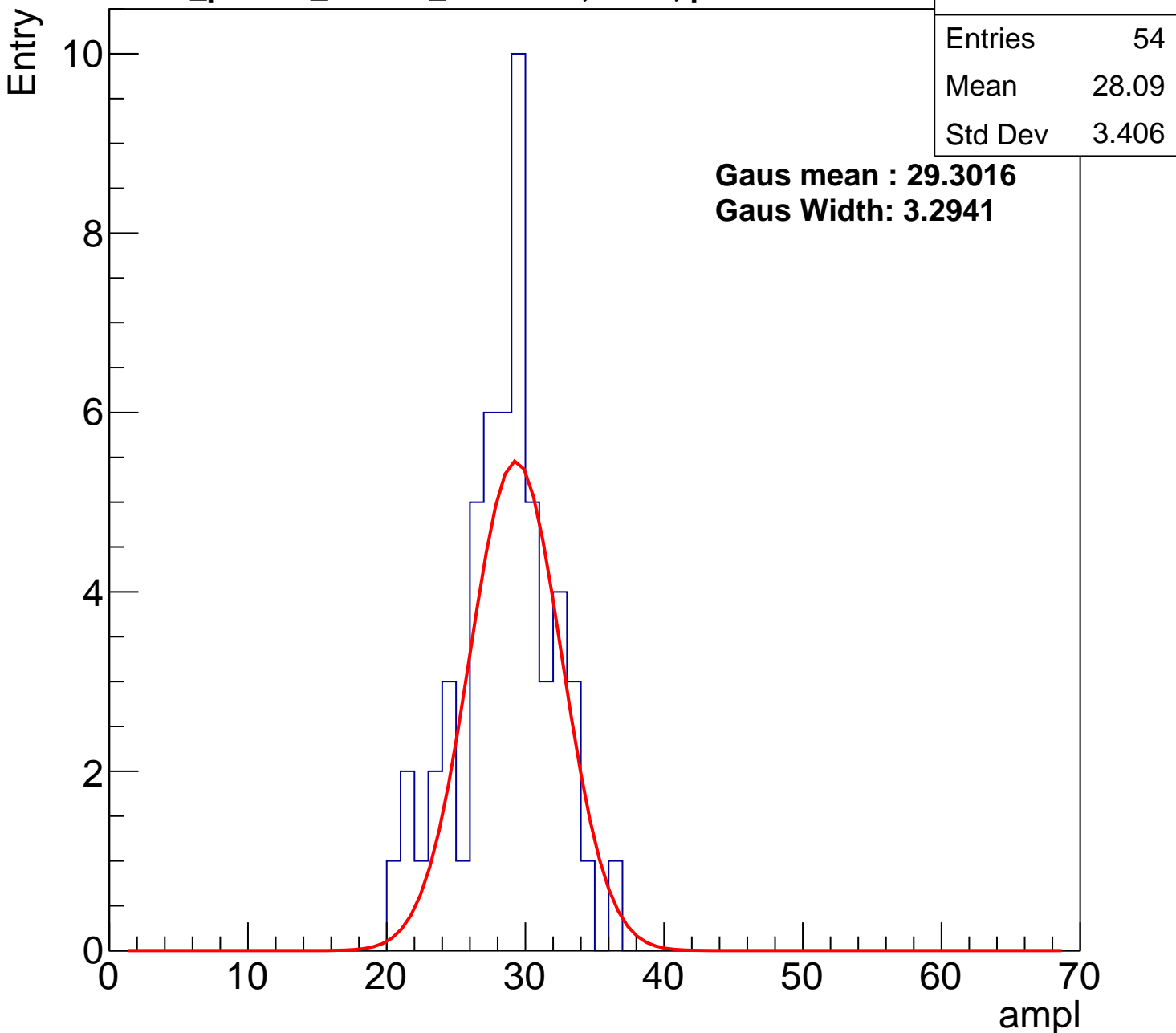
Gaus Width: 3.2941

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

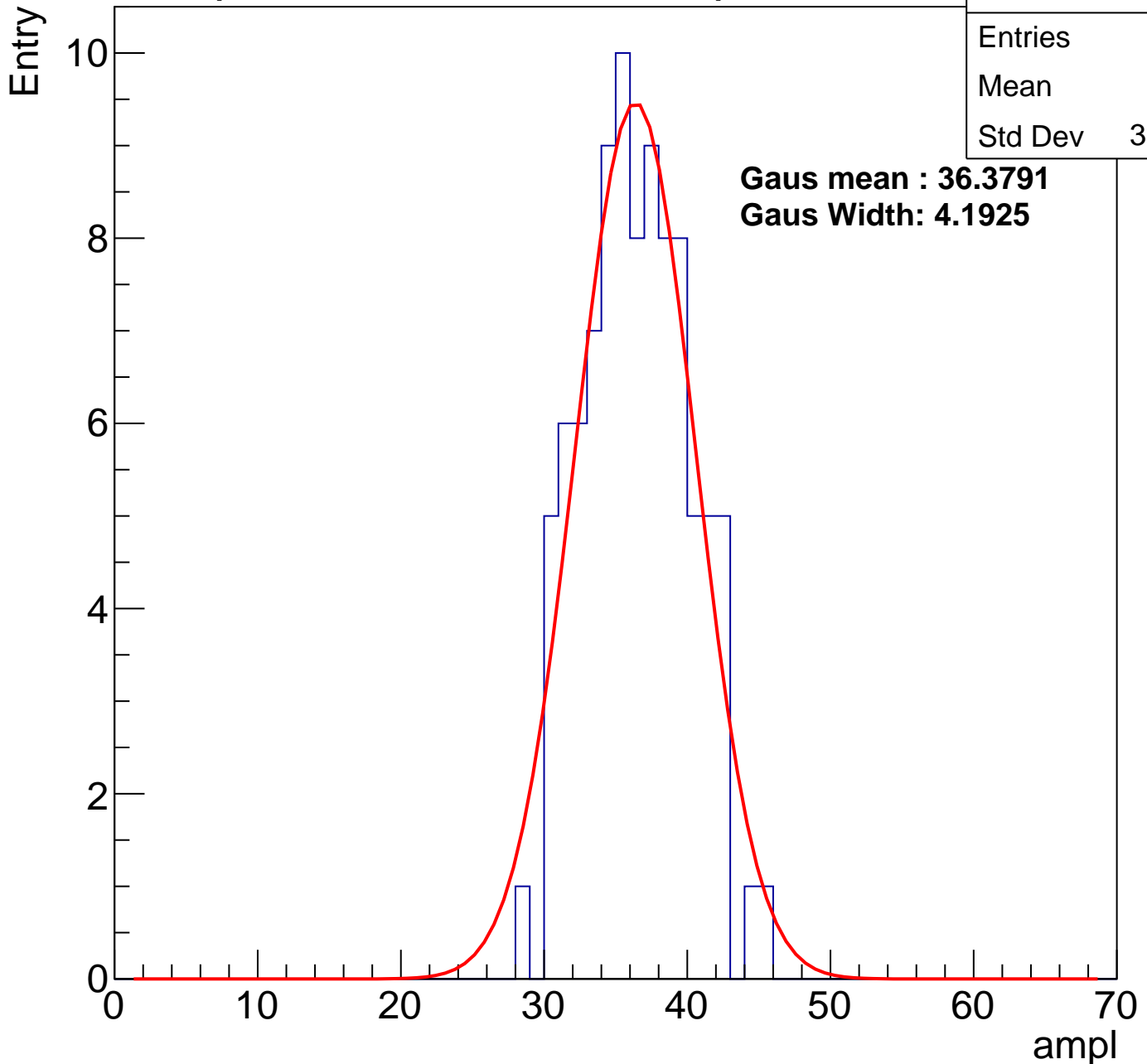


B1L100S, U10-ch38, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	94
Mean	36
Std Dev	3.635

Gaus mean : 36.3791
Gaus Width: 4.1925



B1L100S, U10-ch38, adc2

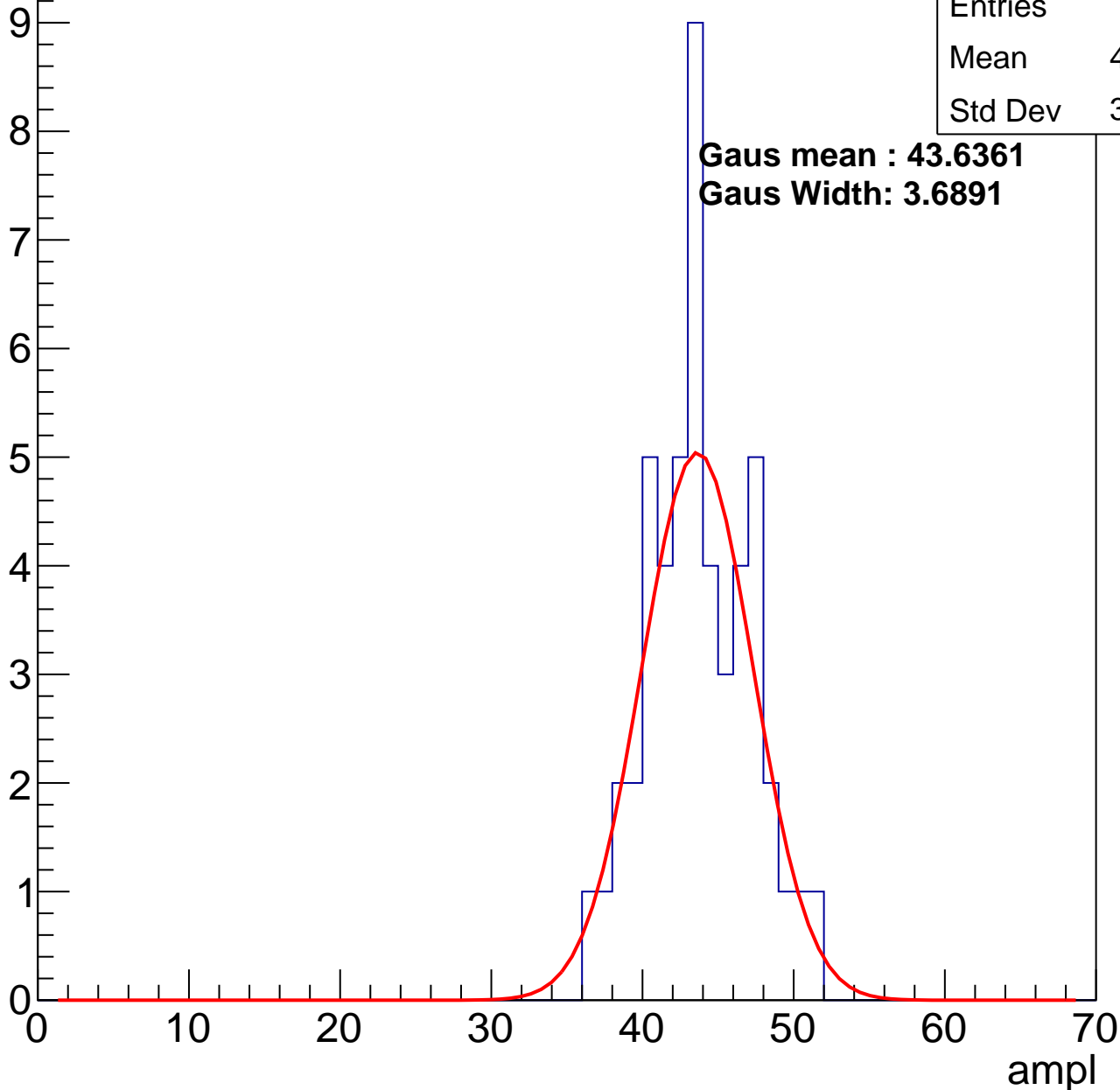
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	43.28
Std Dev	3.347

Gaus mean : 43.6361

Gaus Width: 3.6891

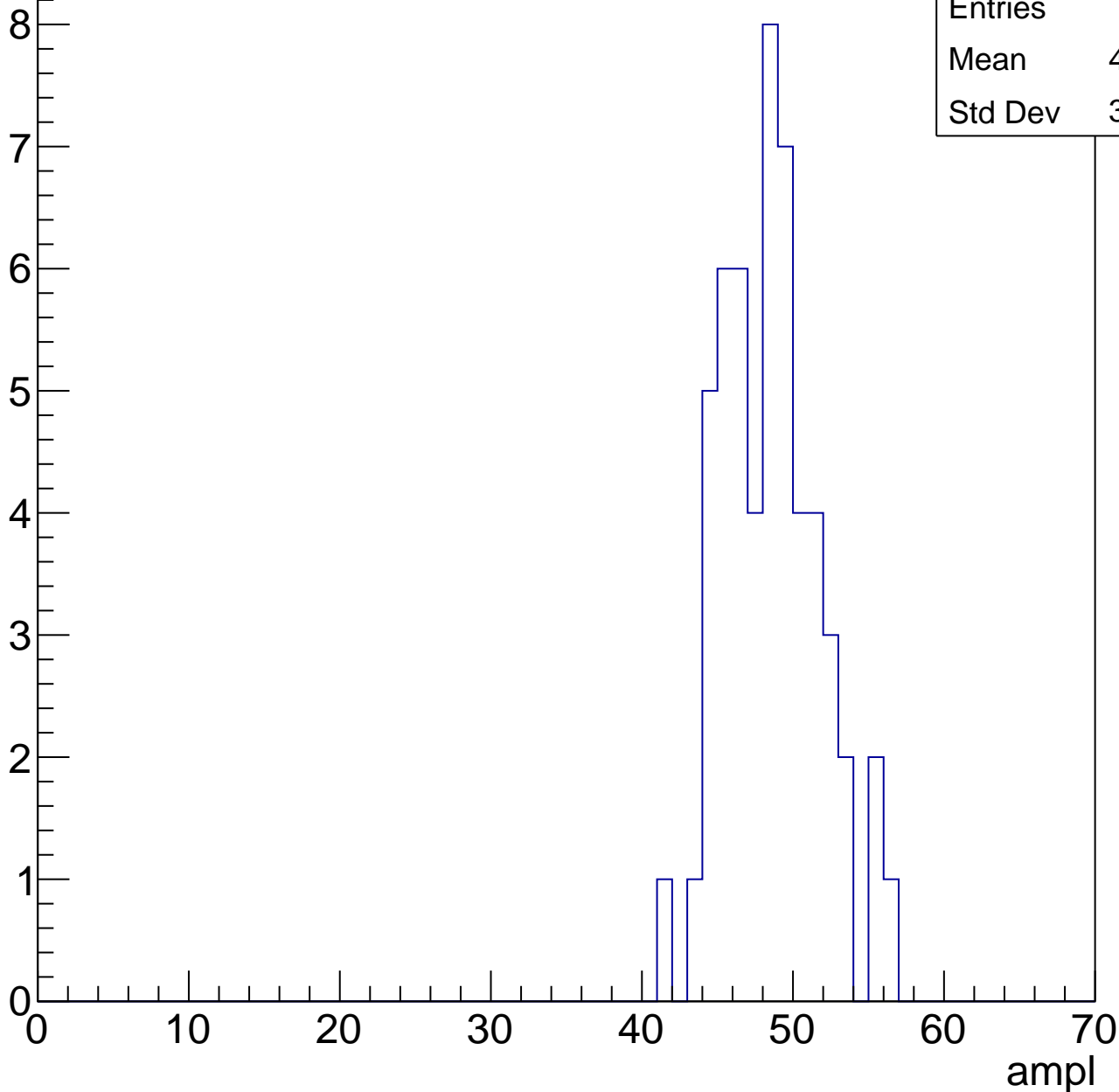


B1L100S, U10-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	48.09
Std Dev	3.205



B1L100S, U10-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

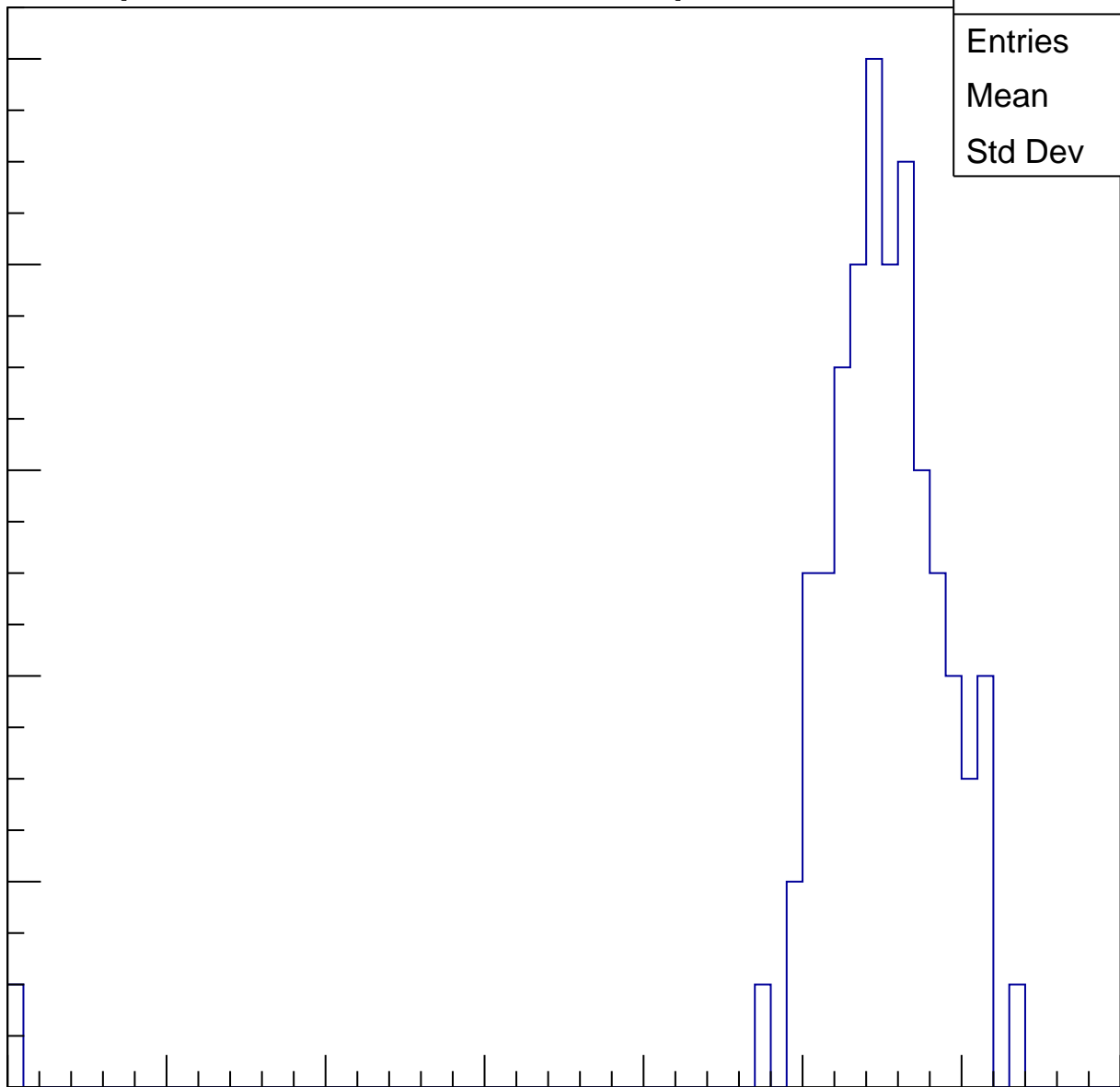
Entries	79
Mean	54.14
Std Dev	6.97

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

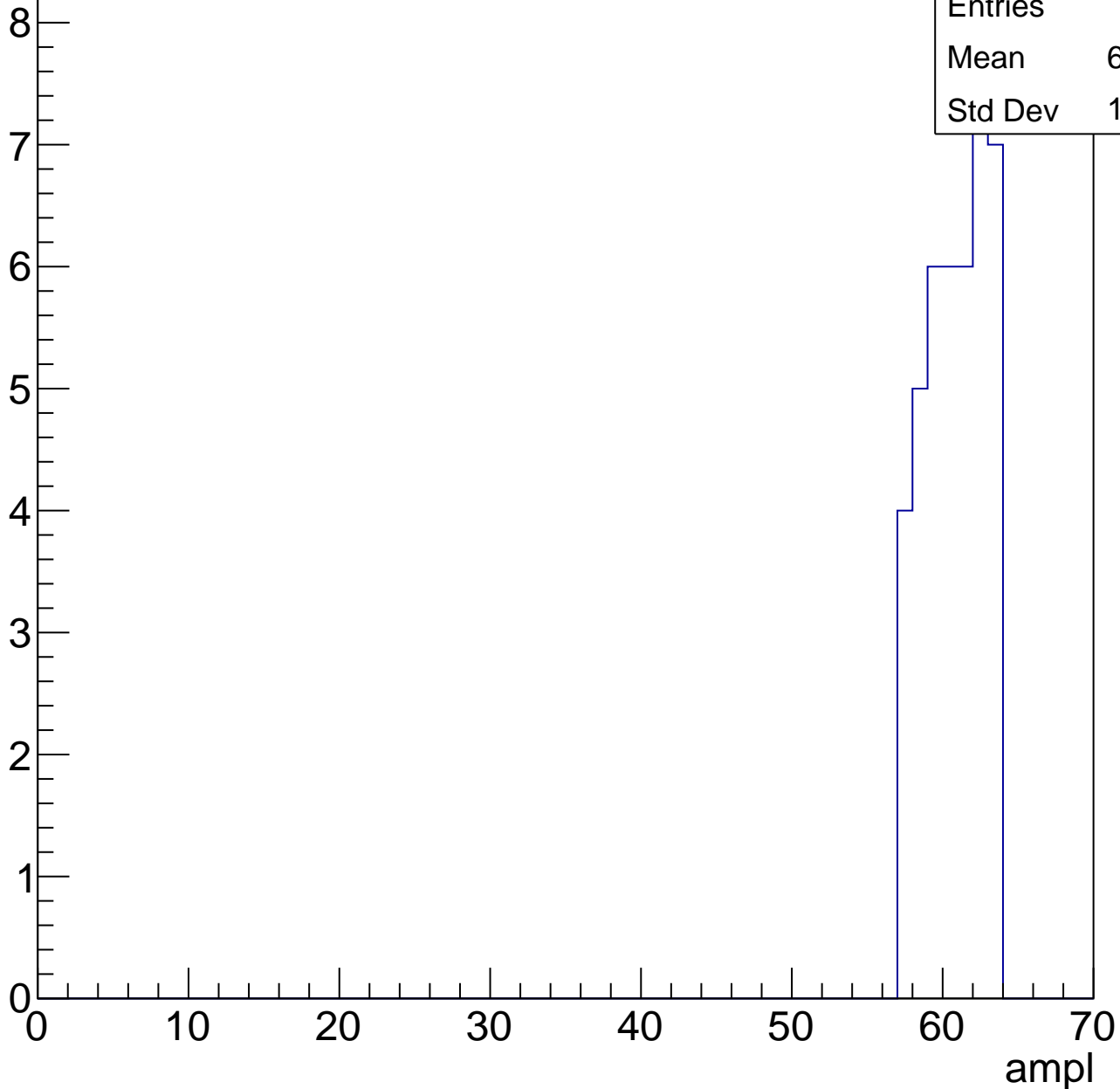


B1L100S, U10-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	60.36
Std Dev	1.937



B1L100S, U10-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U10-ch39, adc0

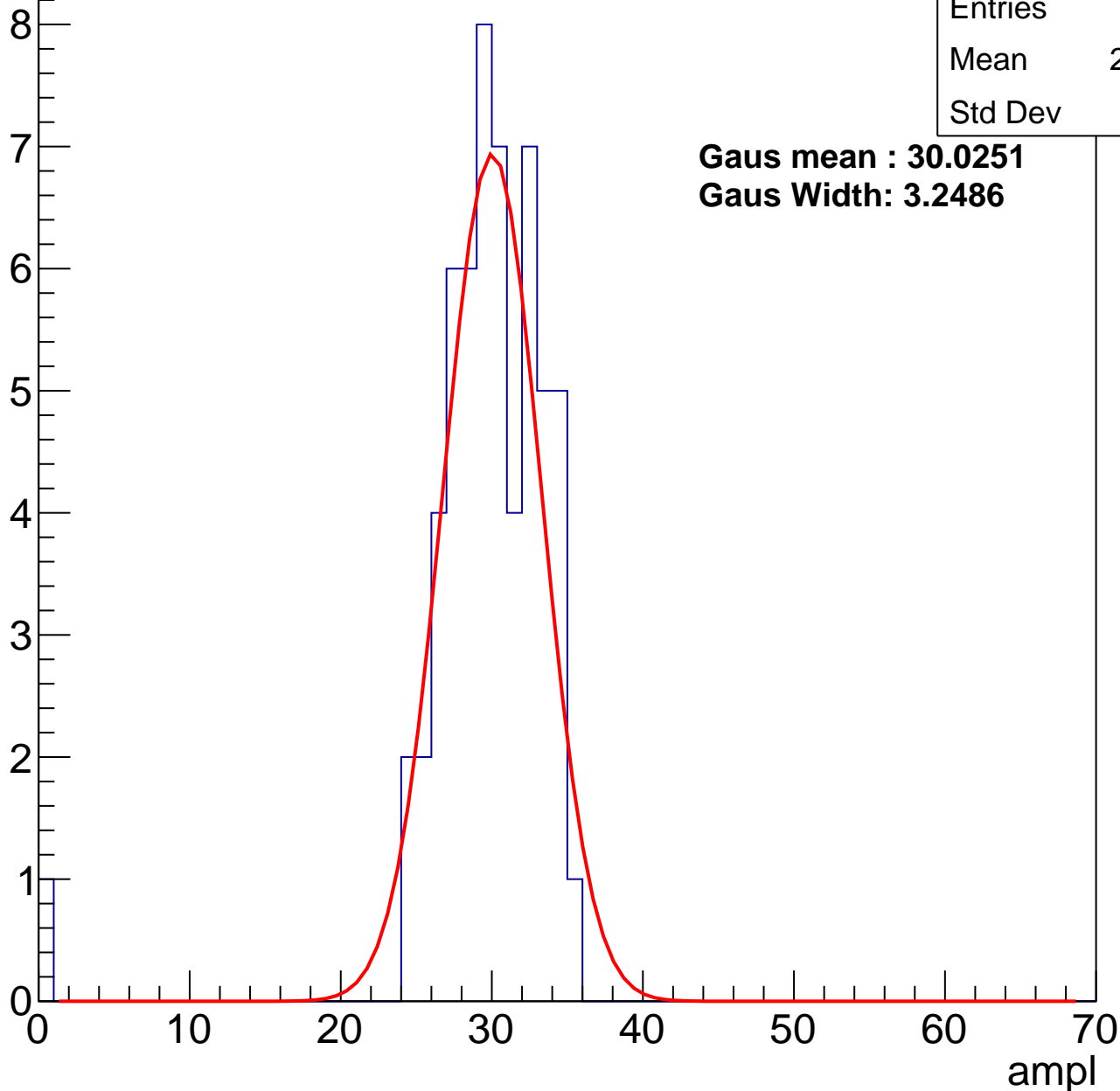
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.17
Std Dev	4.76

Gaus mean : 30.0251

Gaus Width: 3.2486



B1L100S, U10-ch39, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	37.09
Std Dev	3.697

Gaus mean : 37.6318

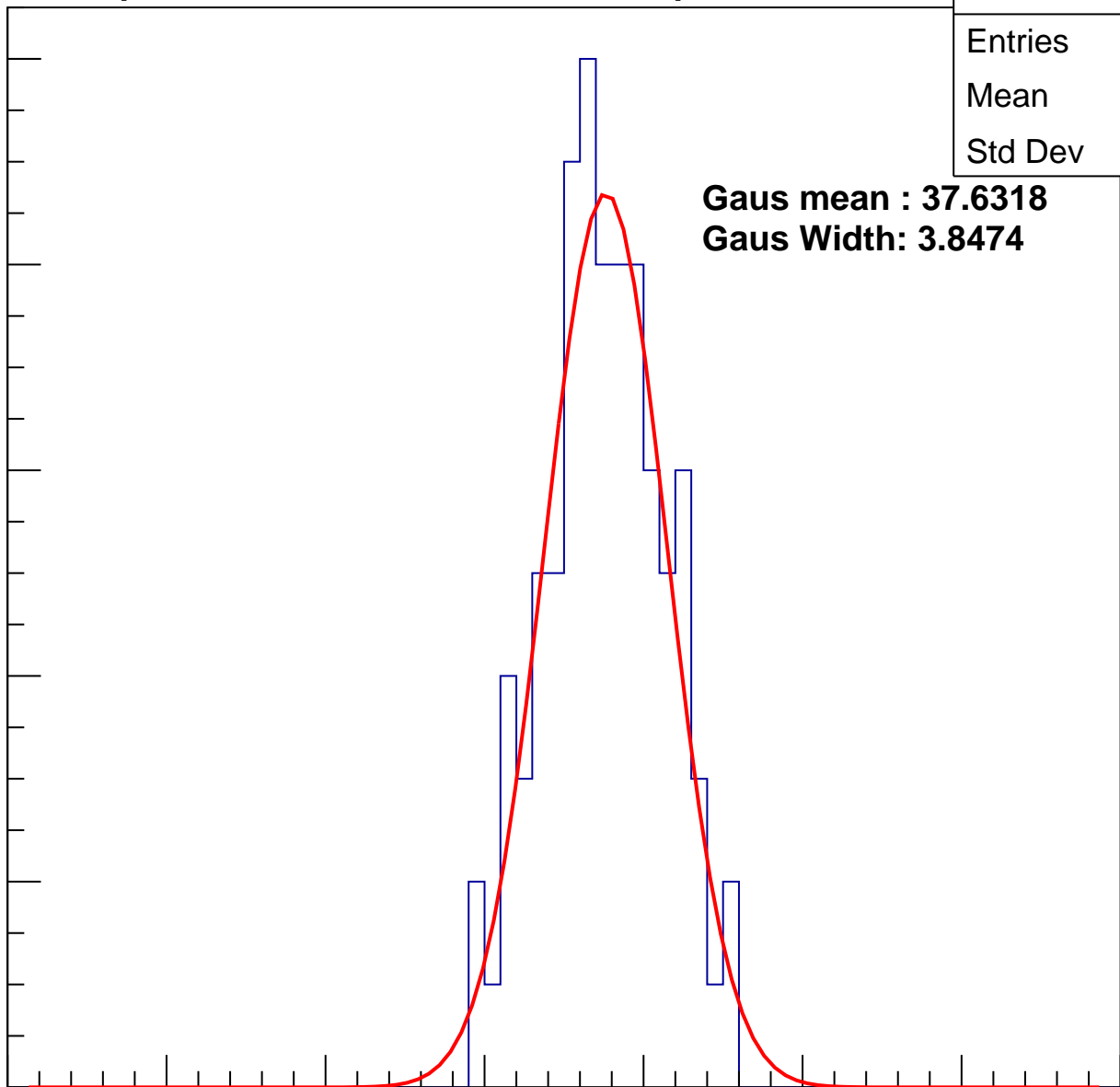
Gaus Width: 3.8474

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch39, adc2

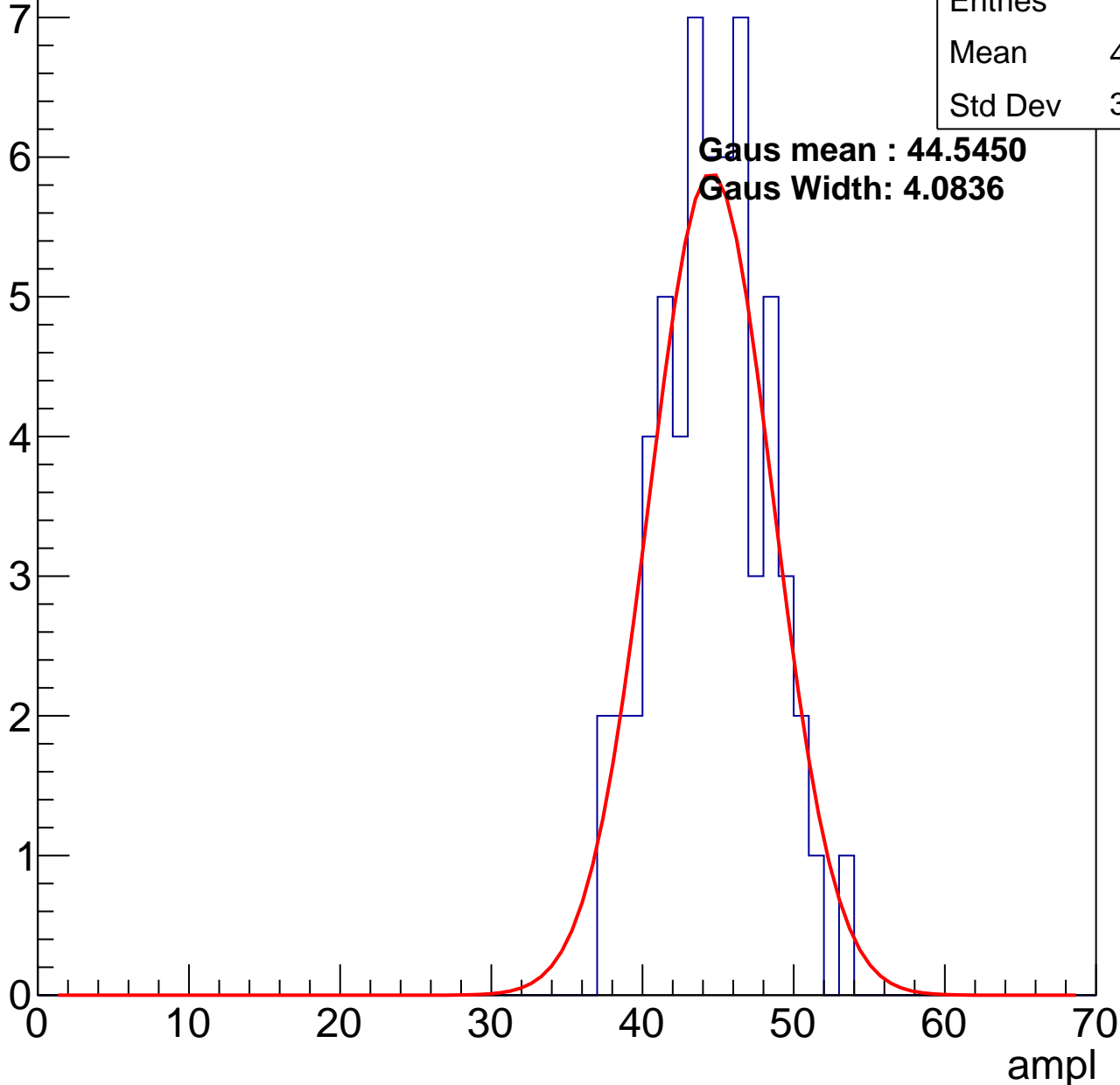
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.17
Std Dev	3.578

Gaus mean : 44.5450

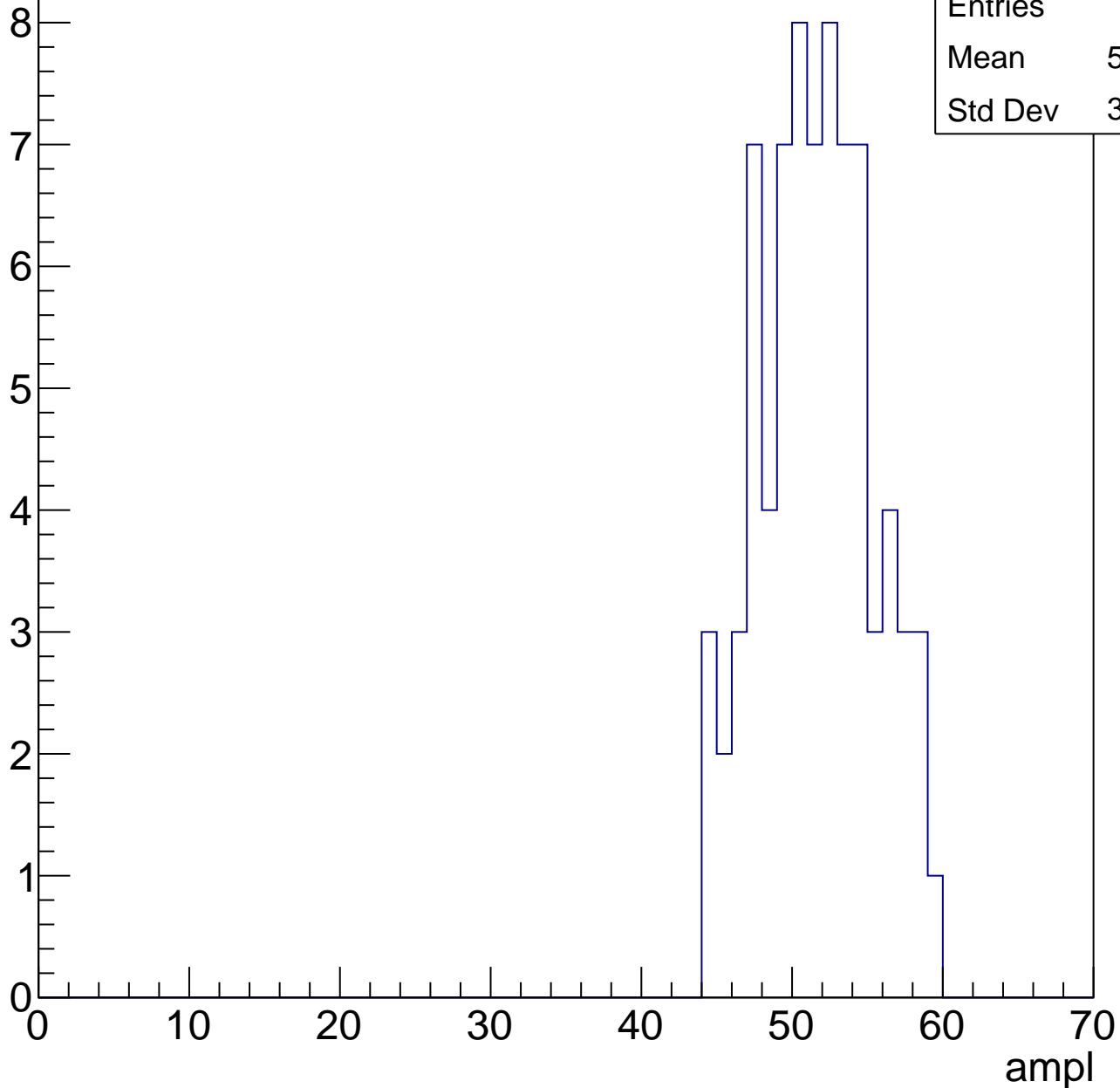
Gaus Width: 4.0836



B1L100S, U10-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



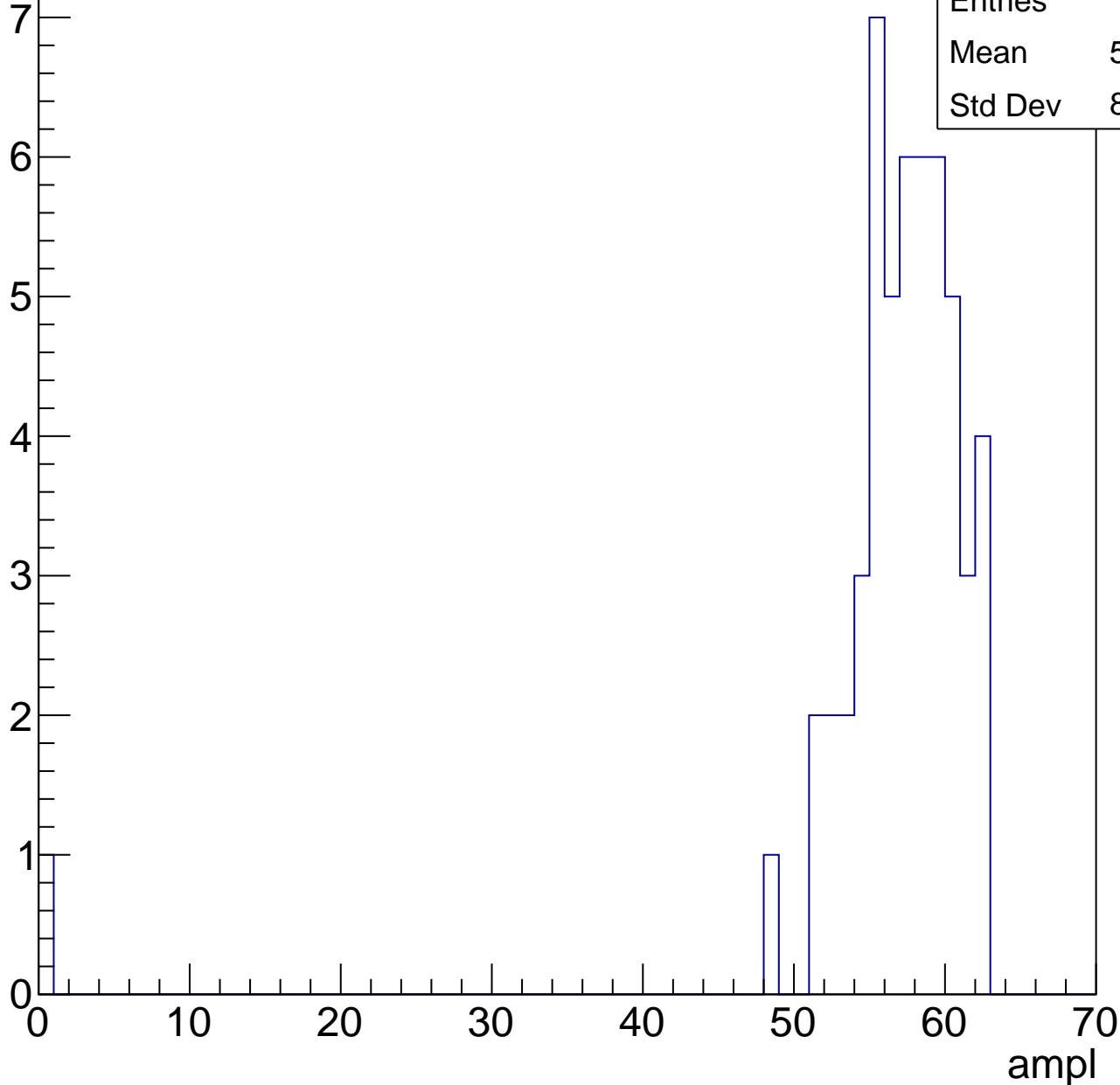
Entries	77
Mean	51.16
Std Dev	3.679

B1L100S, U10-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

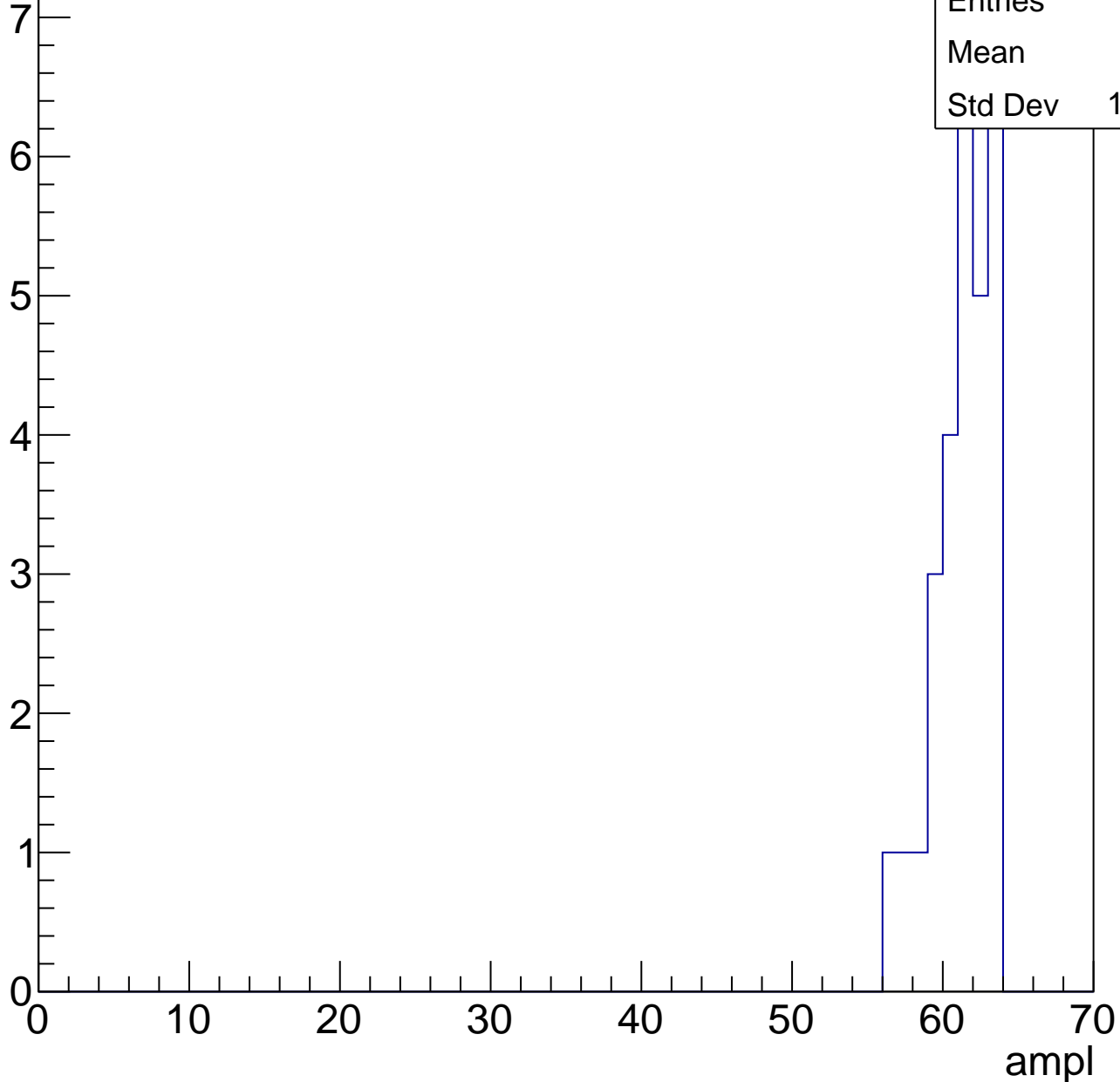
Entries	53
Mean	55.89
Std Dev	8.357



B1L100S, U10-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	29
Mean	60.9
Std Dev	1.845

B1L100S, U10-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch40, adc0

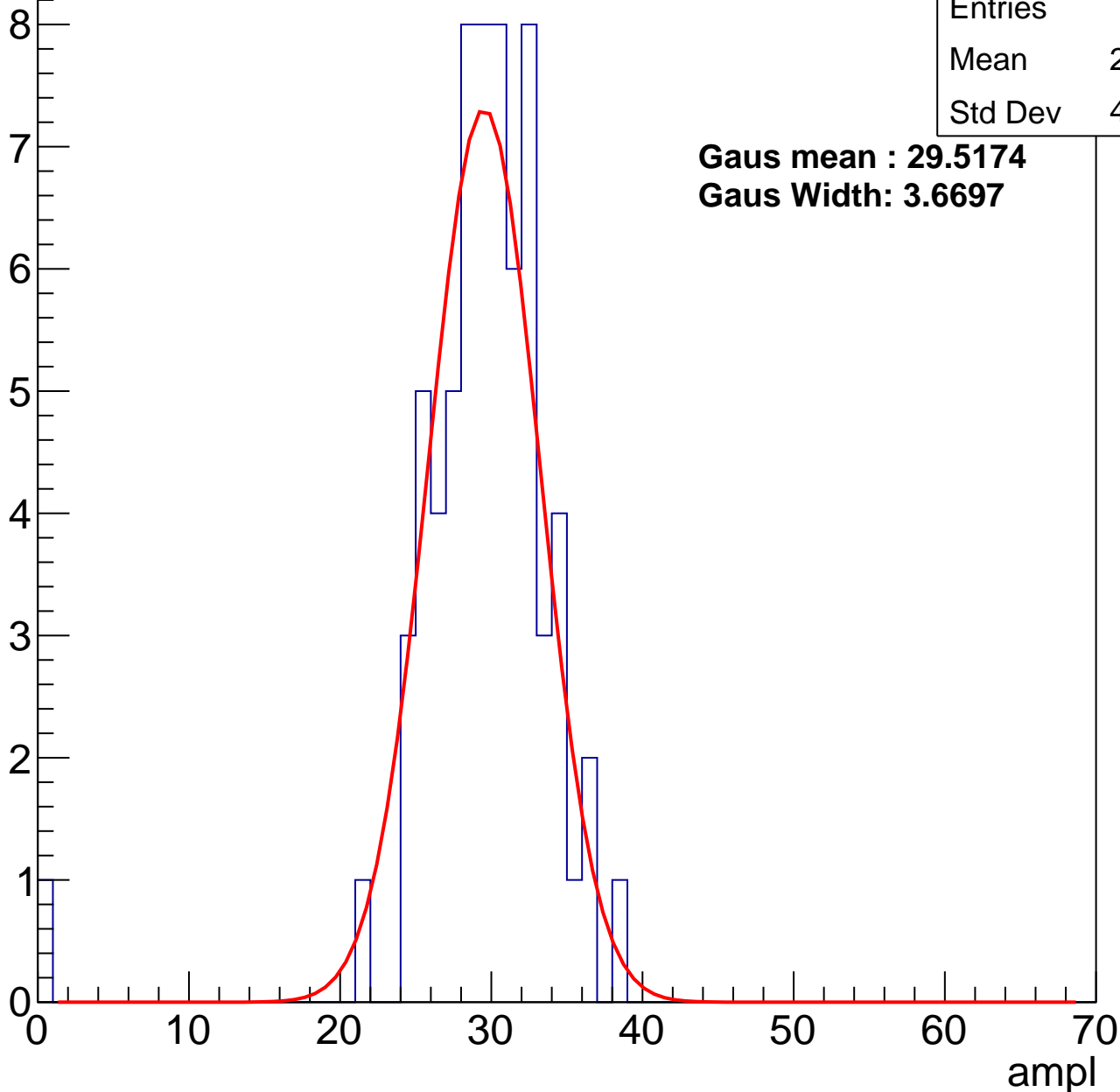
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.04
Std Dev	4.837

Gaus mean : 29.5174

Gaus Width: 3.6697



B1L100S, U10-ch40, adc1

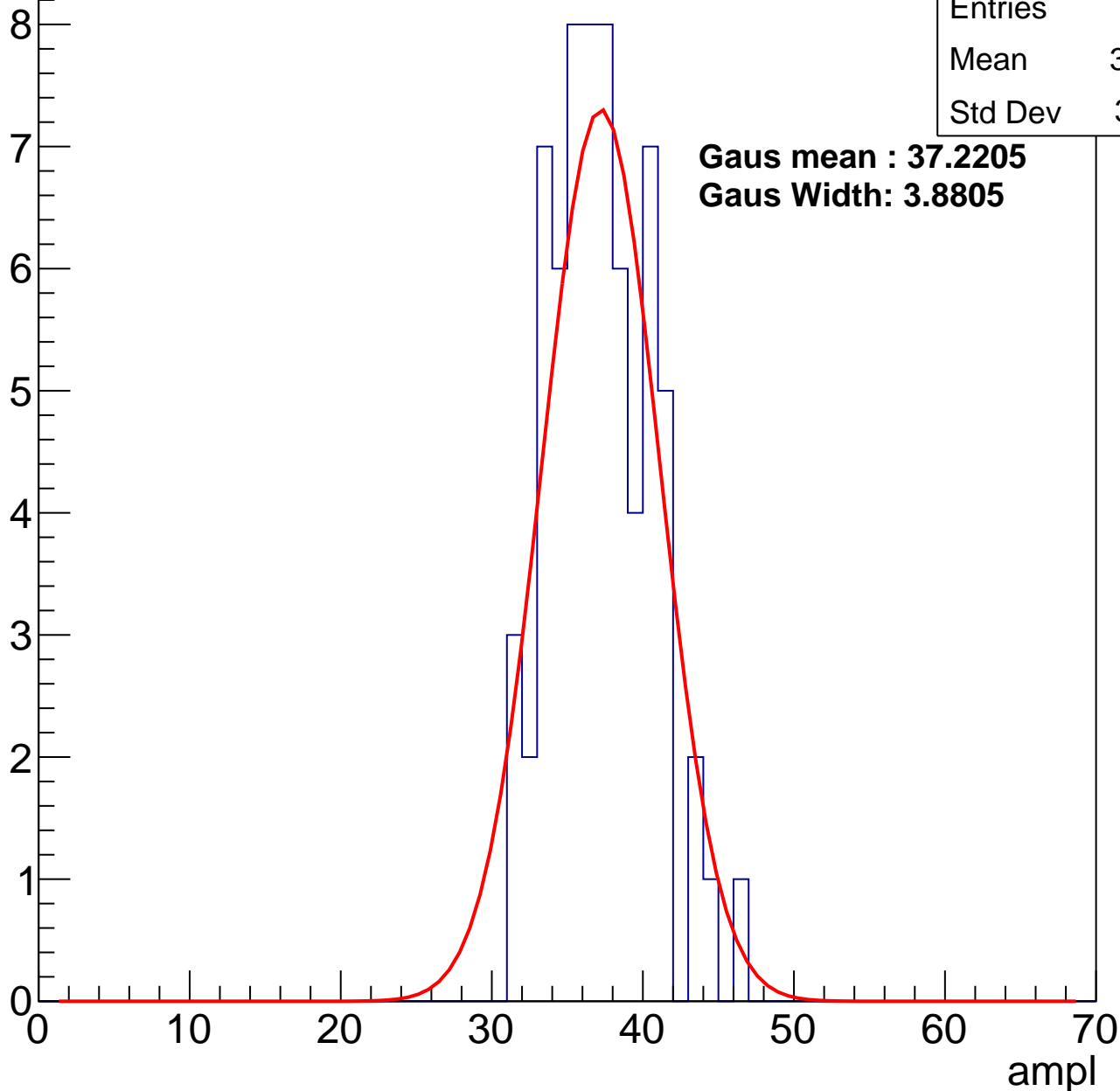
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.78
Std Dev	3.271

Gaus mean : 37.2205

Gaus Width: 3.8805



B1L100S, U10-ch40, adc2

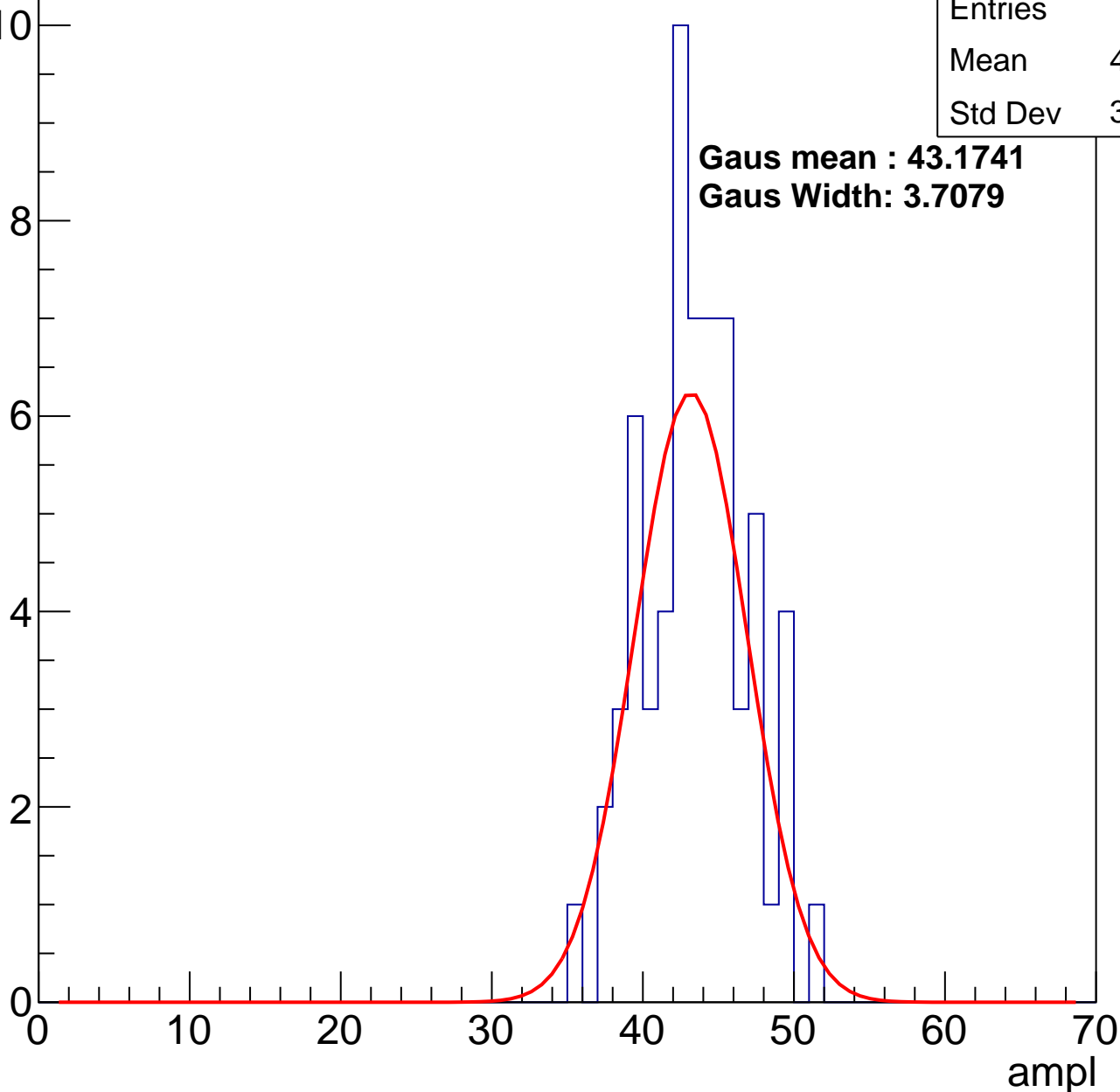
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.02
Std Dev	3.389

Gaus mean : 43.1741

Gaus Width: 3.7079

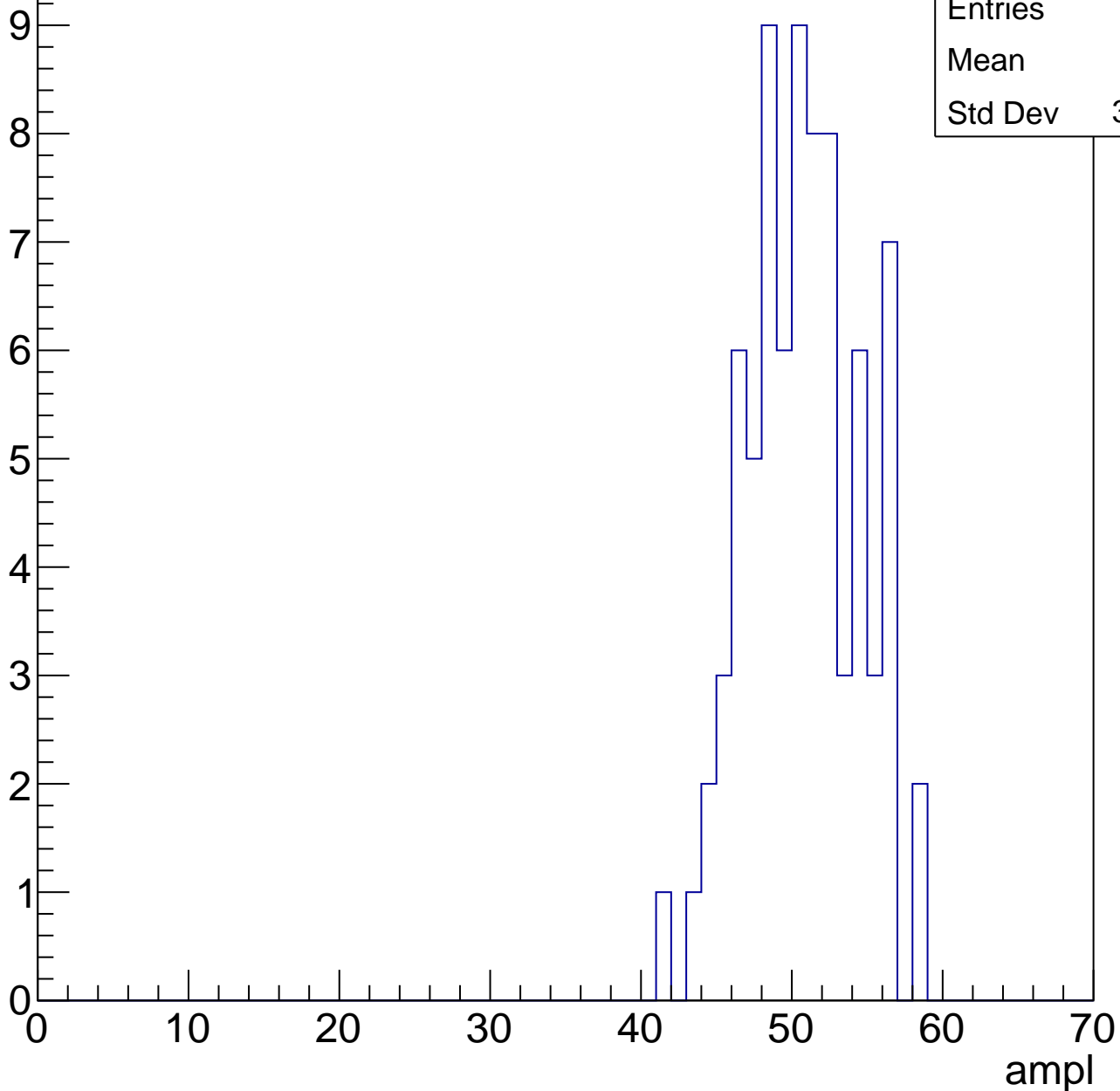


B1L100S, U10-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

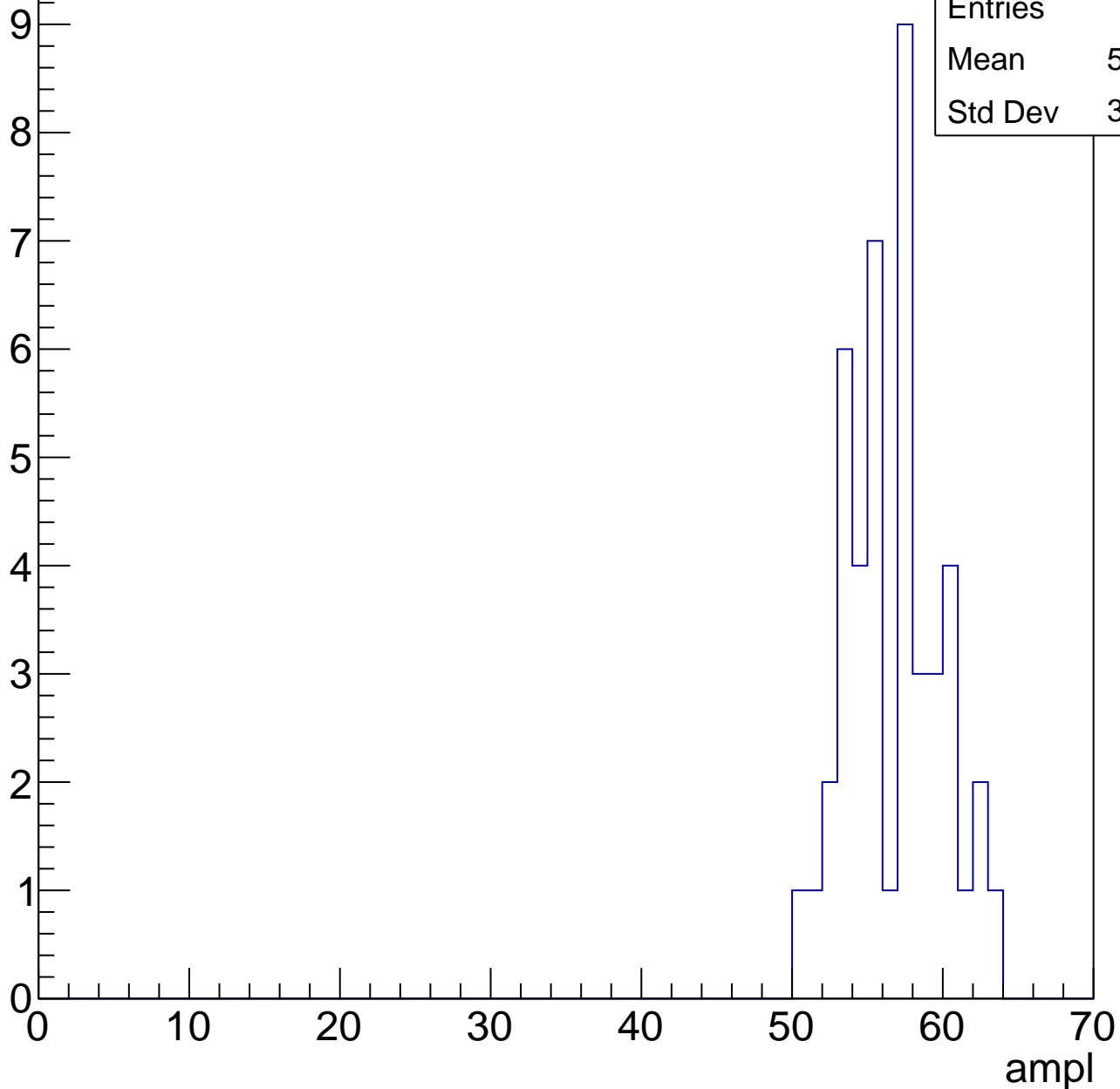
Entries	79
Mean	50.3
Std Dev	3.681



B1L100S, U10-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

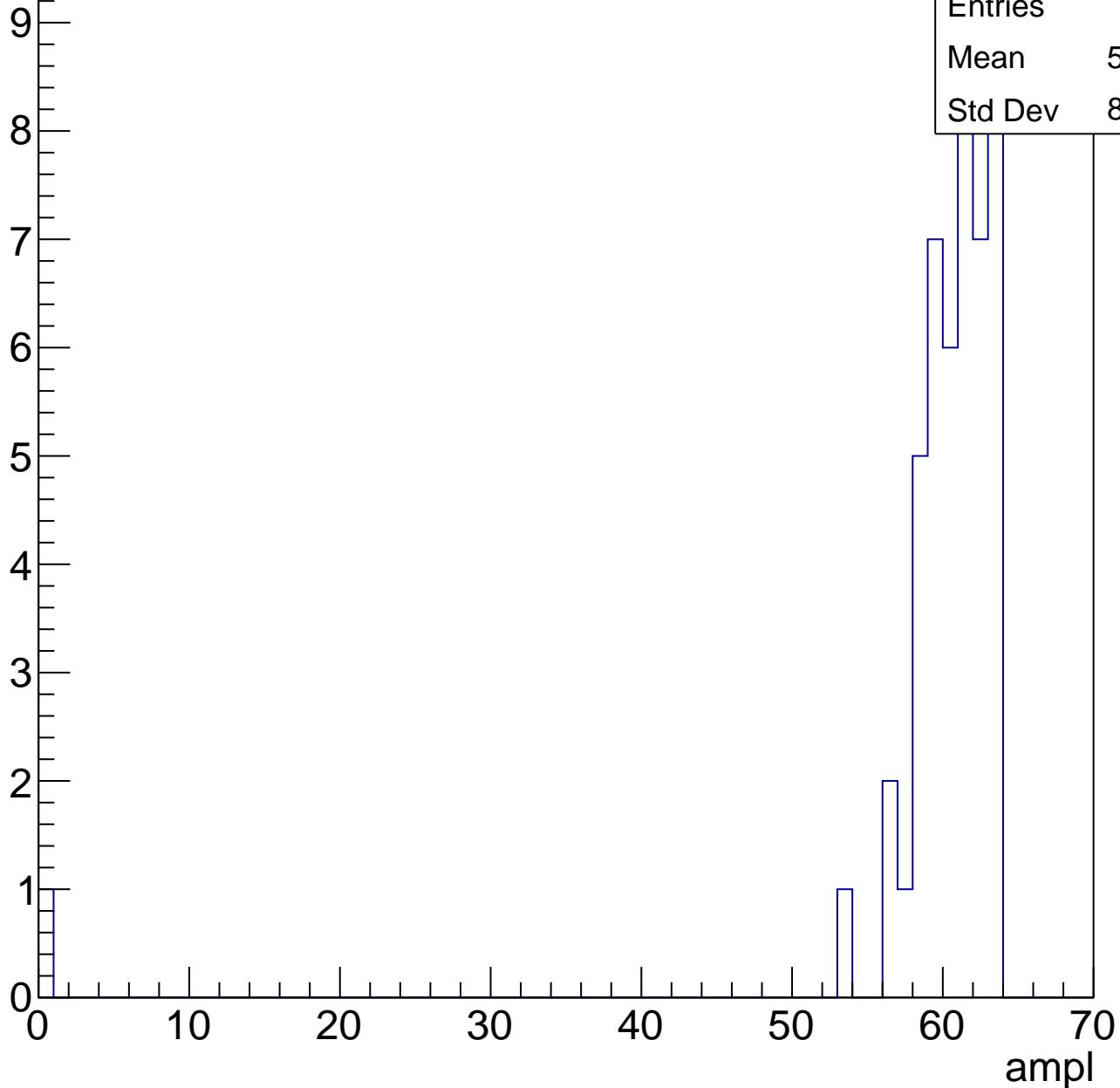
Entry



B1L100S, U10-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	48
Mean	59.06
Std Dev	8.889

B1L100S, U10-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U10-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch41, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	28.15
Std Dev	4.859

Gaus mean : 29.4890

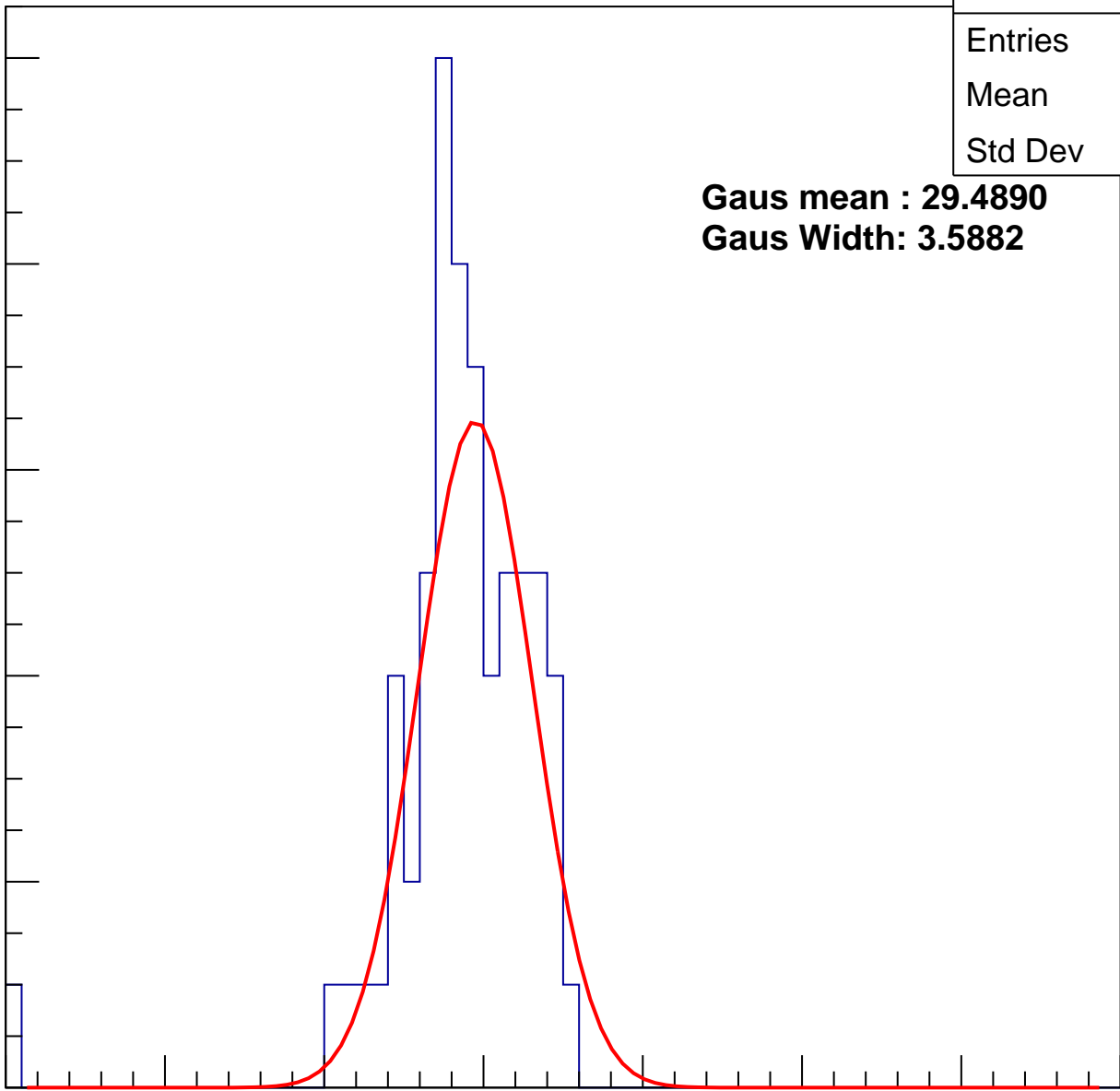
Gaus Width: 3.5882

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	35.69
Std Dev	3.793

Gaus mean : 36.1765

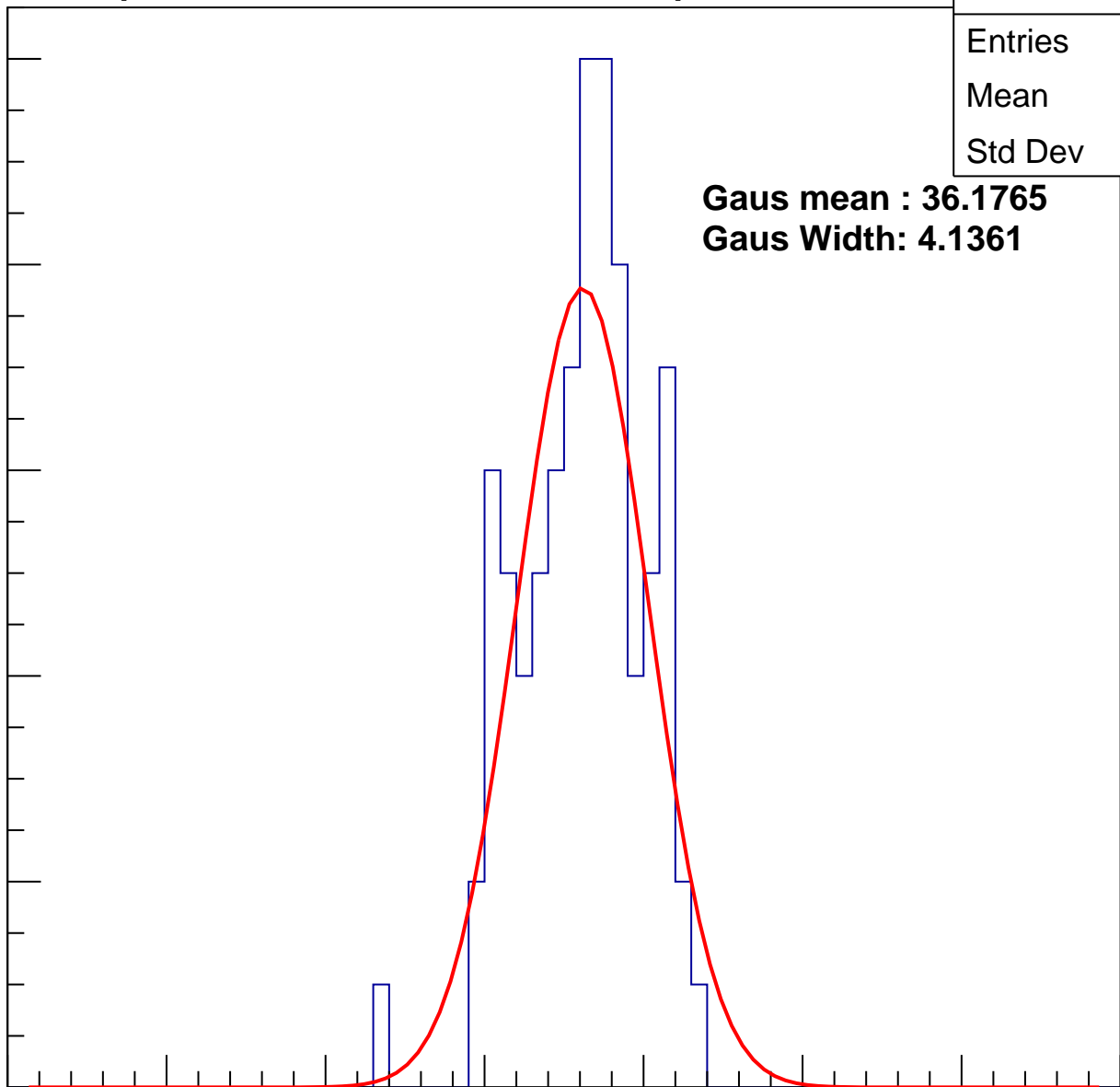
Gaus Width: 4.1361

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch41, adc2

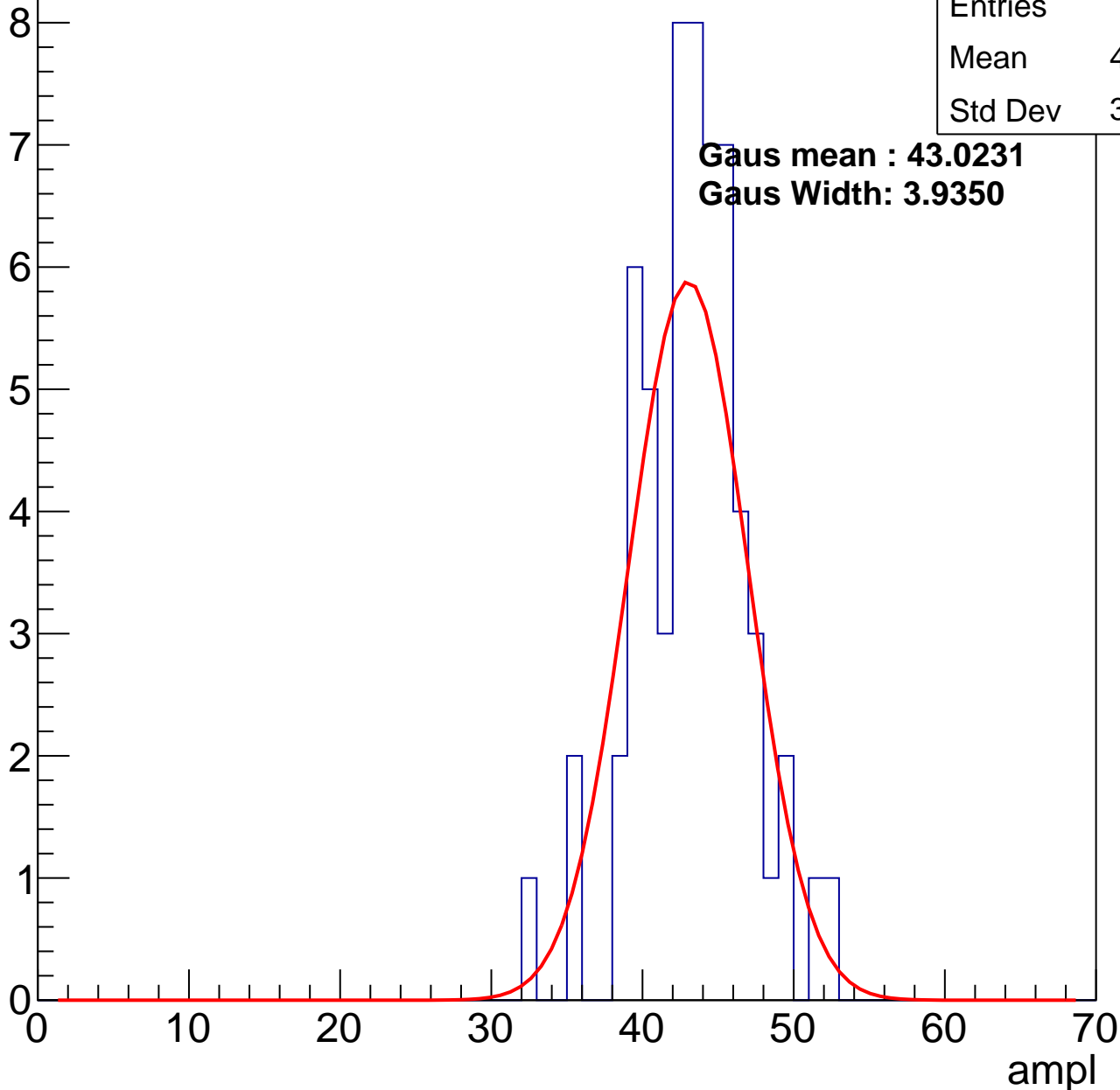
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.82
Std Dev	3.664

Gaus mean : 43.0231

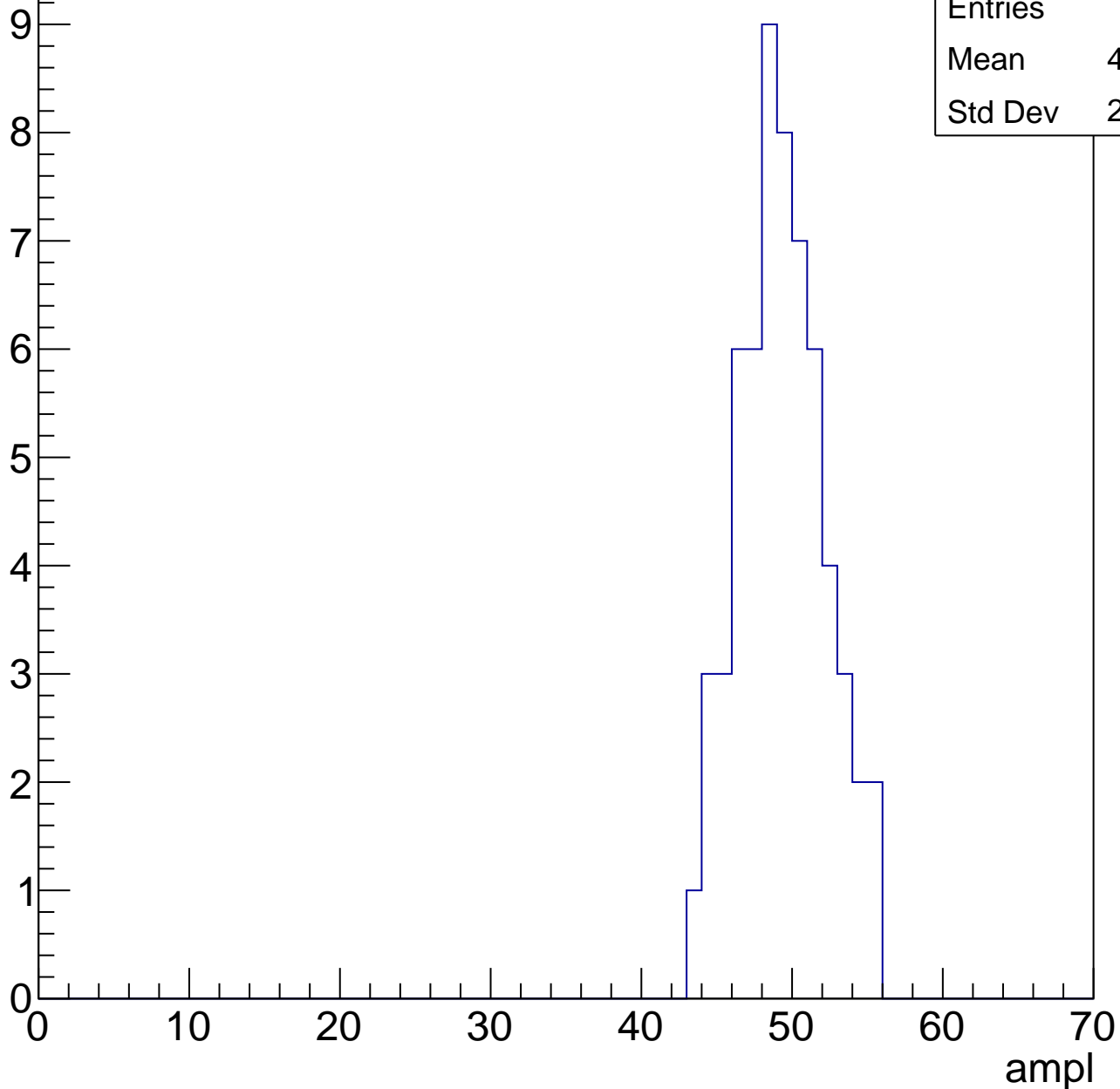
Gaus Width: 3.9350



B1L100S, U10-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



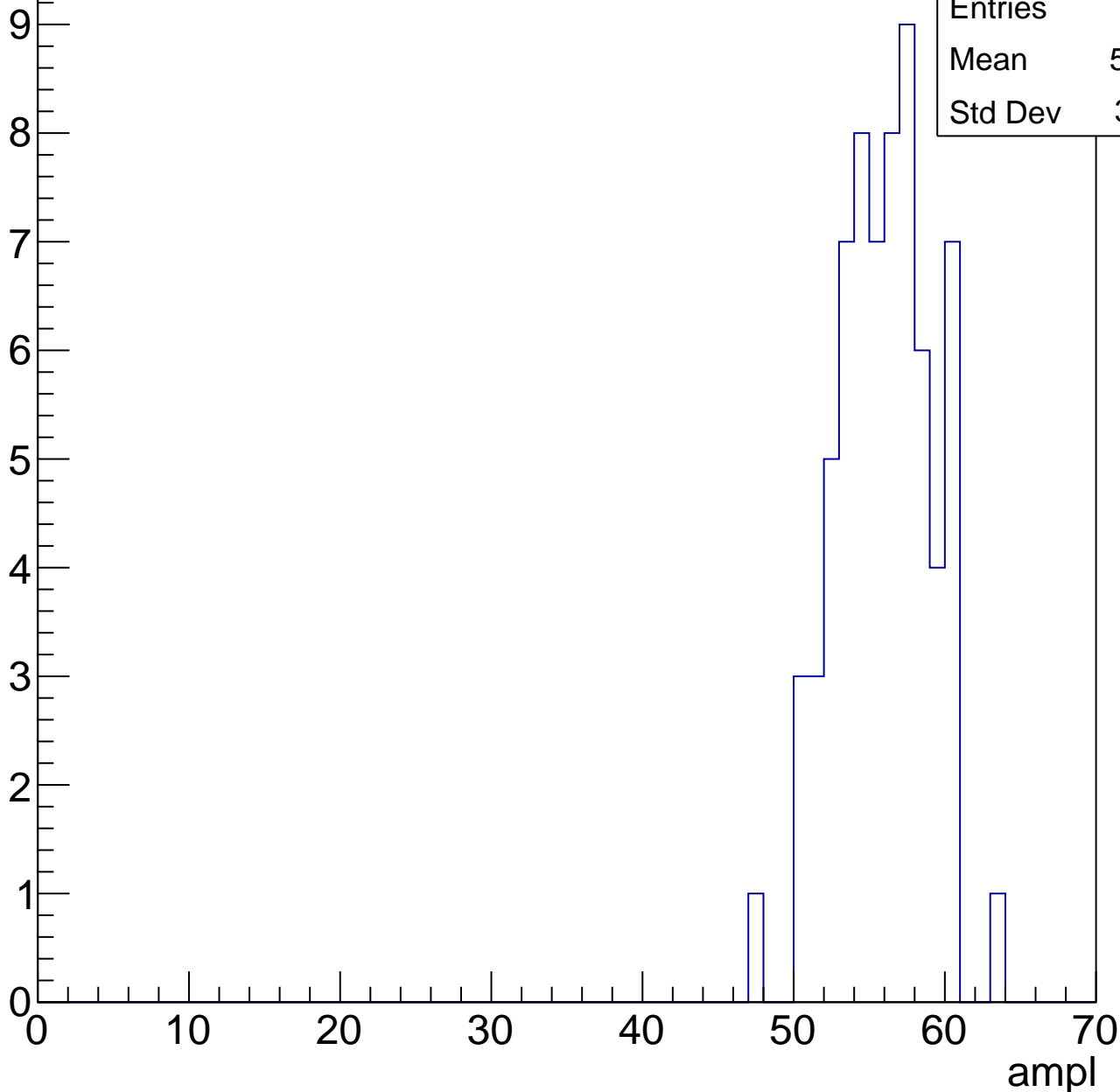
Entries	60
Mean	48.88
Std Dev	2.835

B1L100S, U10-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	55.45
Std Dev	3.081

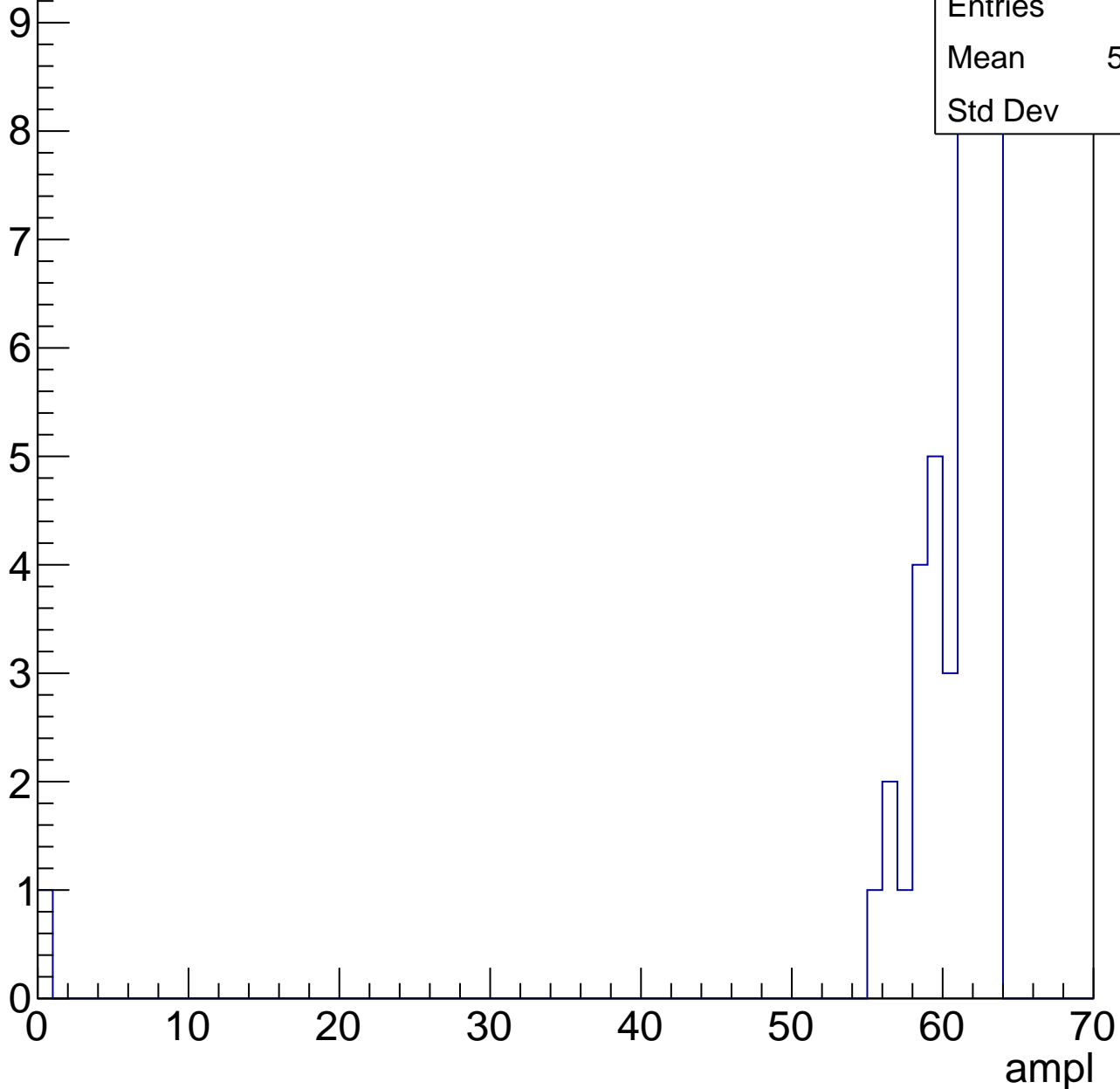


B1L100S, U10-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

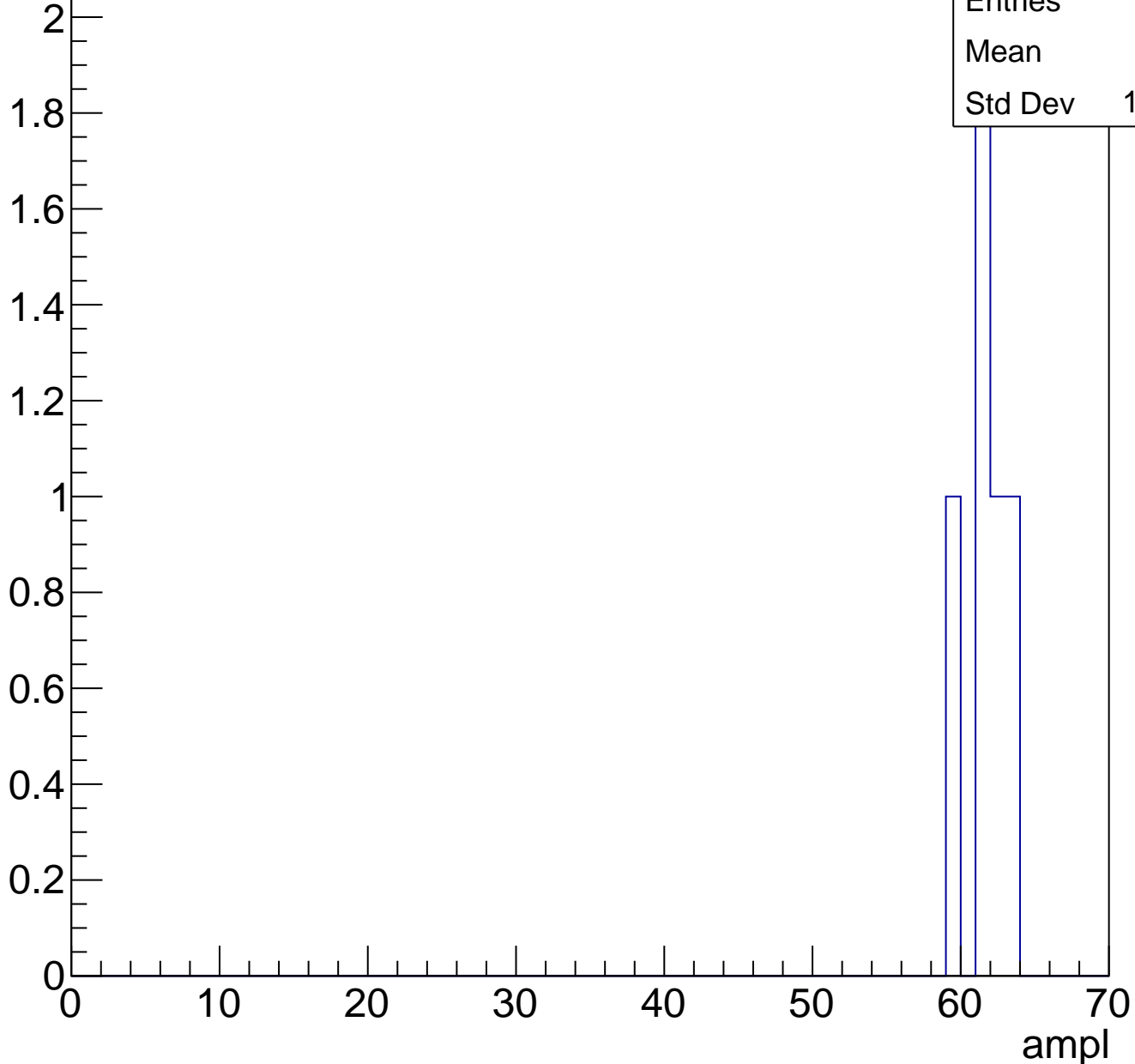
Entries	42
Mean	59.07
Std Dev	9.47



B1L100S, U10-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U10-ch42, adc0

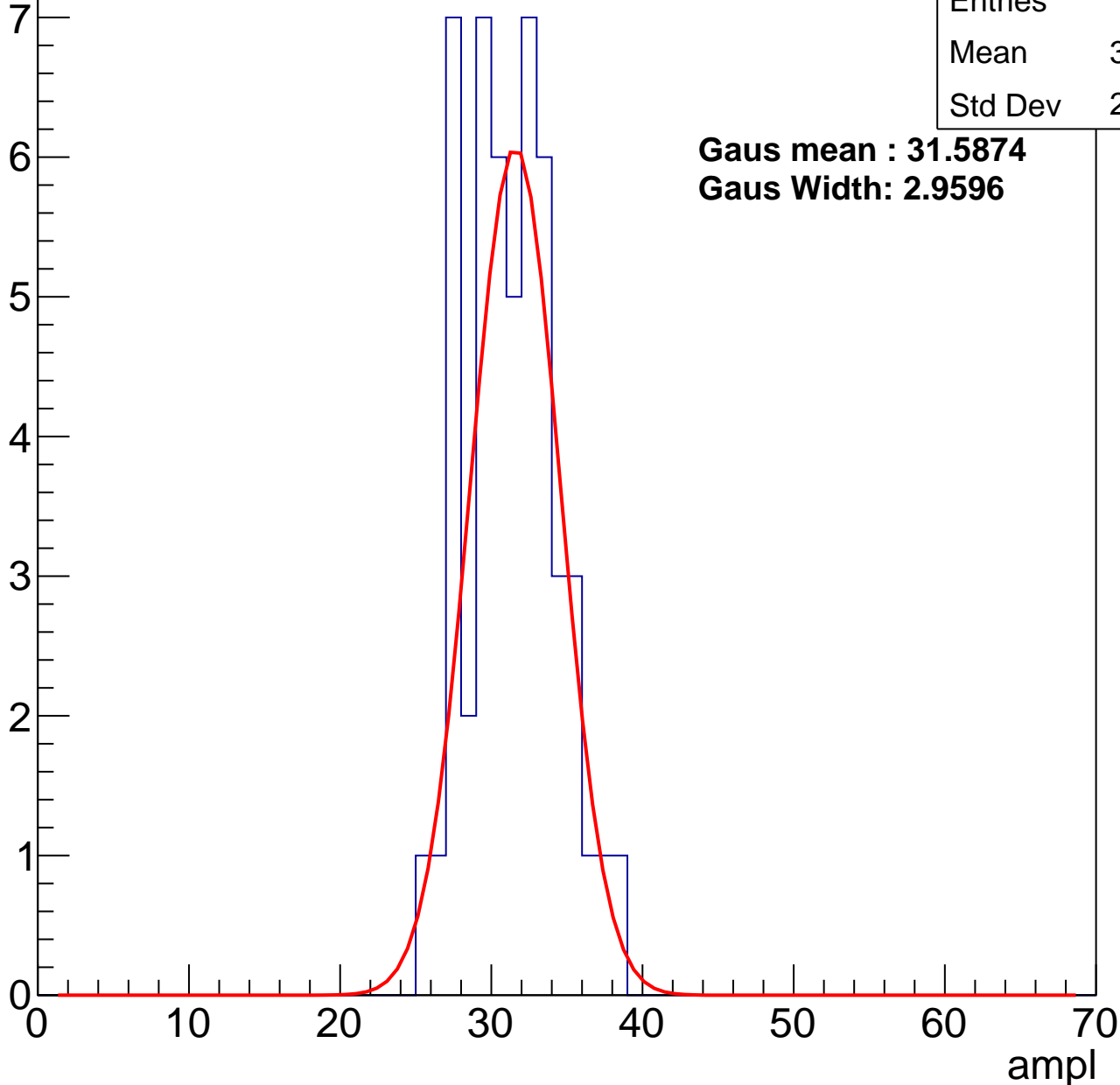
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	30.86
Std Dev	2.937

Gaus mean : 31.5874

Gaus Width: 2.9596



B1L100S, U10-ch42, adc1

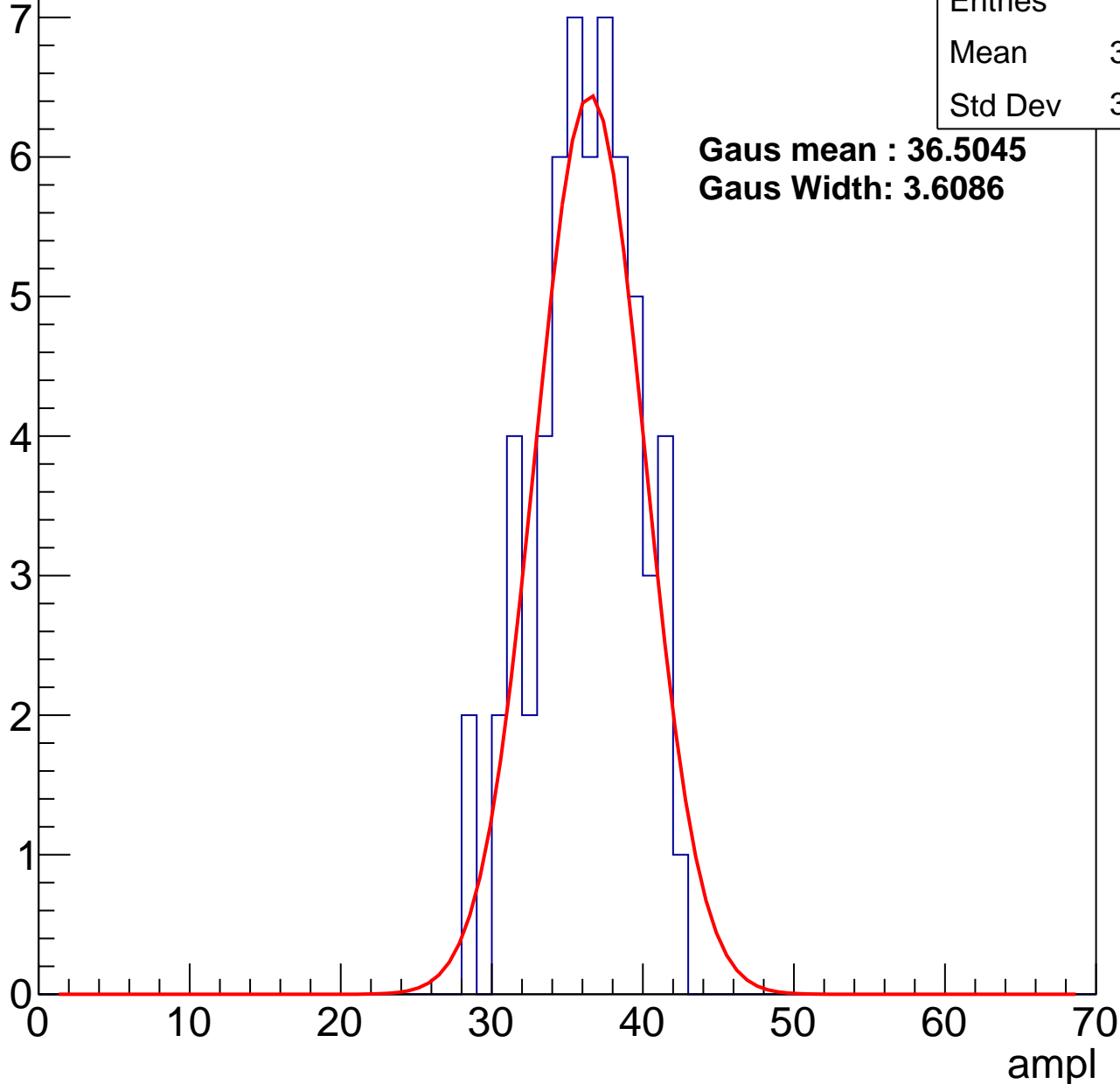
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.75
Std Dev	3.332

Gaus mean : 36.5045

Gaus Width: 3.6086



B1L100S, U10-ch42, adc2

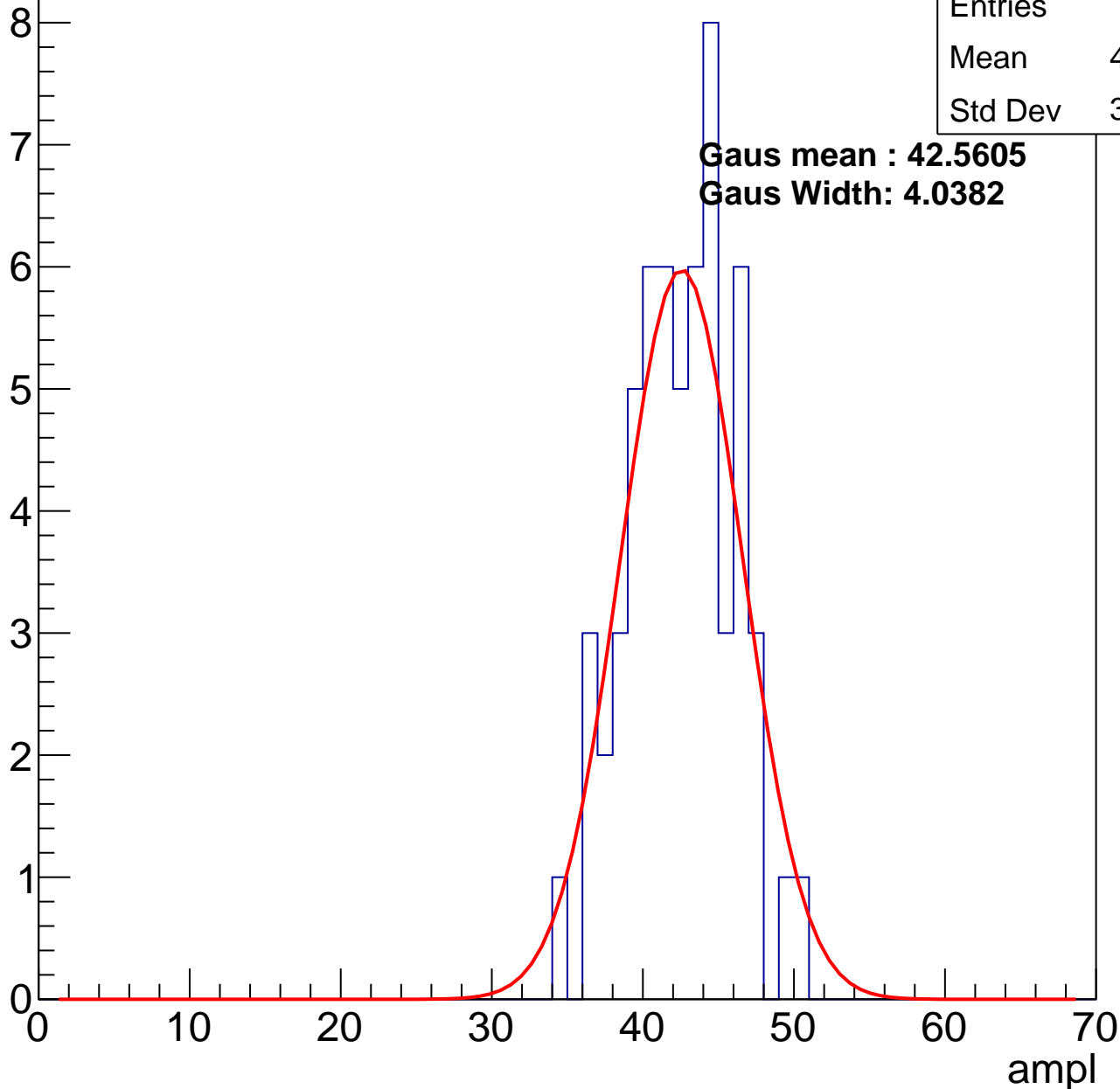
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.07
Std Dev	3.429

Gaus mean : 42.5605

Gaus Width: 4.0382



B1L100S, U10-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

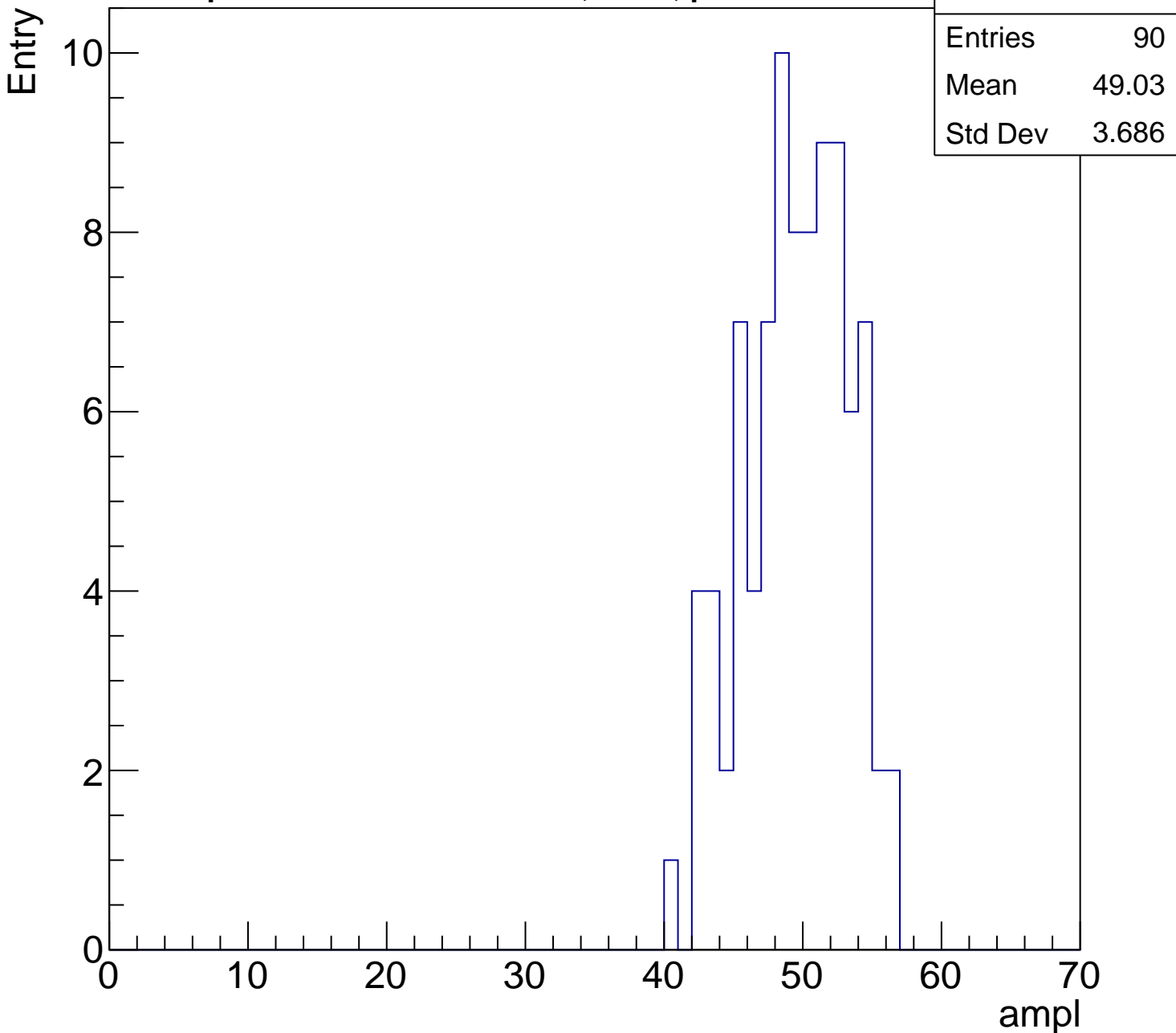
Entries	90
Mean	49.03
Std Dev	3.686

Entry

10
8
6
4
2
0

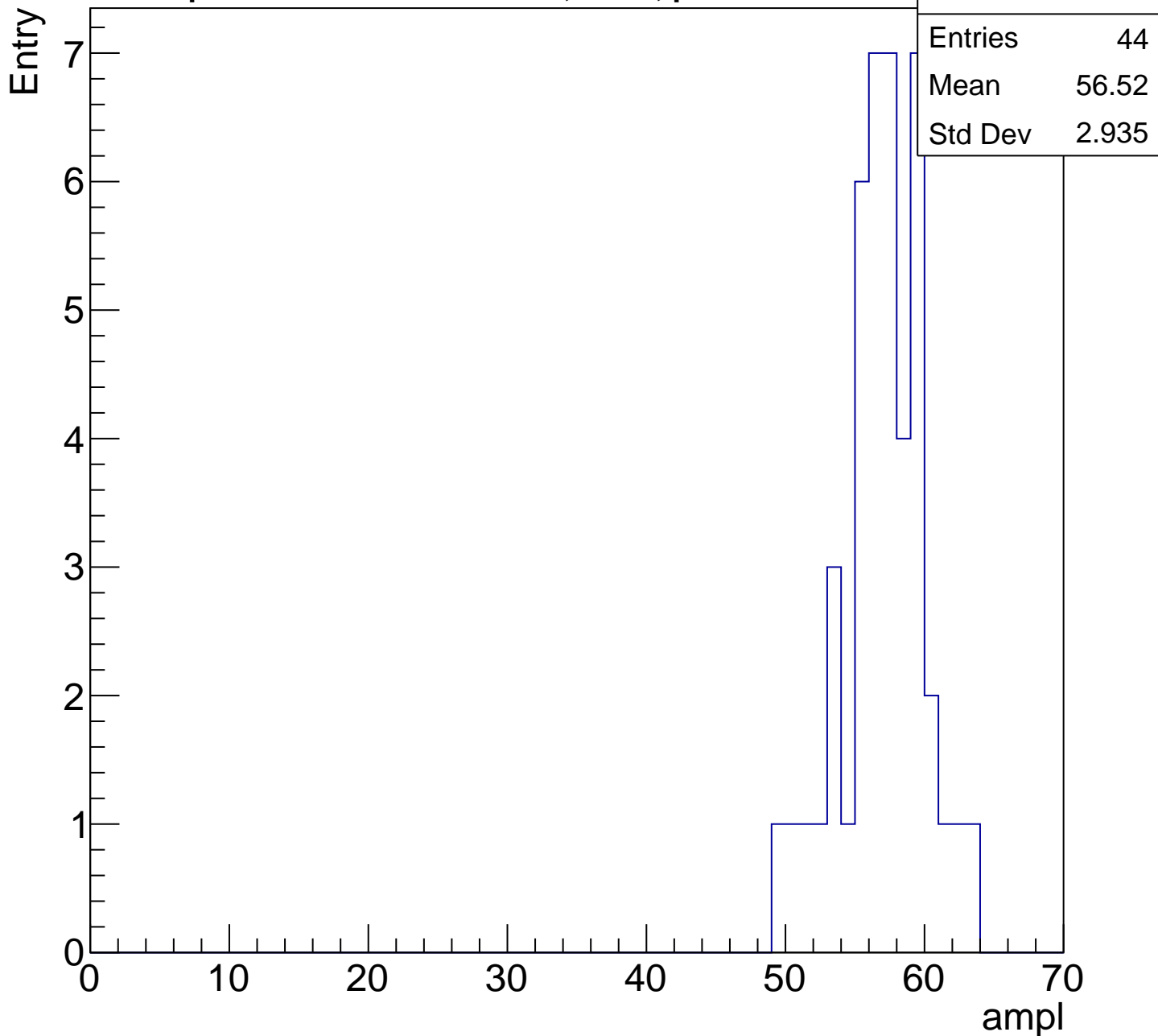
0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

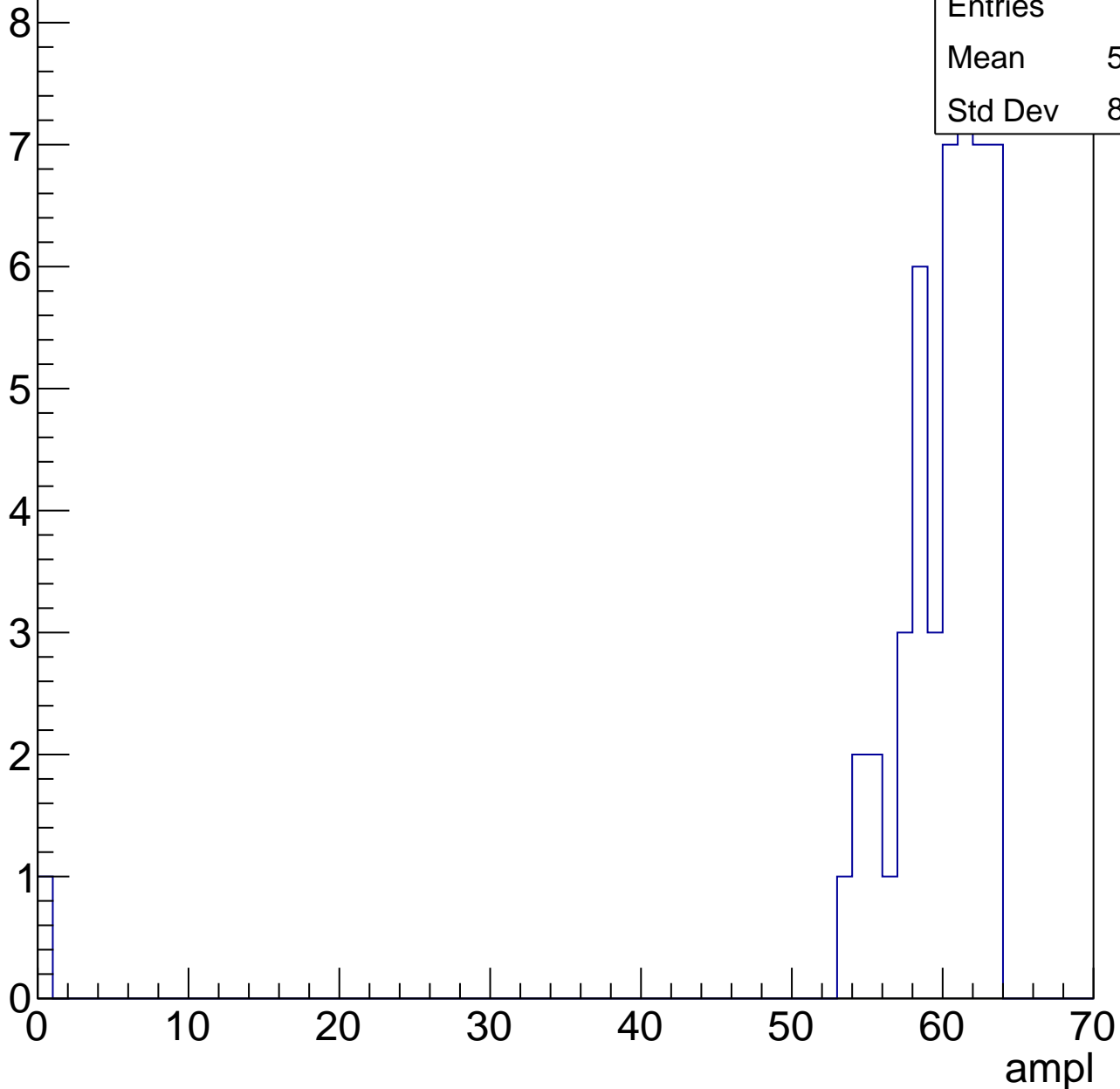


B1L100S, U10-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

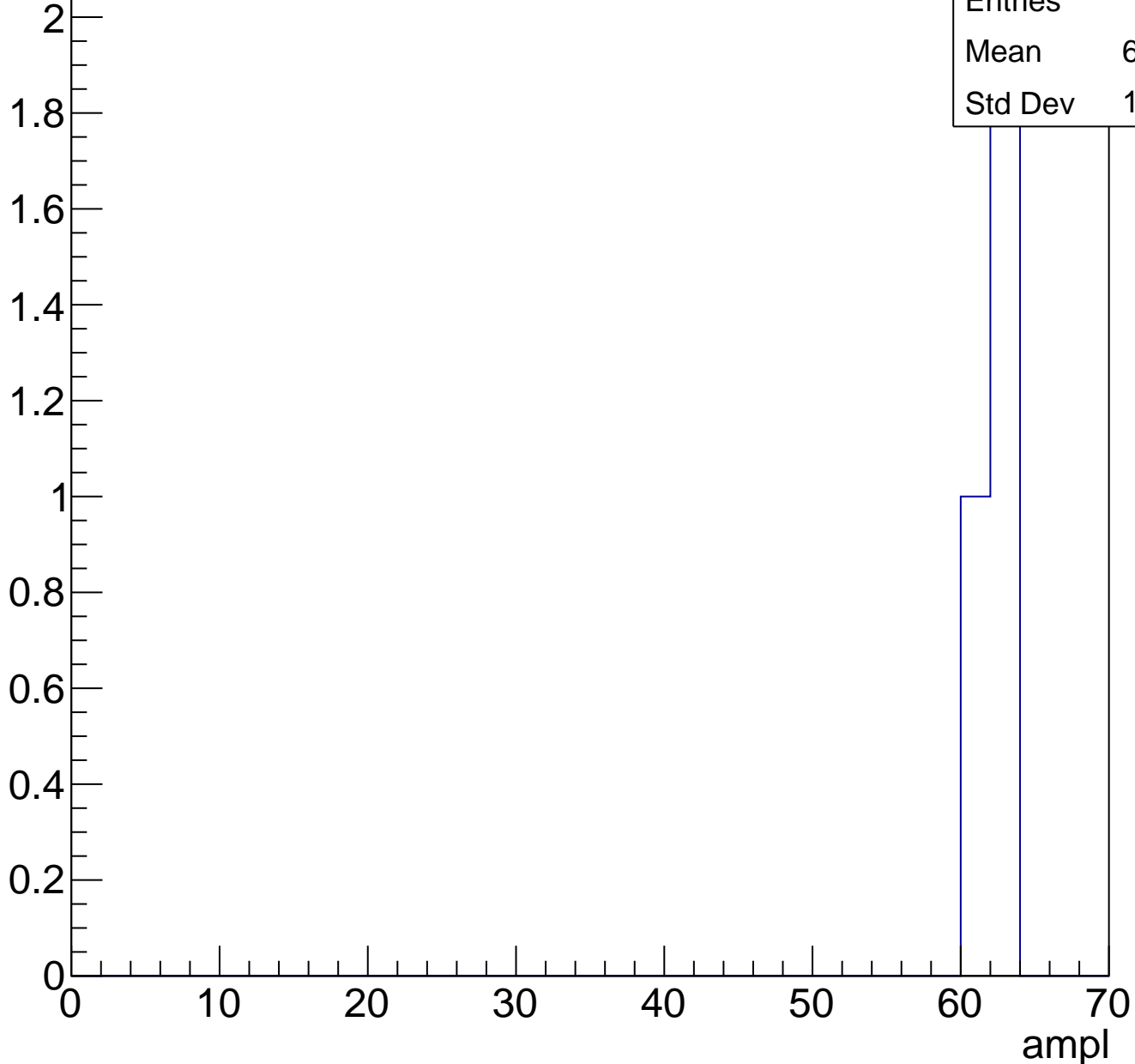
Entries	48
Mean	58.46
Std Dev	8.928



B1L100S, U10-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	24
Std Dev	0

B1L100S, U10-ch43, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	30.81
Std Dev	3.104

Gaus mean : 31.6552

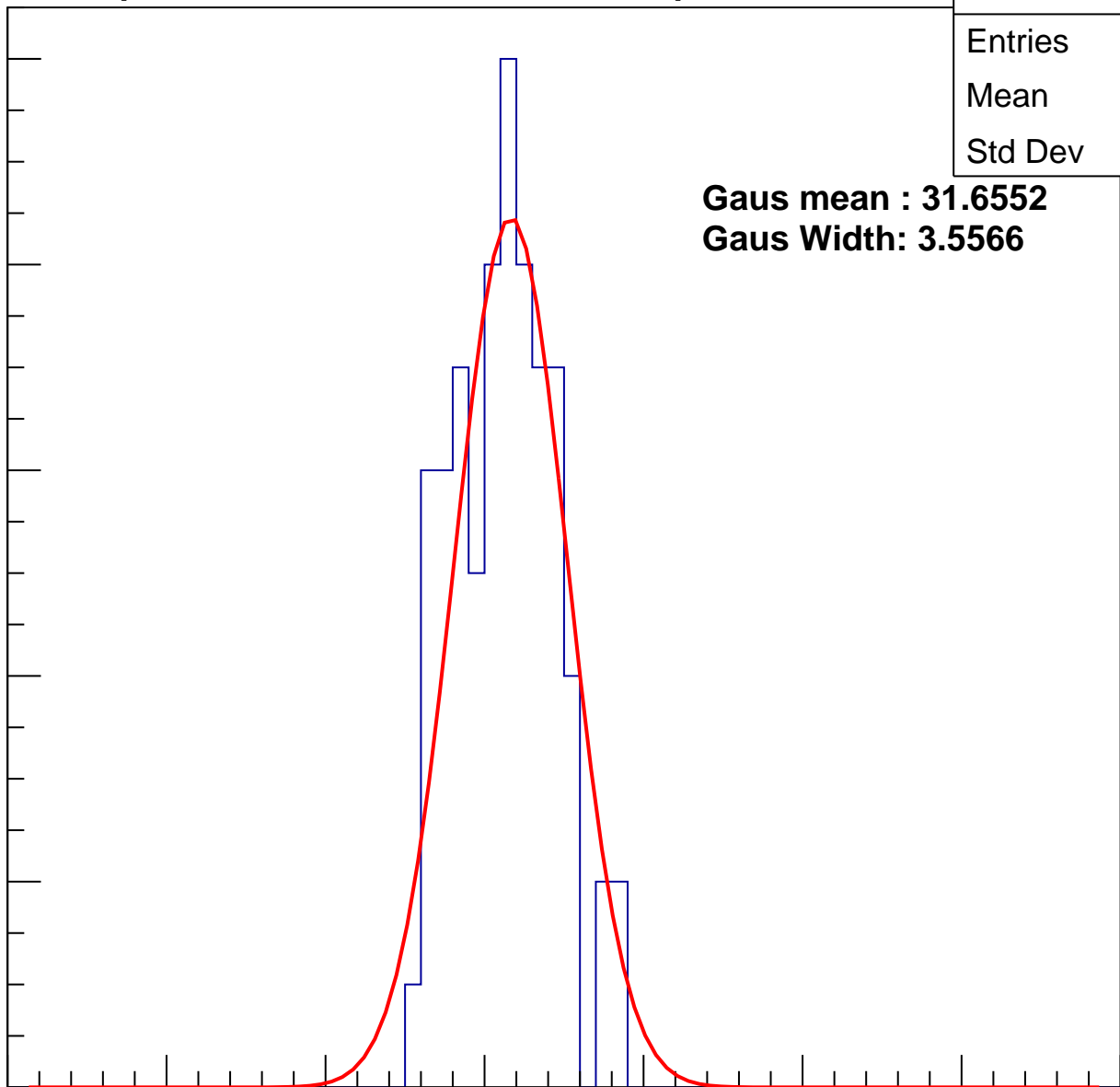
Gaus Width: 3.5566

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch43, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	38.15
Std Dev	3.28

Gaus mean : 38.5920

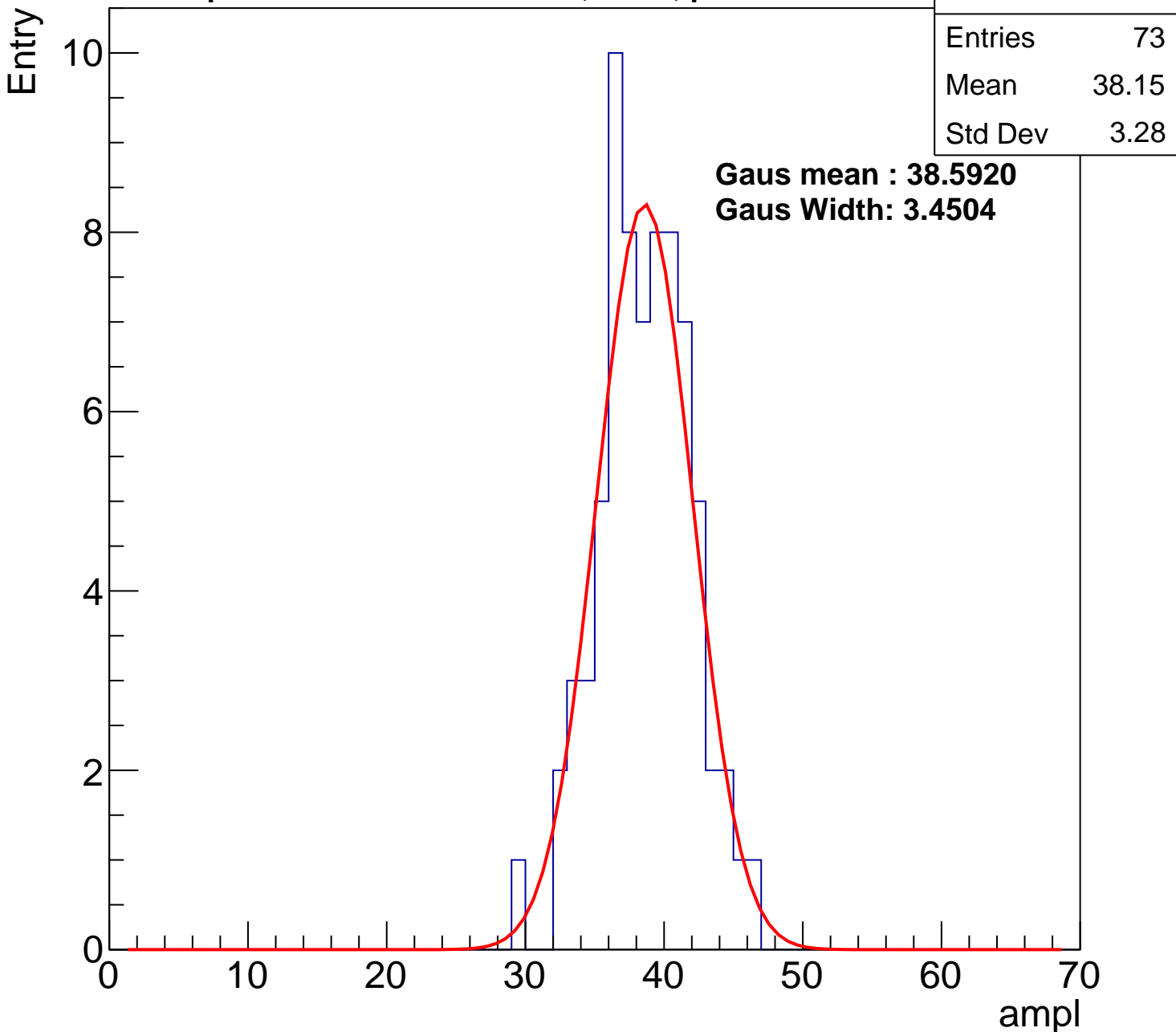
Gaus Width: 3.4504

Entry

10
8
6
4
2
0

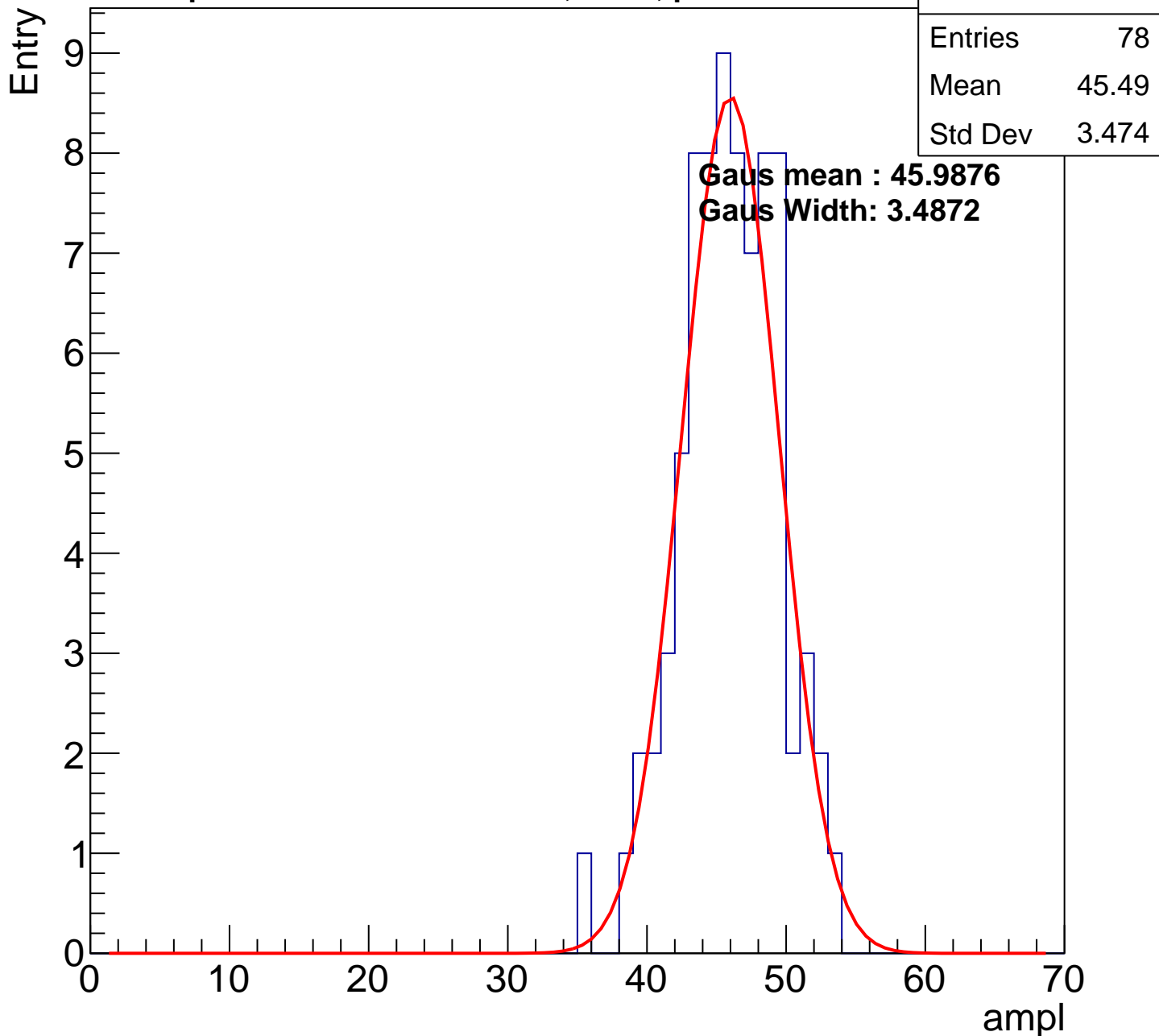
ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch43, adc2

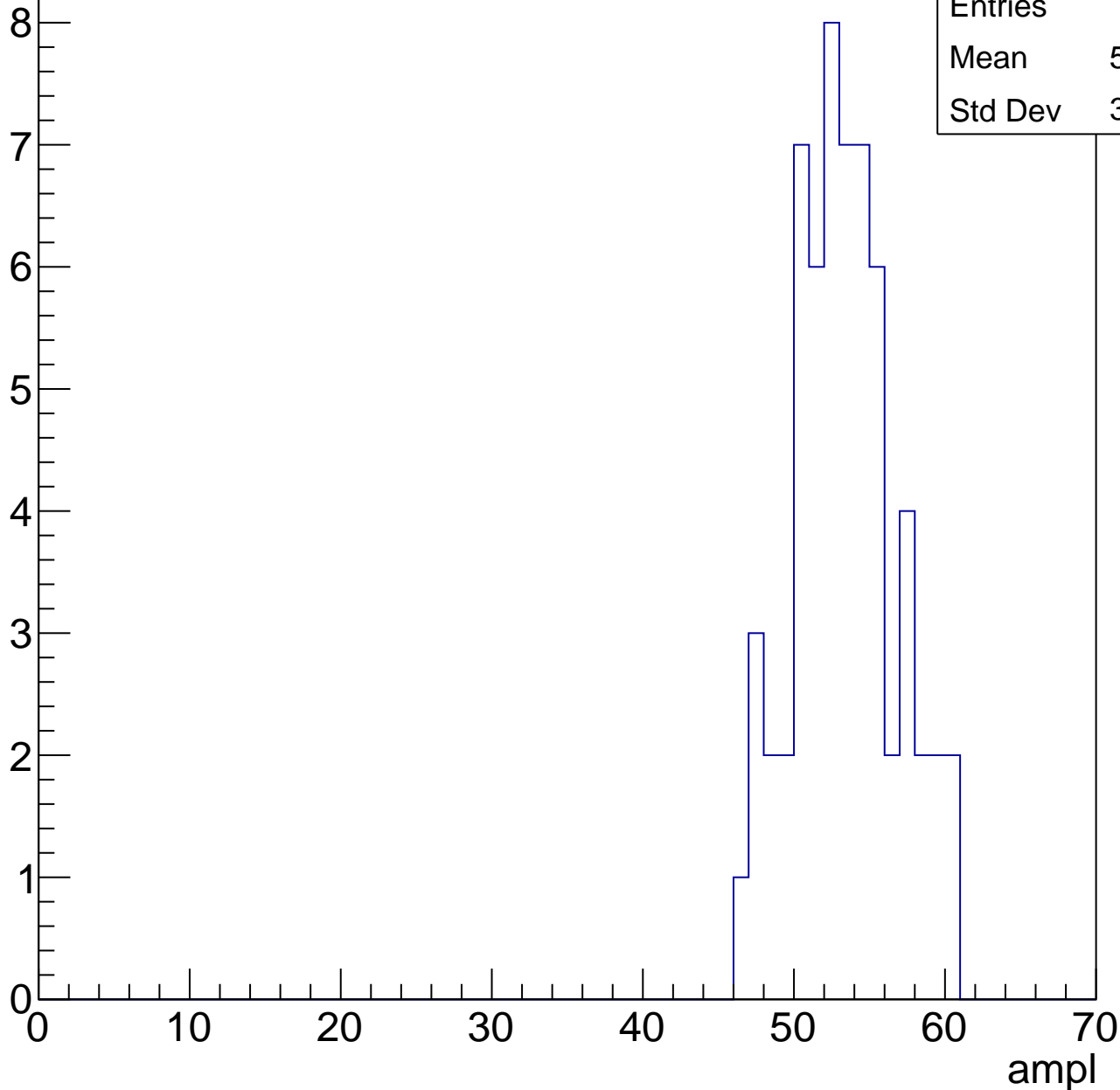
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

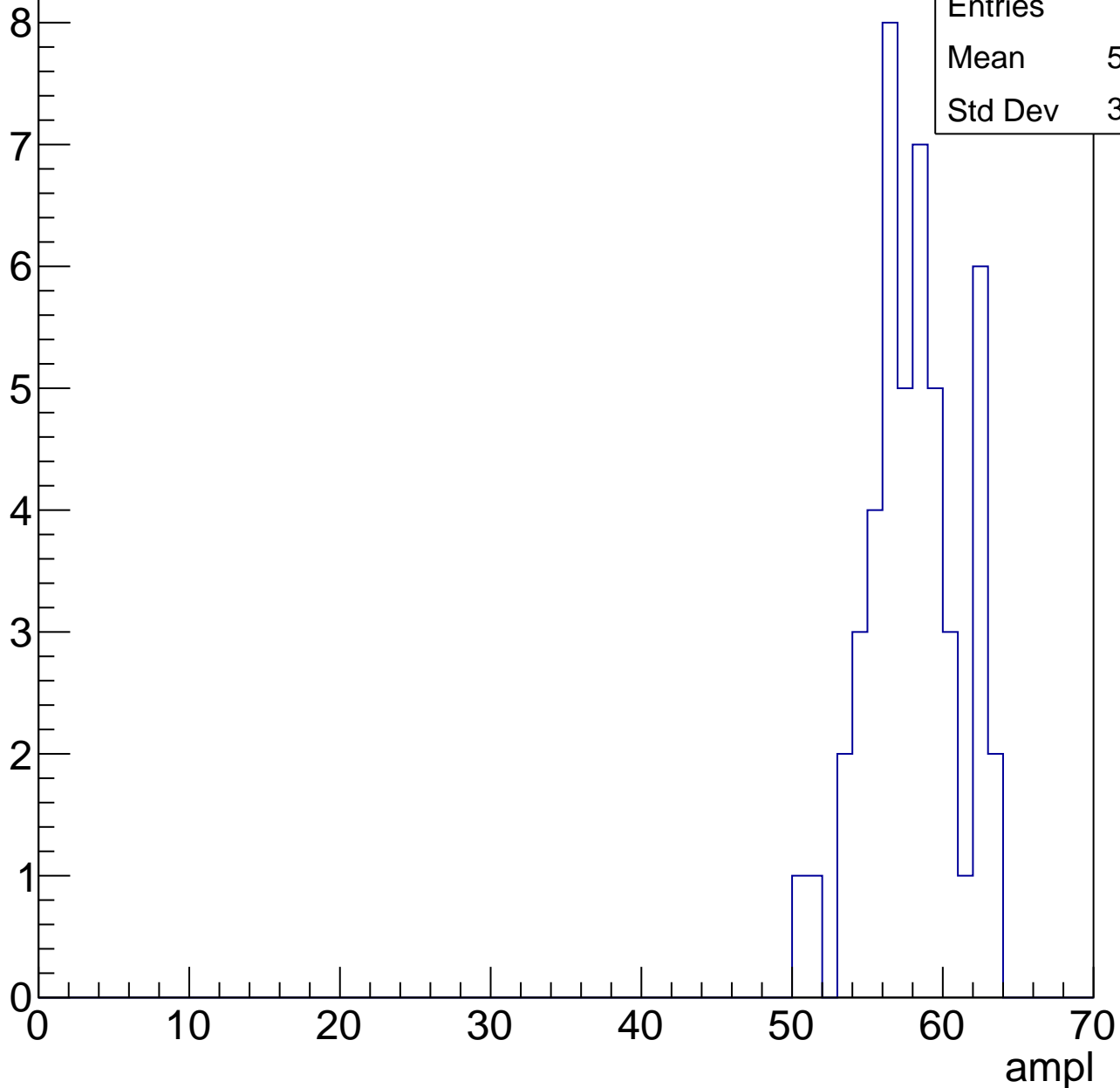


Entries	61
Mean	52.89
Std Dev	3.305

B1L100S, U10-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



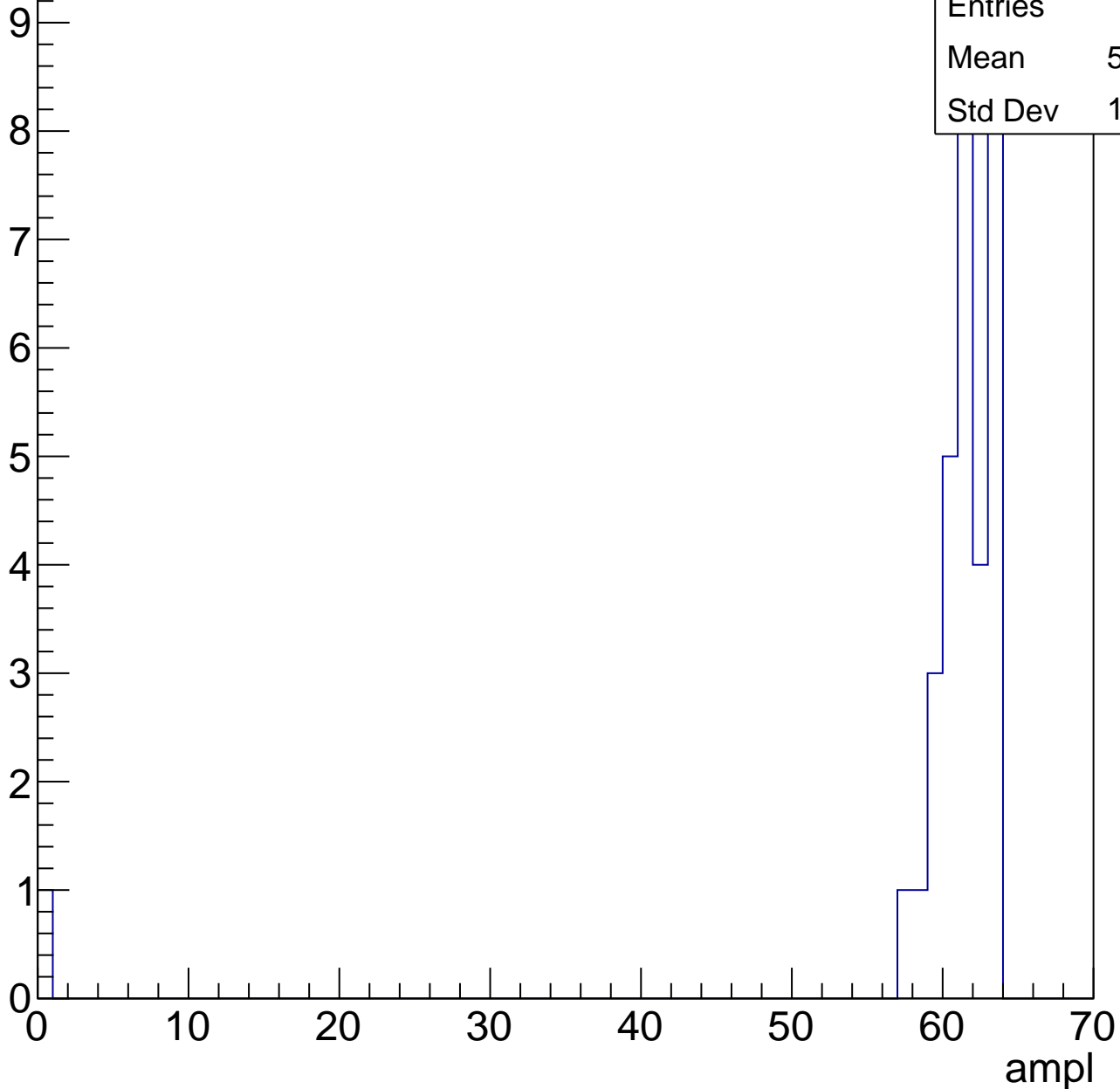
Entries	48
Mean	57.54
Std Dev	3.048

B1L100S, U10-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	59.16
Std Dev	10.74



B1L100S, U10-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch44, adc0

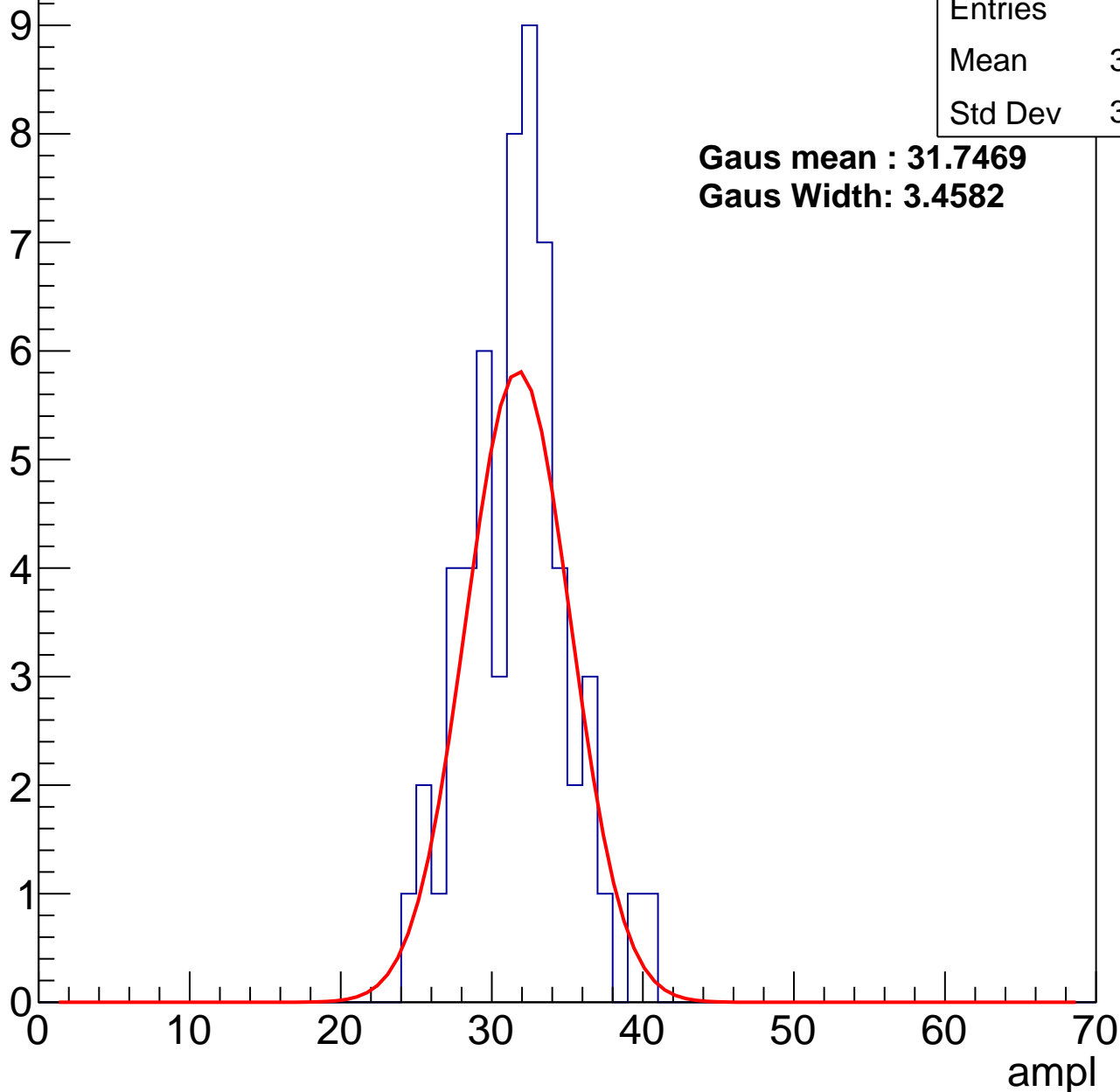
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	31.25
Std Dev	3.326

Gaus mean : 31.7469

Gaus Width: 3.4582



B1L100S, U10-ch44, adc1

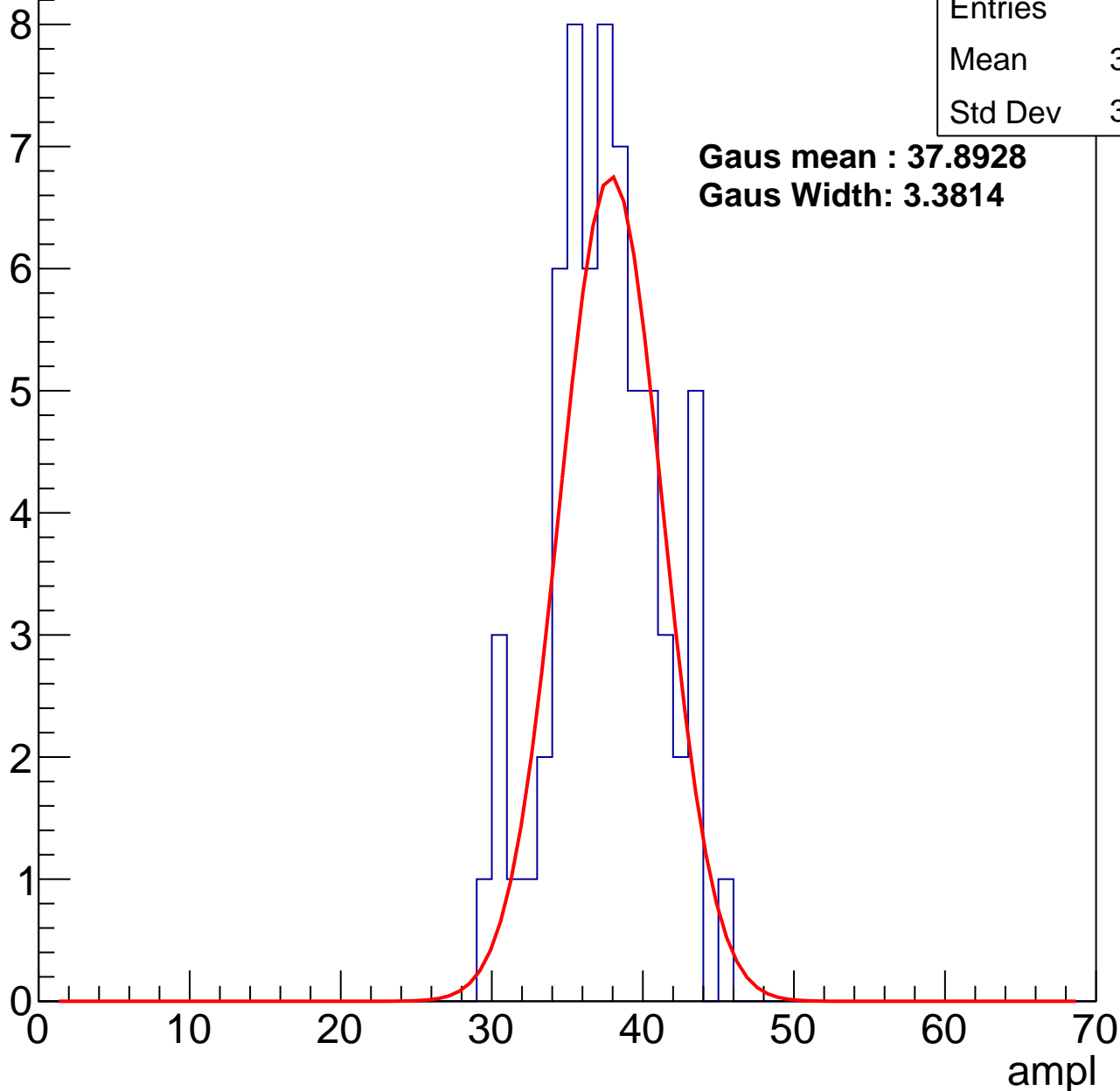
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.06
Std Dev	3.557

Gaus mean : 37.8928

Gaus Width: 3.3814



B1L100S, U10-ch44, adc2

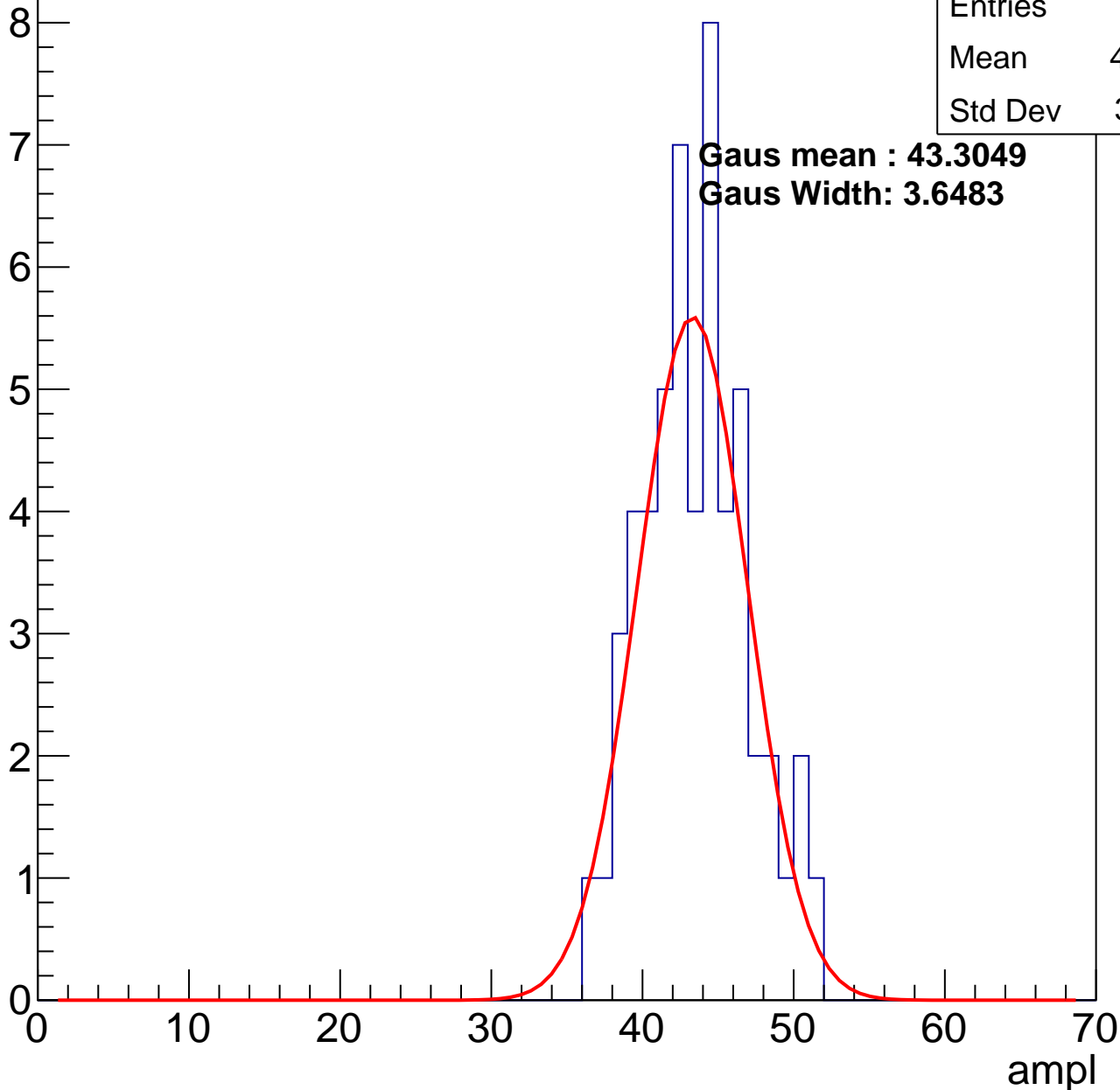
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.07
Std Dev	3.431

Gaus mean : 43.3049

Gaus Width: 3.6483



B1L100S, U10-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	49.86
Std Dev	3.69

Entry

10

8

6

4

2

0

0

10

20

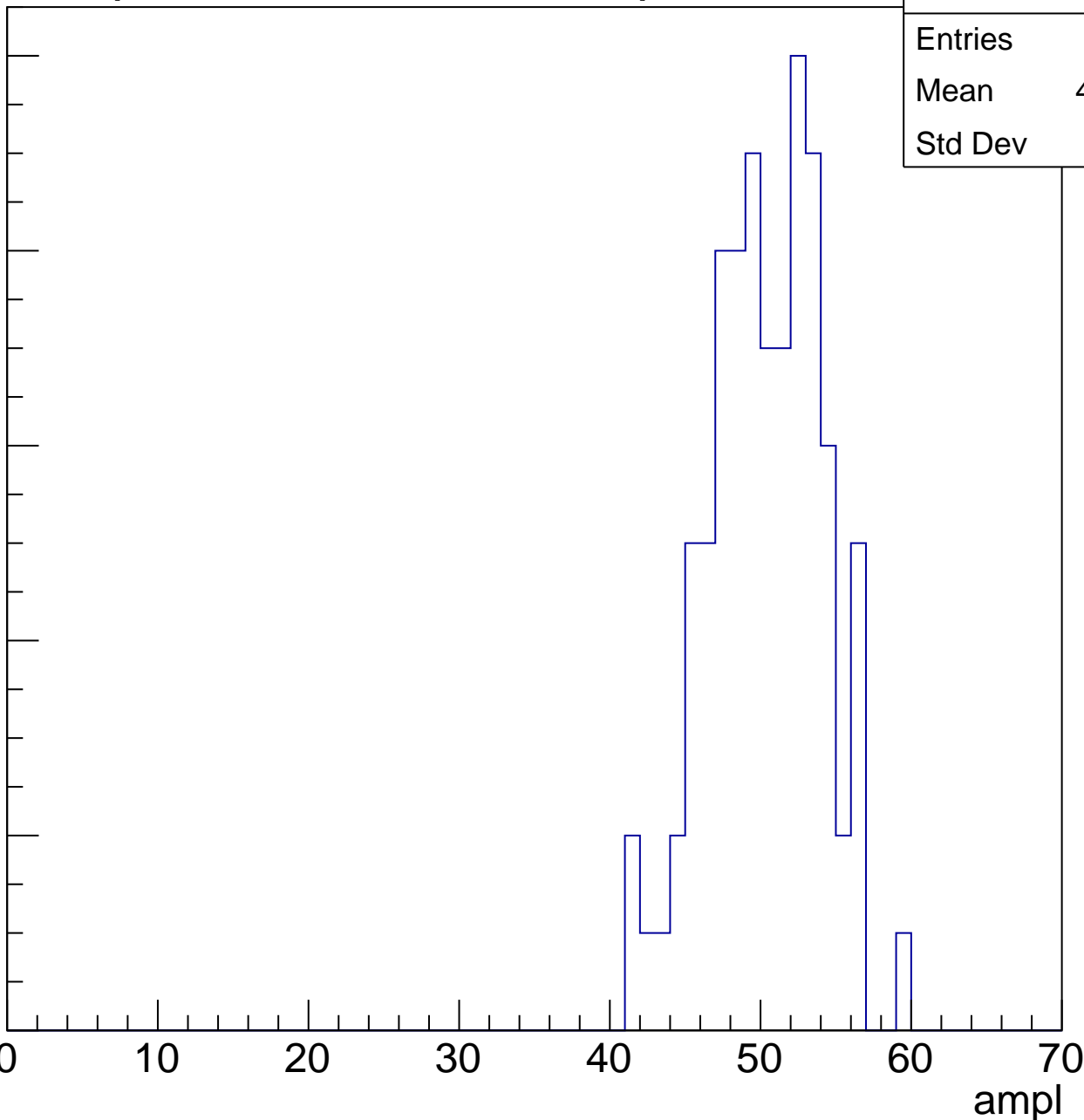
30

40

50

60

ampl

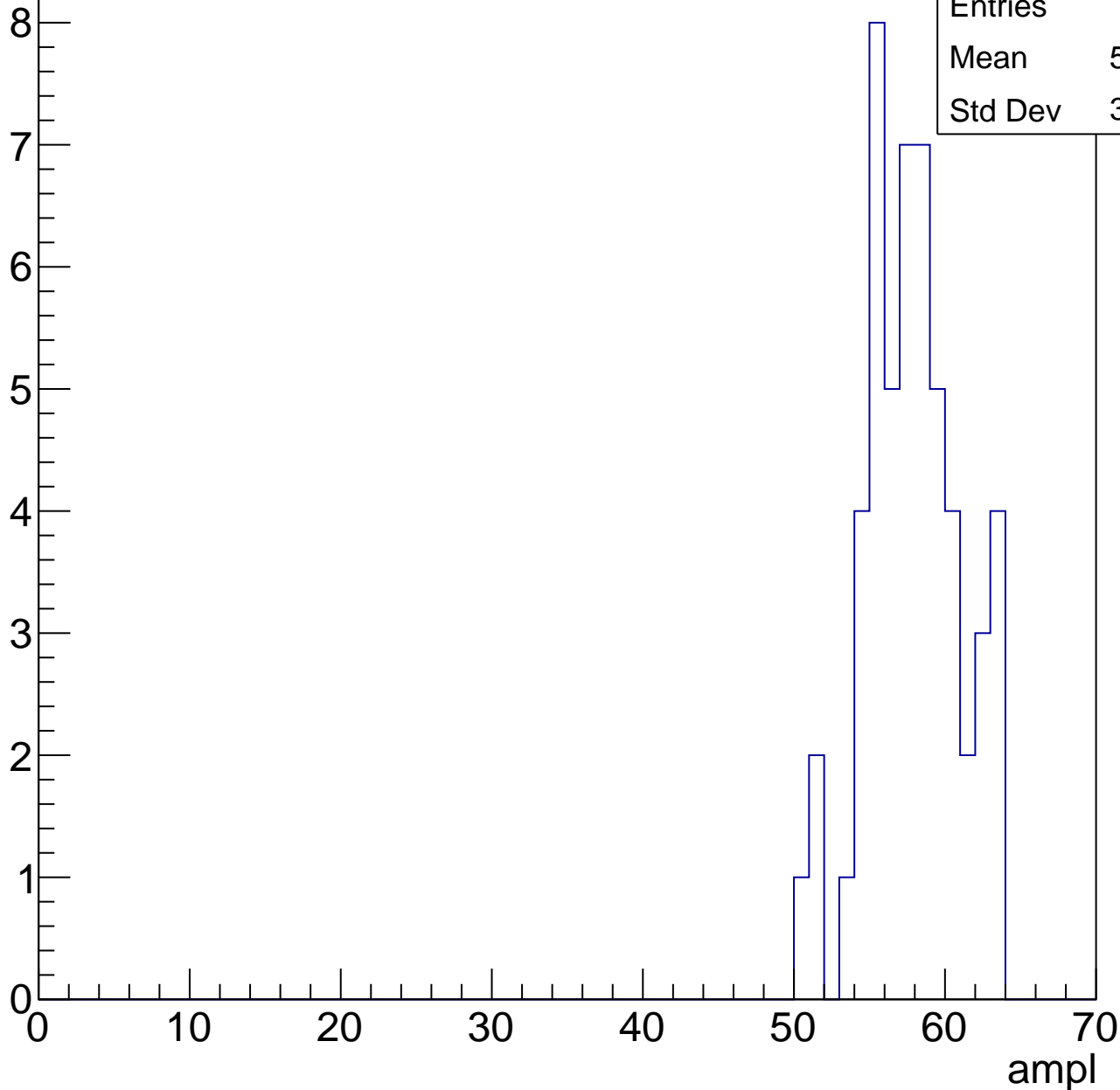


B1L100S, U10-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.38
Std Dev	3.122



B1L100S, U10-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

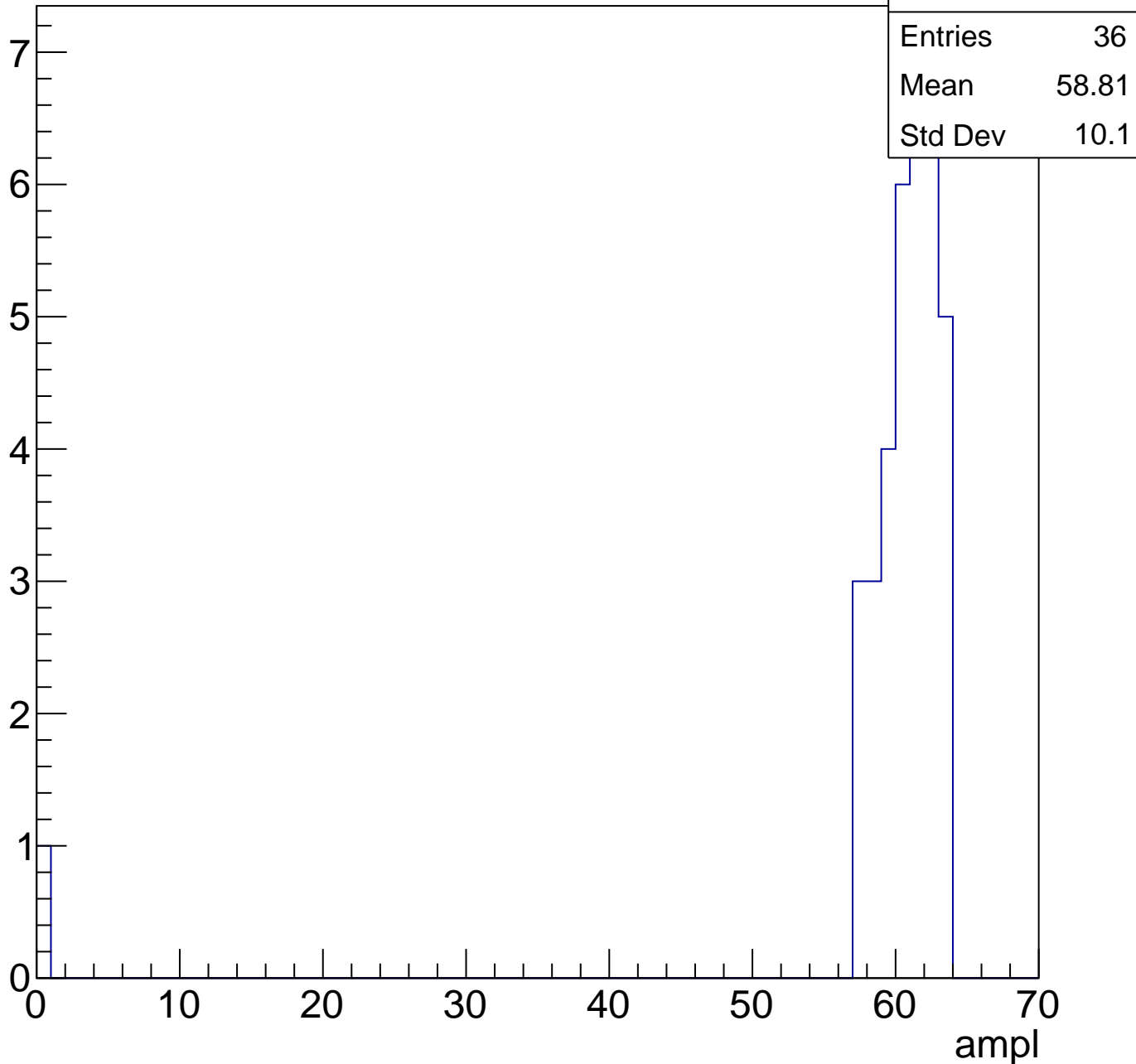
36

Mean

58.81

Std Dev

10.1



B1L100S, U10-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch45, adc0

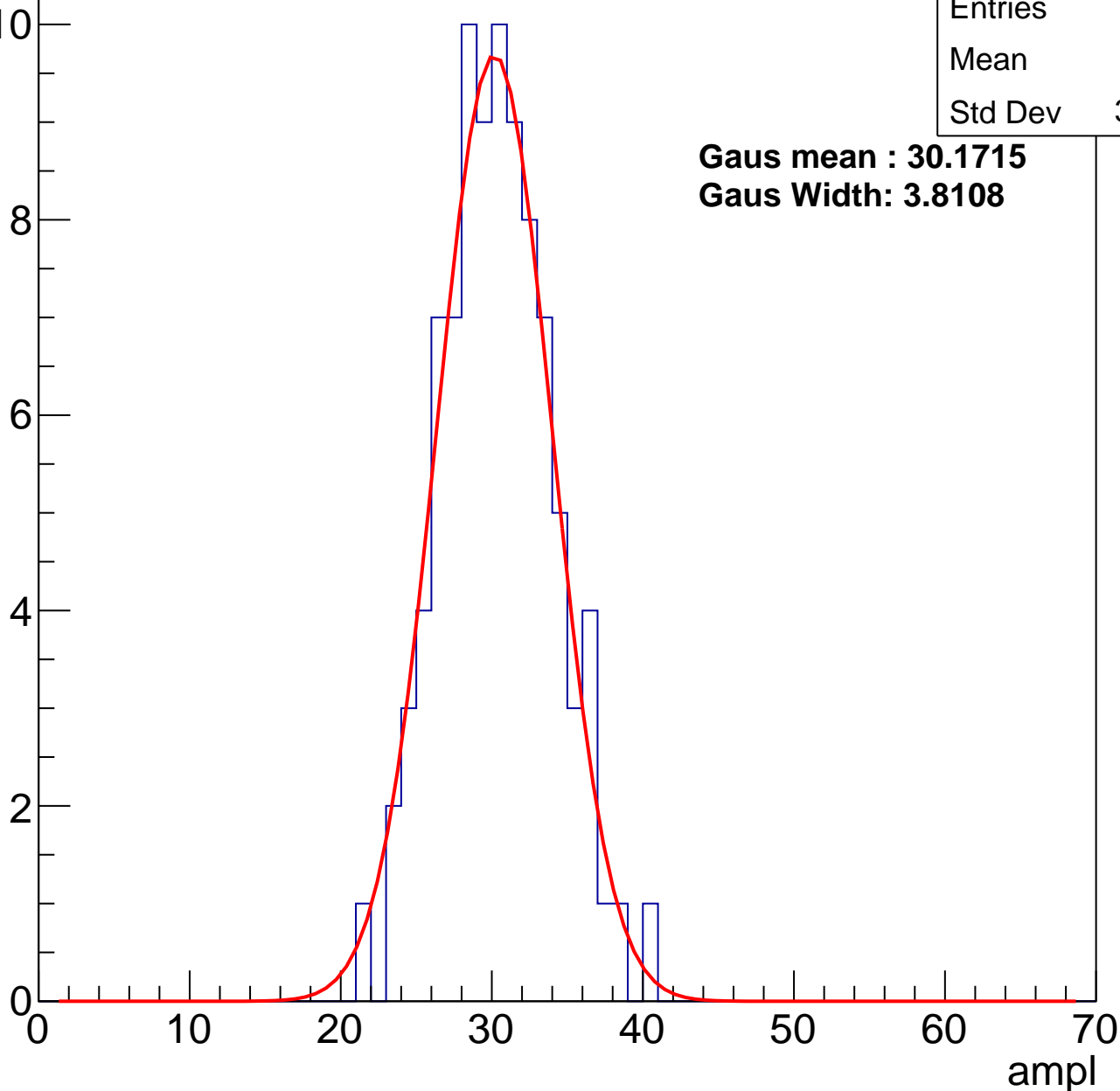
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	92
Mean	29.9
Std Dev	3.651

Gaus mean : 30.1715

Gaus Width: 3.8108



B1L100S, U10-ch45, adc1

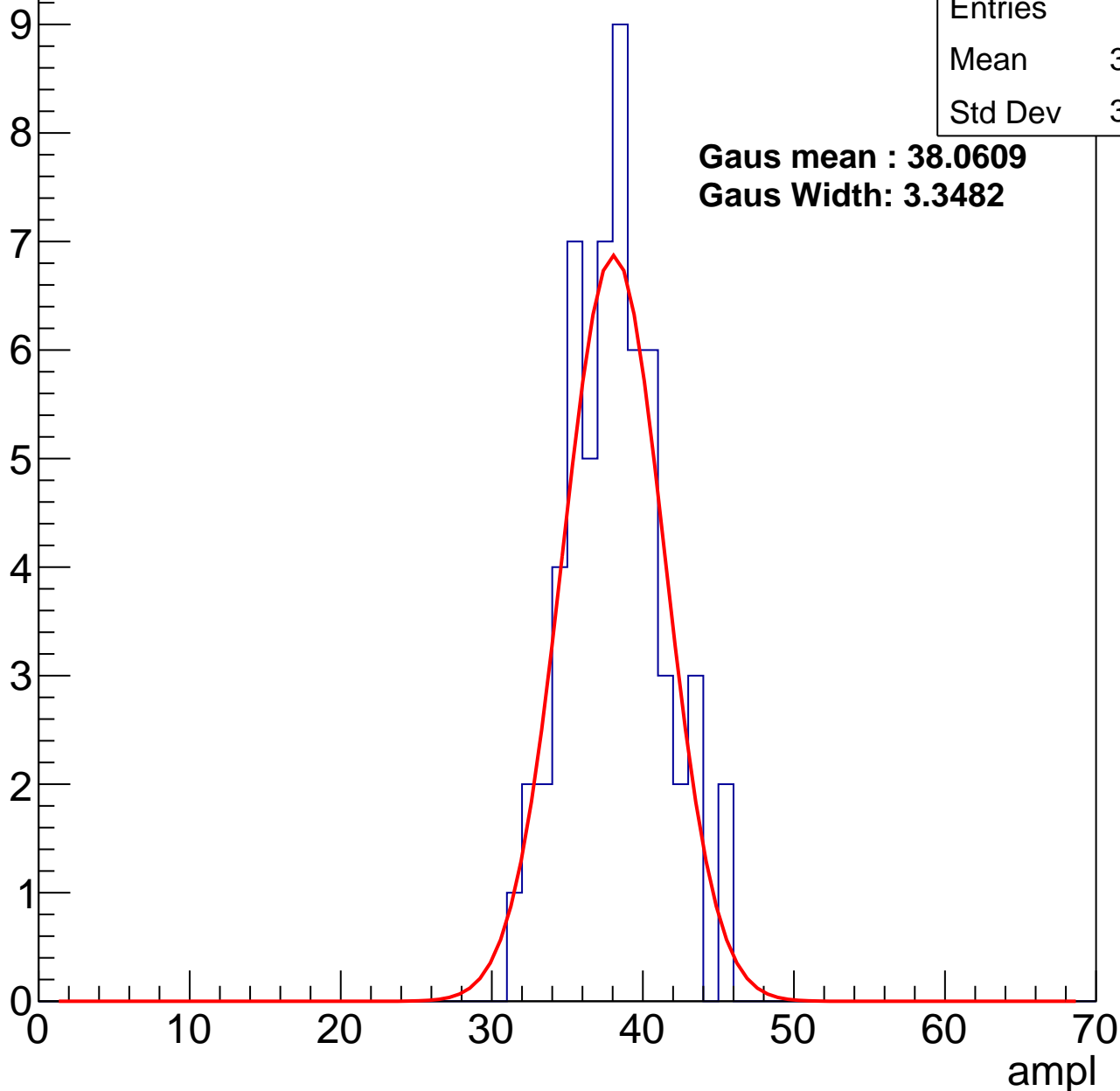
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.68
Std Dev	3.138

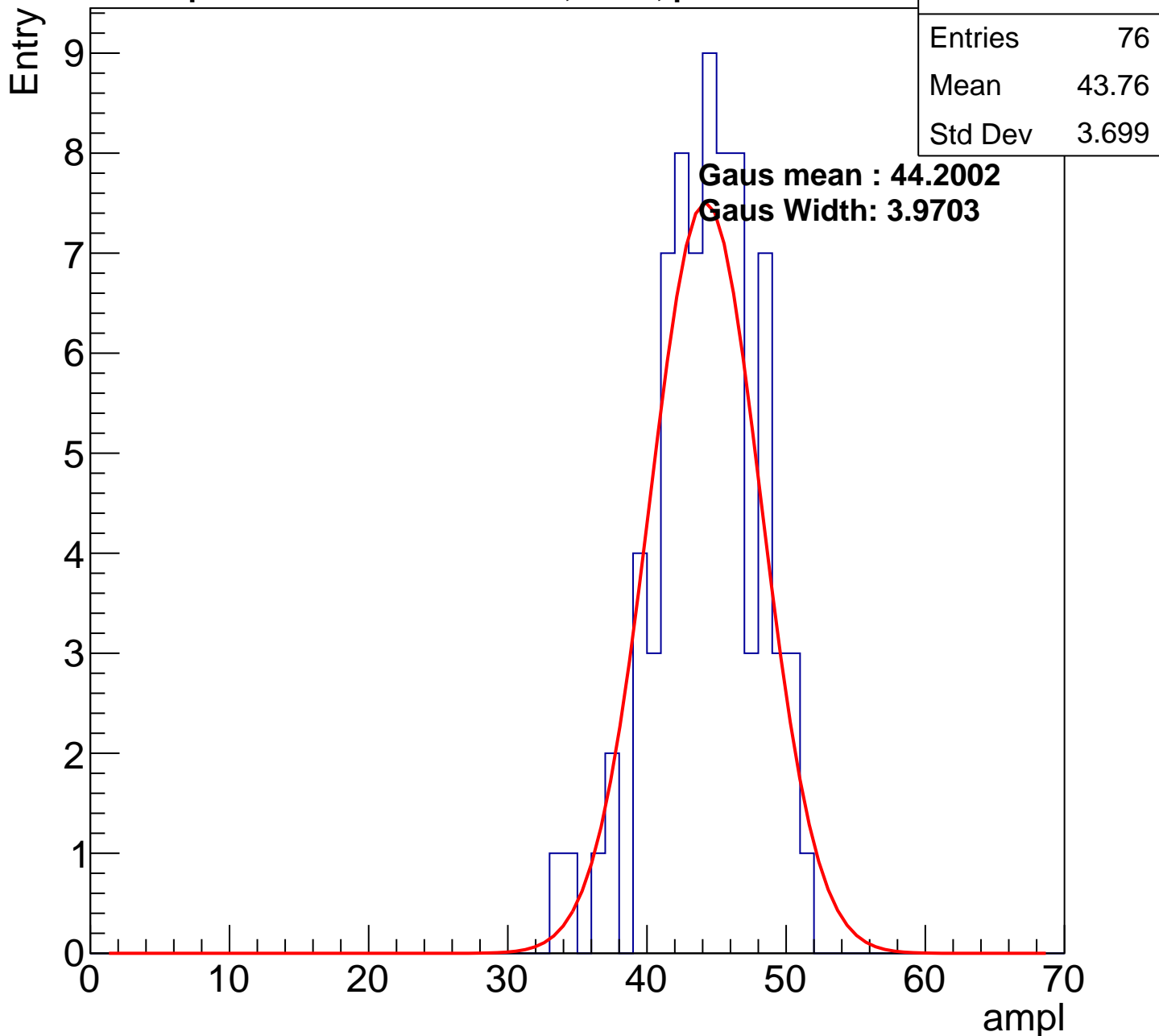
Gaus mean : 38.0609

Gaus Width: 3.3482



B1L100S, U10-ch45, adc2

calib_packv5_042523_0143.root, FC#4, port A2

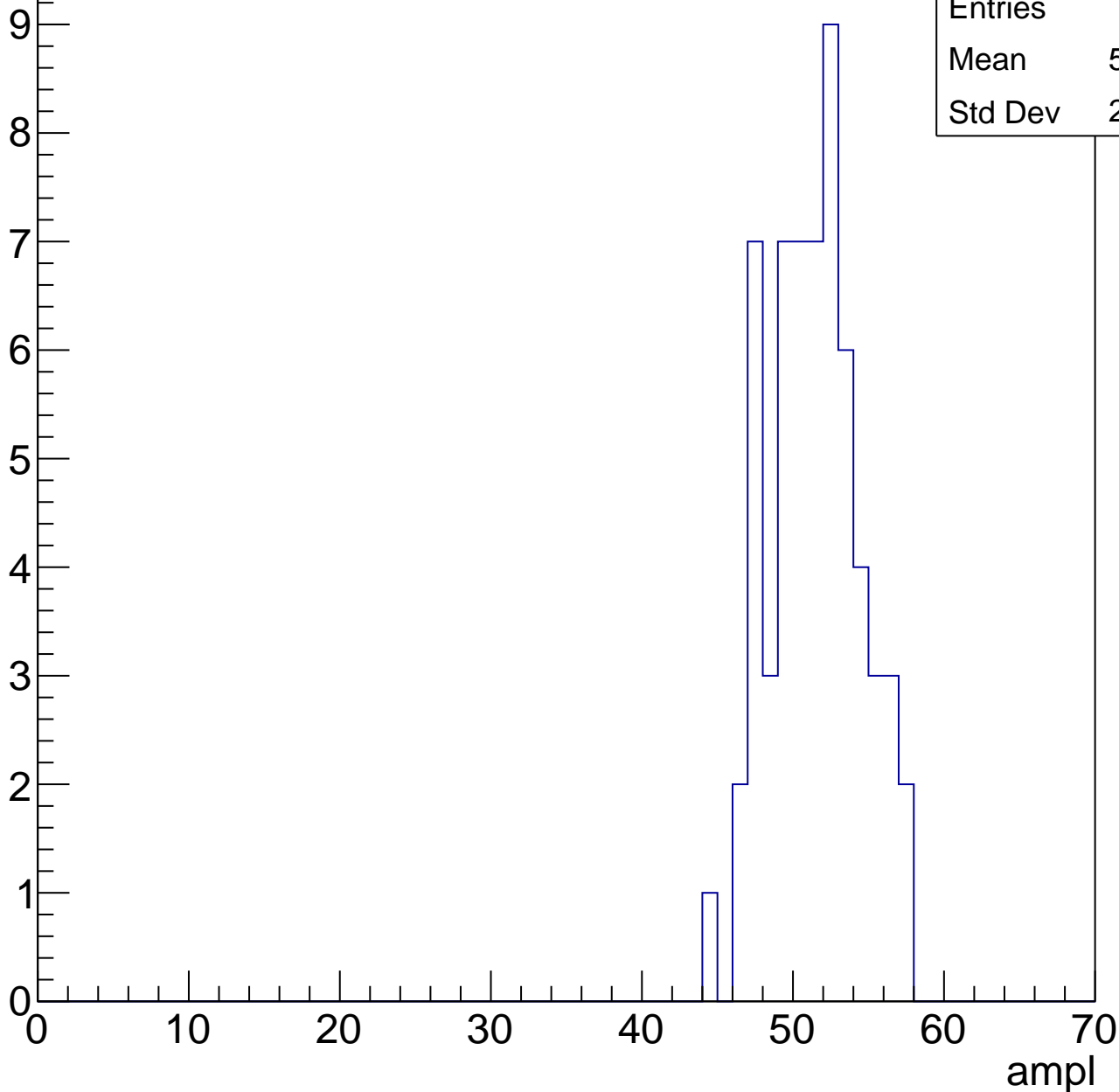


B1L100S, U10-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.95
Std Dev	2.967

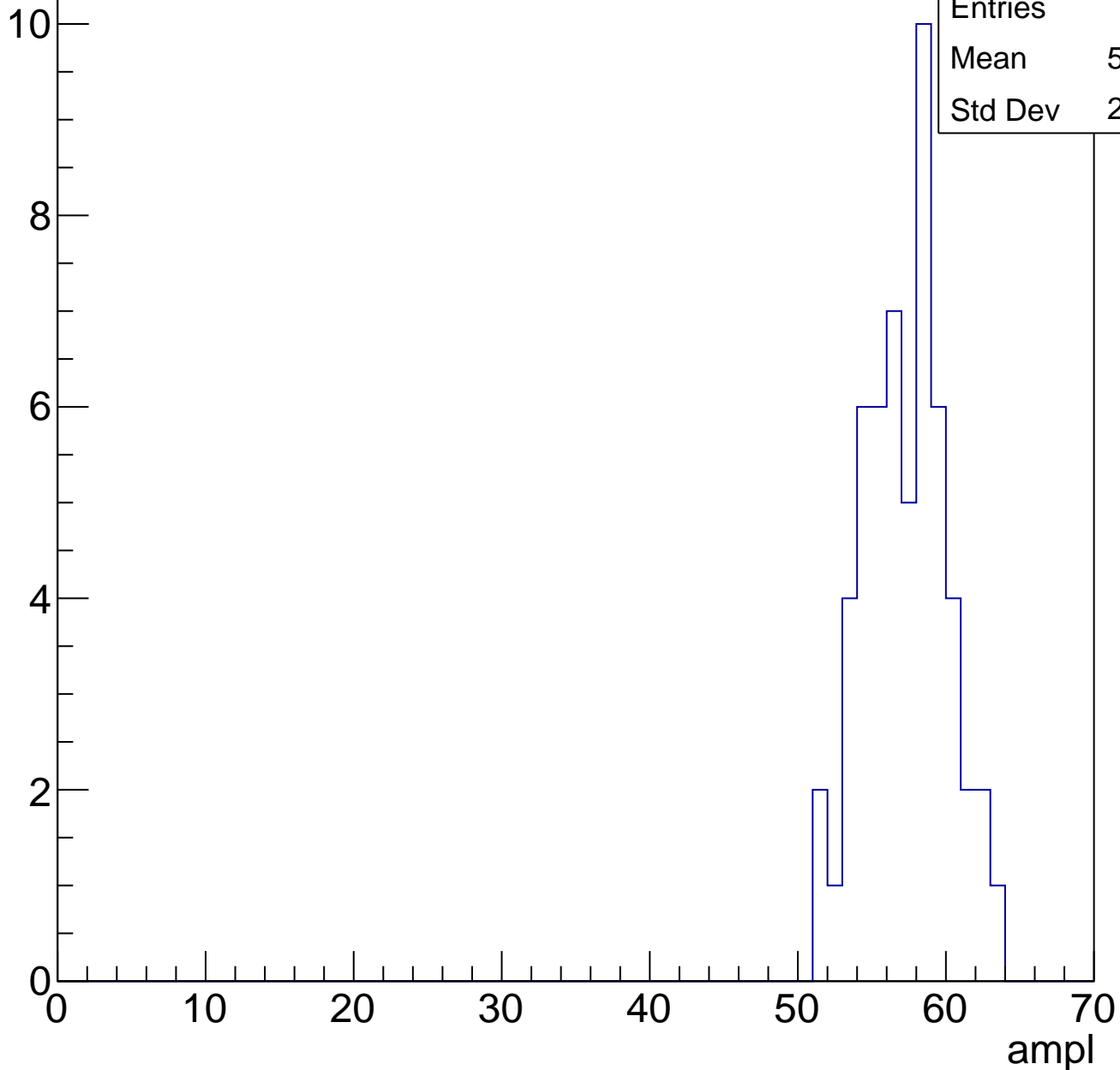


B1L100S, U10-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	56.79
Std Dev	2.776

Entry

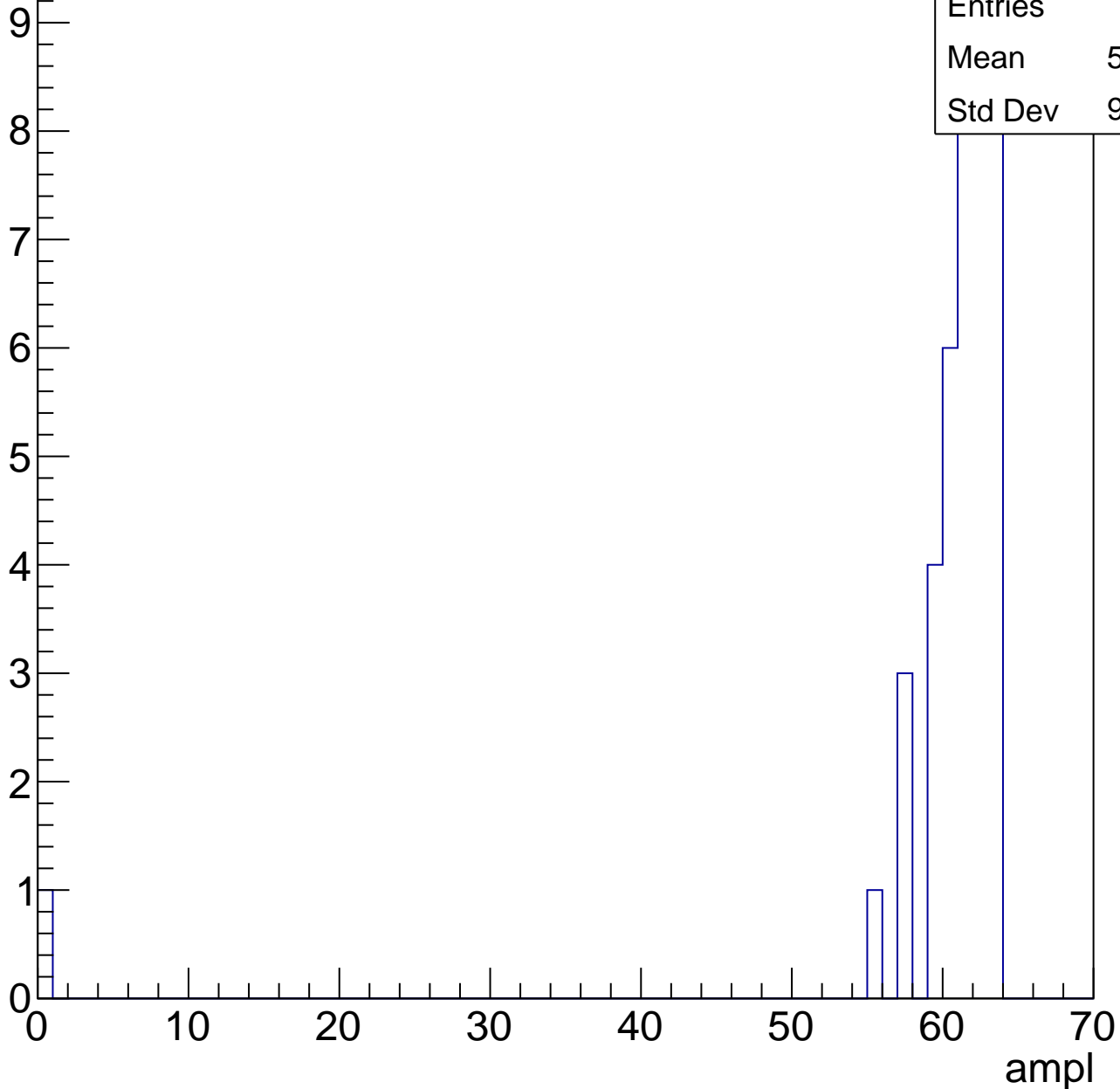


B1L100S, U10-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59.33
Std Dev	9.694



B1L100S, U10-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch46, adc0

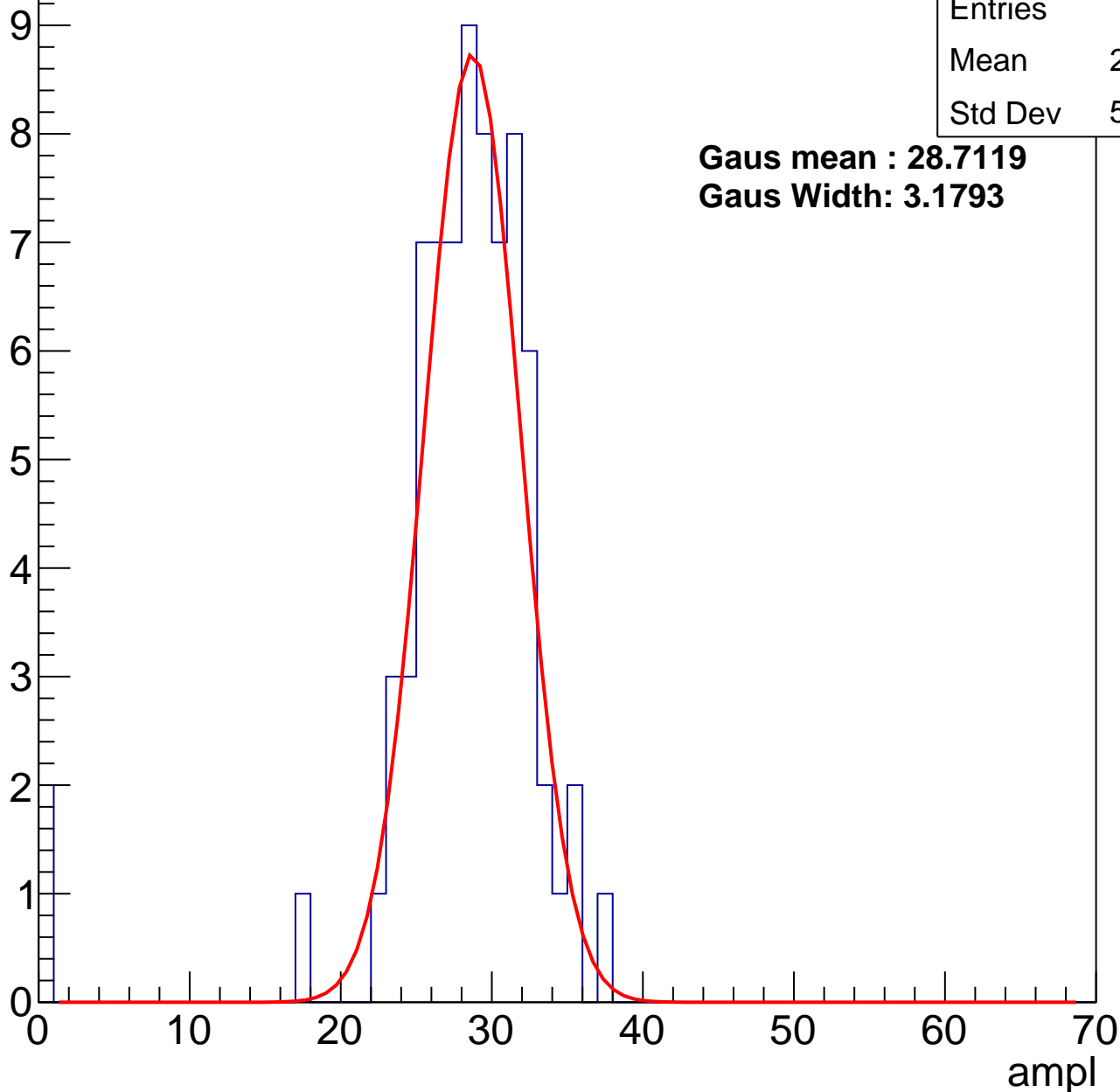
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	27.56
Std Dev	5.657

Gaus mean : 28.7119

Gaus Width: 3.1793



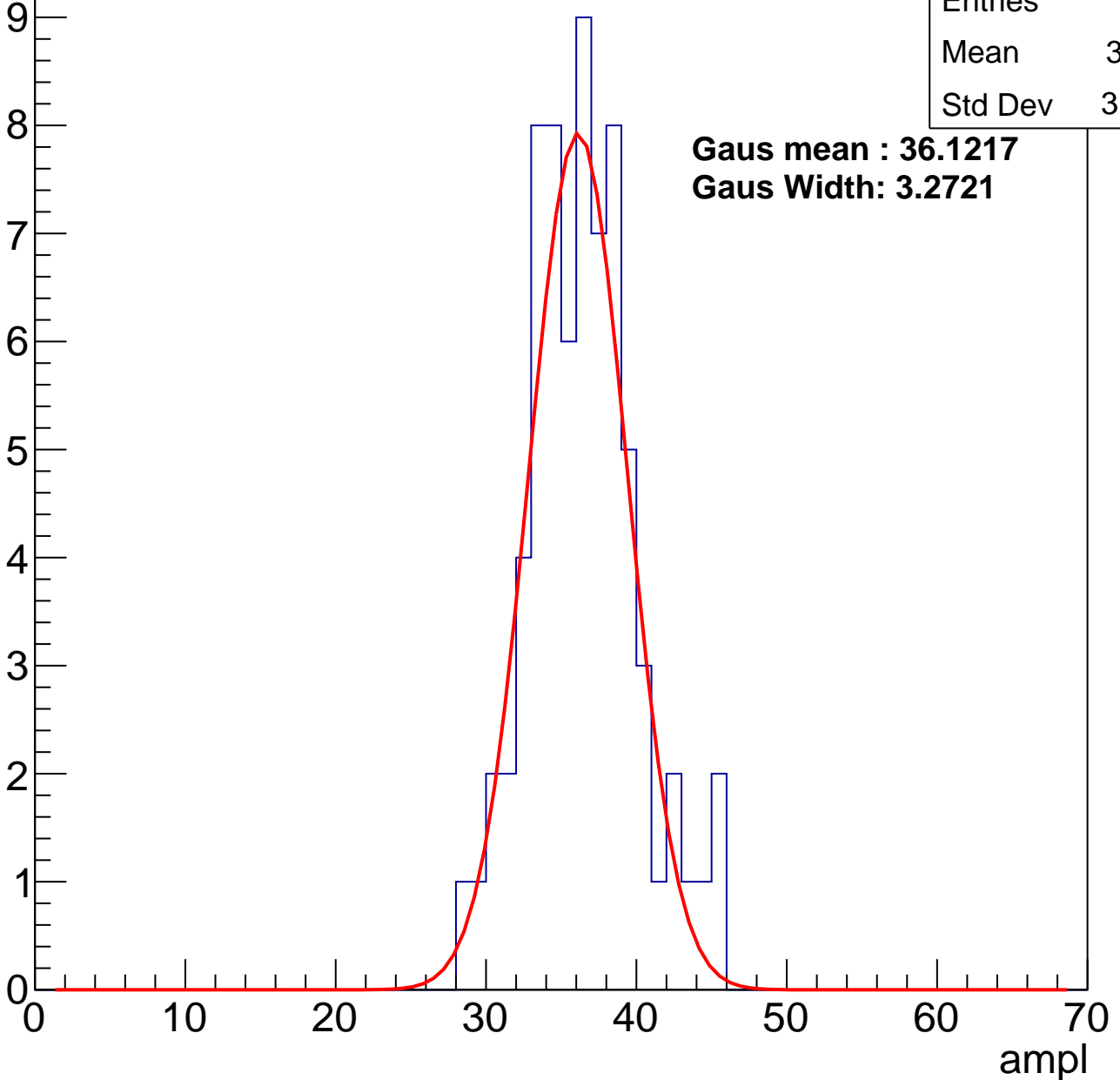
B1L100S, U10-ch46, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.01
Std Dev	3.586

Gaus mean : 36.1217
Gaus Width: 3.2721



B1L100S, U10-ch46, adc2

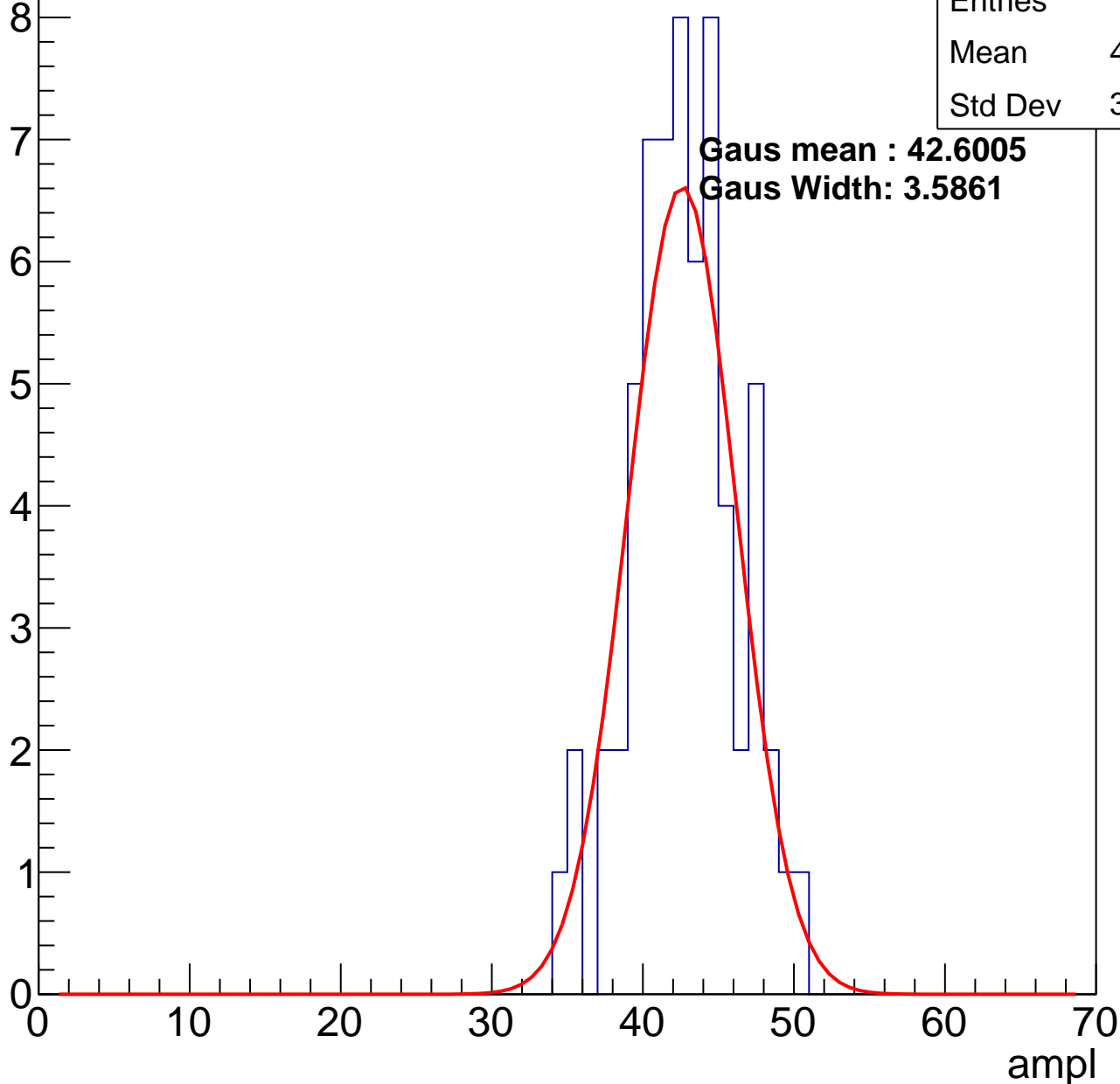
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.29
Std Dev	3.415

Gaus mean : 42.6005

Gaus Width: 3.5861



B1L100S, U10-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

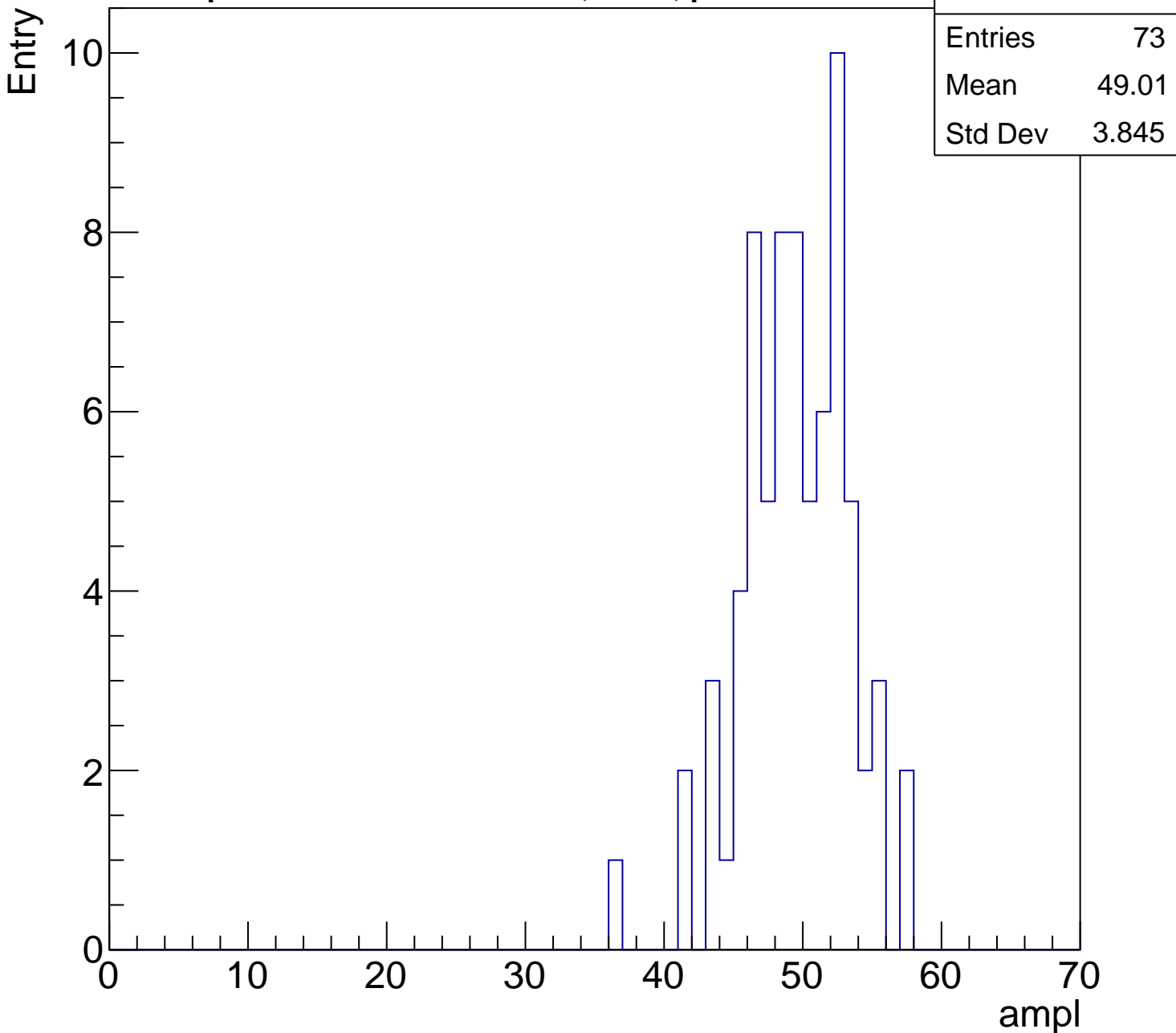
Entries	73
Mean	49.01
Std Dev	3.845

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

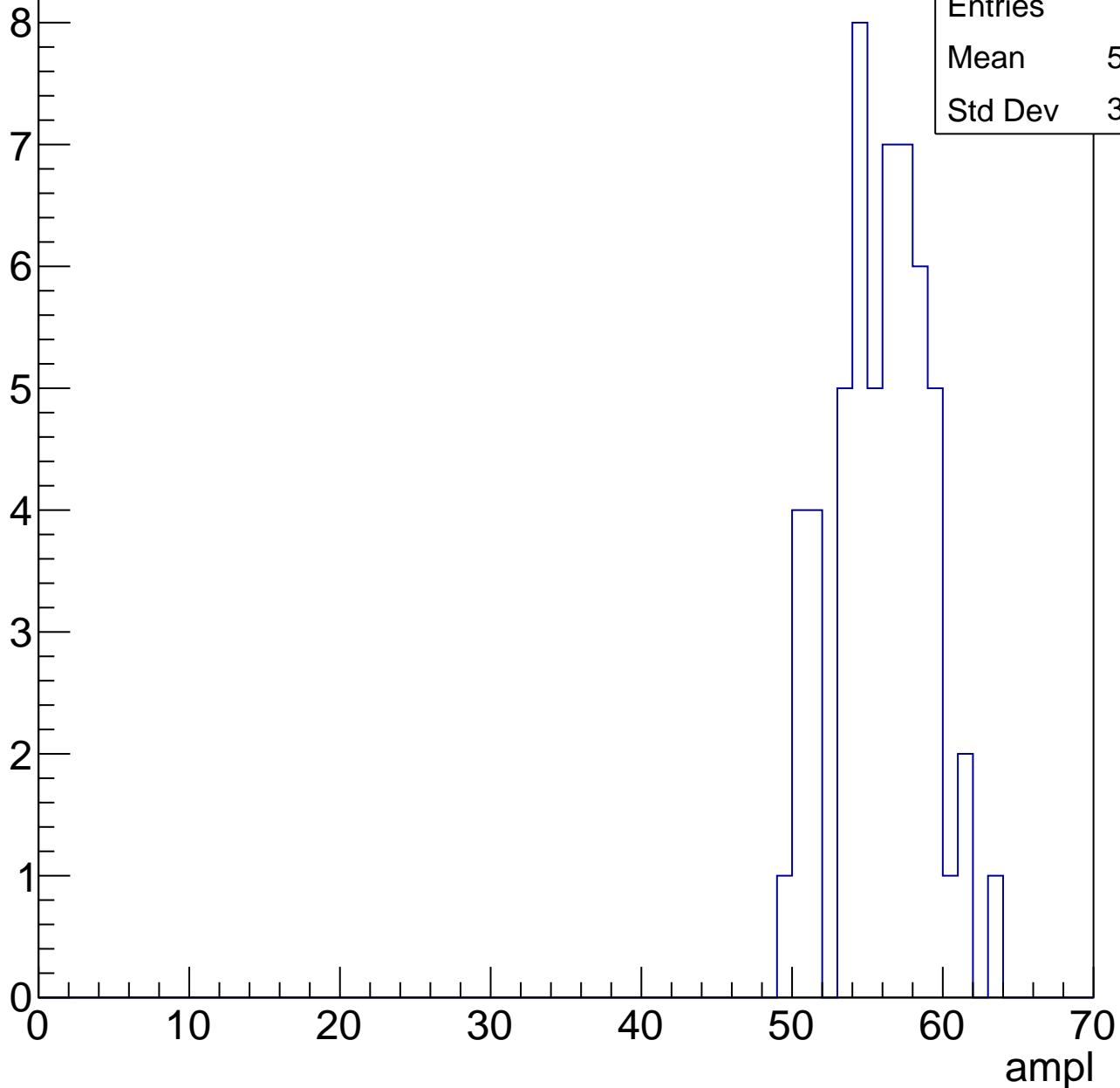


B1L100S, U10-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	55.43
Std Dev	3.127

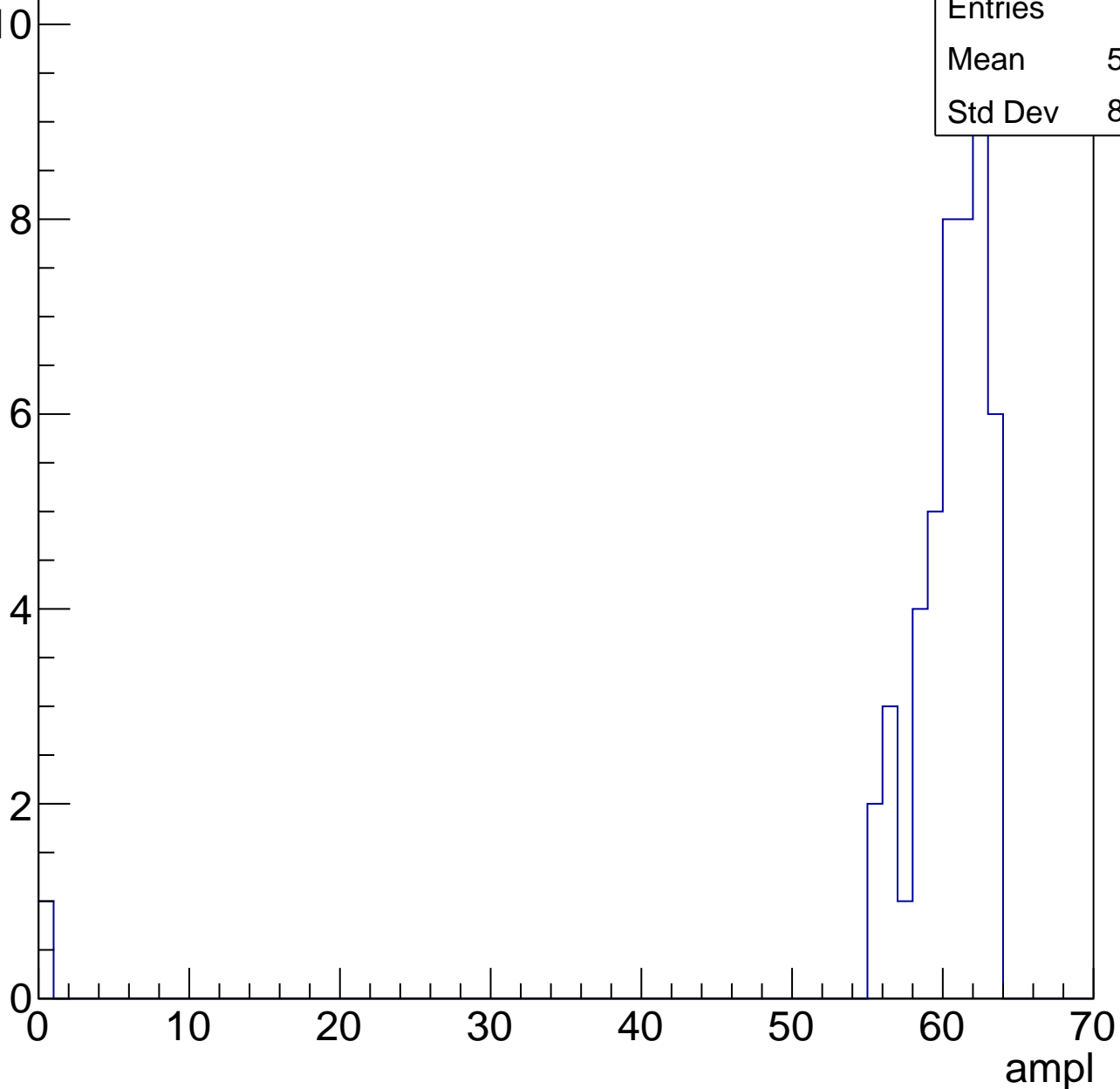


B1L100S, U10-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.92
Std Dev	8.867



B1L100S, U10-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch47, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	30.04
Std Dev	3.676

Gaus mean : 30.1242

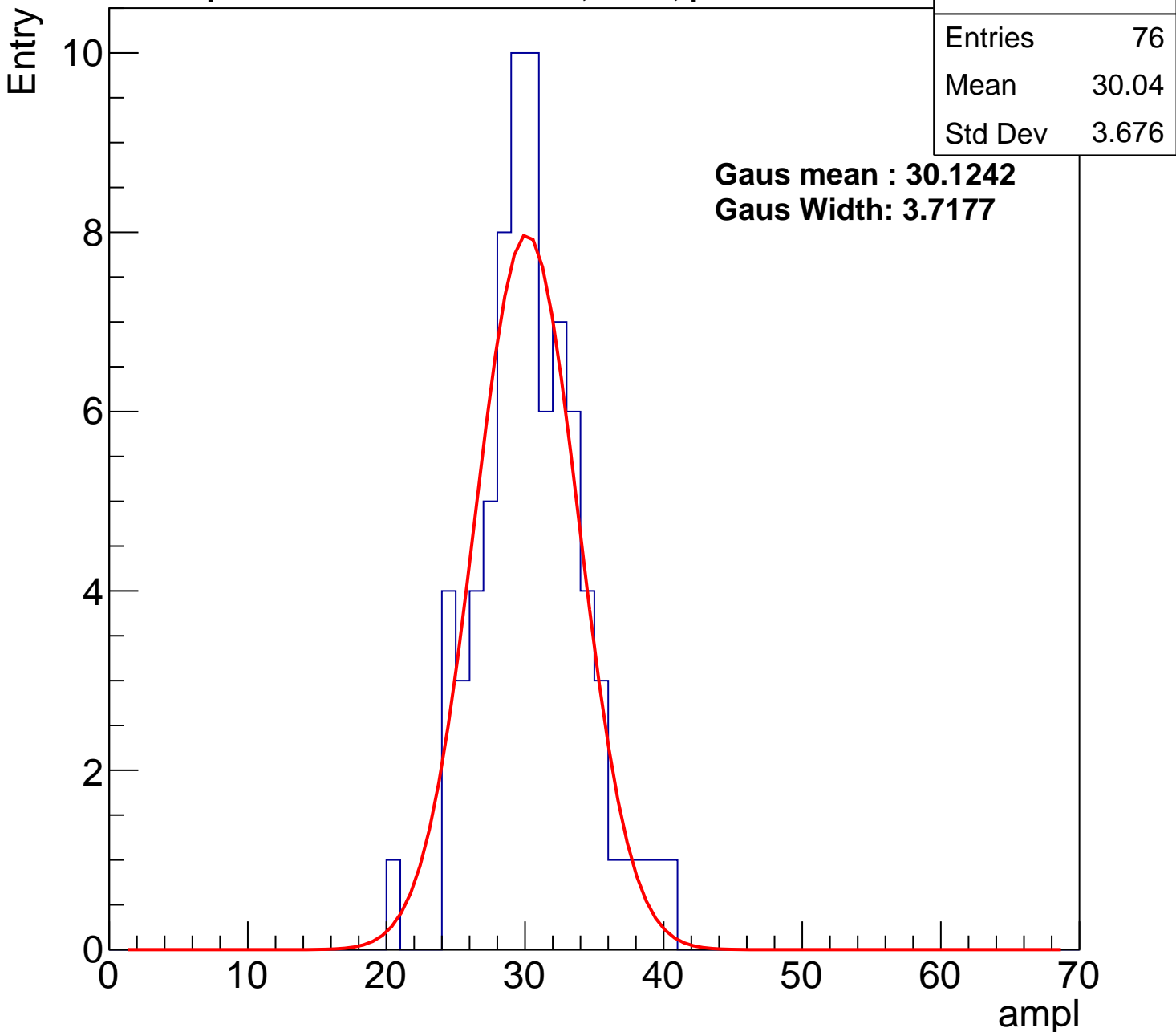
Gaus Width: 3.7177

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch47, adc1

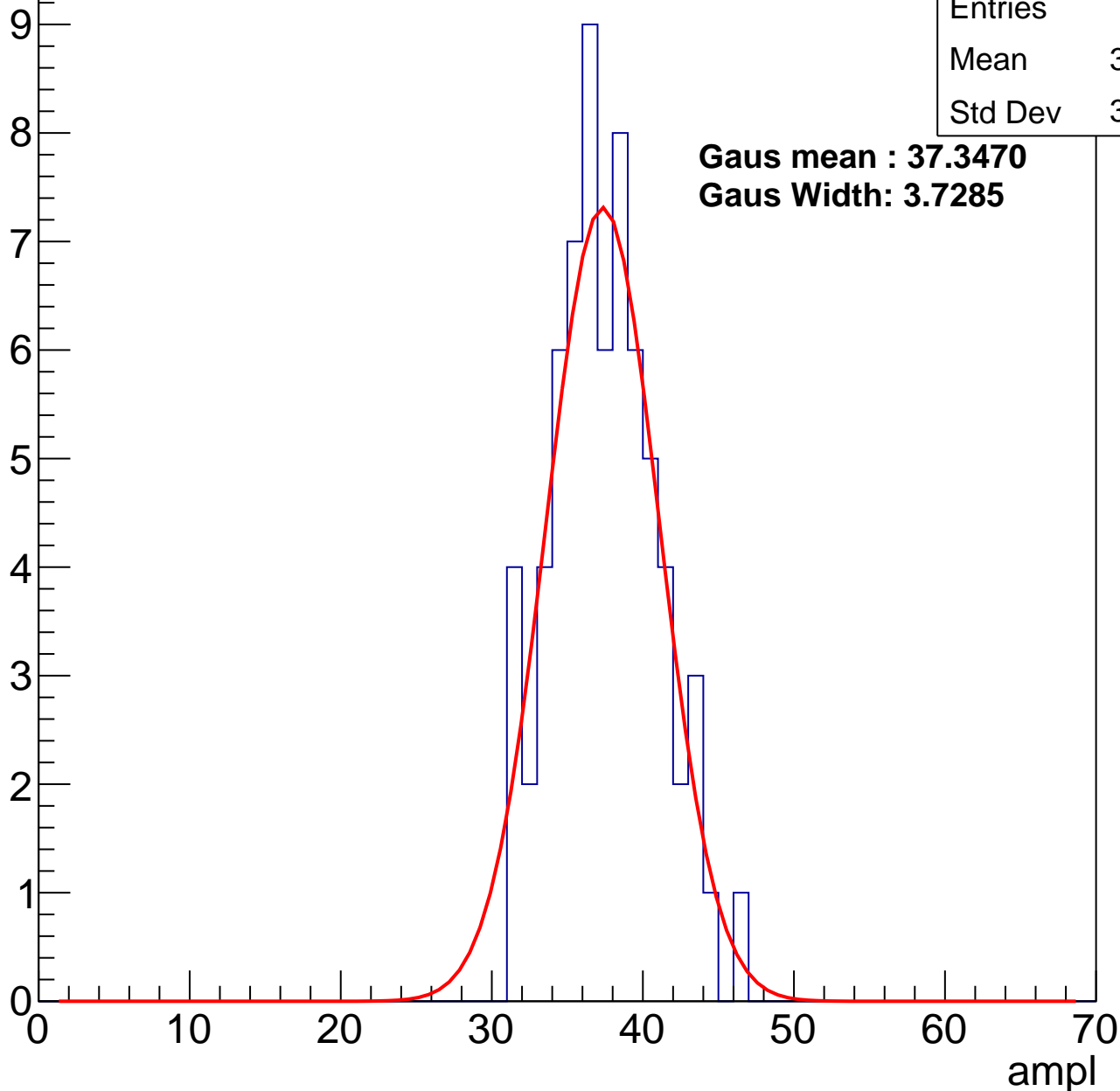
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.06
Std Dev	3.382

Gaus mean : 37.3470

Gaus Width: 3.7285



B1L100S, U10-ch47, adc2

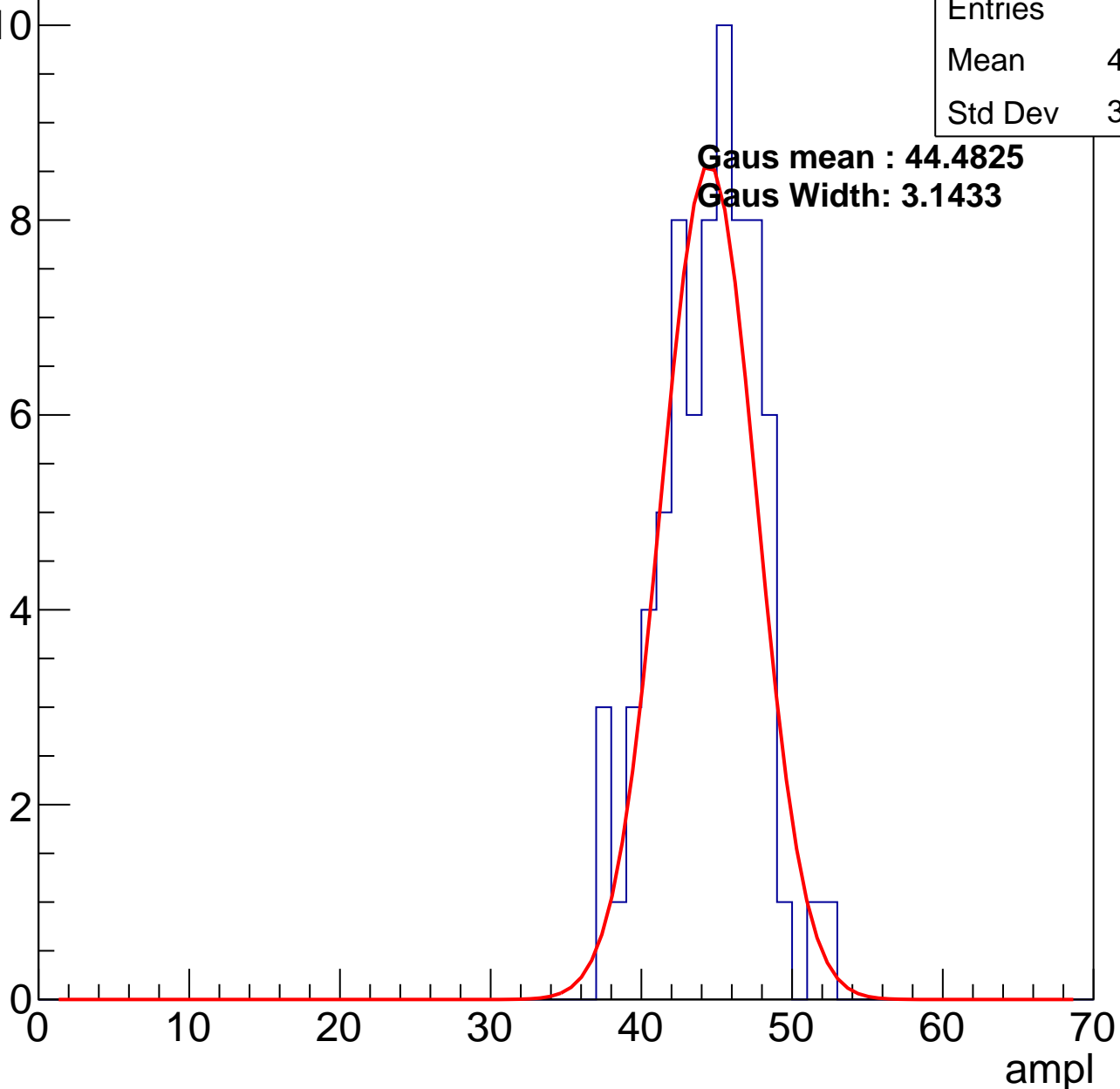
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	43.99
Std Dev	3.212

Gaus mean : 44.4825

Gaus Width: 3.1433

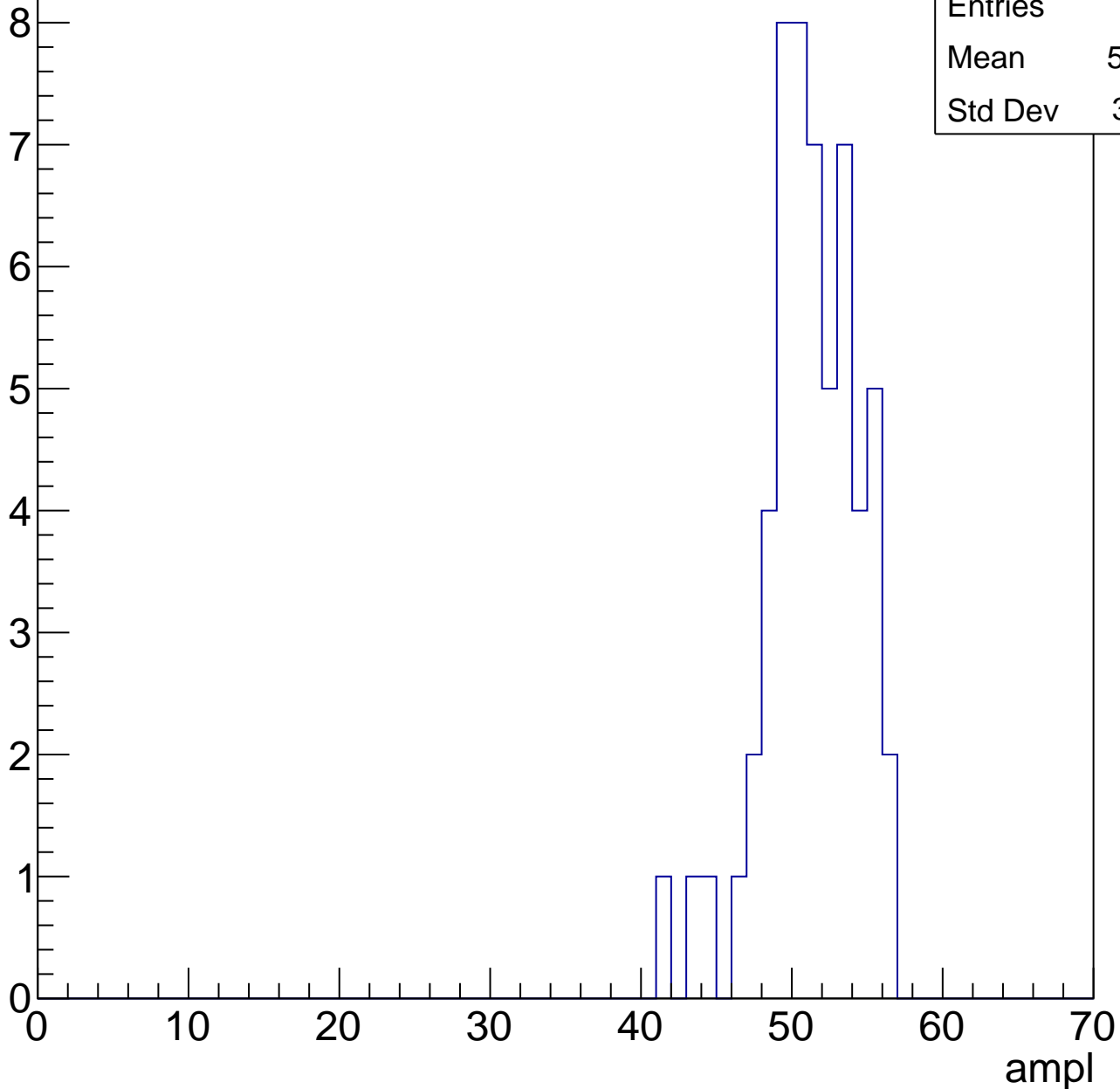


B1L100S, U10-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

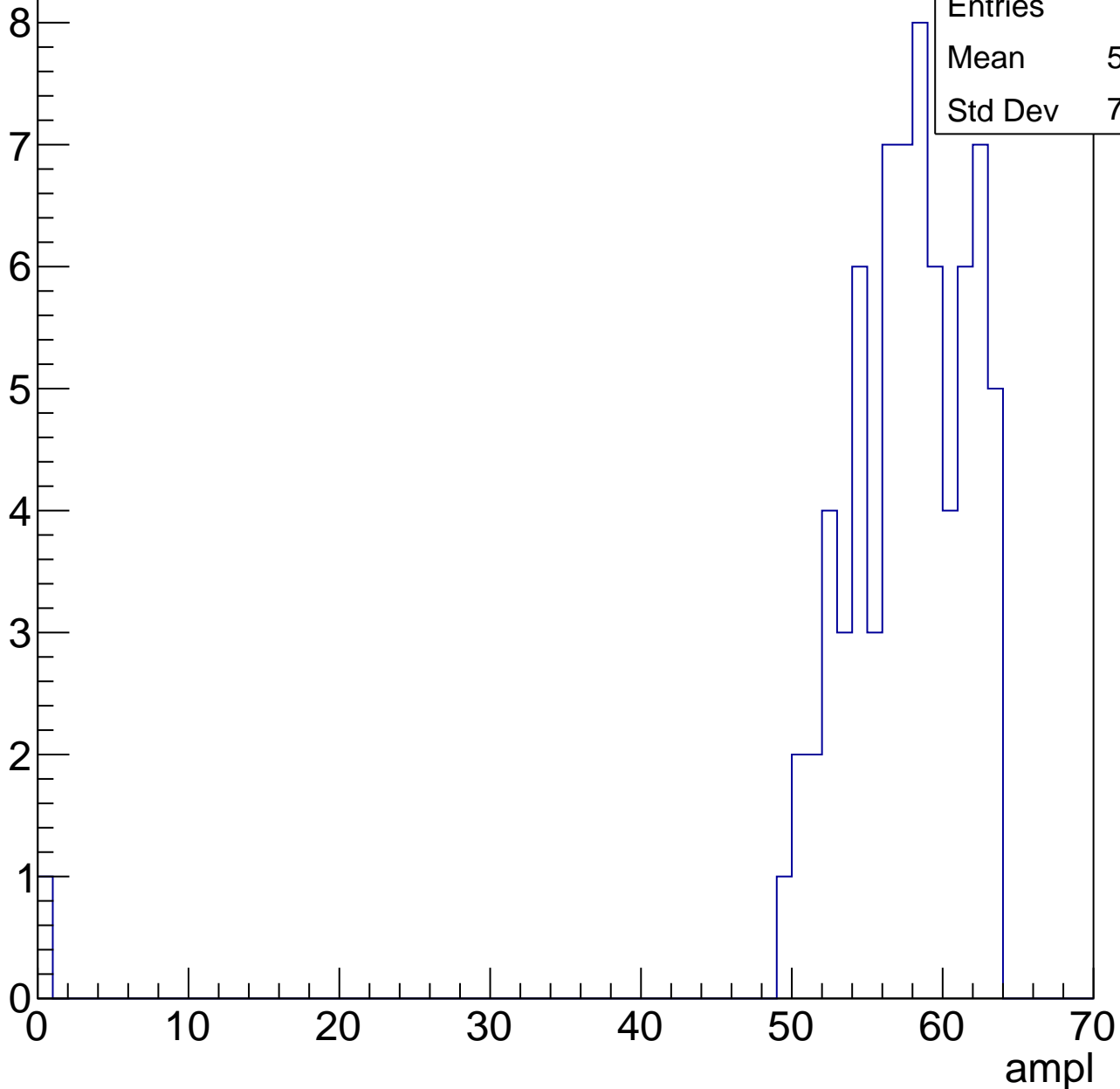
Entries	56
Mean	50.77
Std Dev	3.111



B1L100S, U10-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

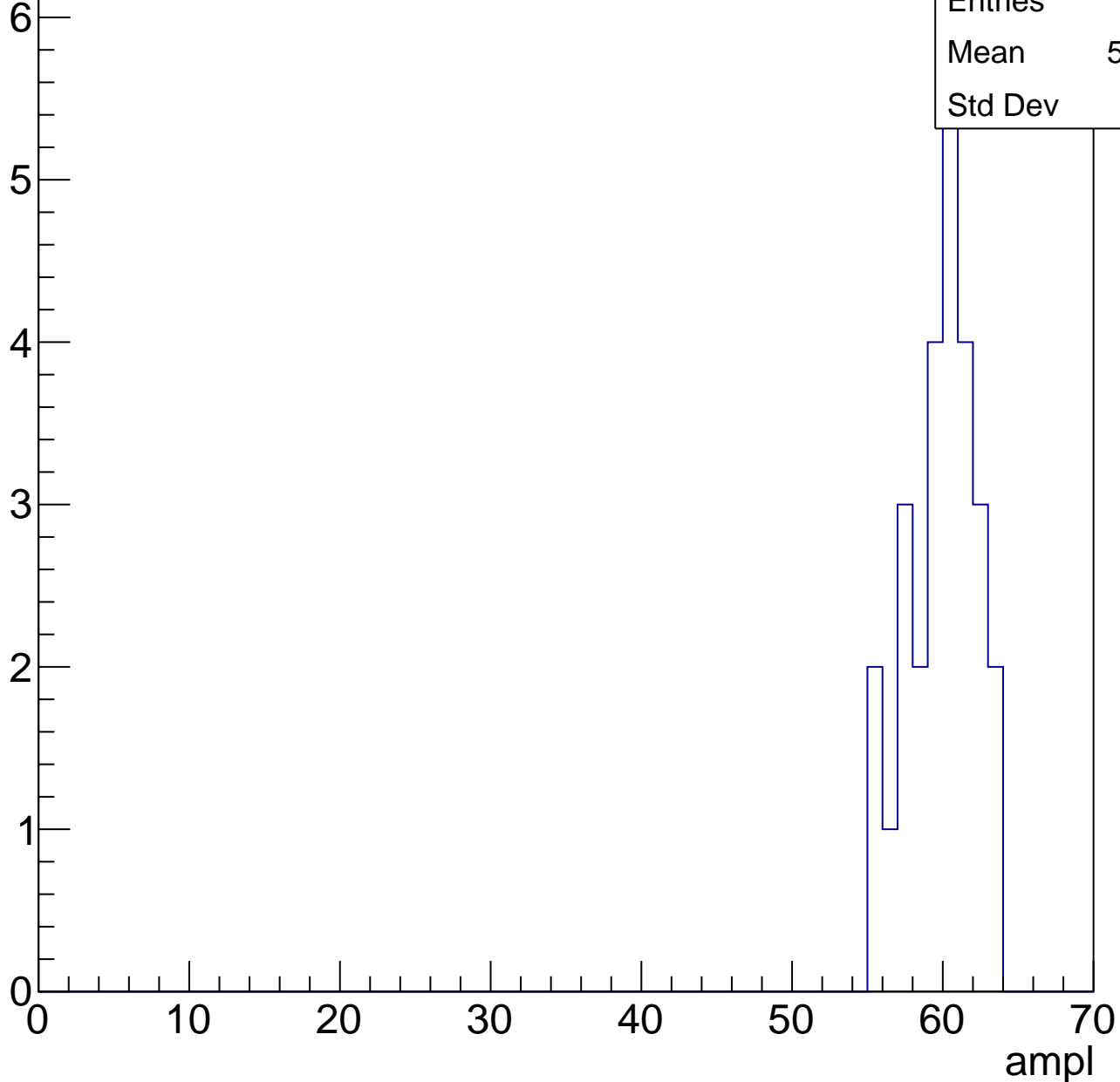


Entries	72
Mean	56.54
Std Dev	7.645

B1L100S, U10-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch48, adc0

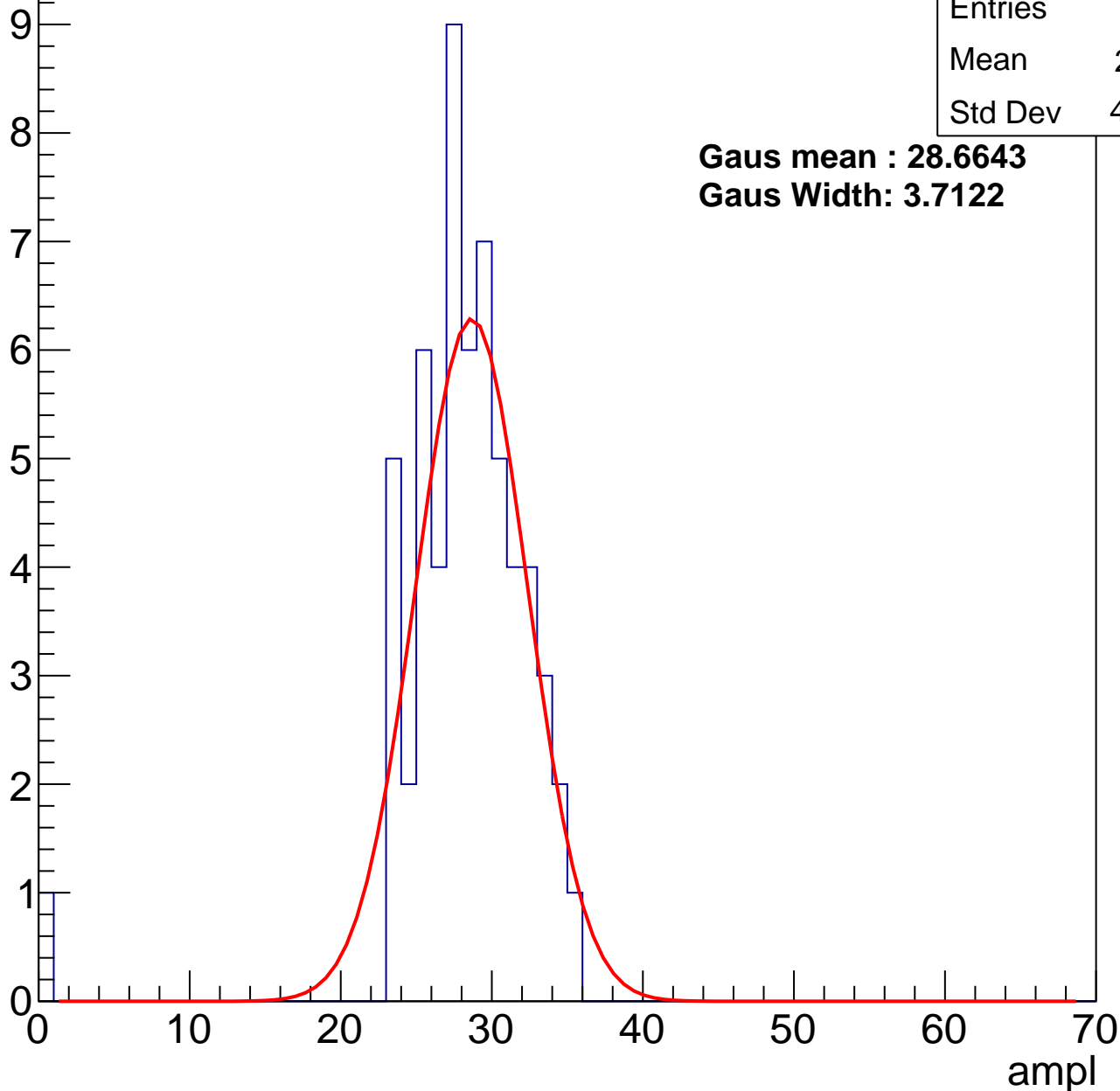
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	27.71
Std Dev	4.762

Gaus mean : 28.6643

Gaus Width: 3.7122



B1L100S, U10-ch48, adc1

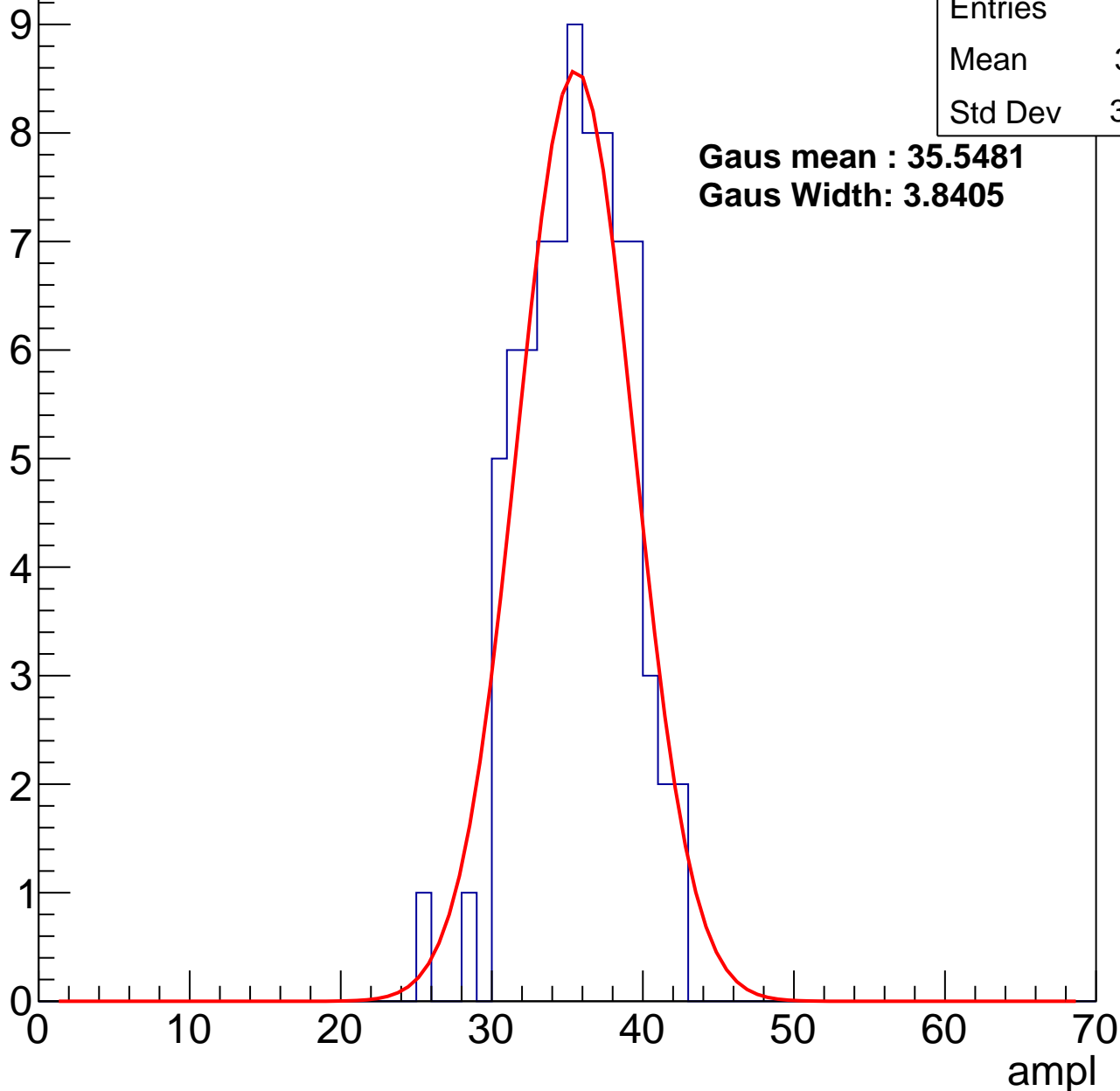
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.11
Std Dev	3.409

Gaus mean : 35.5481

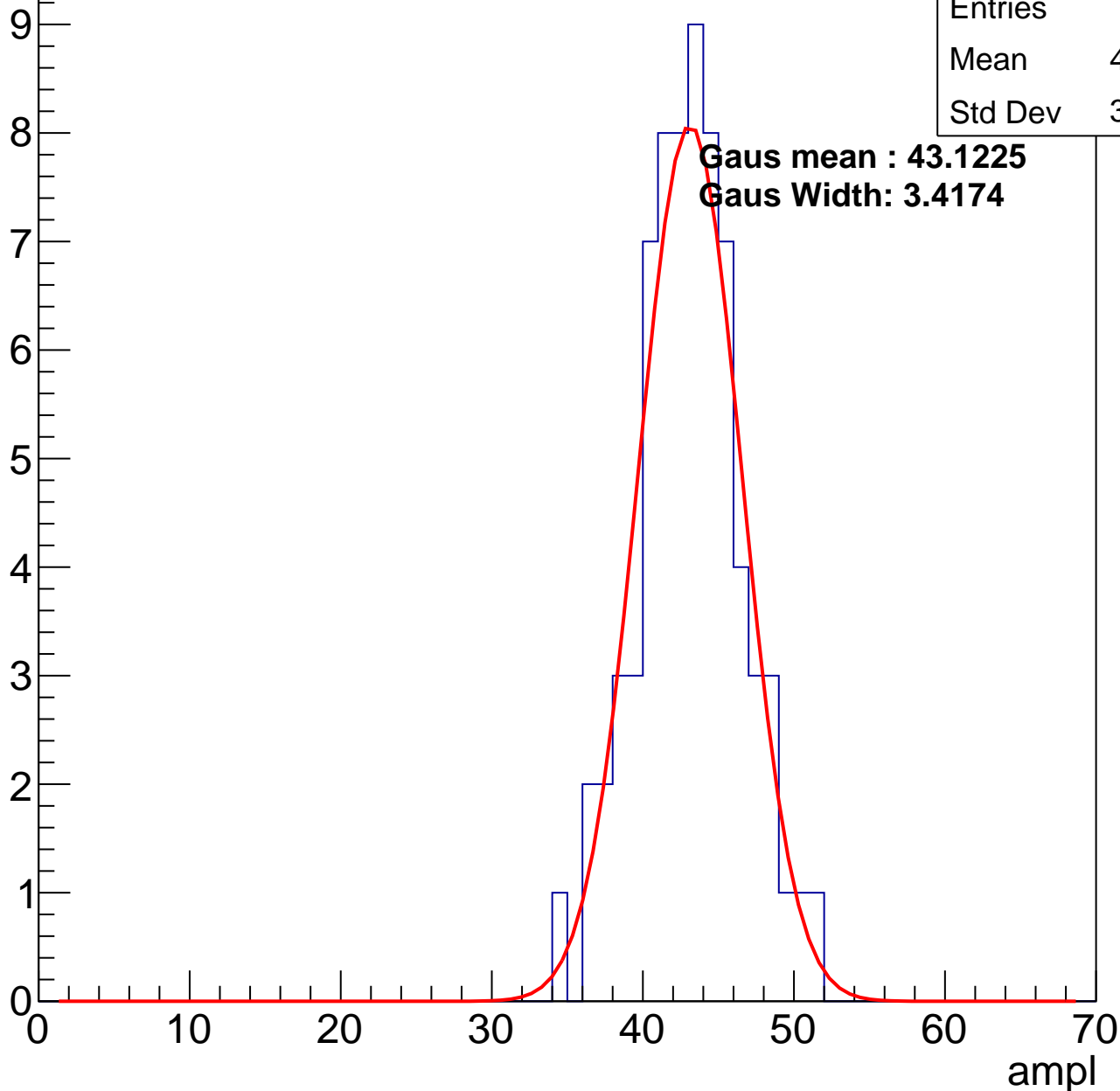
Gaus Width: 3.8405



B1L100S, U10-ch48, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



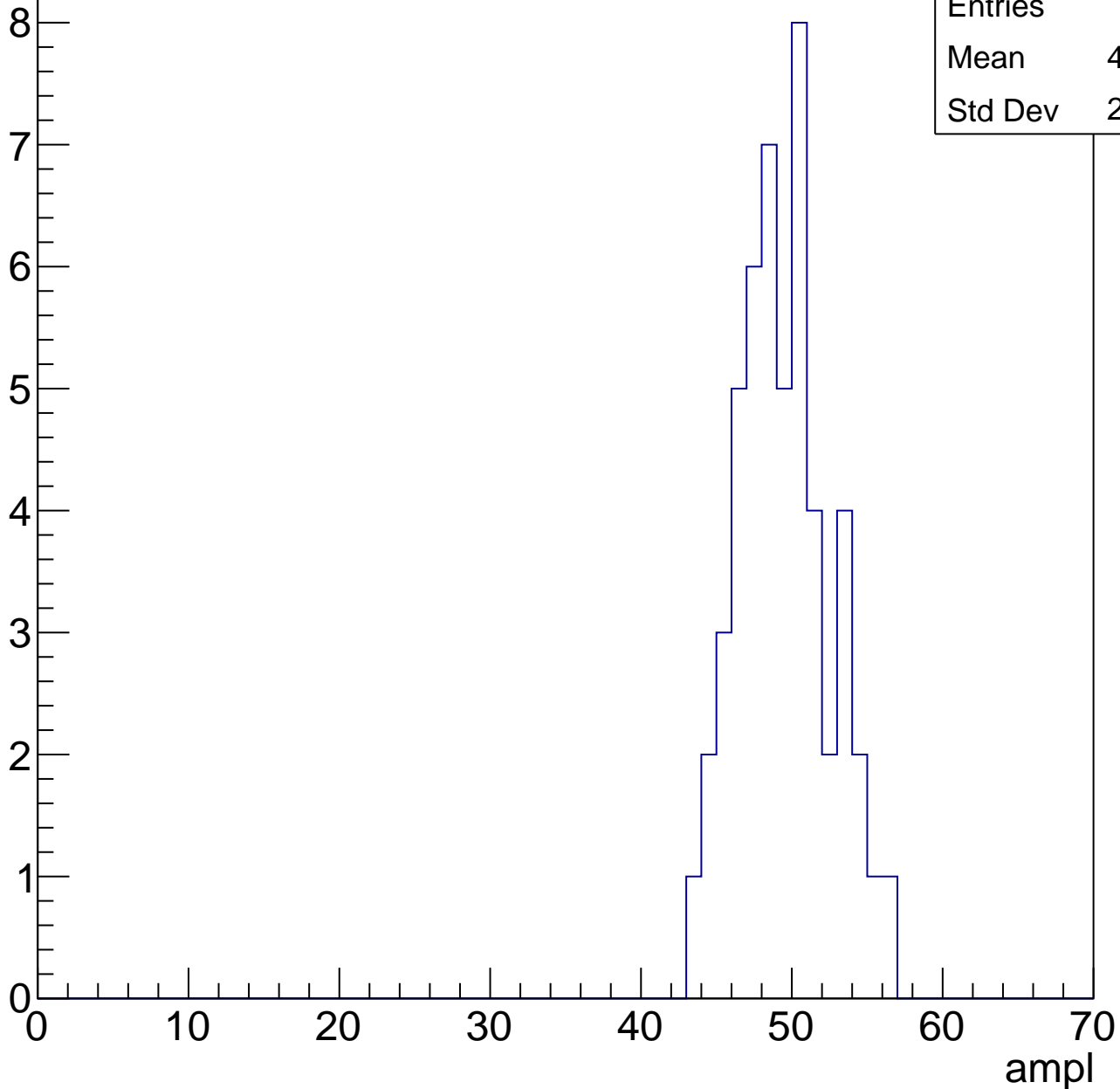
Entries	71
Mean	42.65
Std Dev	3.382

B1L100S, U10-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

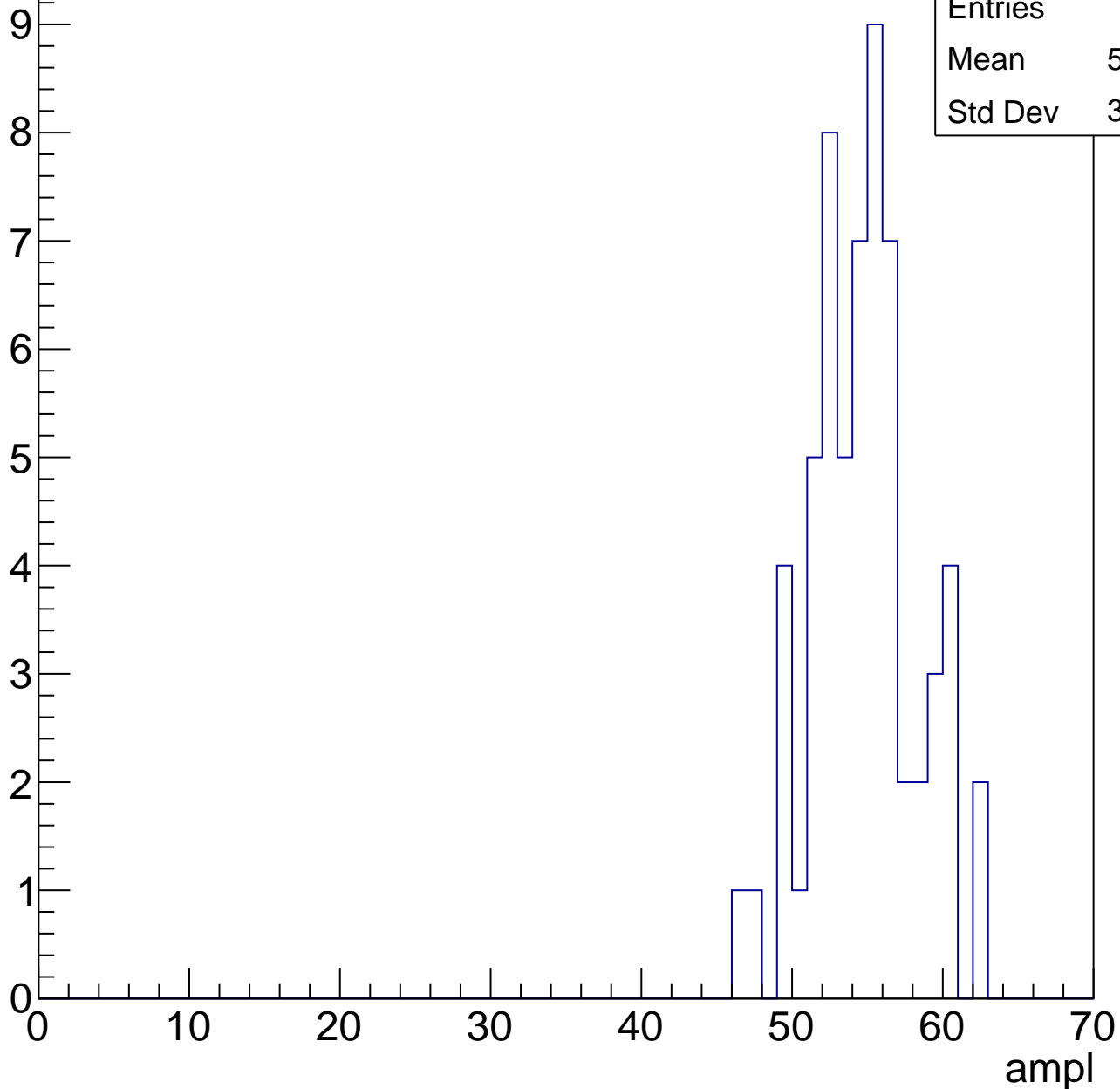
Entries	51
Mean	48.98
Std Dev	2.974



B1L100S, U10-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

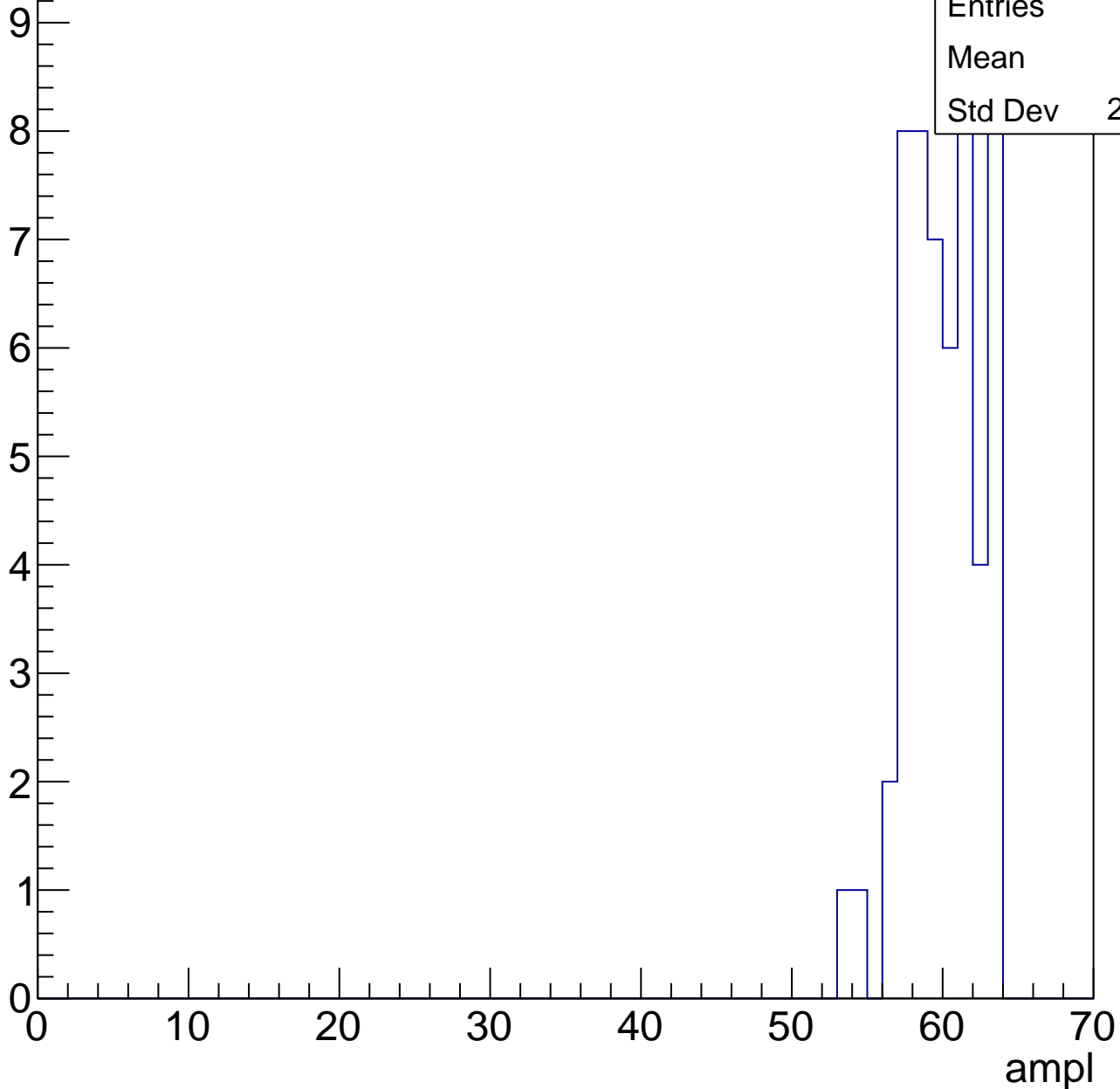


B1L100S, U10-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

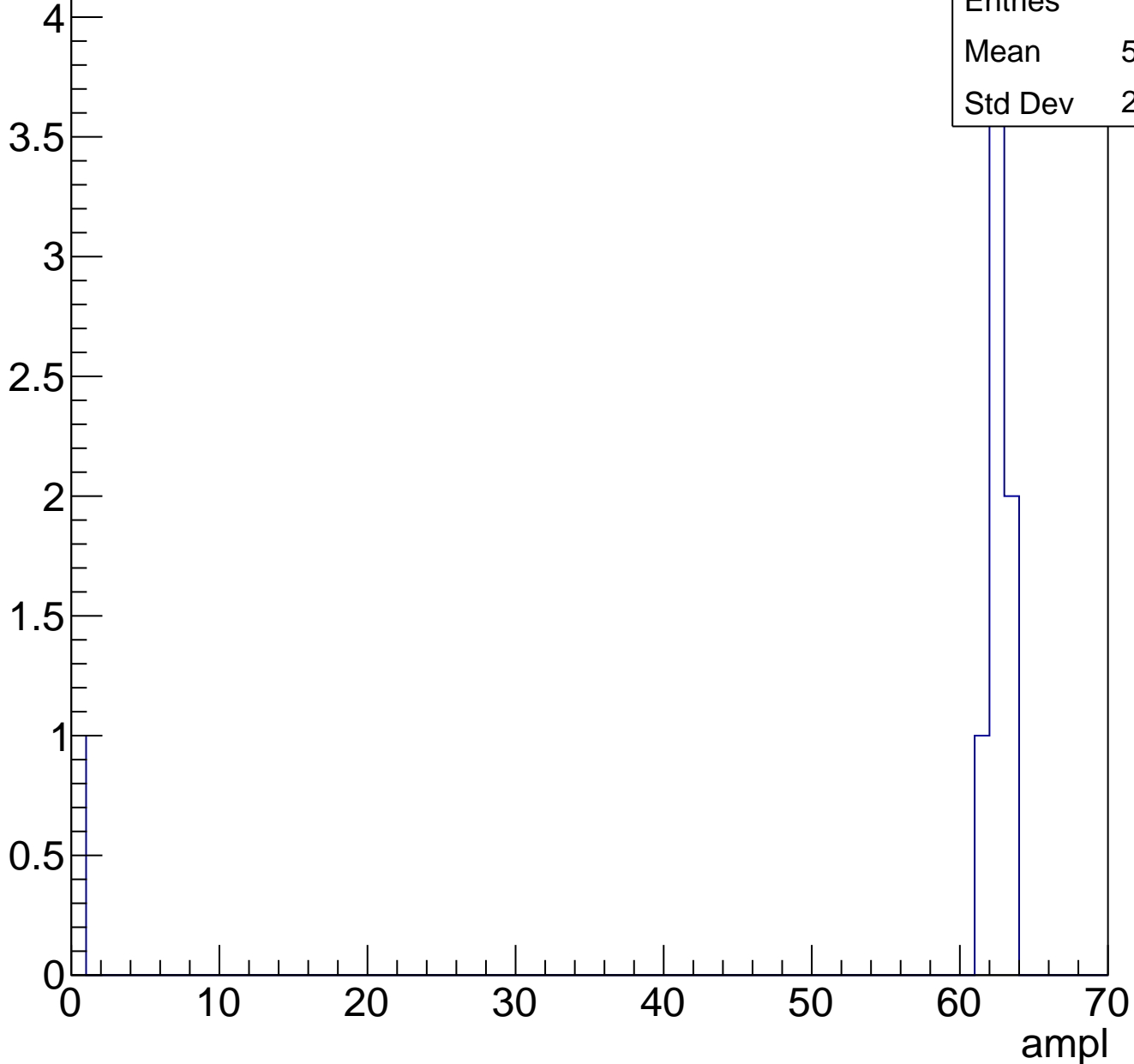
Entries	54
Mean	59.5
Std Dev	2.402



B1L100S, U10-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	8
Mean	54.38
Std Dev	20.56

B1L100S, U10-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch49, adc0

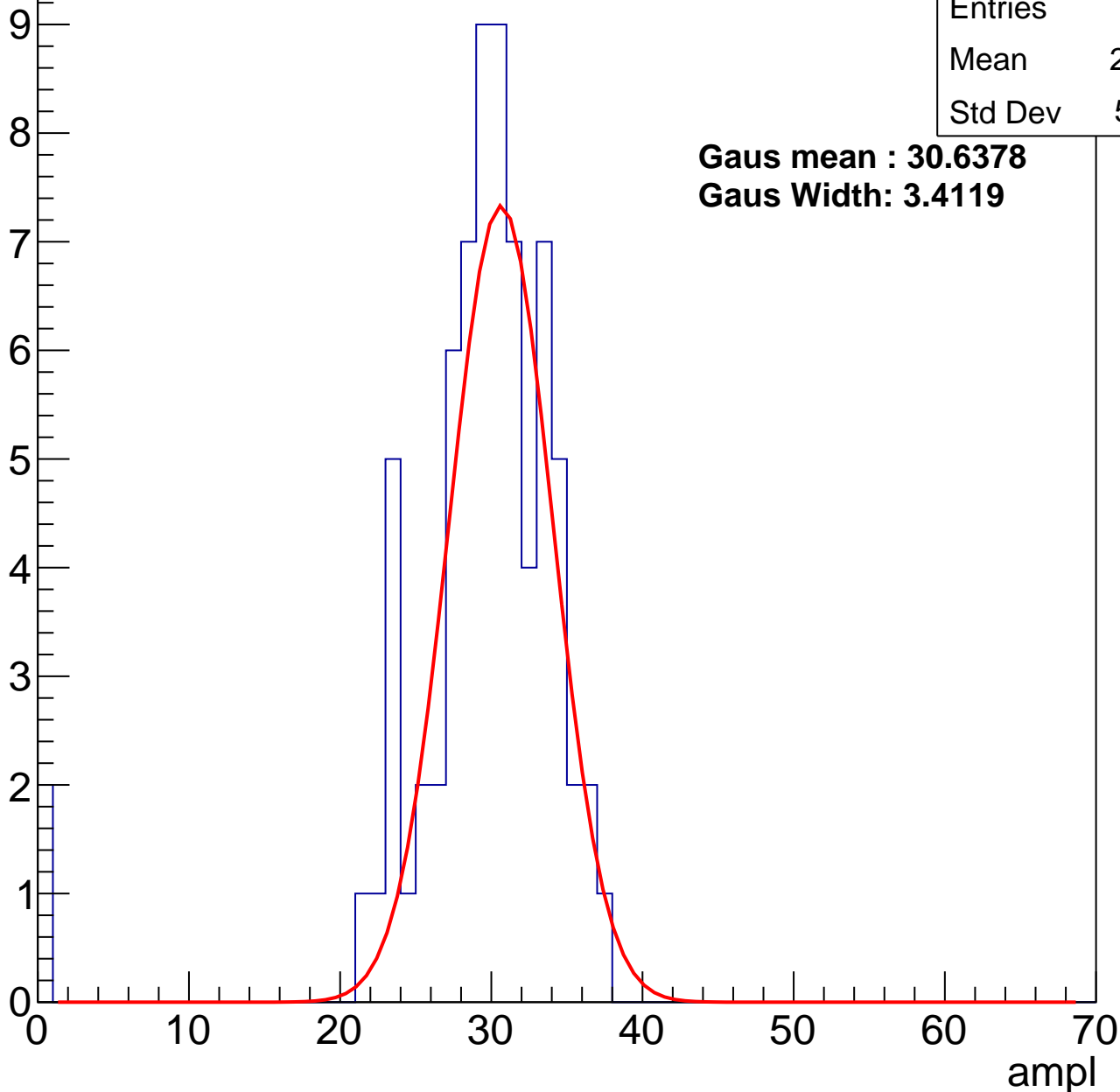
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.74
Std Dev	5.991

Gaus mean : 30.6378

Gaus Width: 3.4119



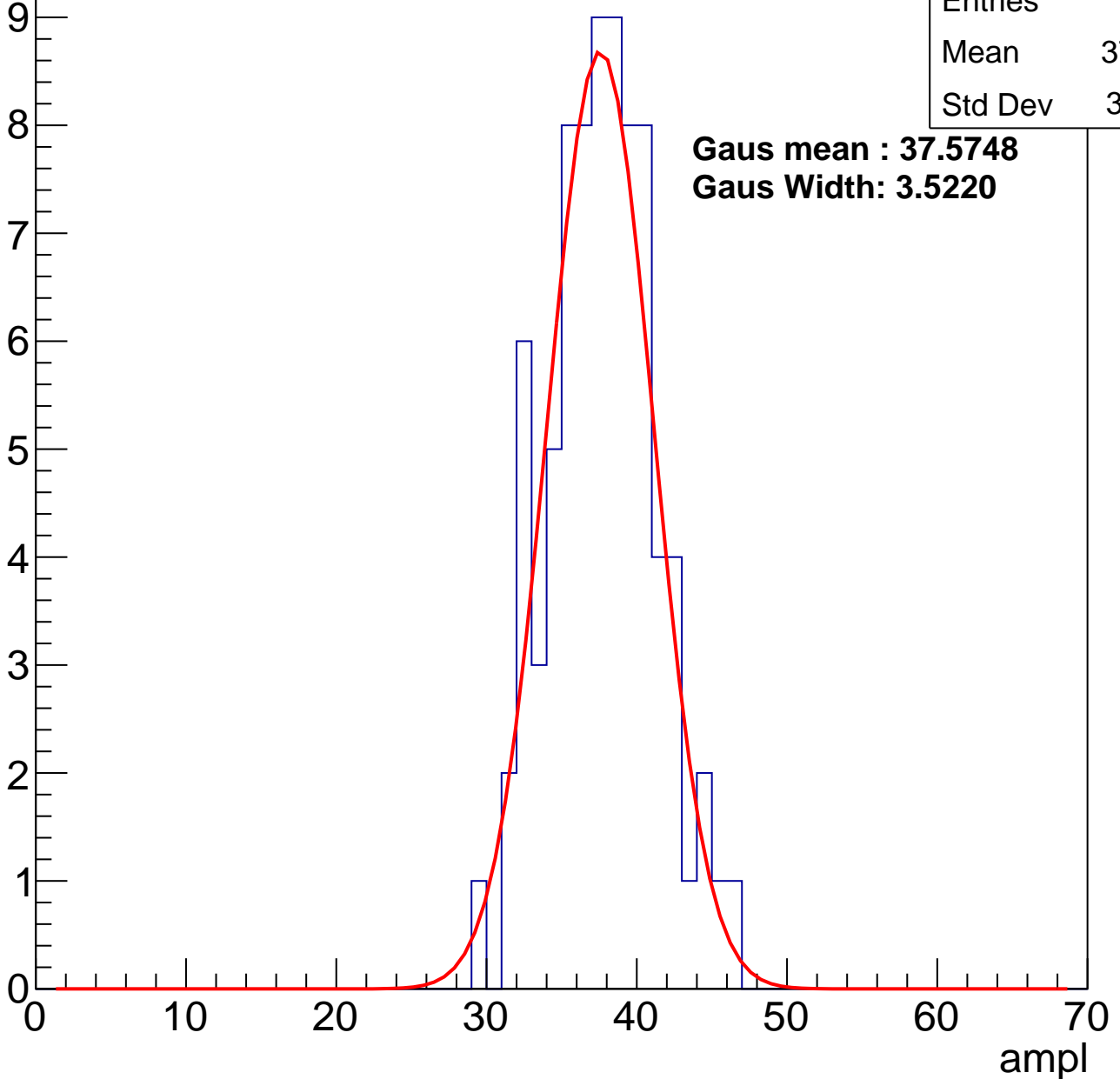
B1L100S, U10-ch49, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	37.26
Std Dev	3.481

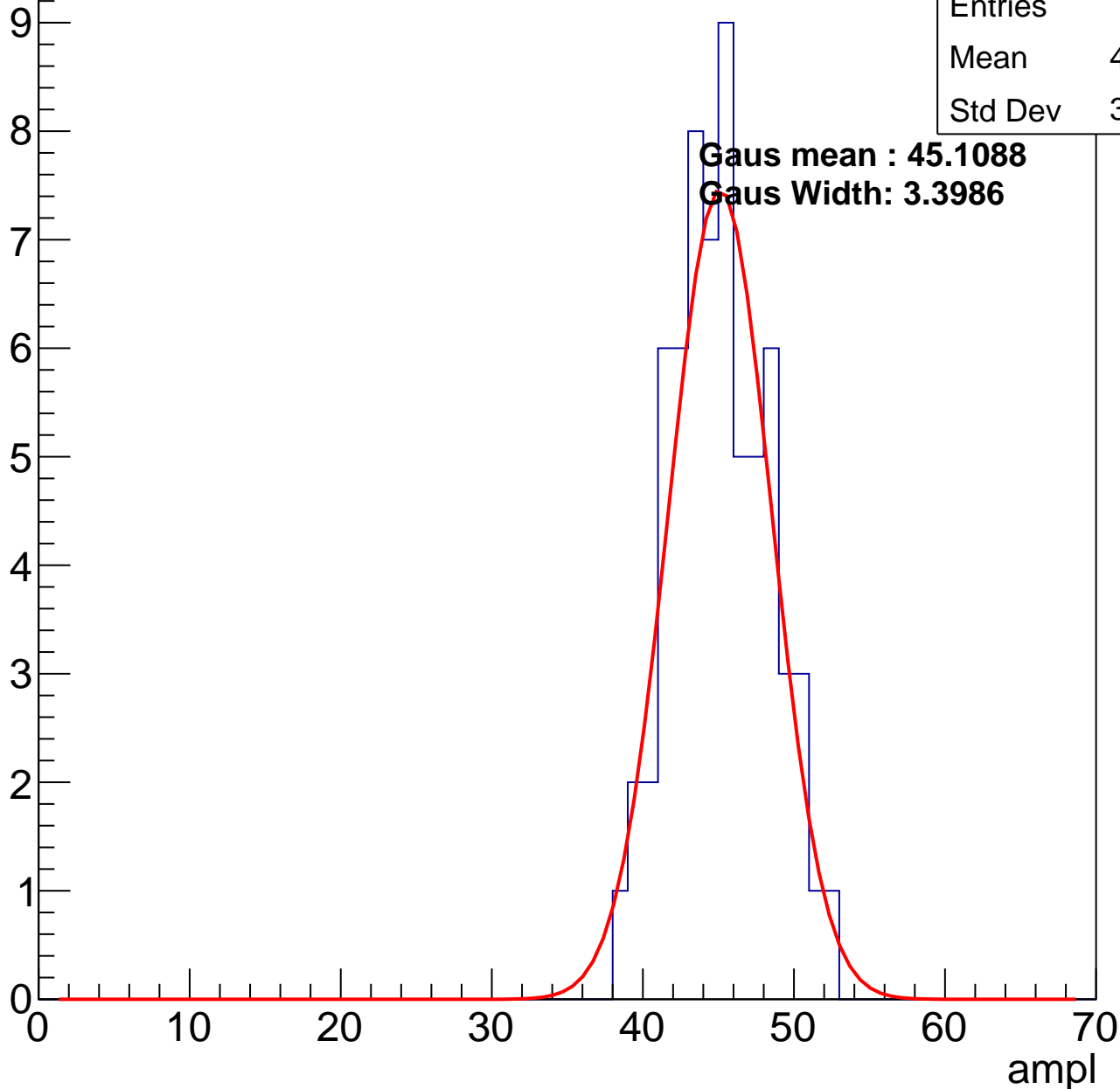
Gaus mean : 37.5748
Gaus Width: 3.5220



B1L100S, U10-ch49, adc2

calib_packv5_042523_0143.root, FC#4, port A2

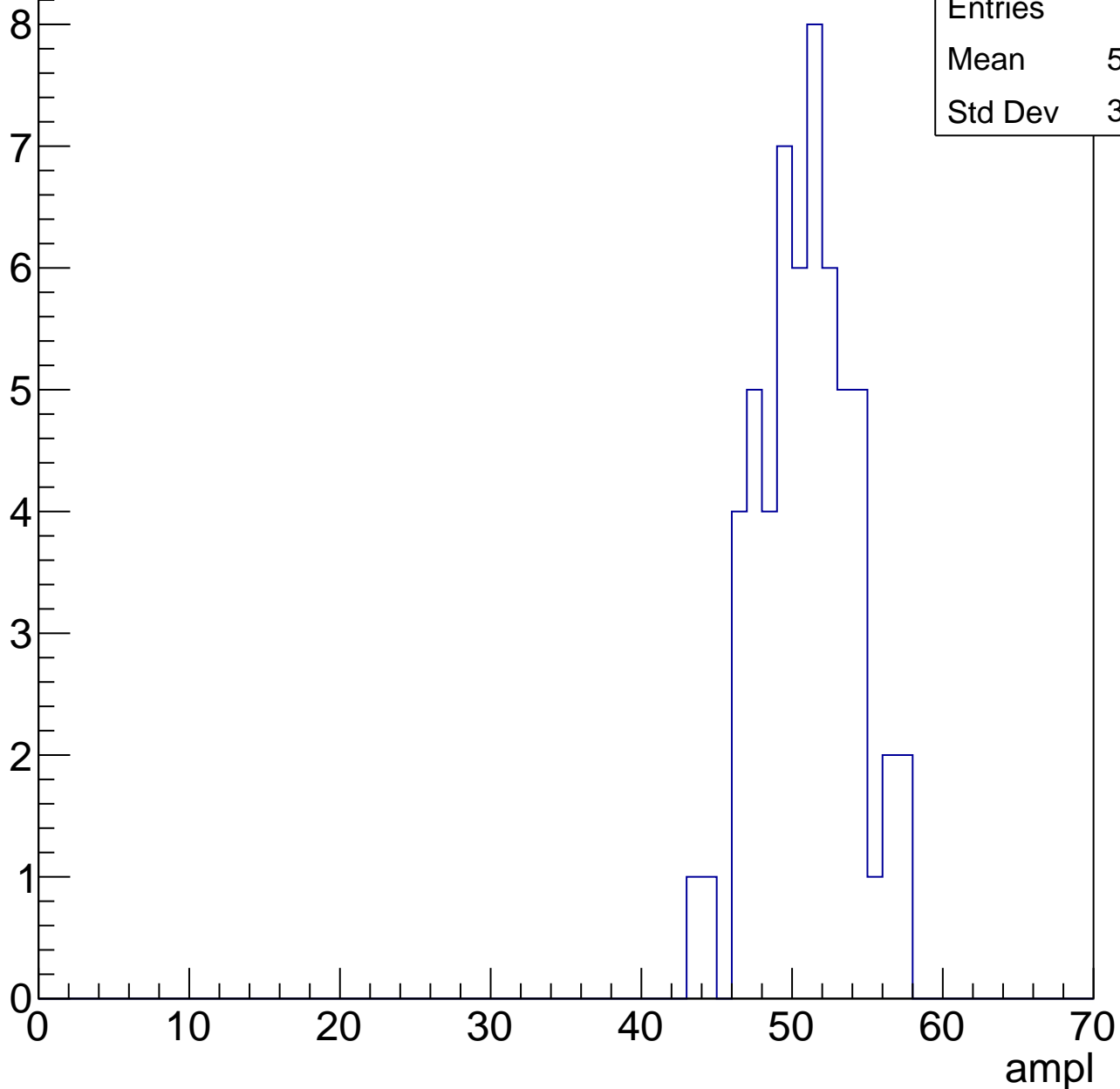
Entry



B1L100S, U10-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

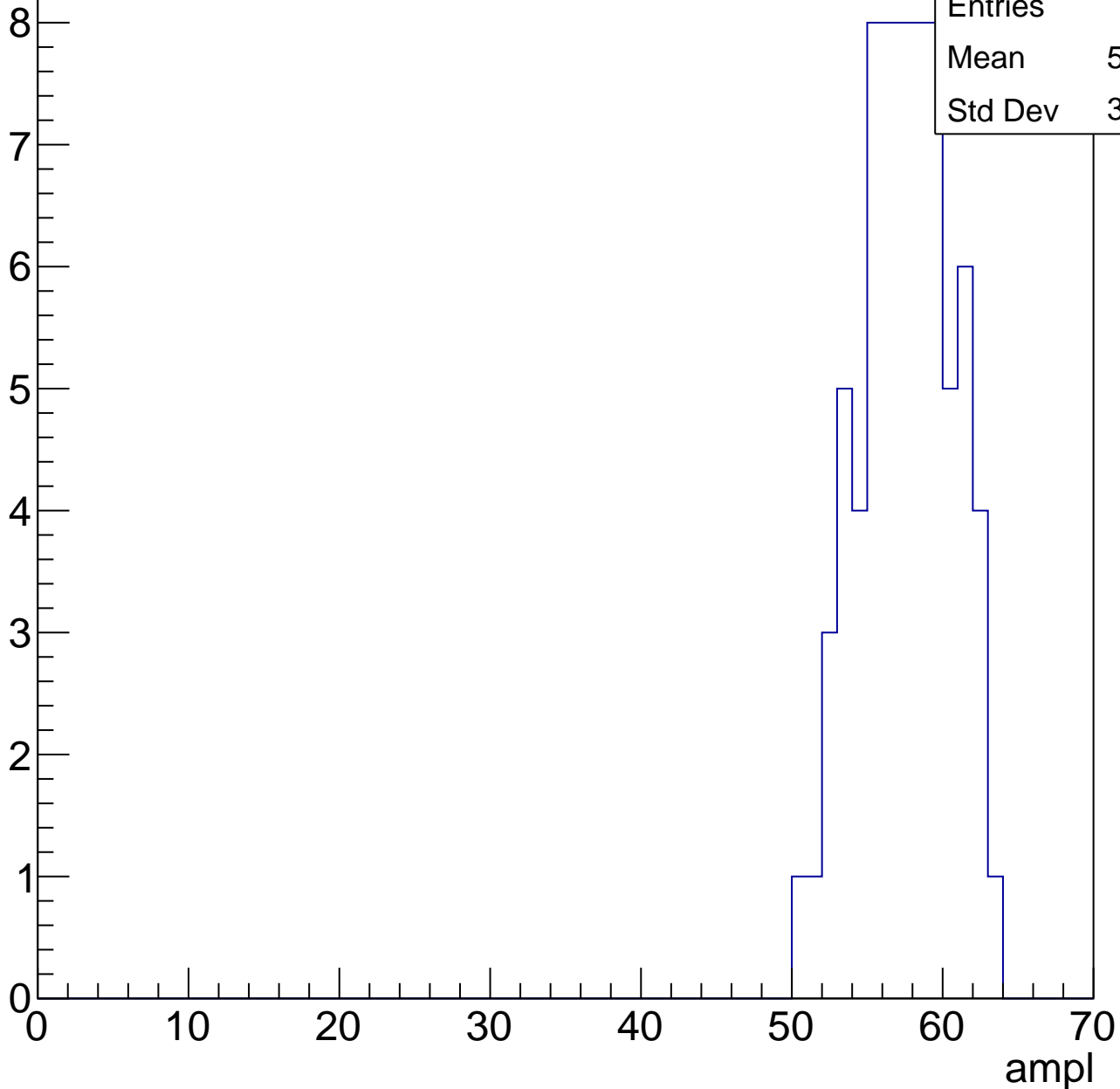


B1L100S, U10-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	57.07
Std Dev	3.006

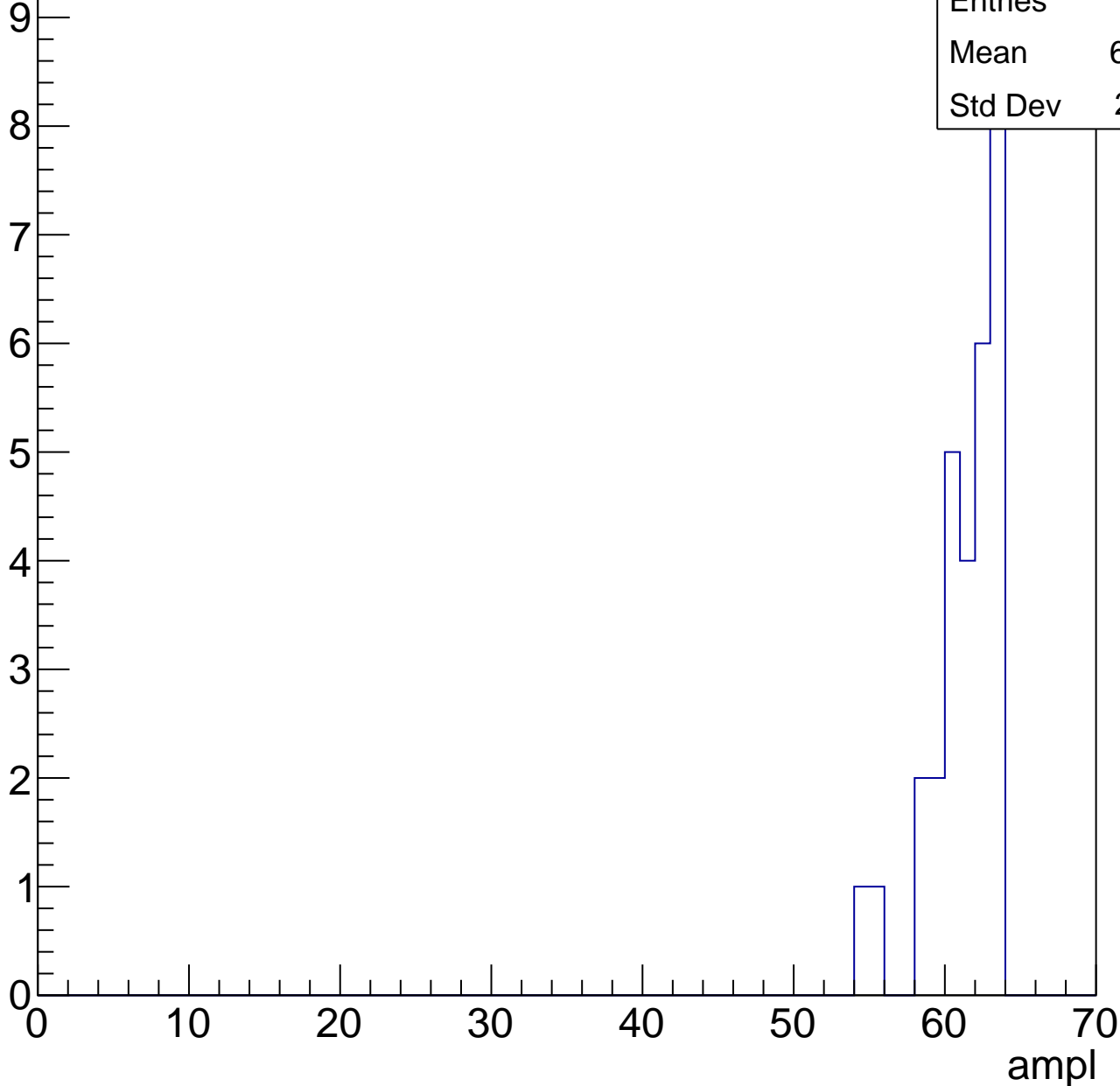


B1L100S, U10-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.87
Std Dev	2.291



B1L100S, U10-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch50, adc0

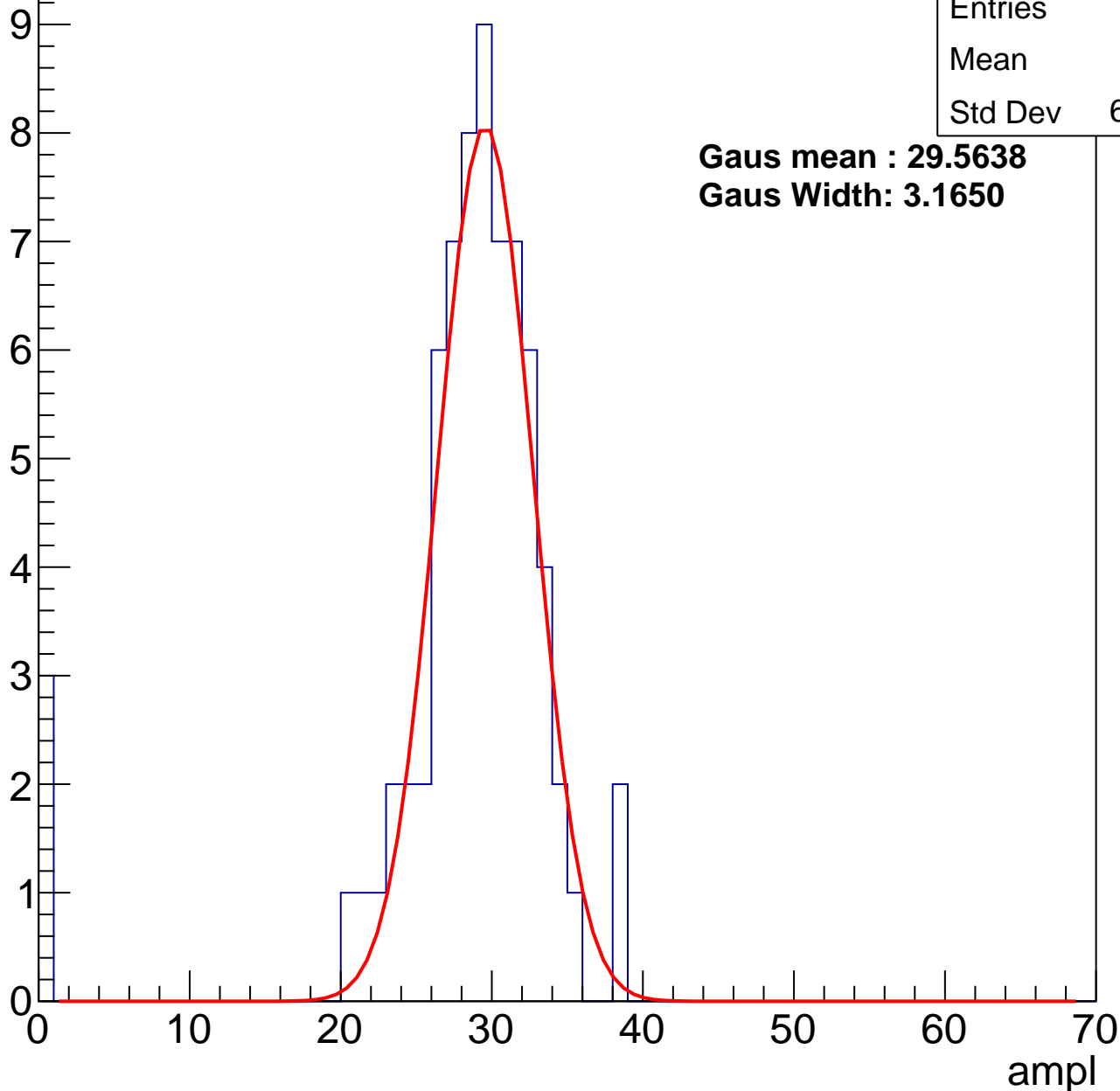
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.7
Std Dev	6.758

Gaus mean : 29.5638

Gaus Width: 3.1650



B1L100S, U10-ch50, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	36.34
Std Dev	3.416

Gaus mean : 36.6252

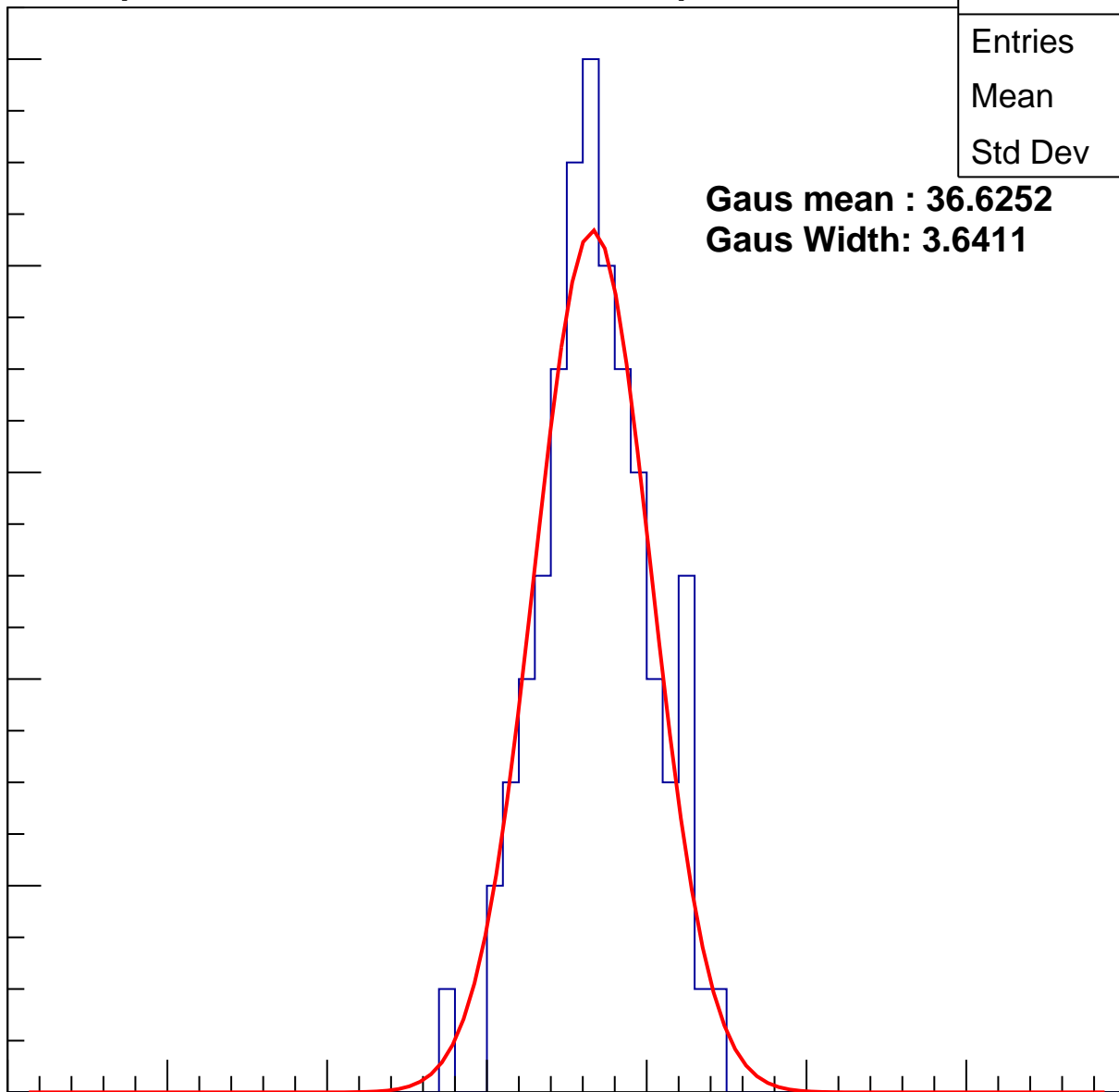
Gaus Width: 3.6411

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch50, adc2

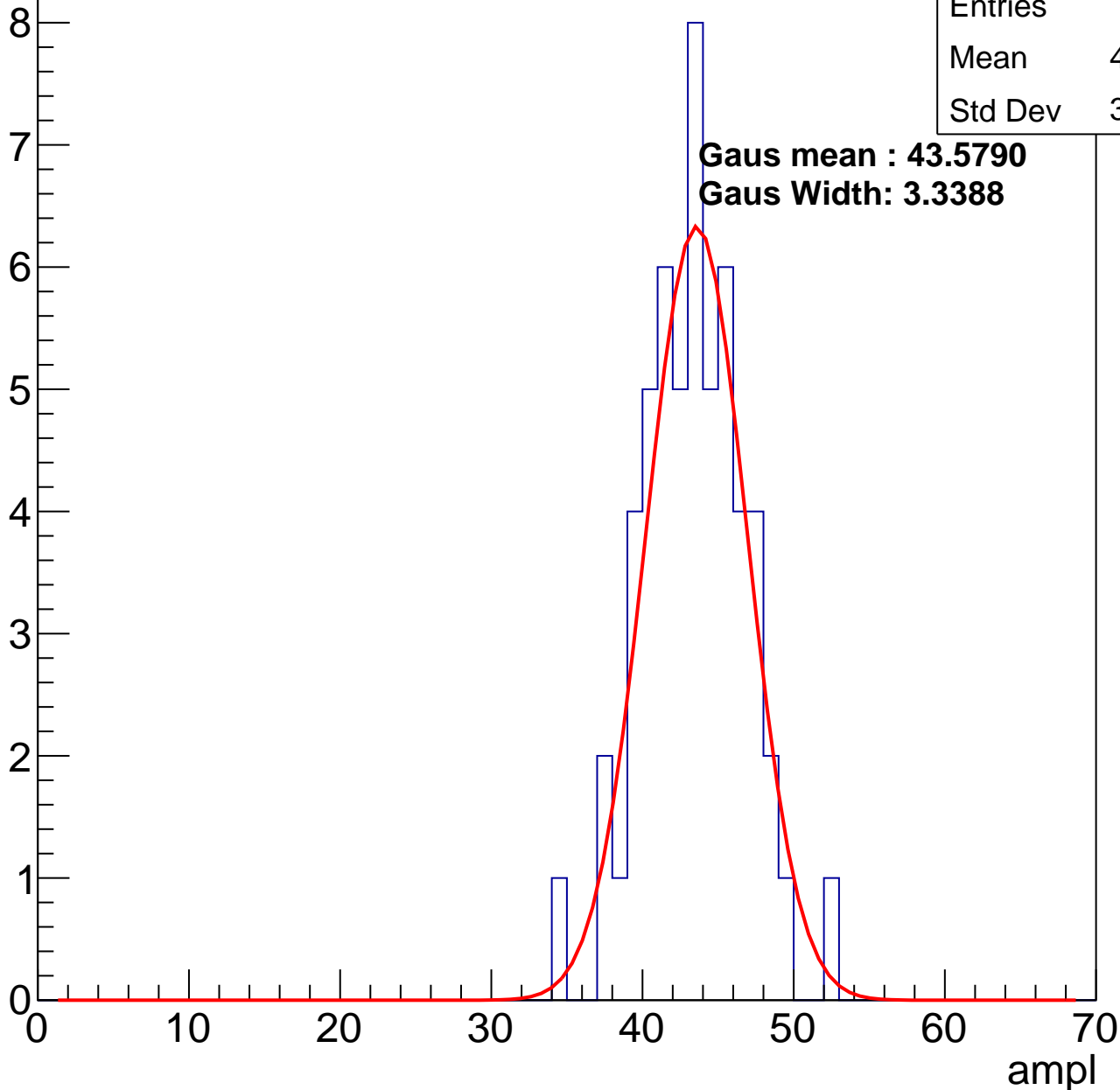
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.93
Std Dev	3.335

Gaus mean : 43.5790

Gaus Width: 3.3388

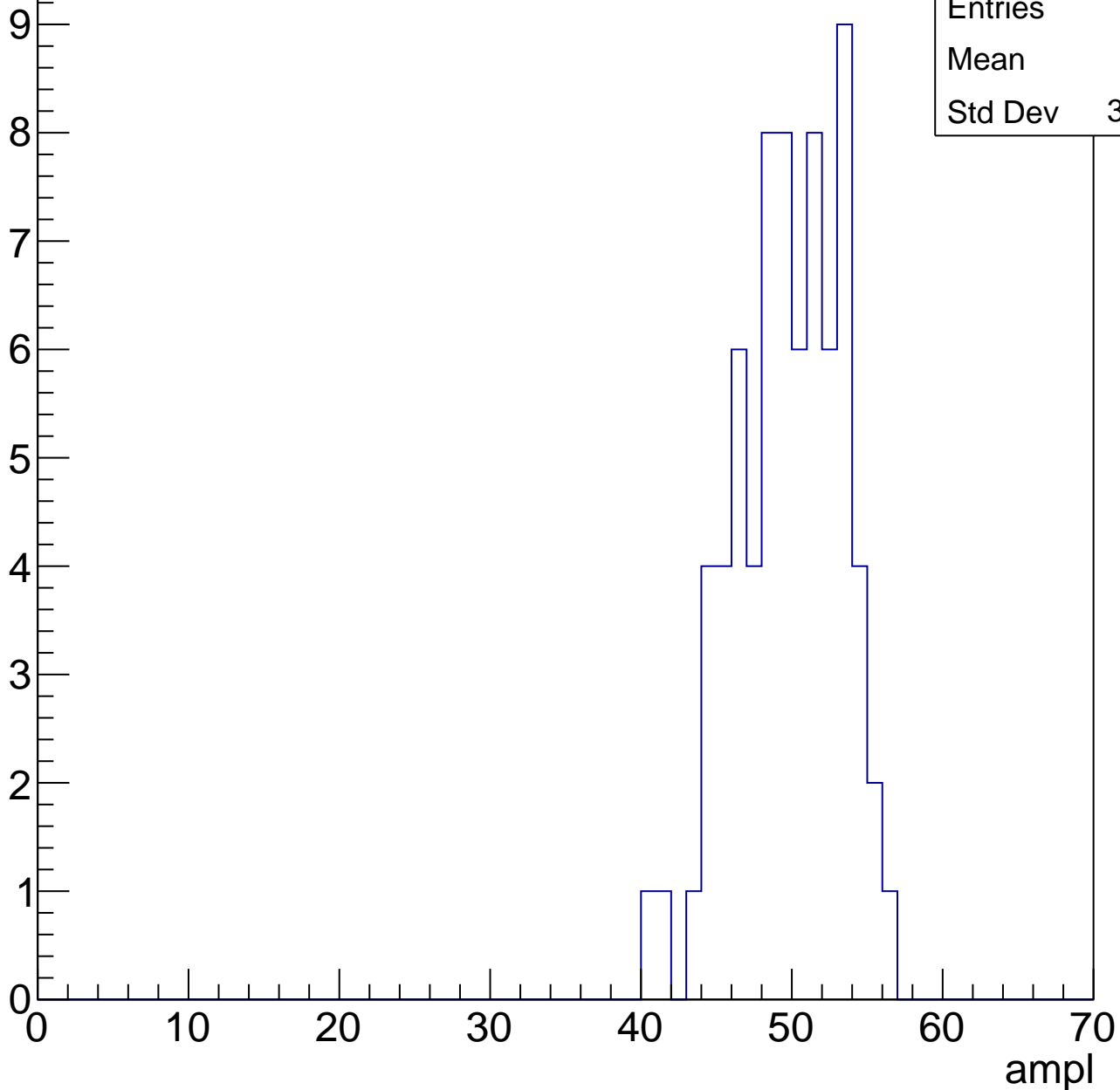


B1L100S, U10-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.3
Std Dev	3.459

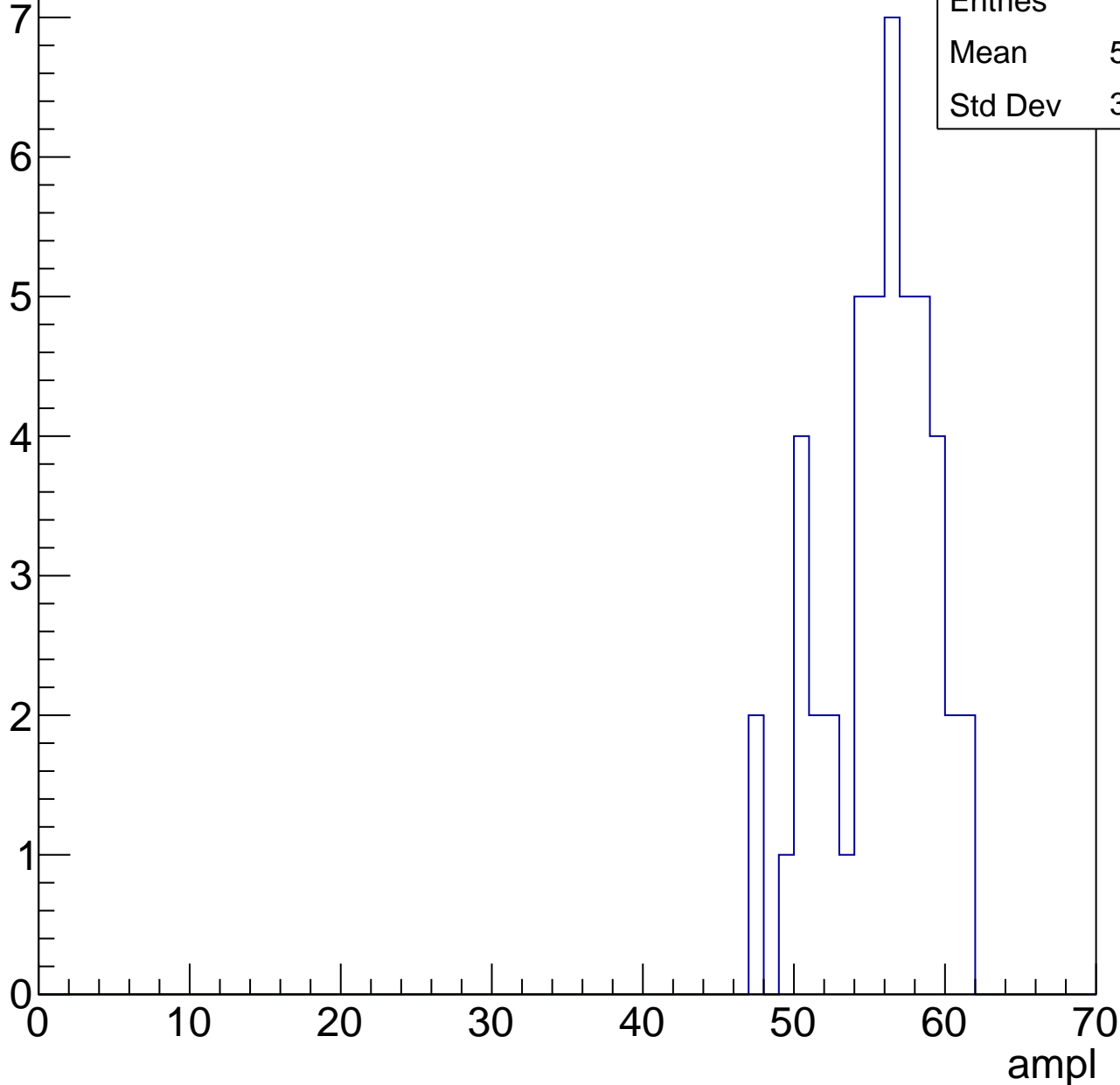


B1L100S, U10-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

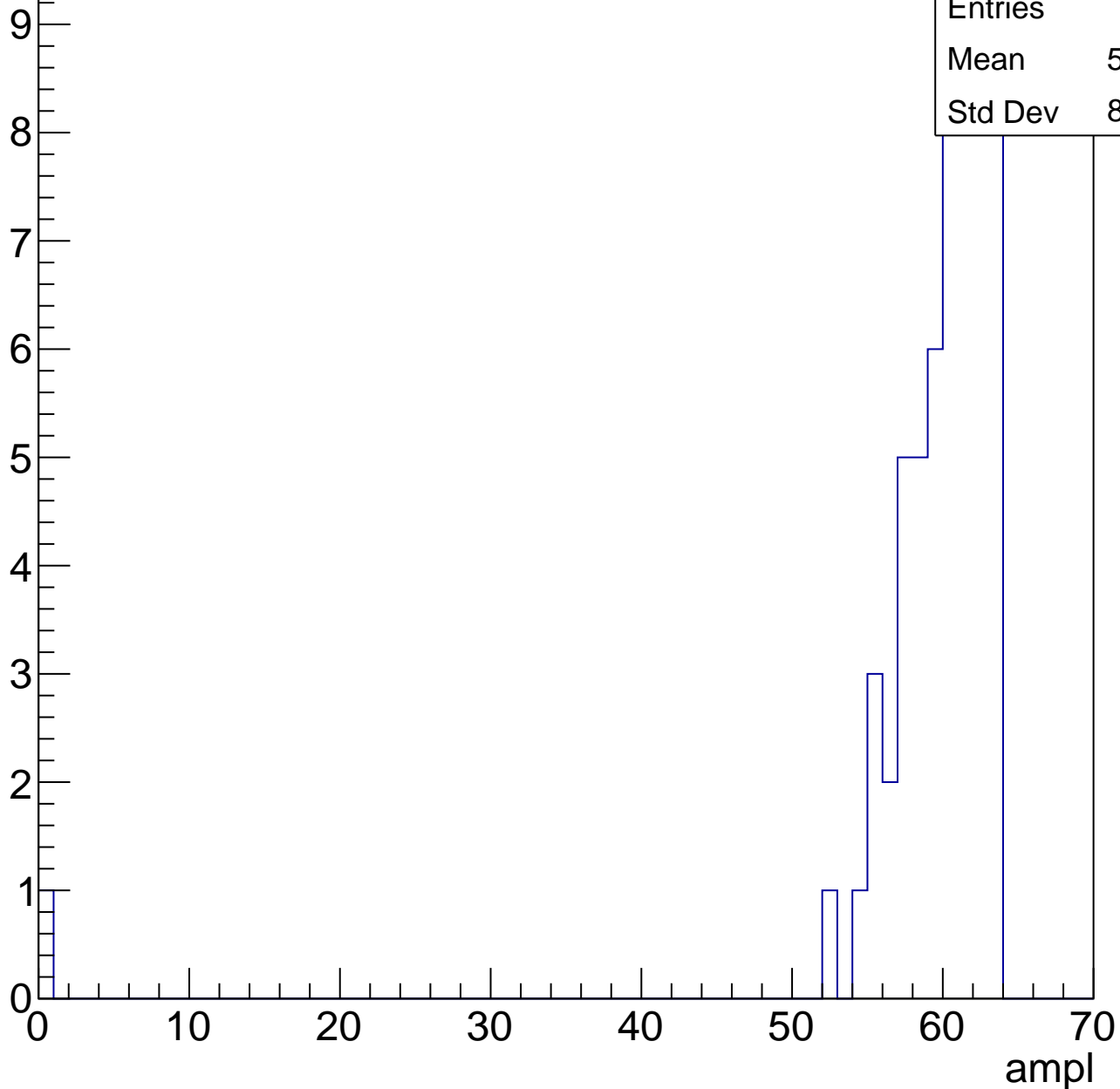
Entries	47
Mean	55.15
Std Dev	3.513



B1L100S, U10-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch51, adc0

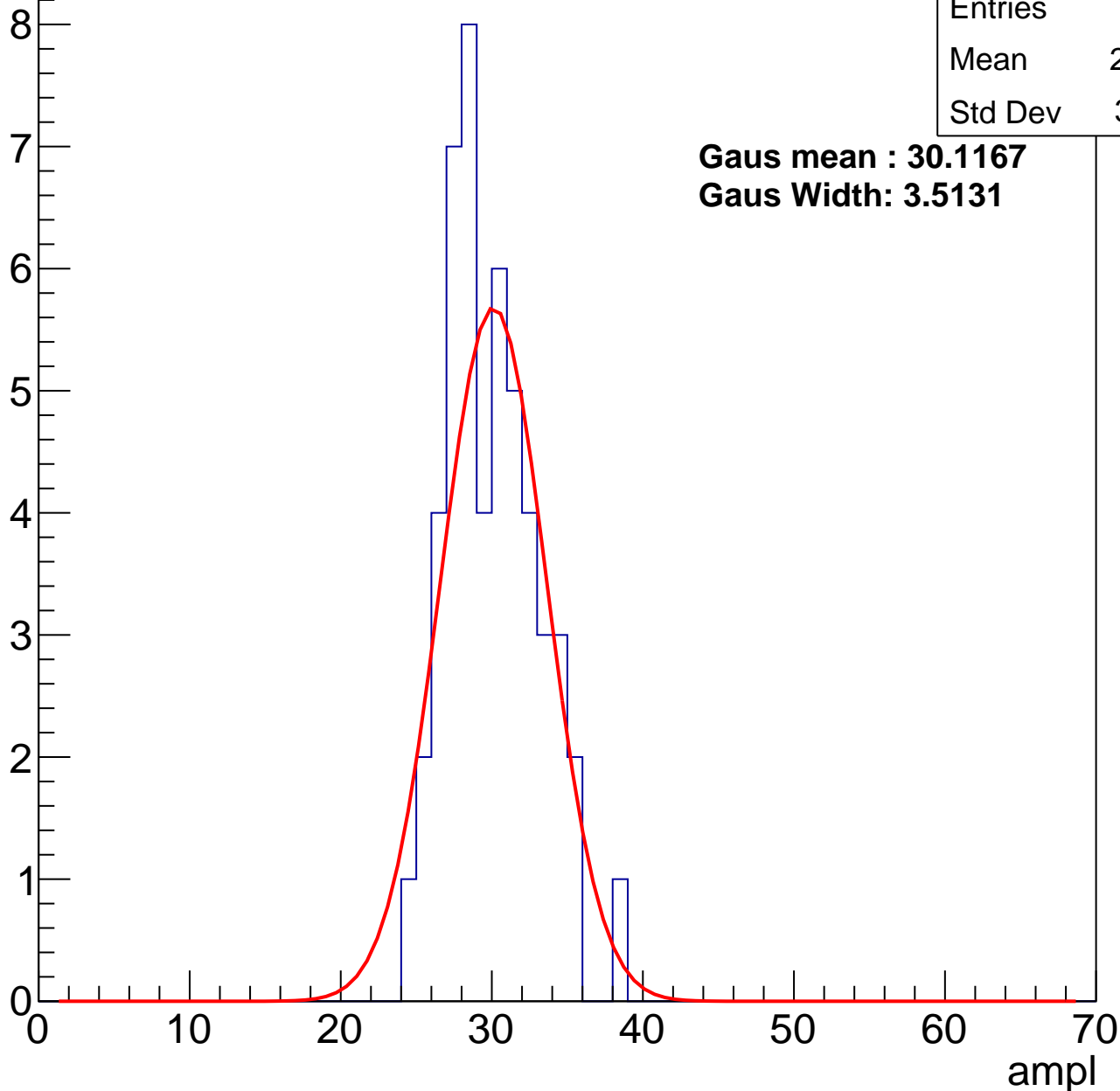
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	29.58
Std Dev	3.001

Gaus mean : 30.1167

Gaus Width: 3.5131



B1L100S, U10-ch51, adc1

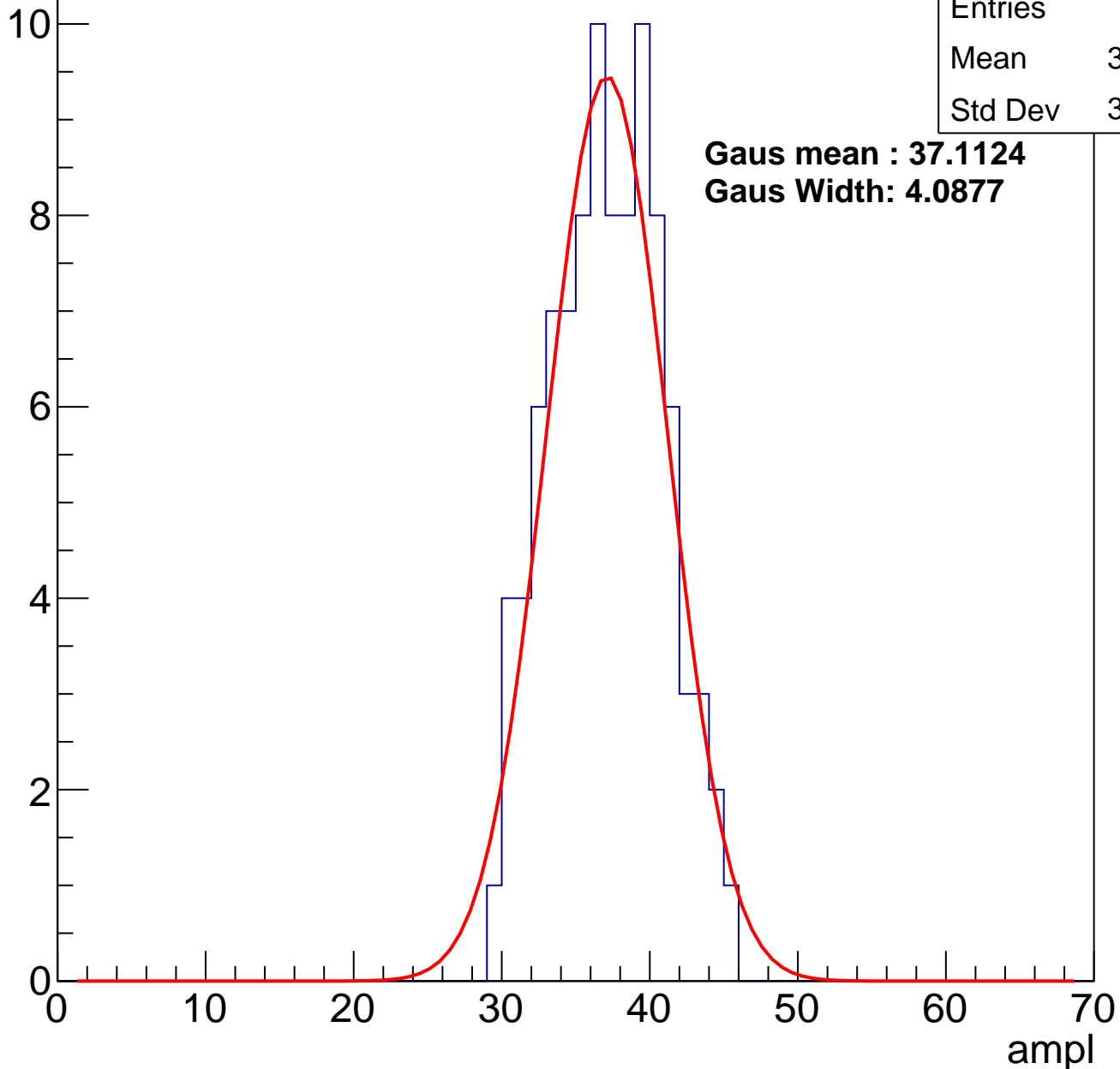
calib_packv5_042523_0143.root, FC#4, port A2

Entries	96
Mean	36.65
Std Dev	3.708

Gaus mean : 37.1124

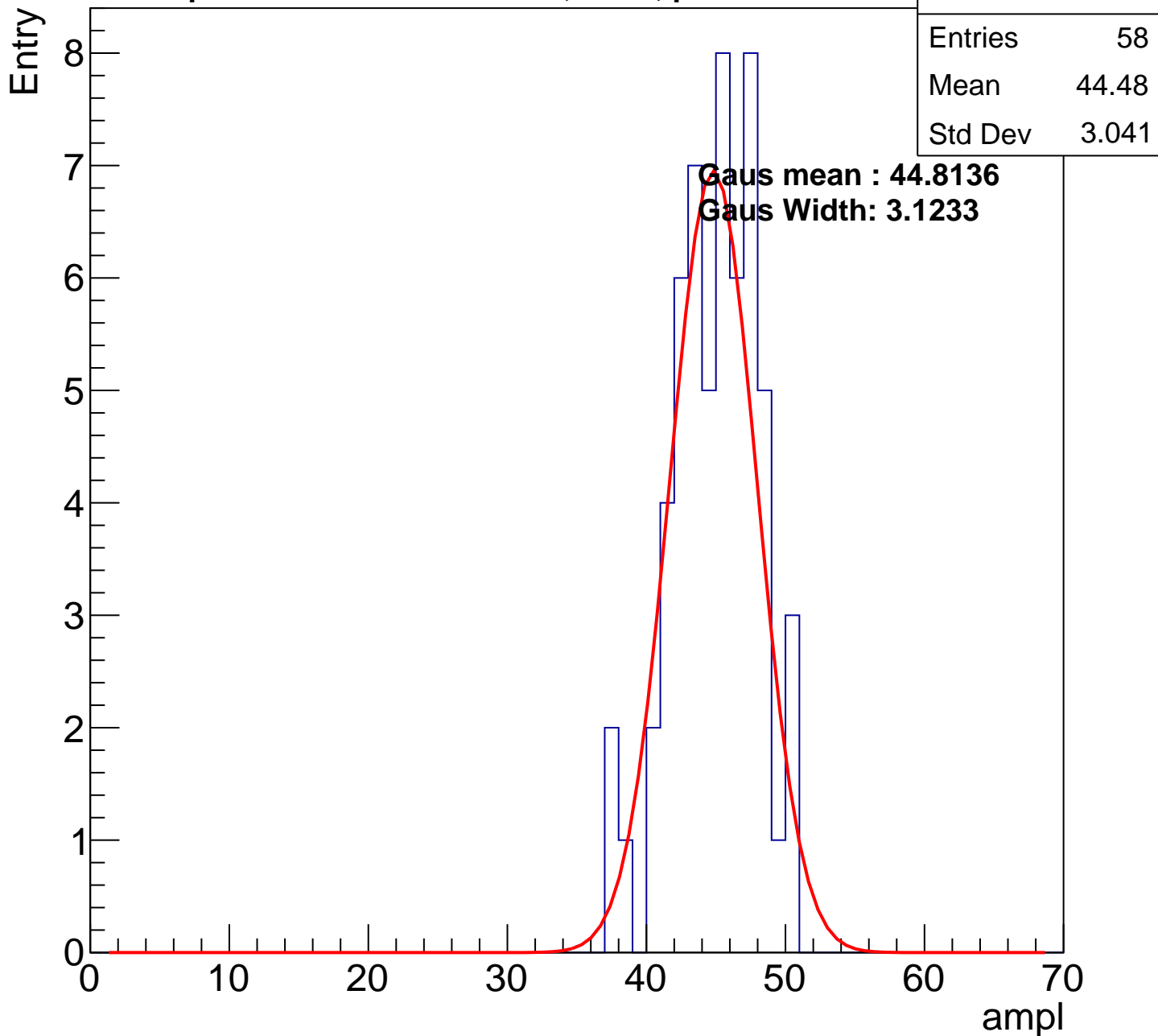
Gaus Width: 4.0877

Entry



B1L100S, U10-ch51, adc2

calib_packv5_042523_0143.root, FC#4, port A2

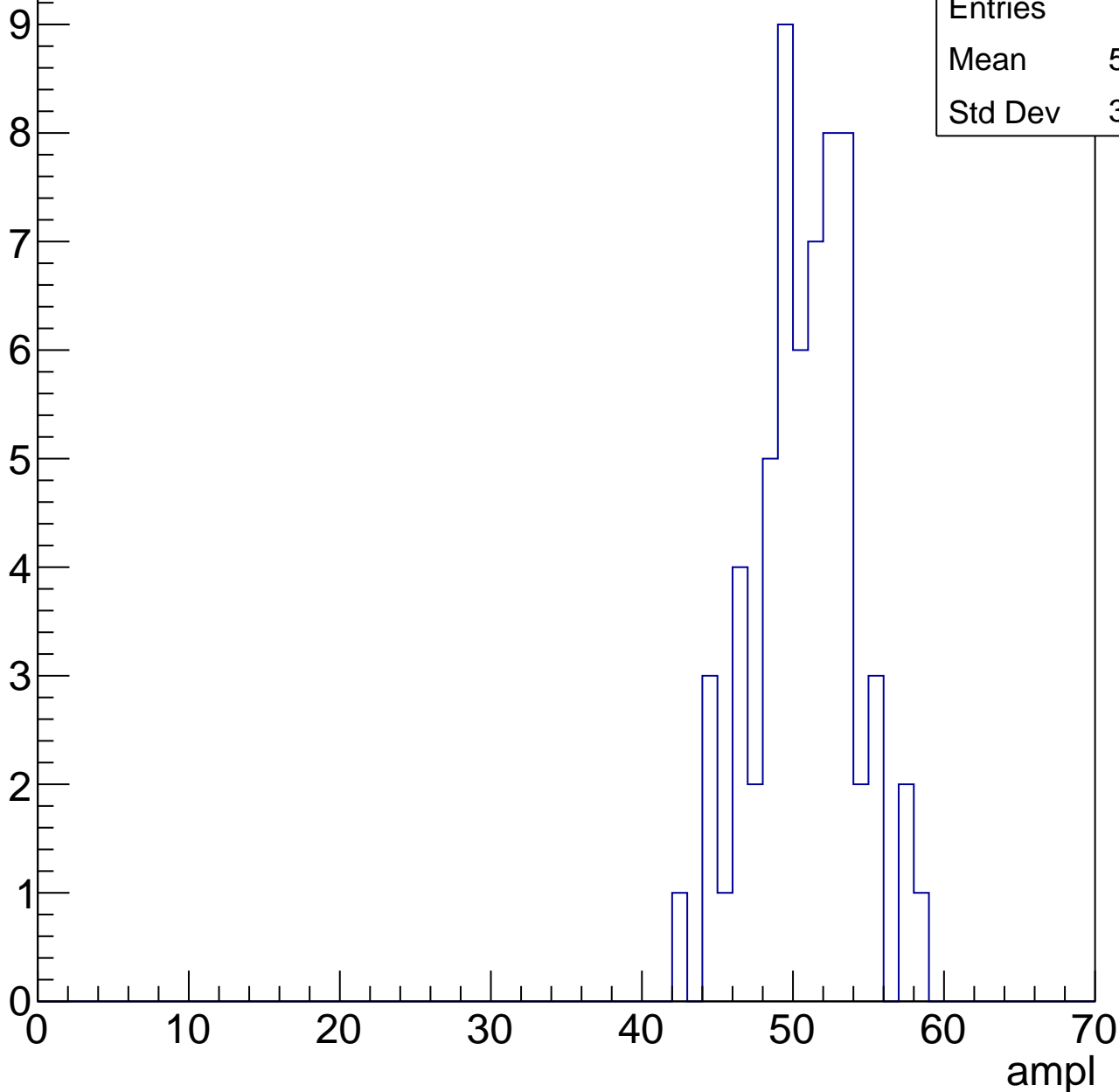


B1L100S, U10-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.32
Std Dev	3.316

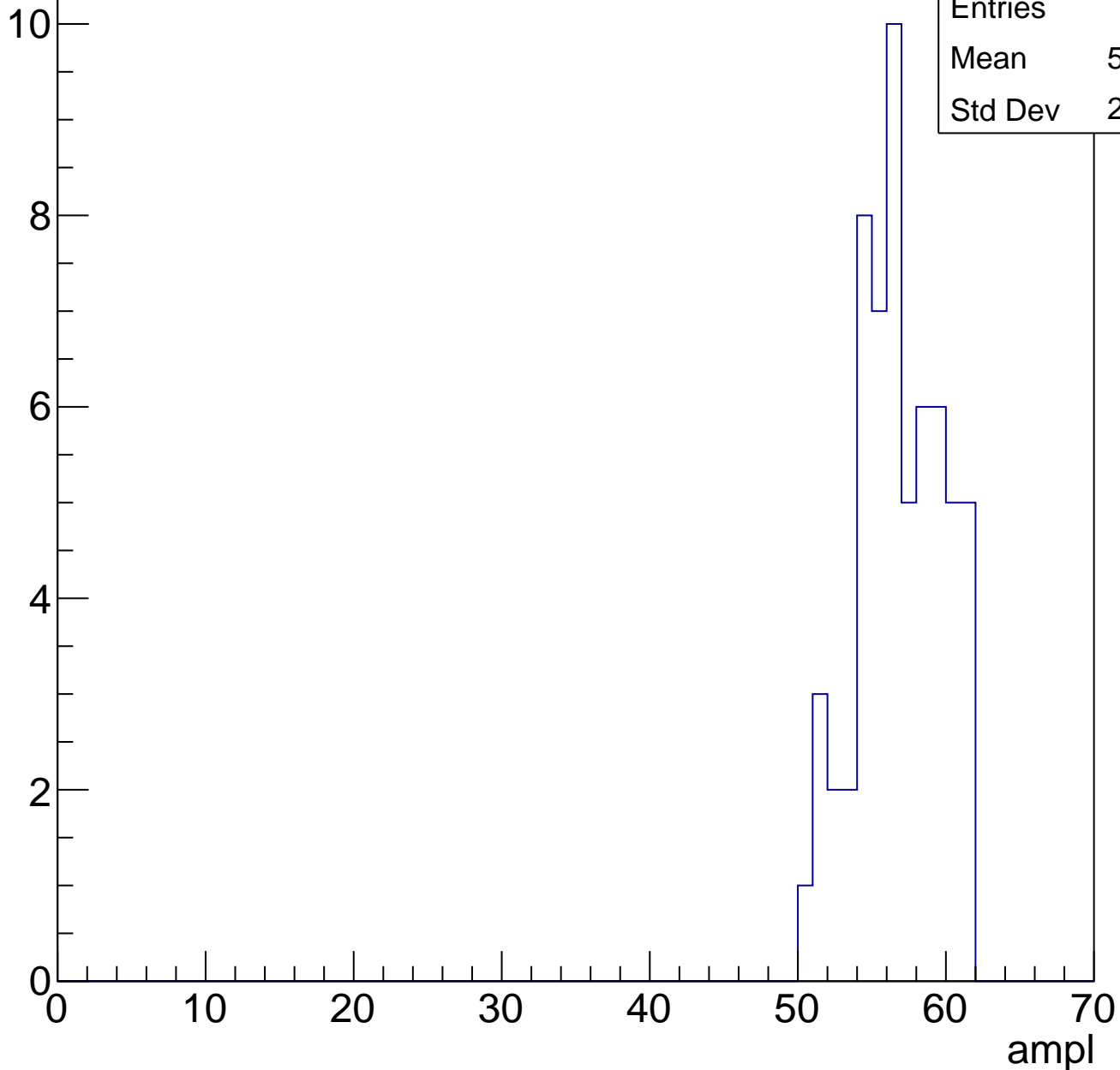


B1L100S, U10-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	56.37
Std Dev	2.828

Entry



B1L100S, U10-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	39
Mean	59.23
Std Dev	9.789

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L100S, U10-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch52, adc0

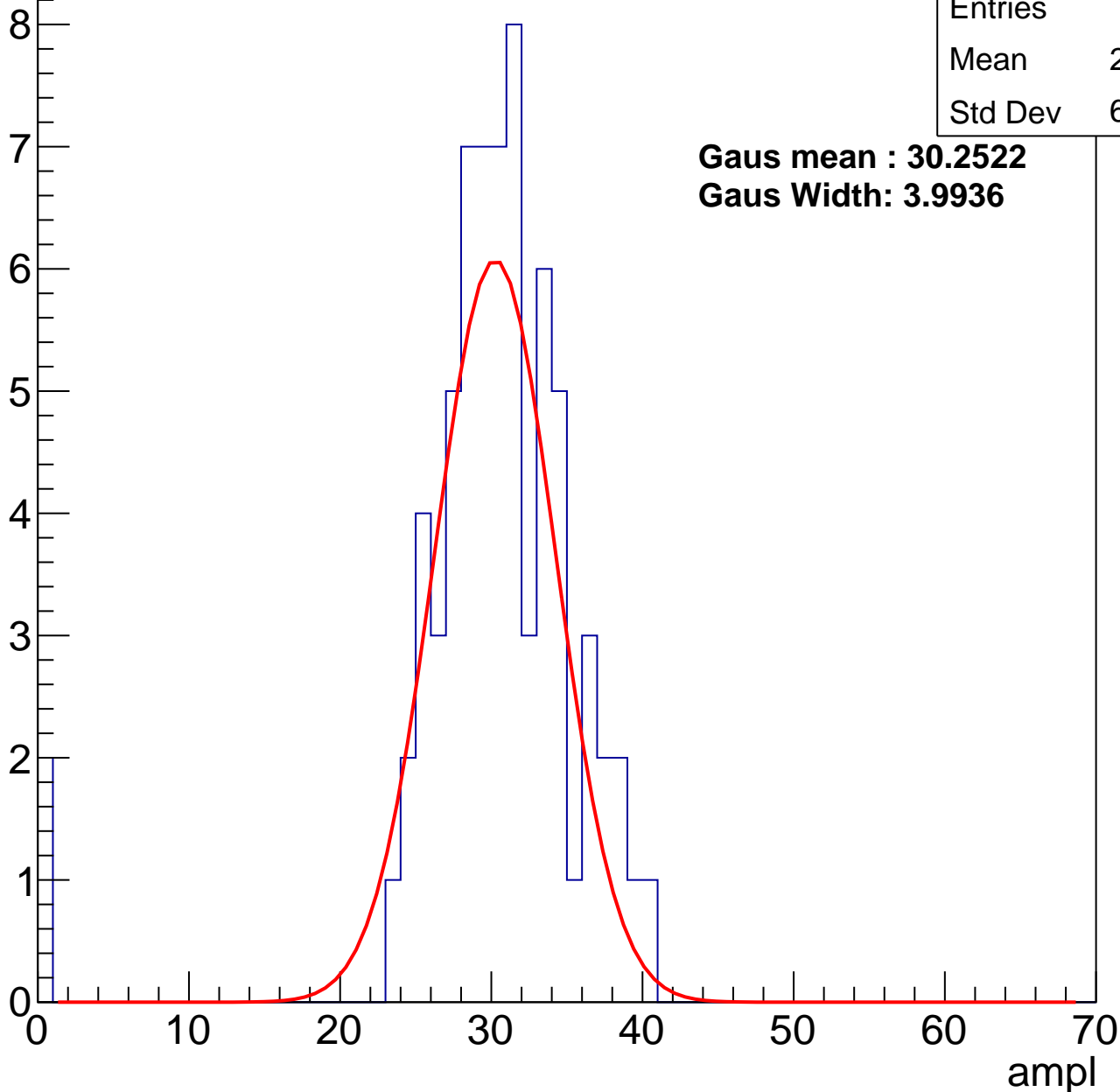
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.67
Std Dev	6.364

Gaus mean : 30.2522

Gaus Width: 3.9936



B1L100S, U10-ch52, adc1

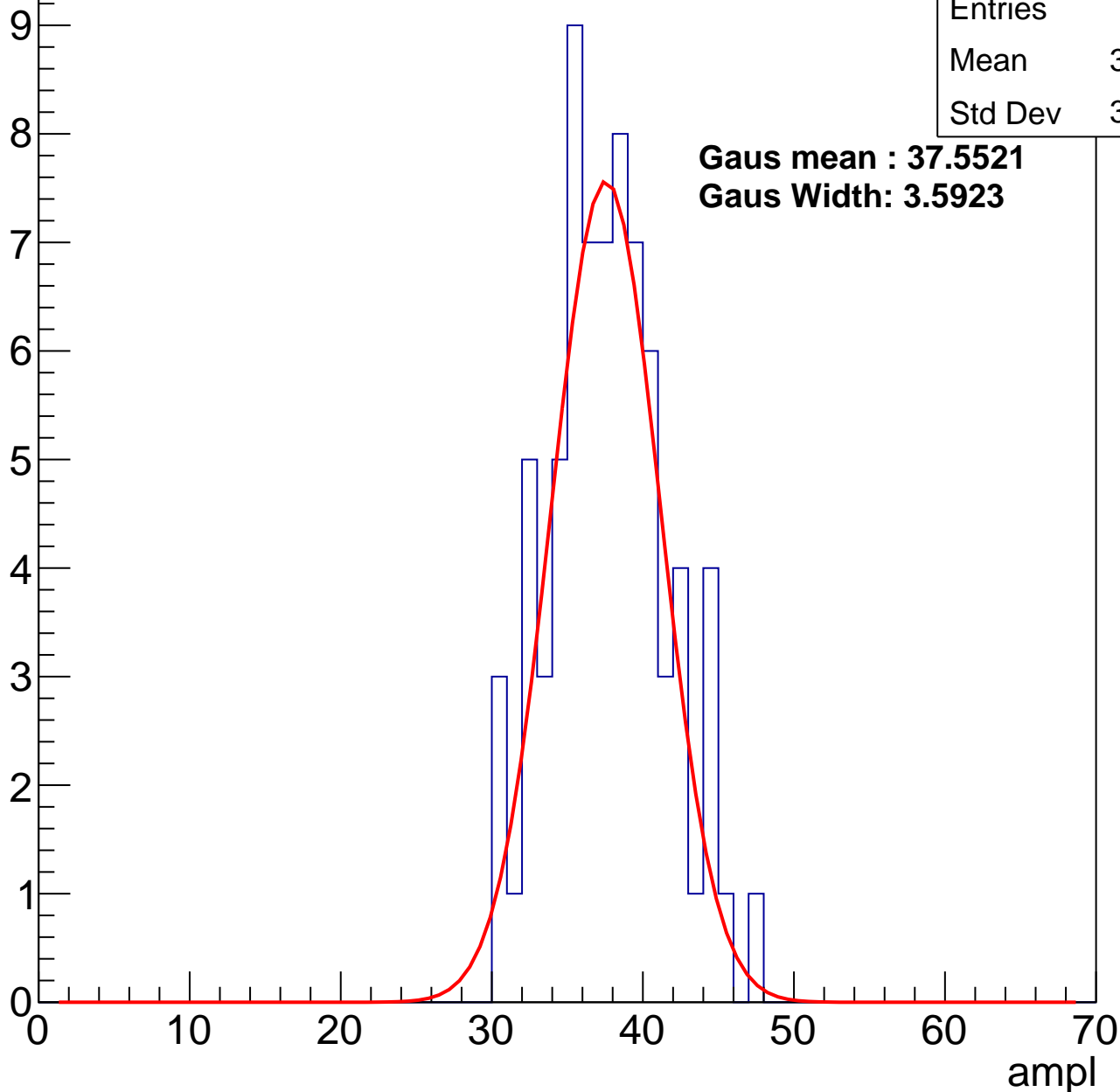
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.27
Std Dev	3.782

Gaus mean : 37.5521

Gaus Width: 3.5923



B1L100S, U10-ch52, adc2

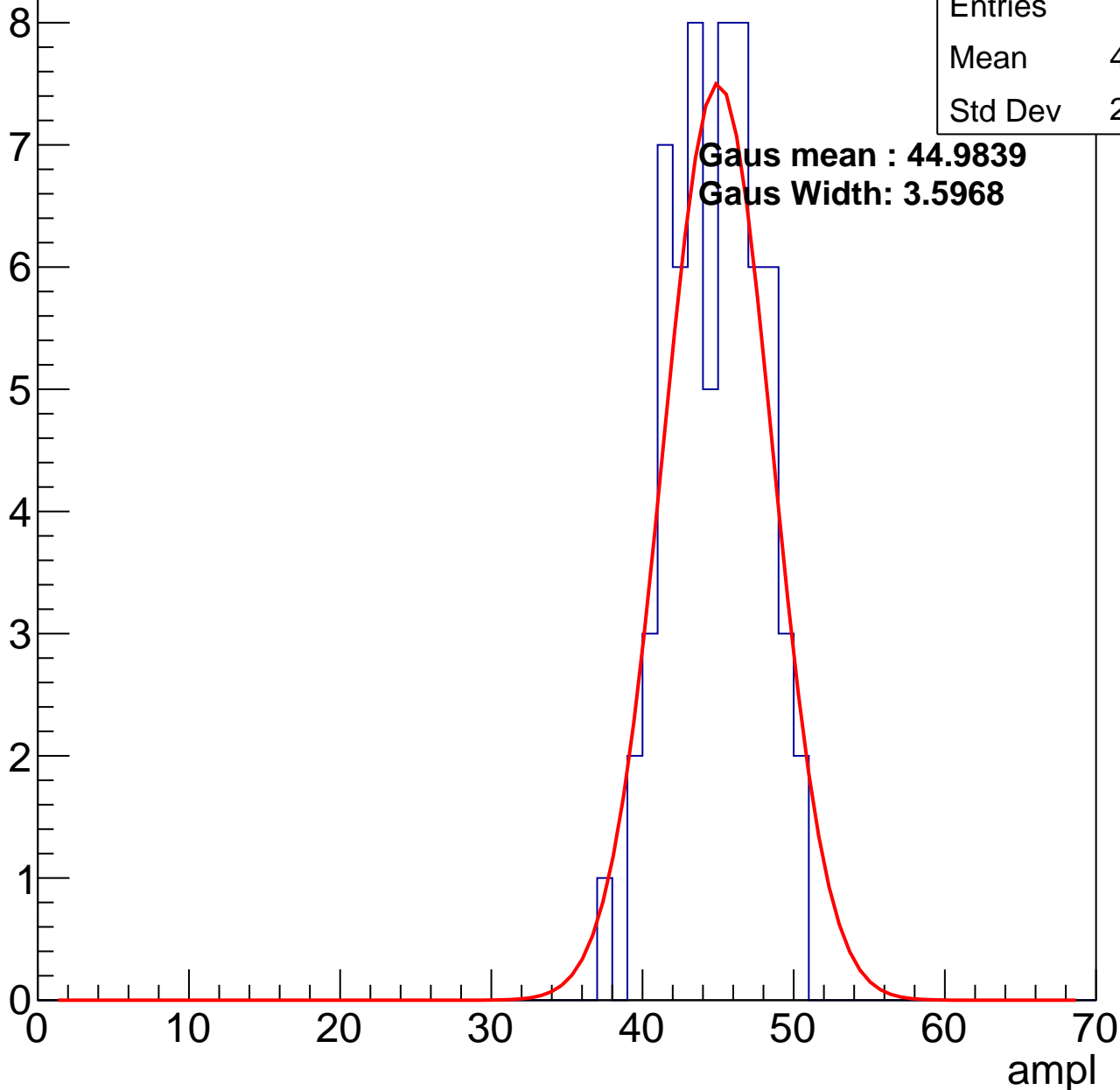
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.35
Std Dev	2.964

Gaus mean : 44.9839

Gaus Width: 3.5968

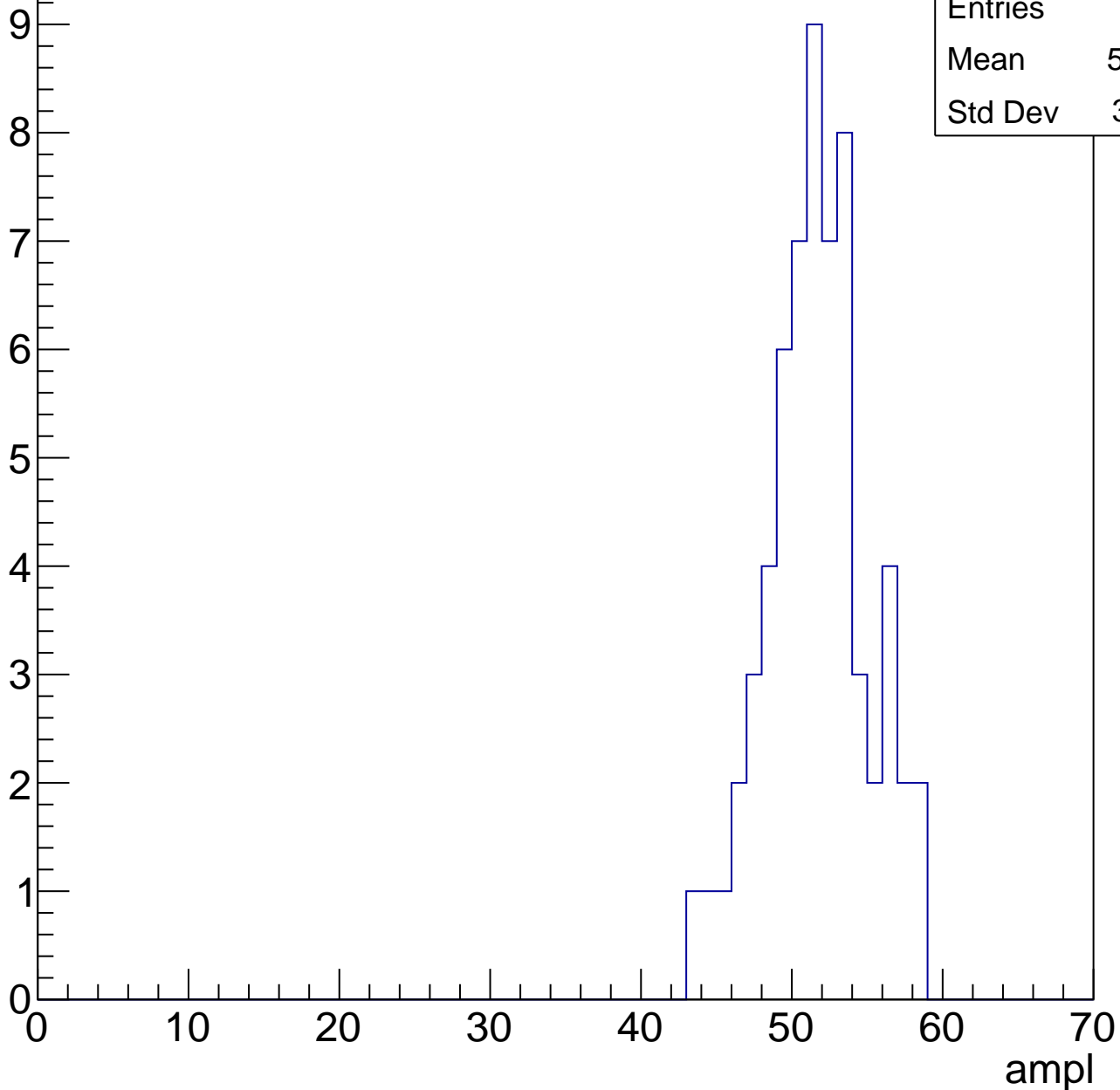


B1L100S, U10-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

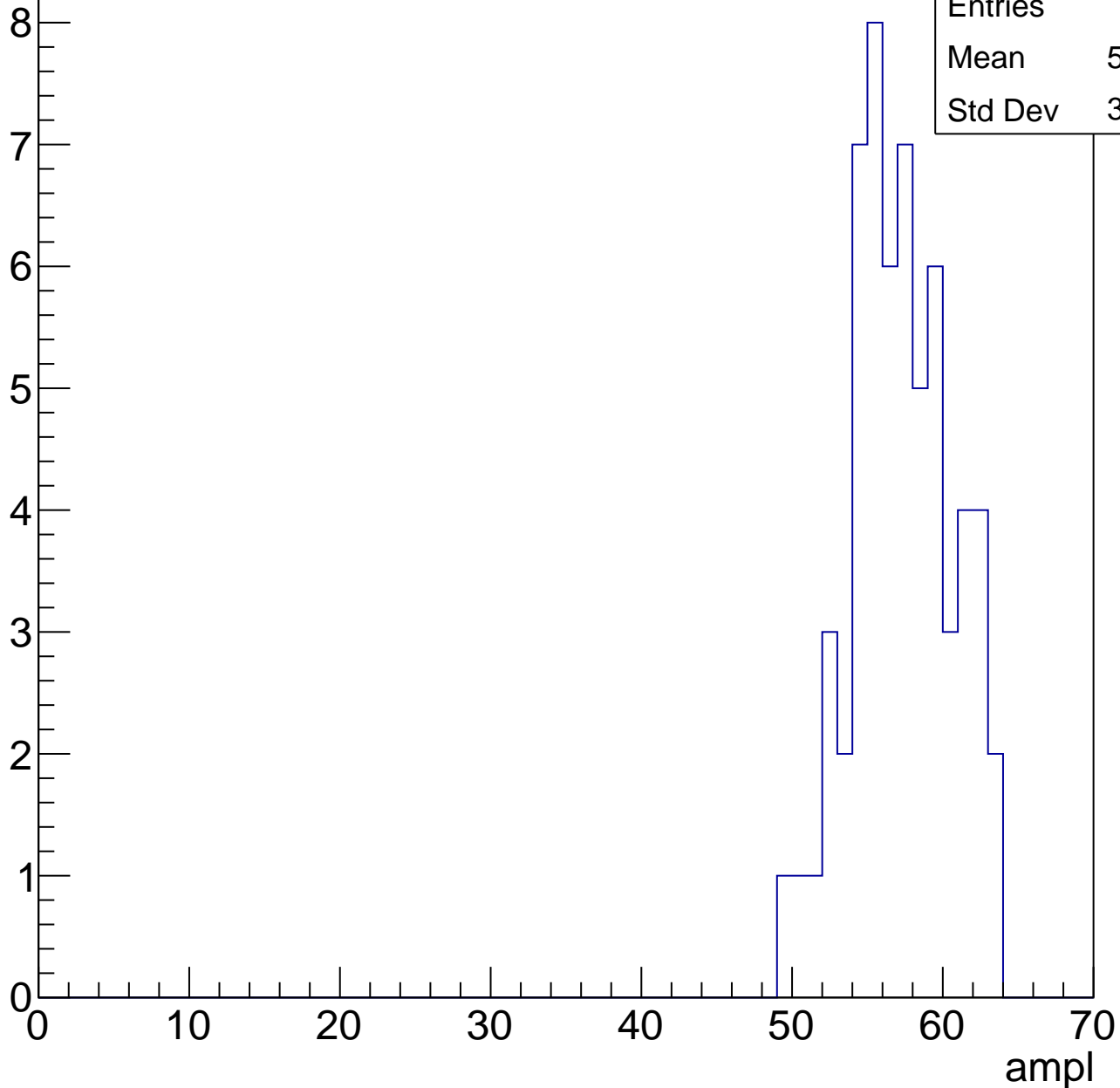
Entries	62
Mean	51.19
Std Dev	3.311



B1L100S, U10-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 35

Mean 59.03

Std Dev 10.26

ampl

0

10

20

30

40

50

60

70

B1L100S, U10-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch53, adc0

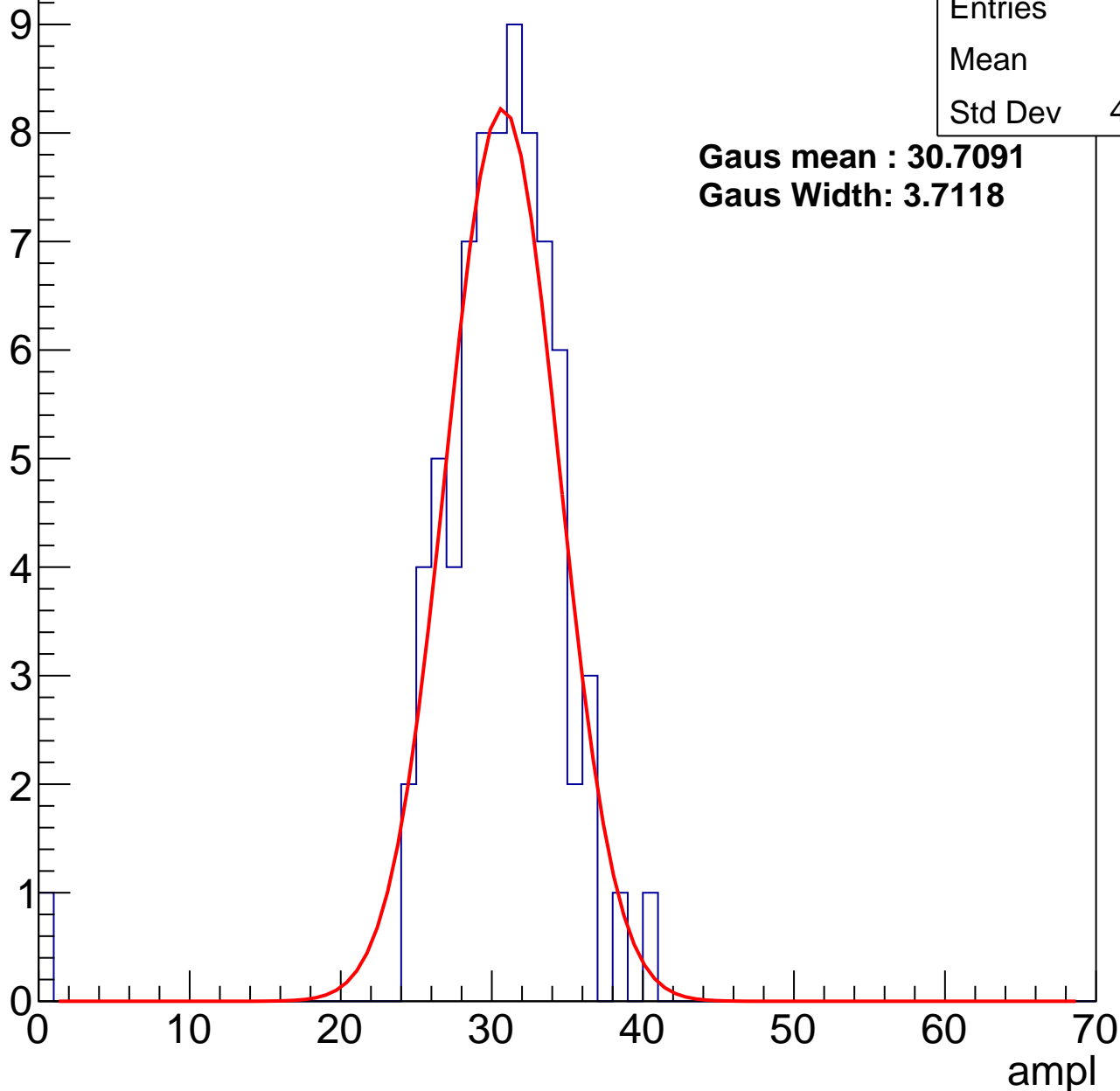
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30
Std Dev	4.793

Gaus mean : 30.7091

Gaus Width: 3.7118



B1L100S, U10-ch53, adc1

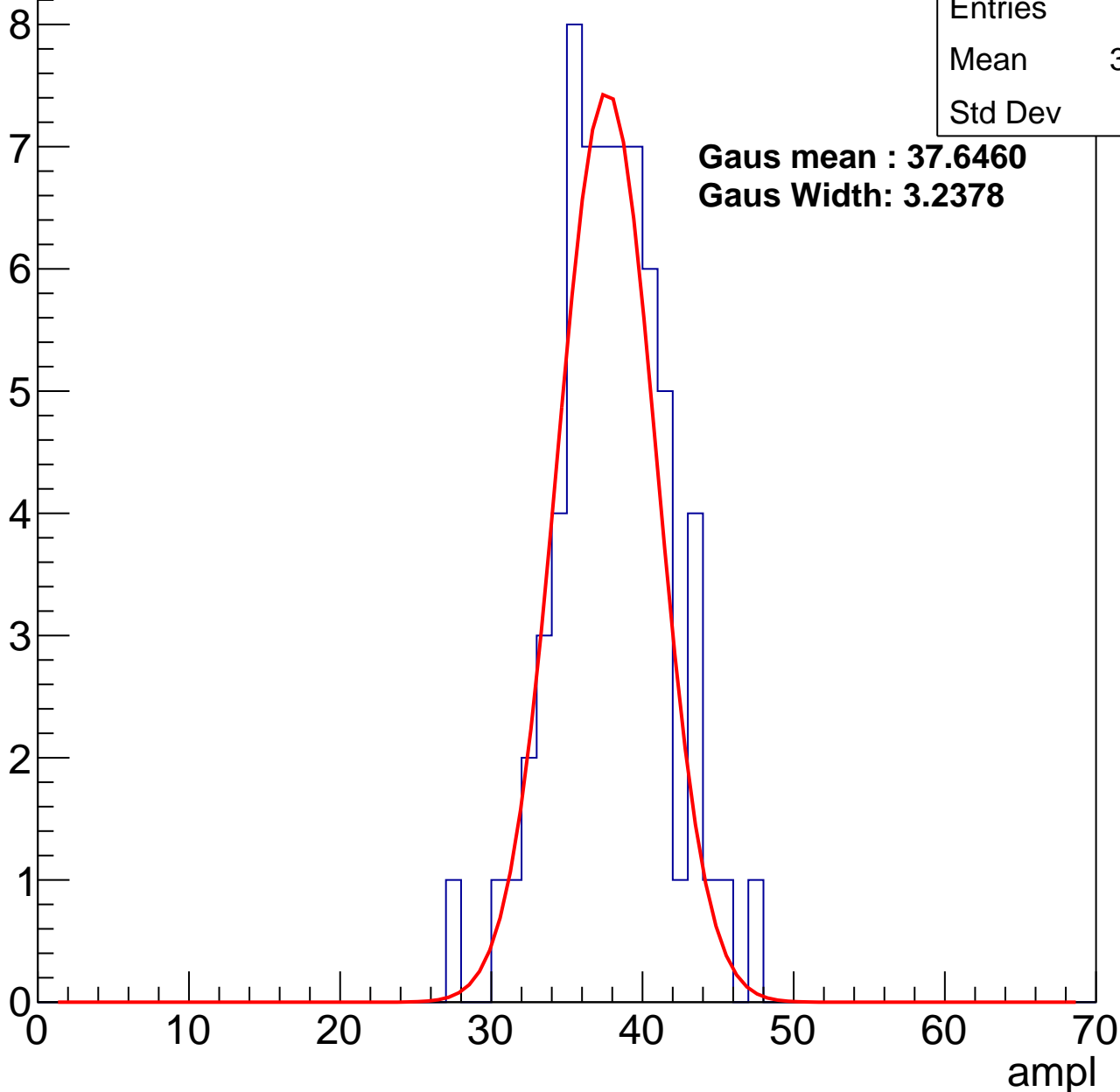
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.49
Std Dev	3.65

Gaus mean : 37.6460

Gaus Width: 3.2378



B1L100S, U10-ch53, adc2

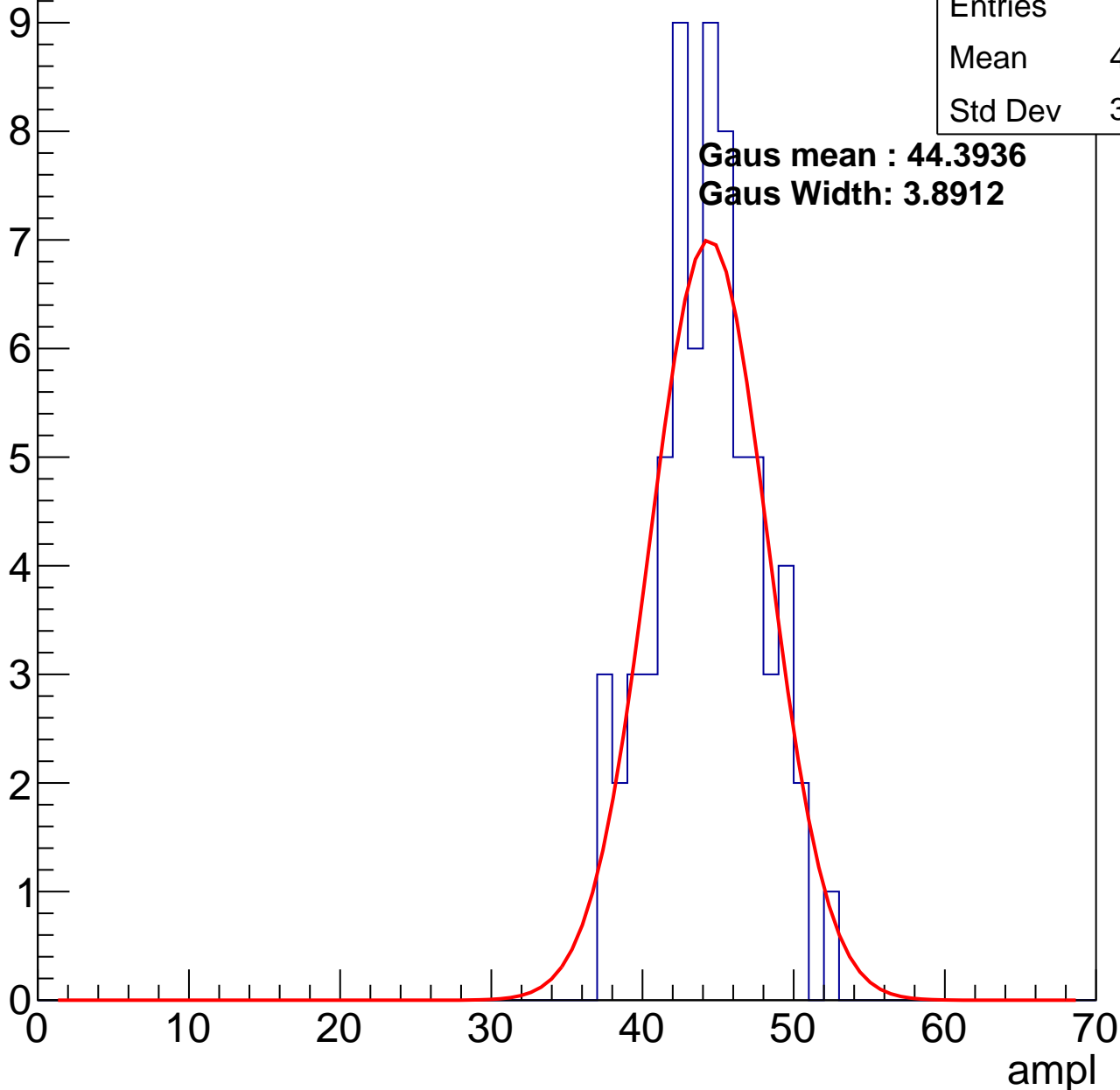
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.79
Std Dev	3.394

Gaus mean : 44.3936

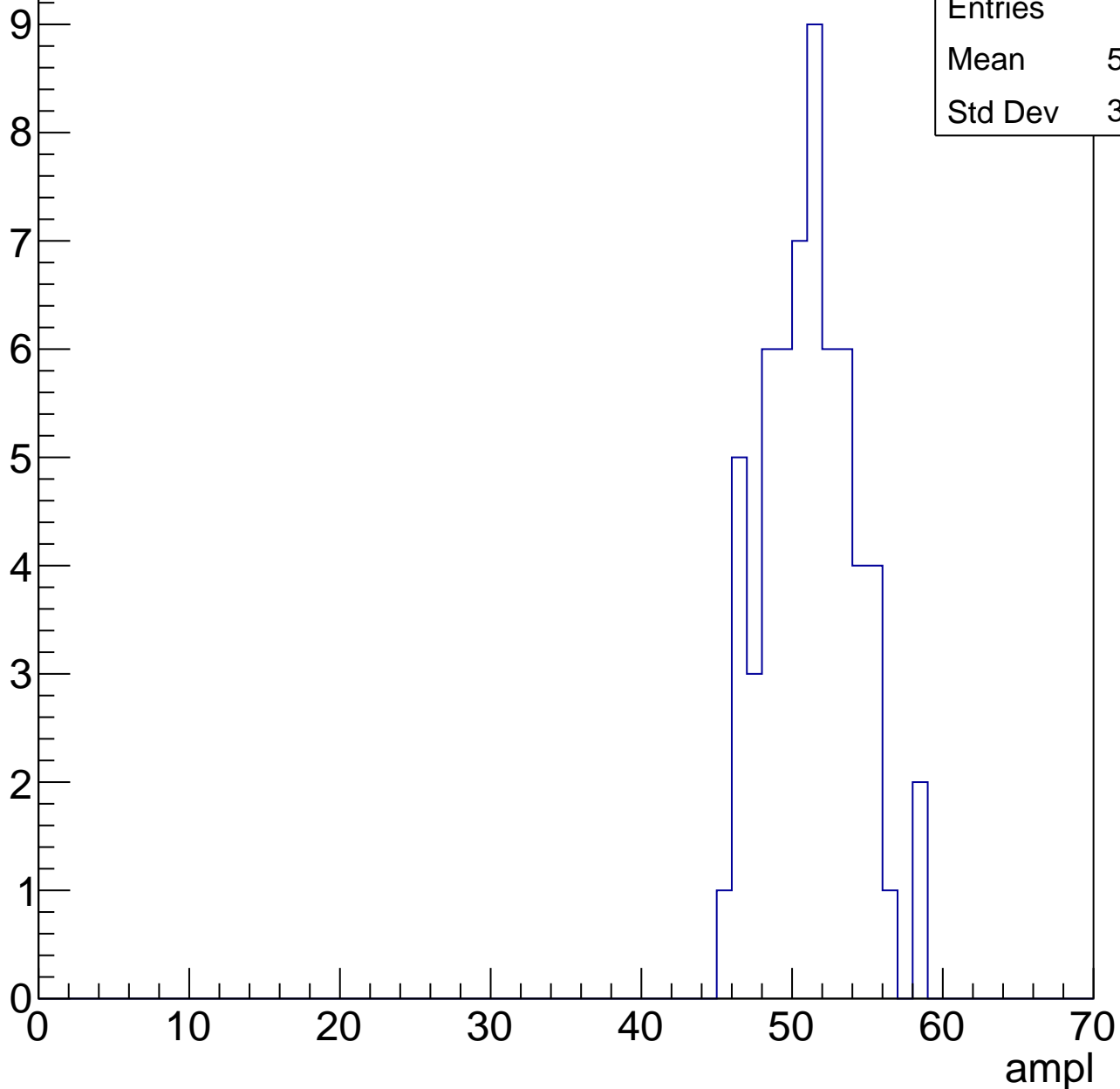
Gaus Width: 3.8912



B1L100S, U10-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



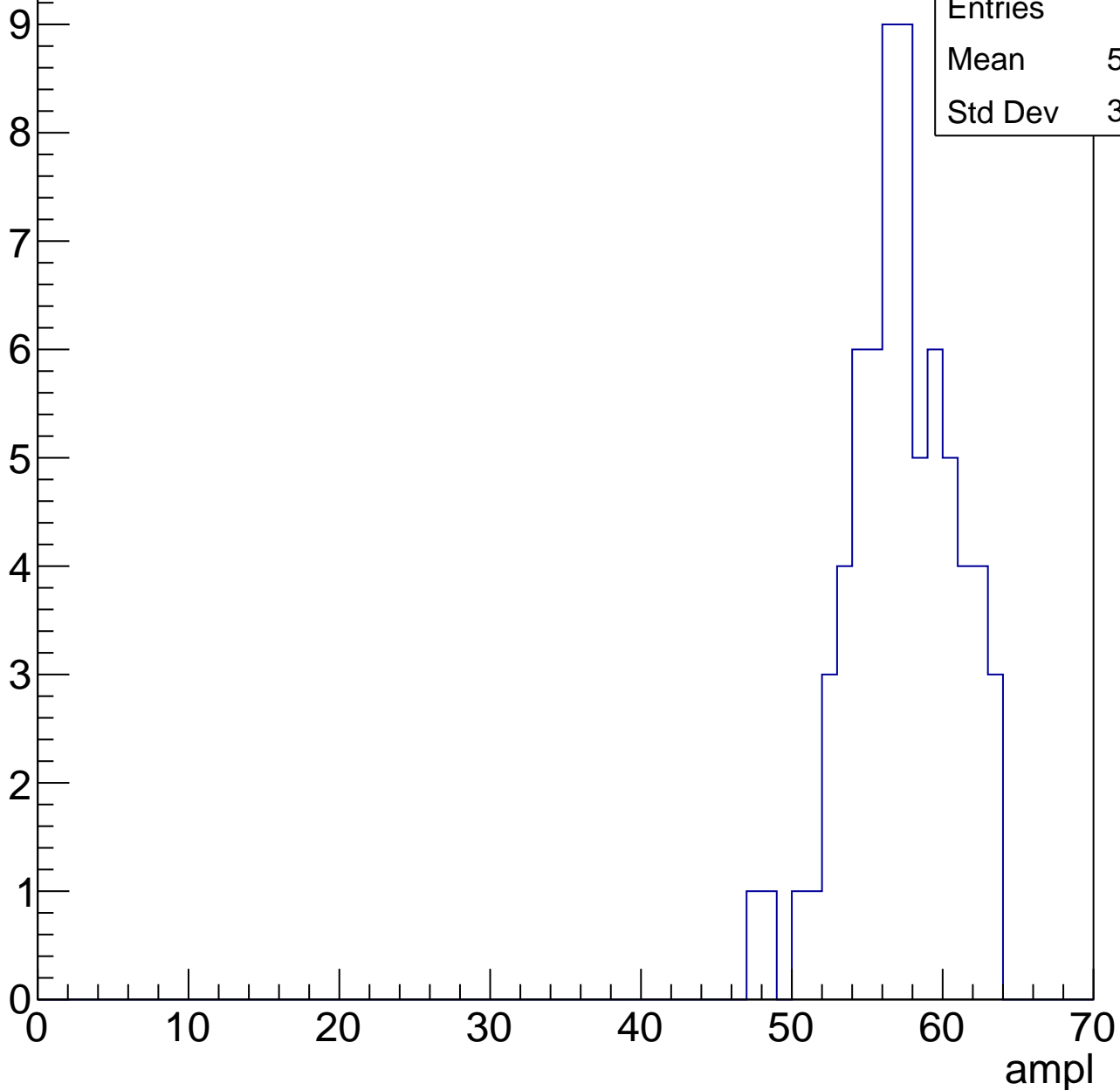
Entries	60
Mean	50.75
Std Dev	3.009

B1L100S, U10-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

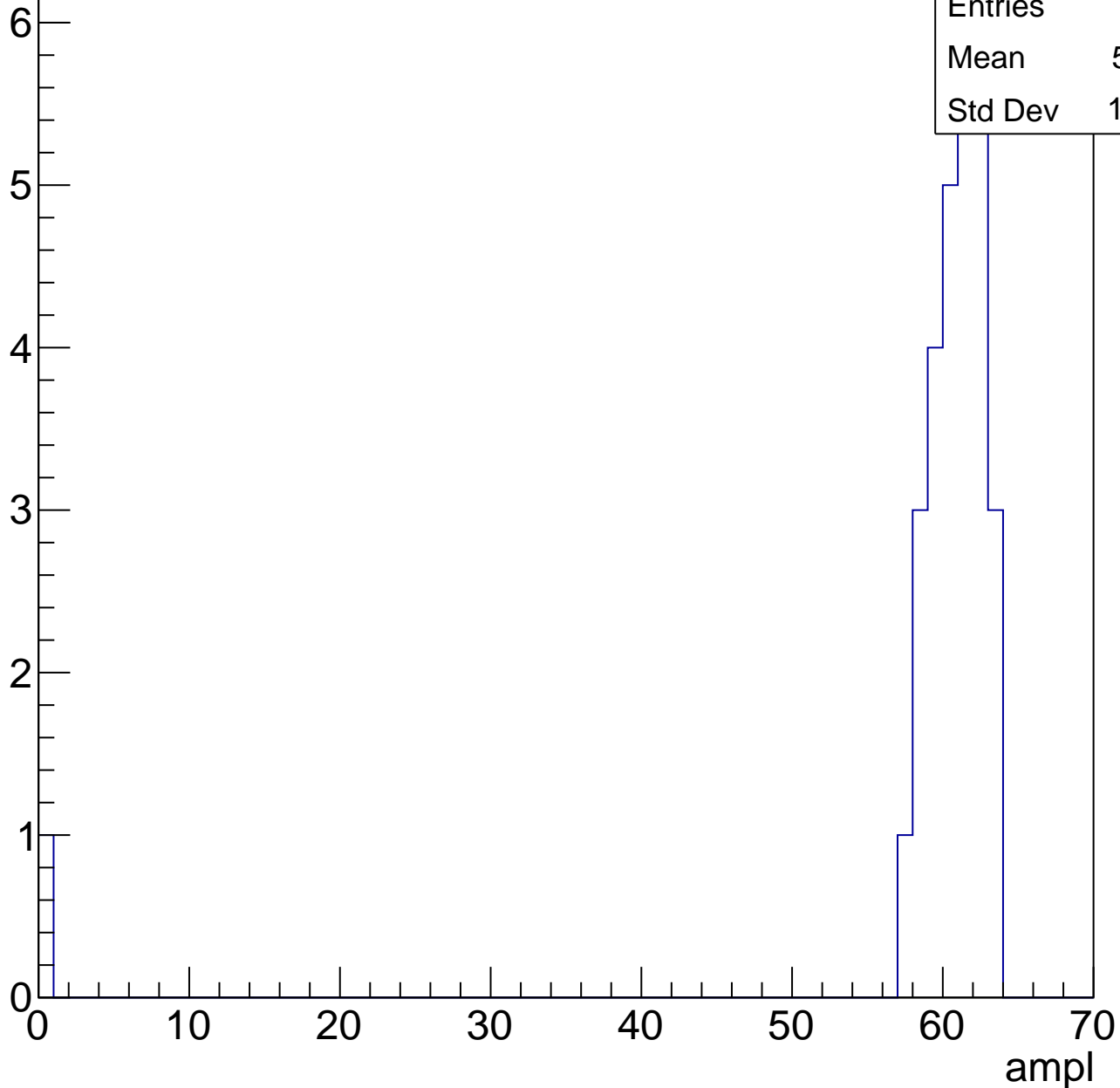
Entries	68
Mean	56.76
Std Dev	3.503



B1L100S, U10-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	29
Mean	58.41
Std Dev	11.16

B1L100S, U10-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch54, adc0

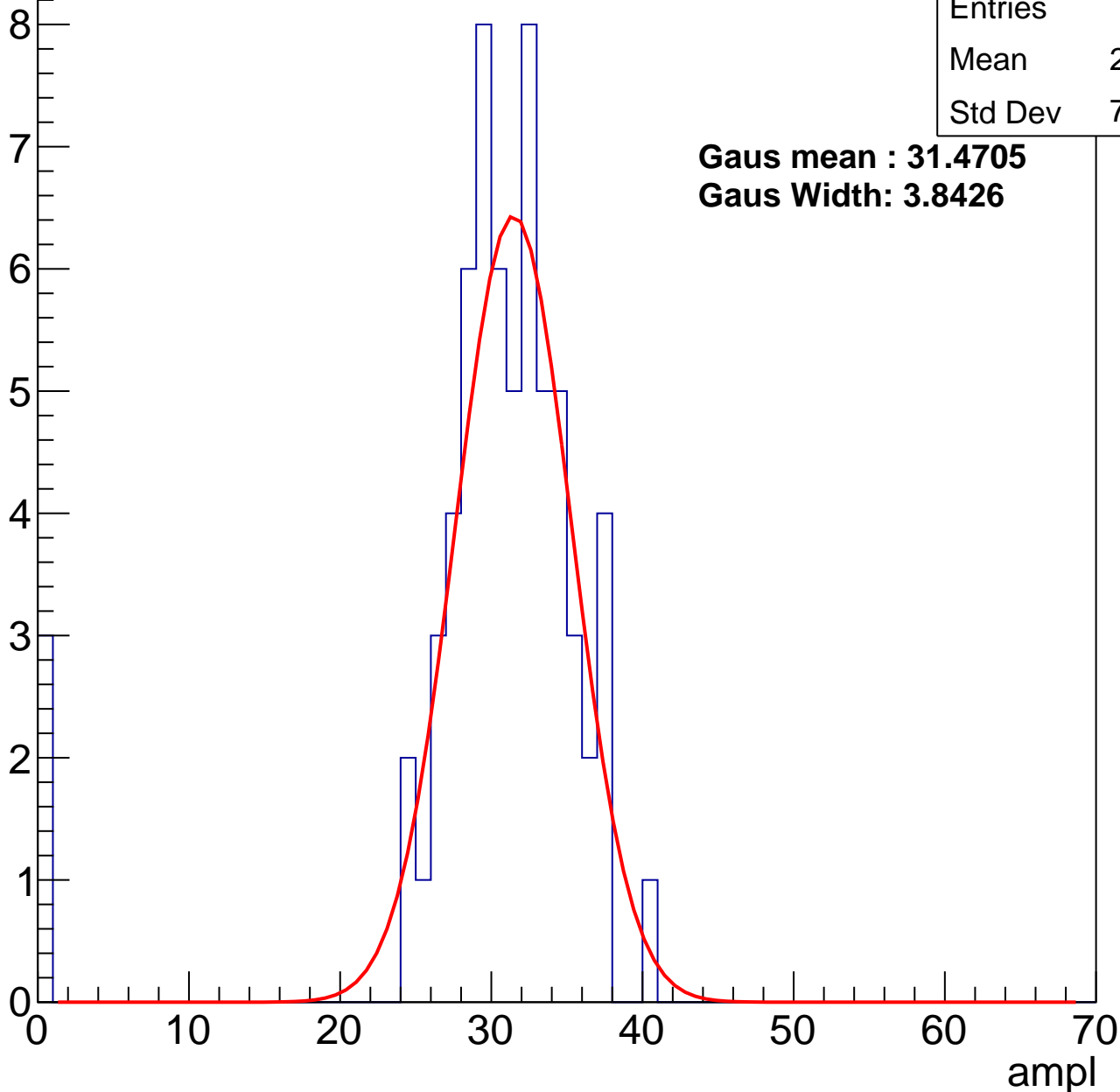
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.55
Std Dev	7.287

Gaus mean : 31.4705

Gaus Width: 3.8426



B1L100S, U10-ch54, adc1

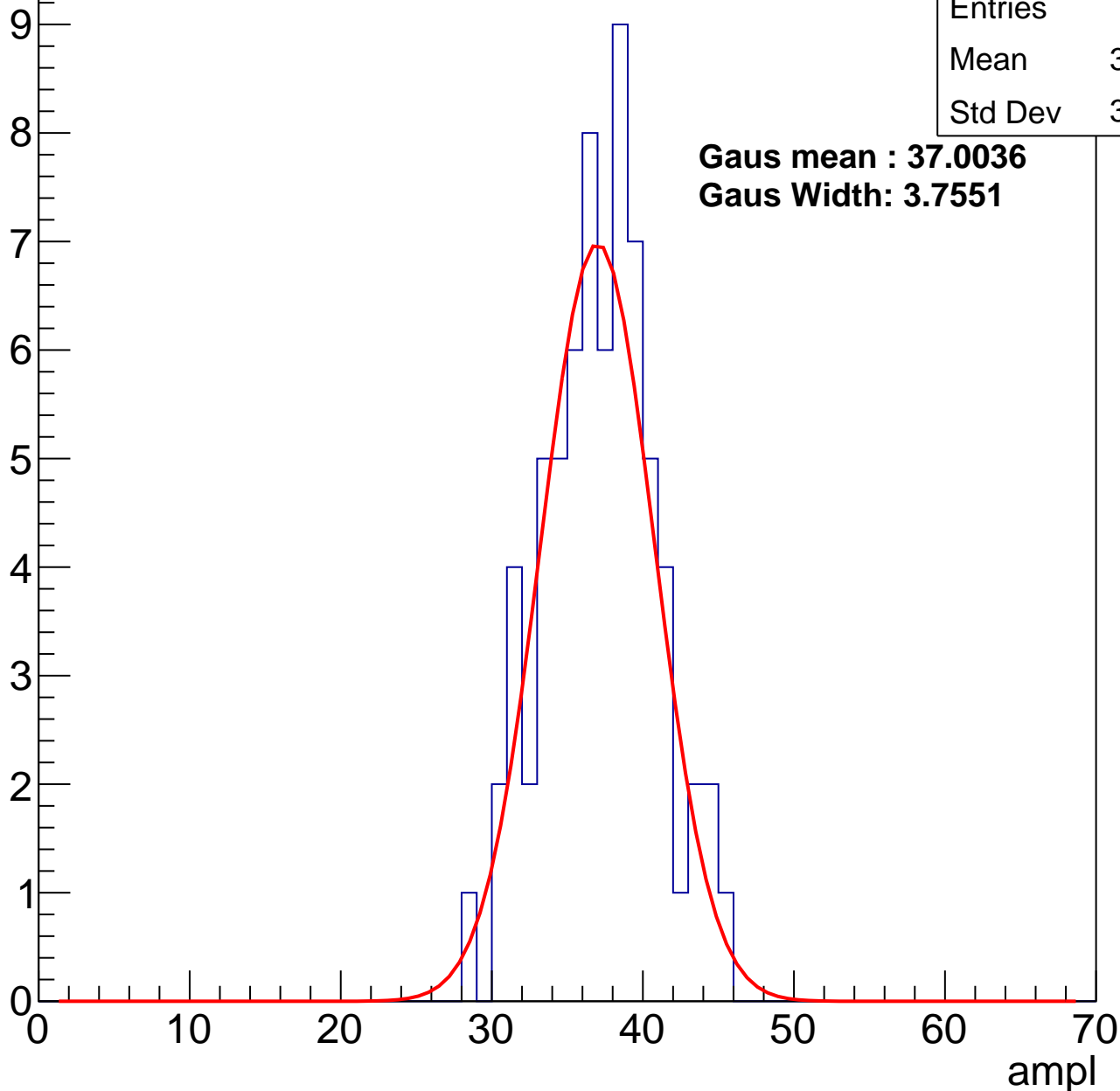
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.73
Std Dev	3.645

Gaus mean : 37.0036

Gaus Width: 3.7551



B1L100S, U10-ch54, adc2

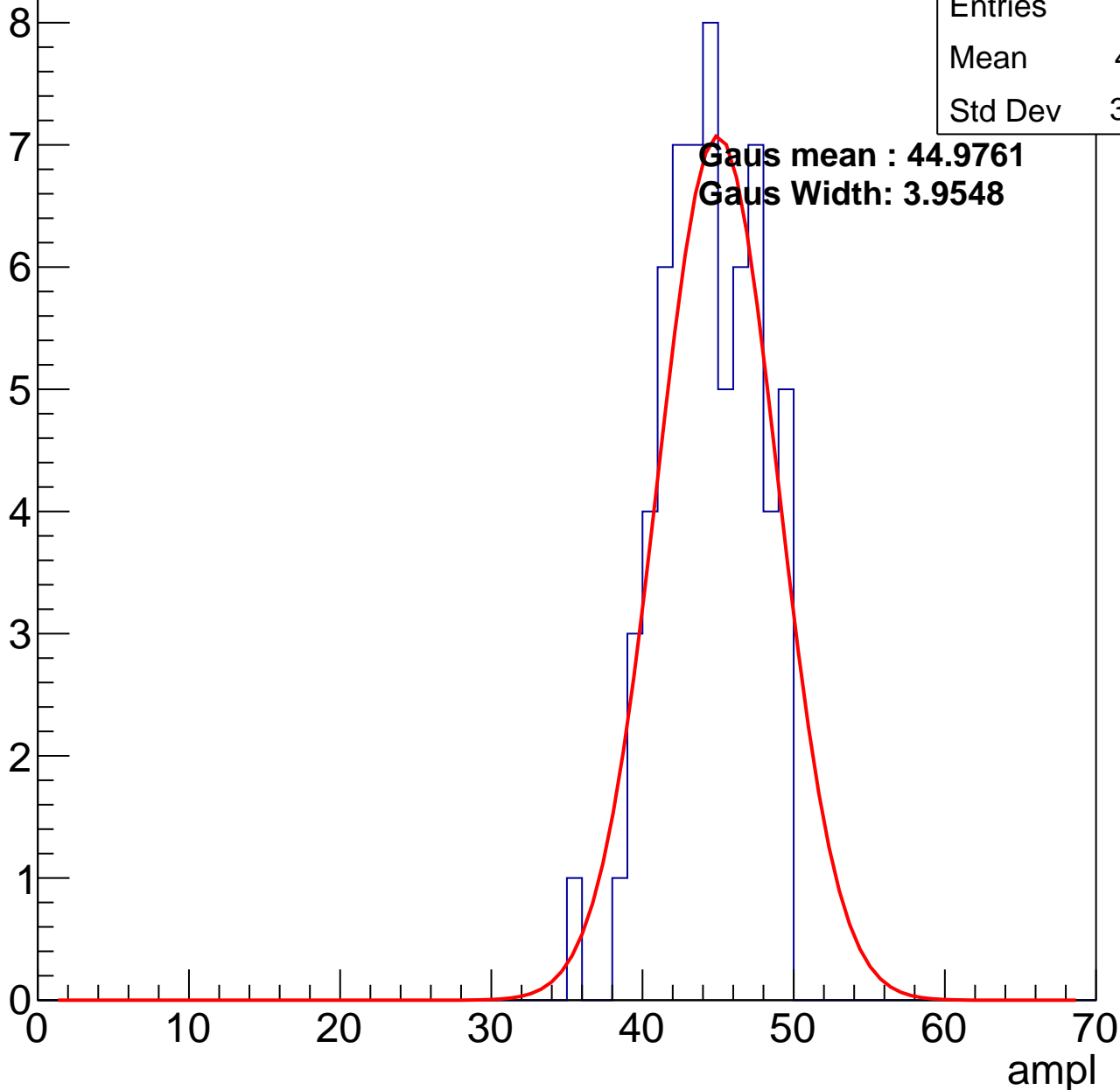
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.91
Std Dev	3.126

Gaus mean : 44.9761

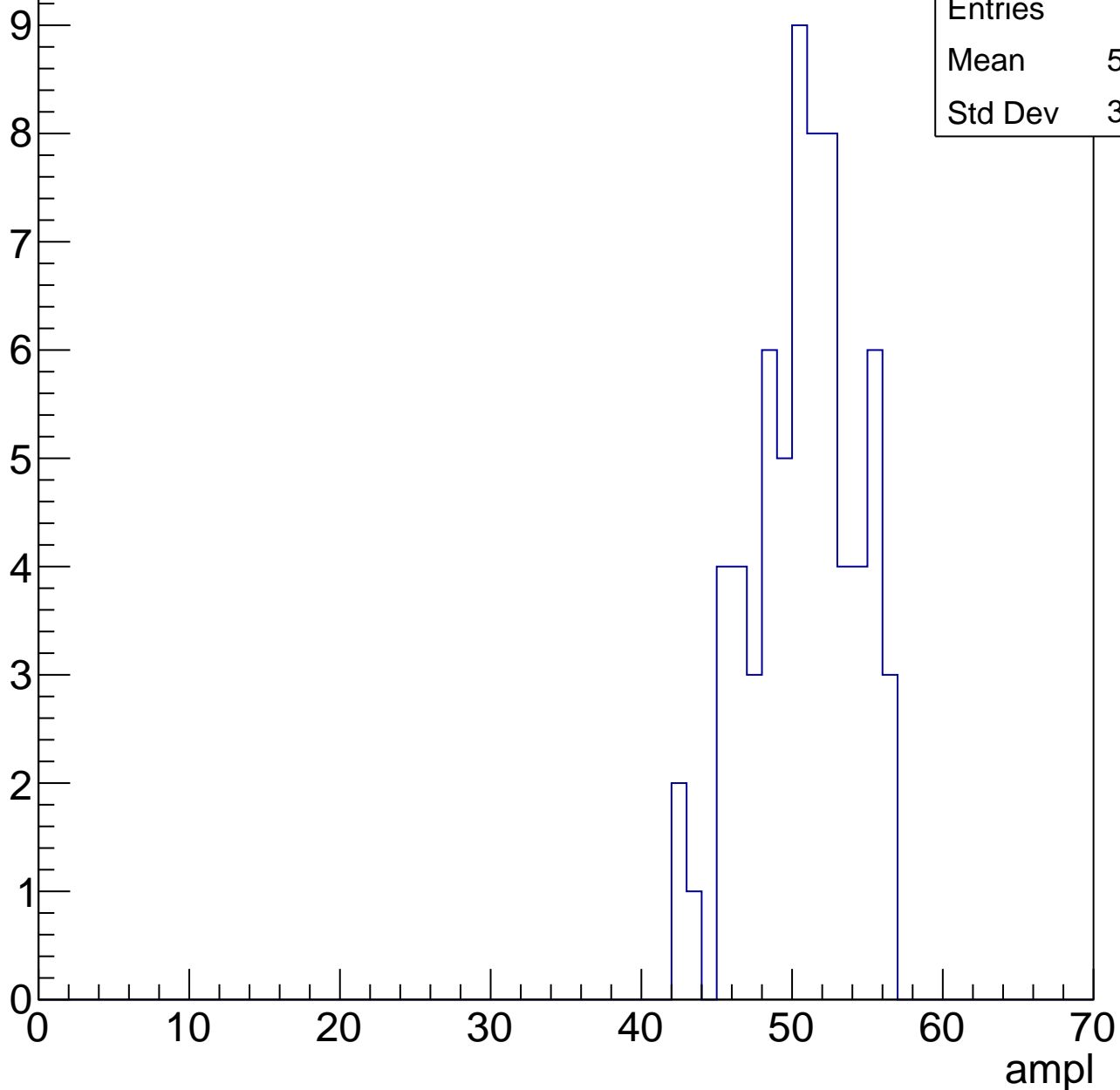
Gaus Width: 3.9548



B1L100S, U10-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

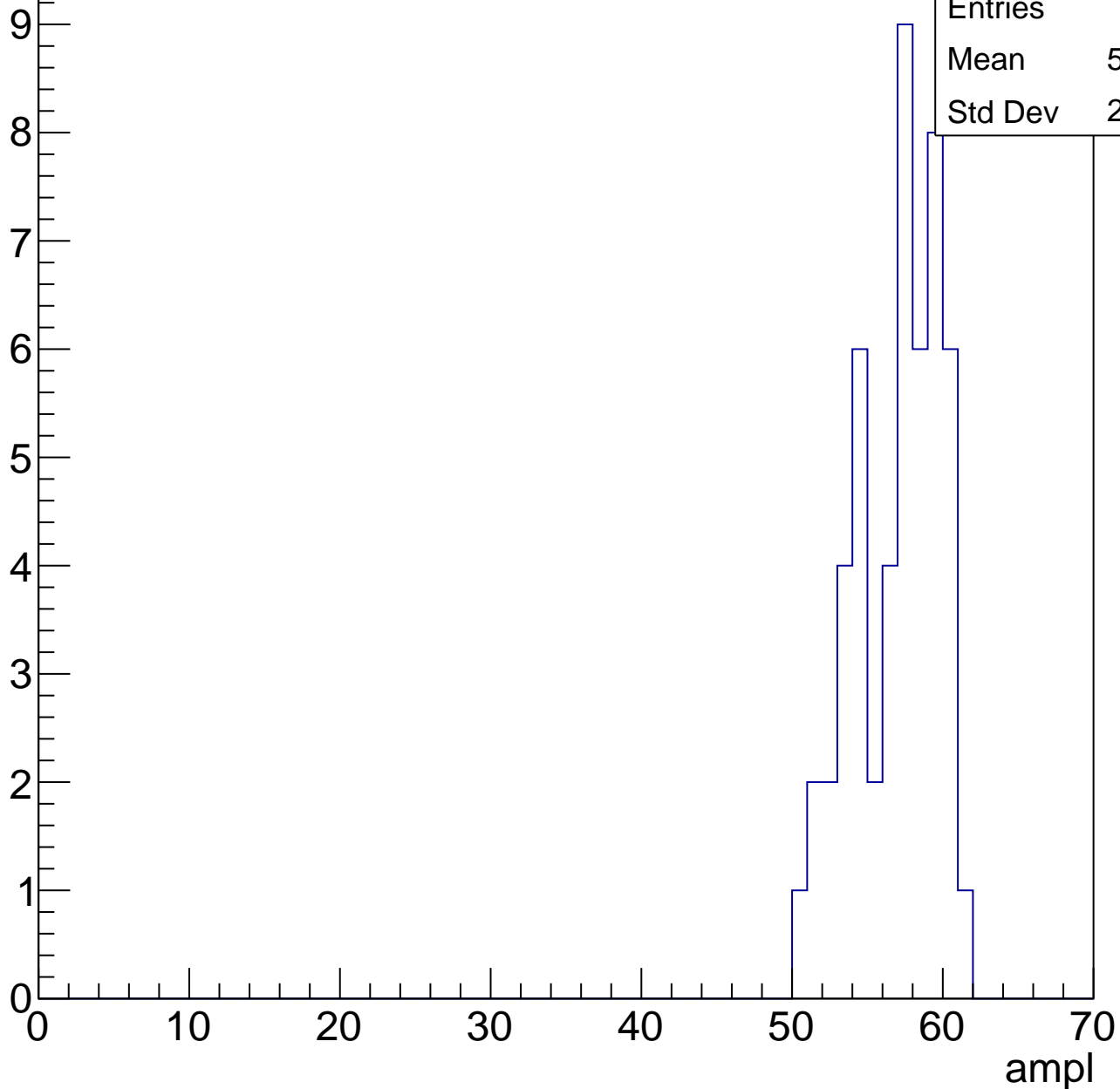
Entry



B1L100S, U10-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	51
Mean	56.47
Std Dev	2.782

B1L100S, U10-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	47
Mean	58.87
Std Dev	9.101

B1L100S, U10-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U10-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L100S, U10-ch55, adc0

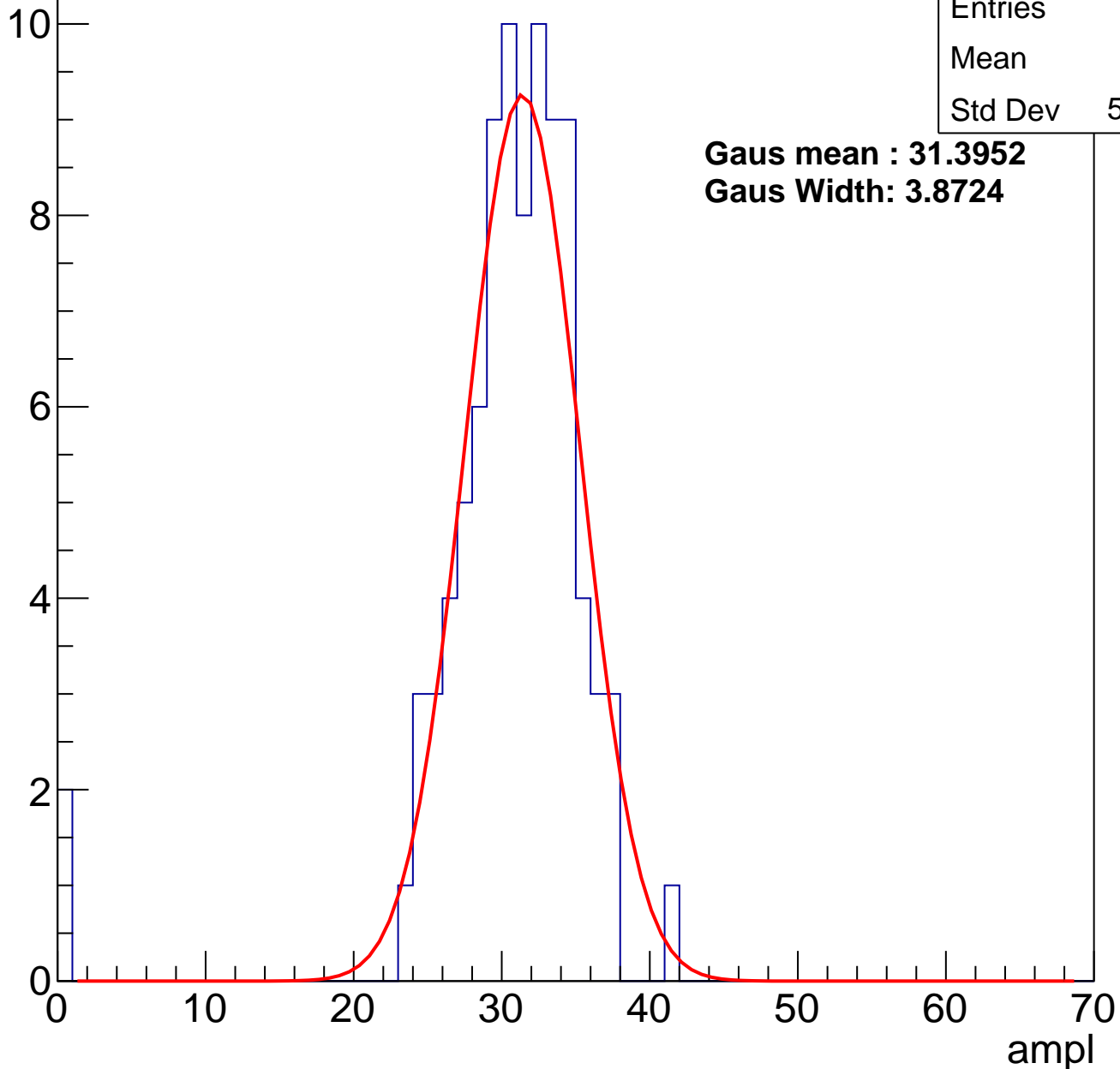
calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	30.1
Std Dev	5.698

Gaus mean : 31.3952

Gaus Width: 3.8724

Entry



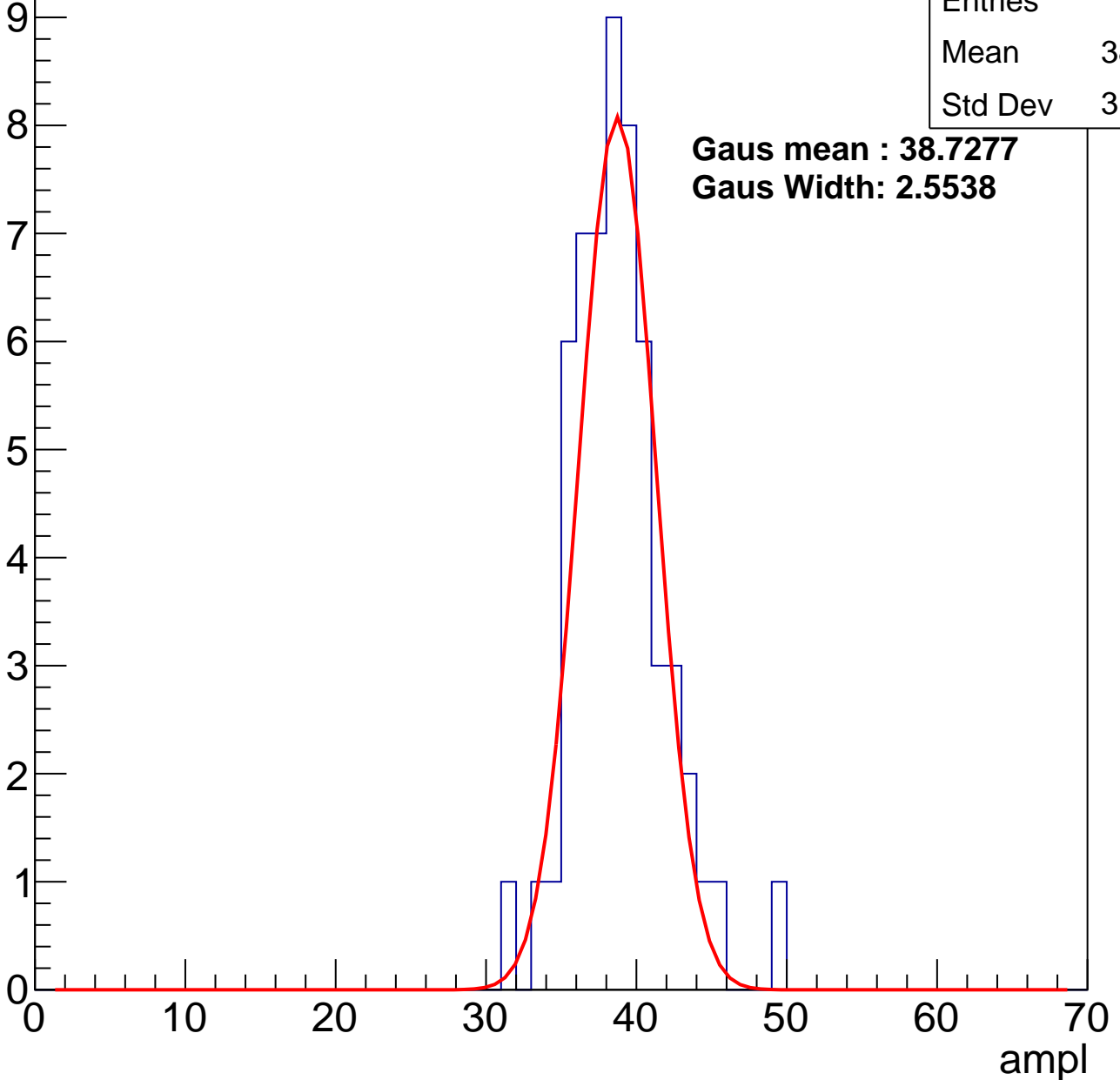
B1L100S, U10-ch55, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	38.35
Std Dev	3.064

Gaus mean : 38.7277
Gaus Width: 2.5538



B1L100S, U10-ch55, adc2

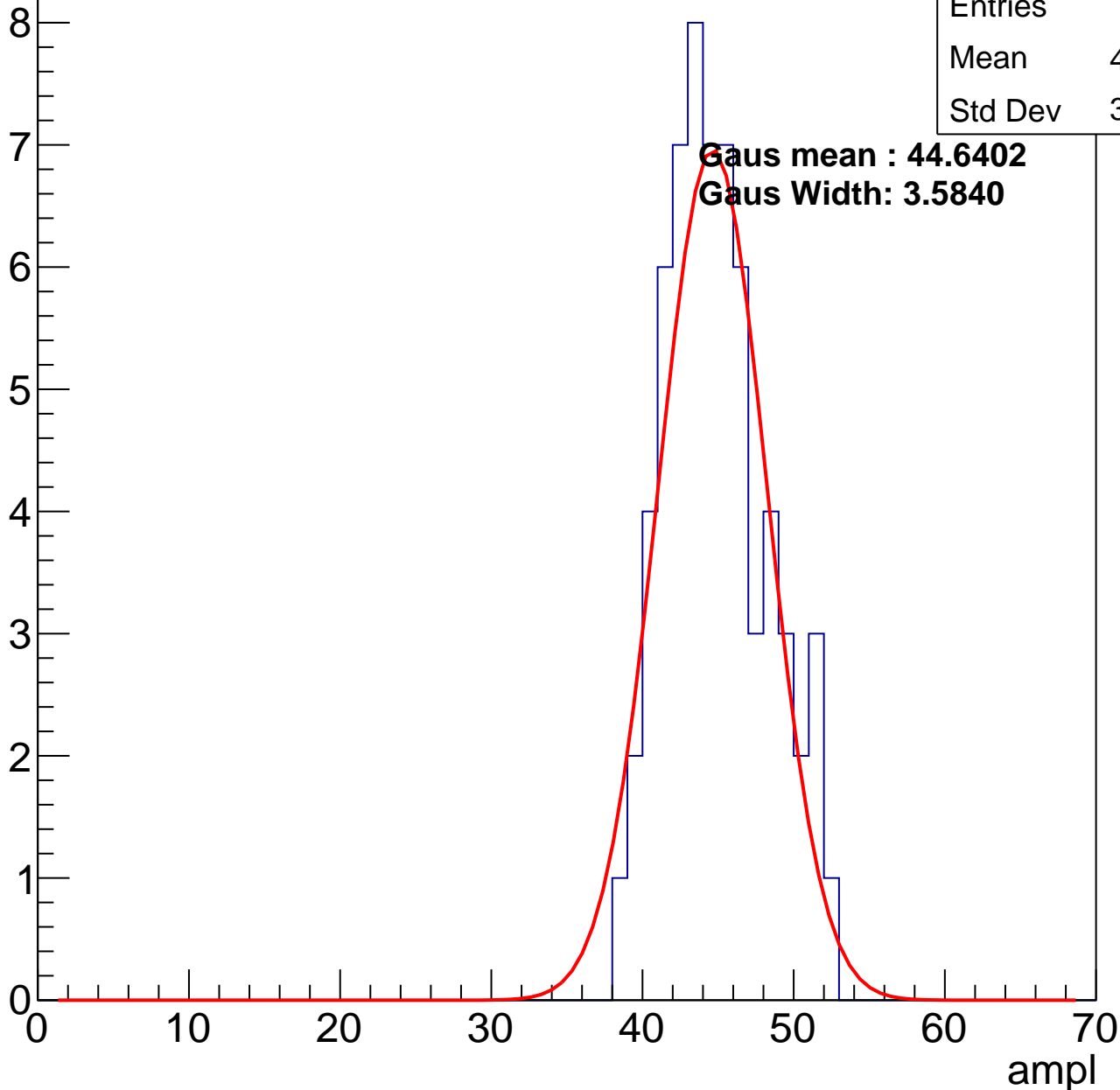
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.44
Std Dev	3.325

Gaus mean : 44.6402

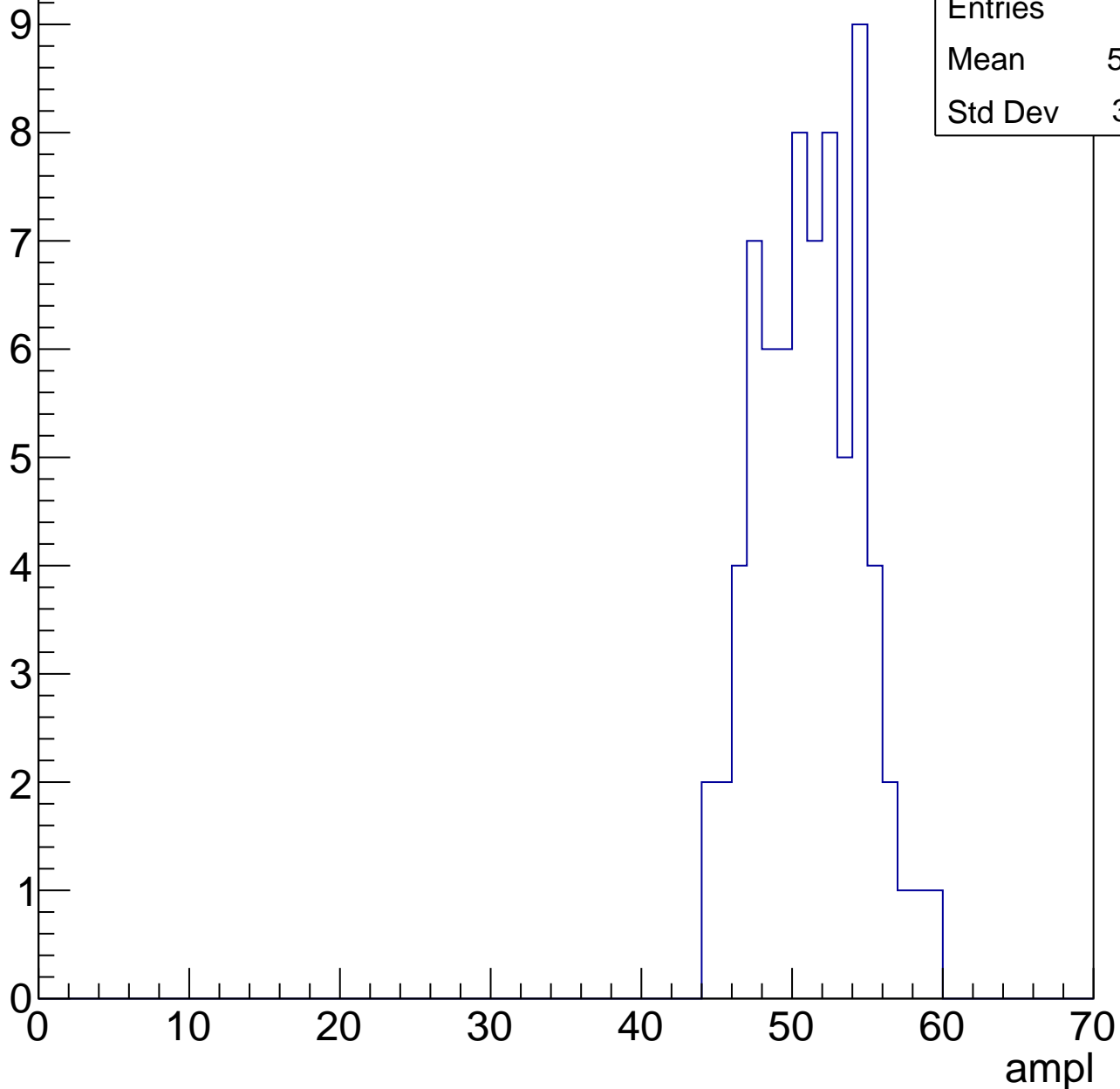
Gaus Width: 3.5840



B1L100S, U10-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

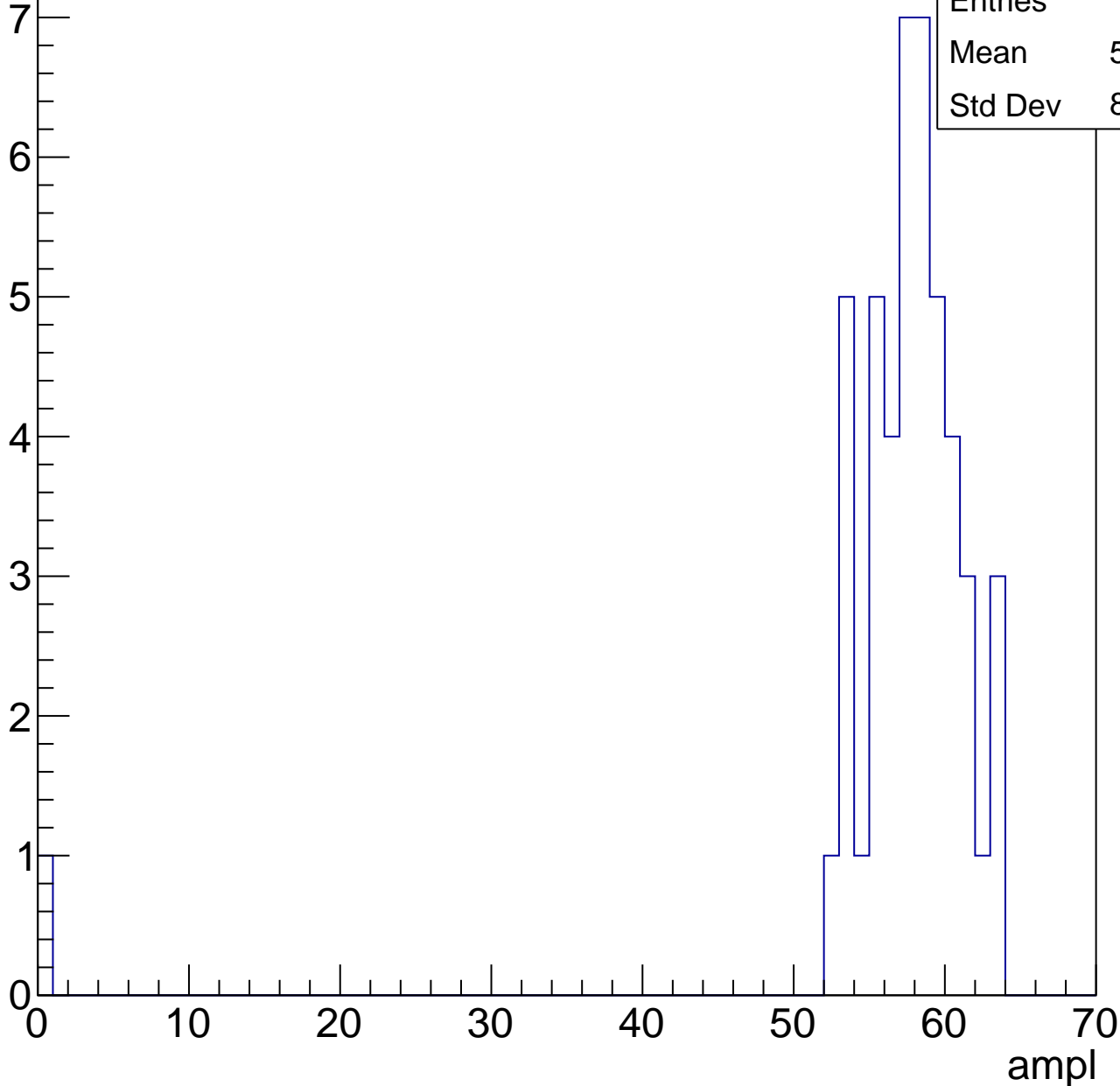


B1L100S, U10-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

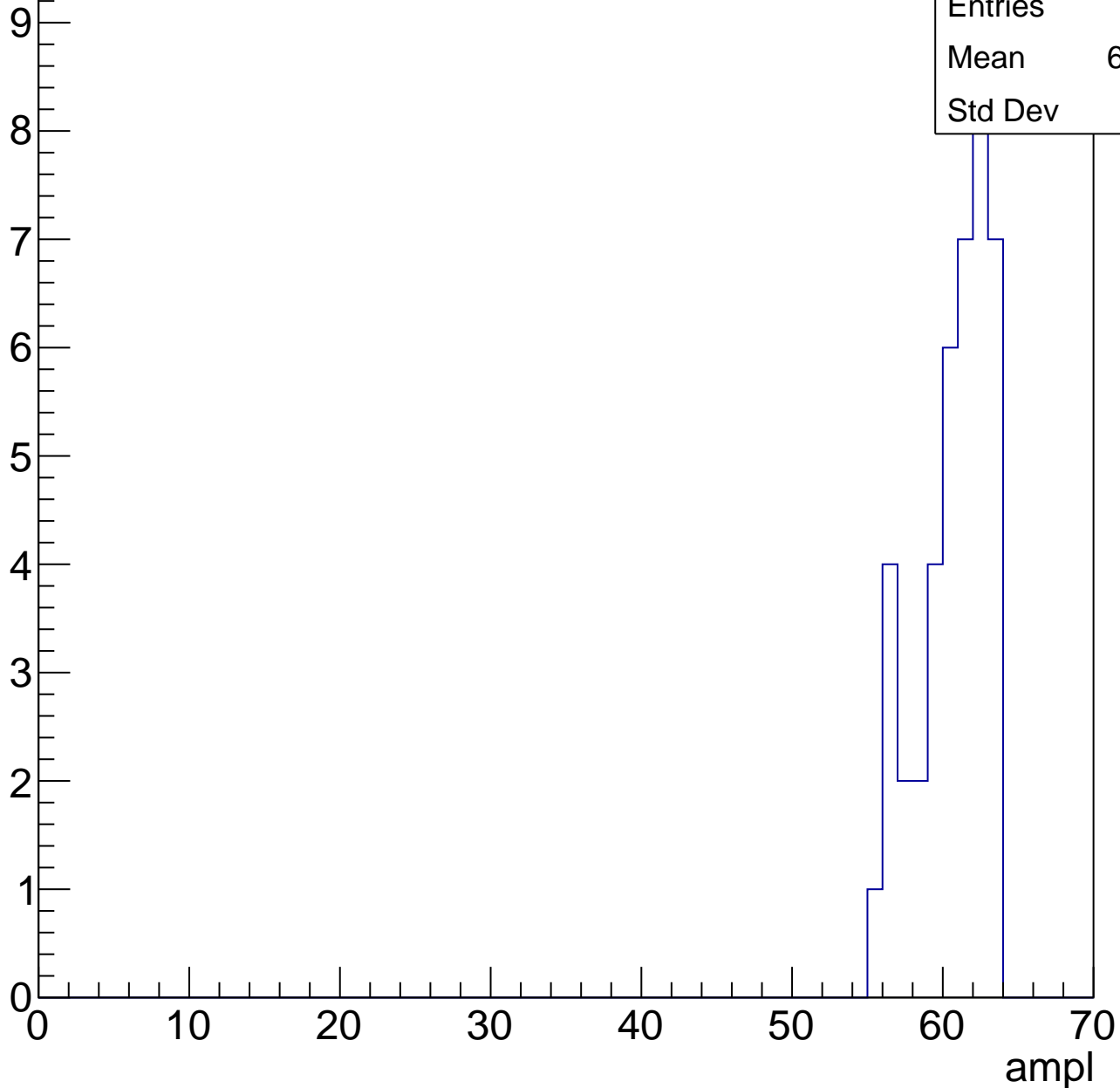
Entries	47
Mean	56.26
Std Dev	8.758



B1L100S, U10-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	42
Mean	60.26
Std Dev	2.3

B1L100S, U10-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch56, adc0

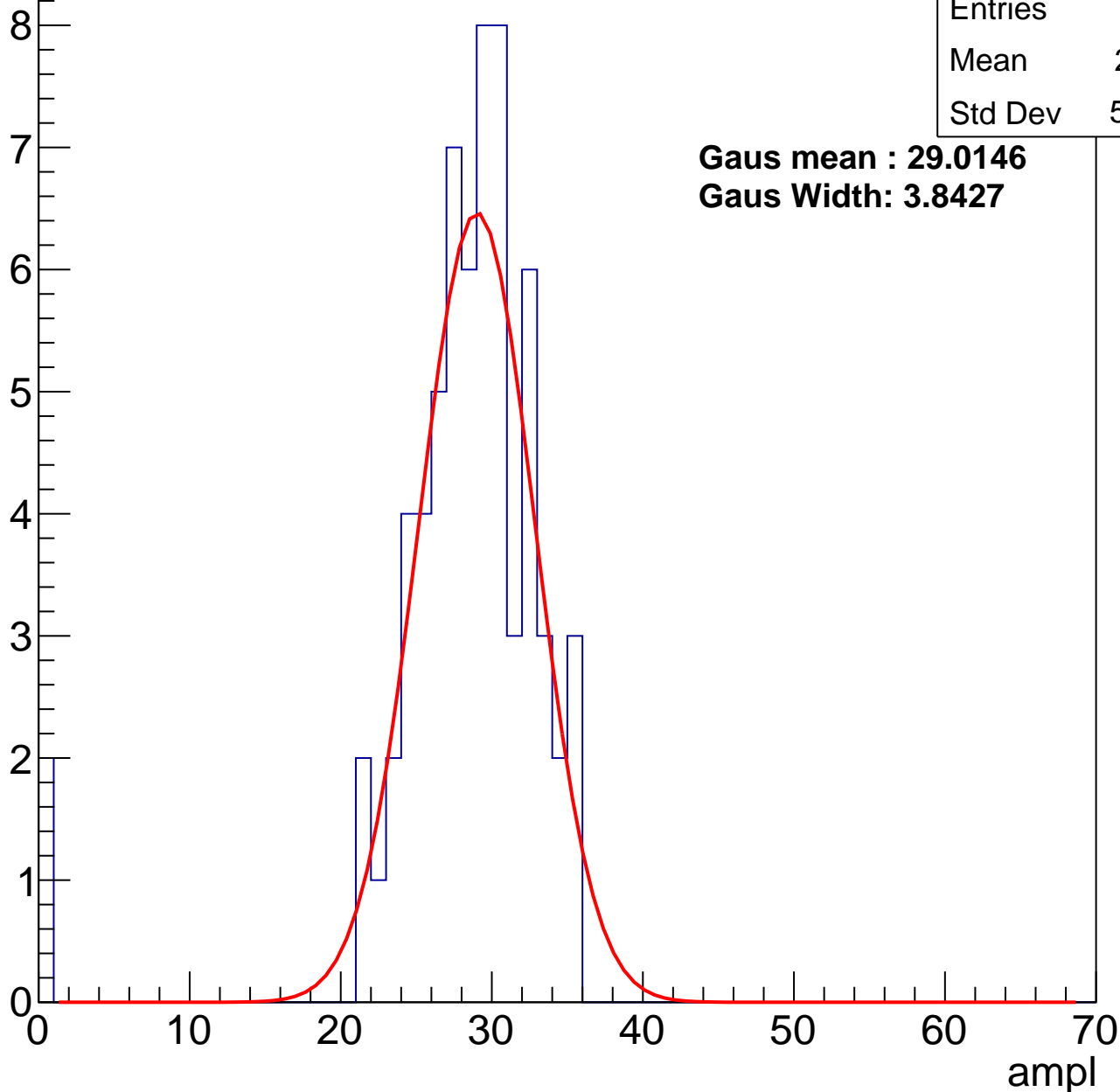
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	27.61
Std Dev	5.936

Gaus mean : 29.0146

Gaus Width: 3.8427



B1L100S, U10-ch56, adc1

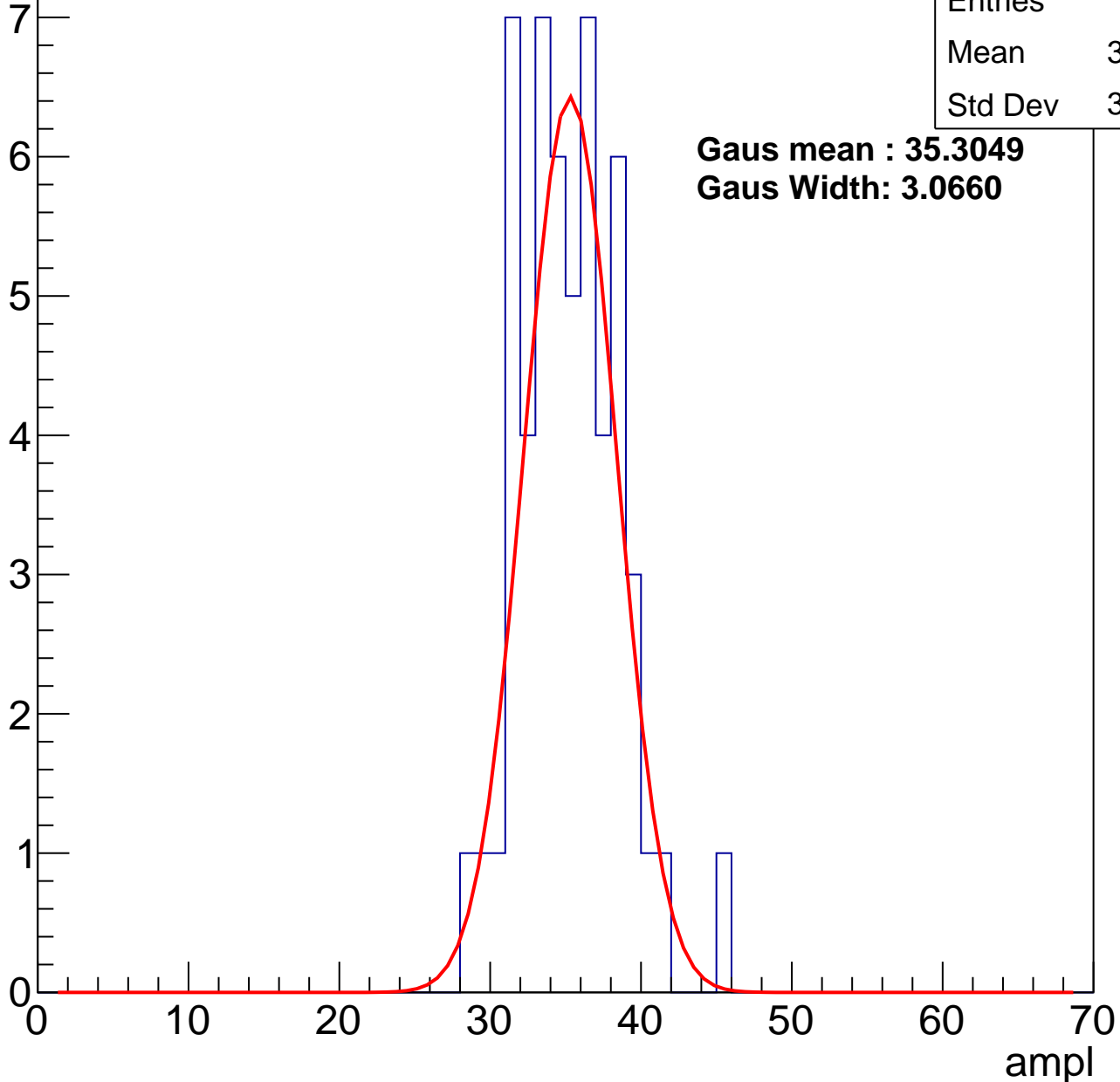
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	34.78
Std Dev	3.234

Gaus mean : 35.3049

Gaus Width: 3.0660



B1L100S, U10-ch56, adc2

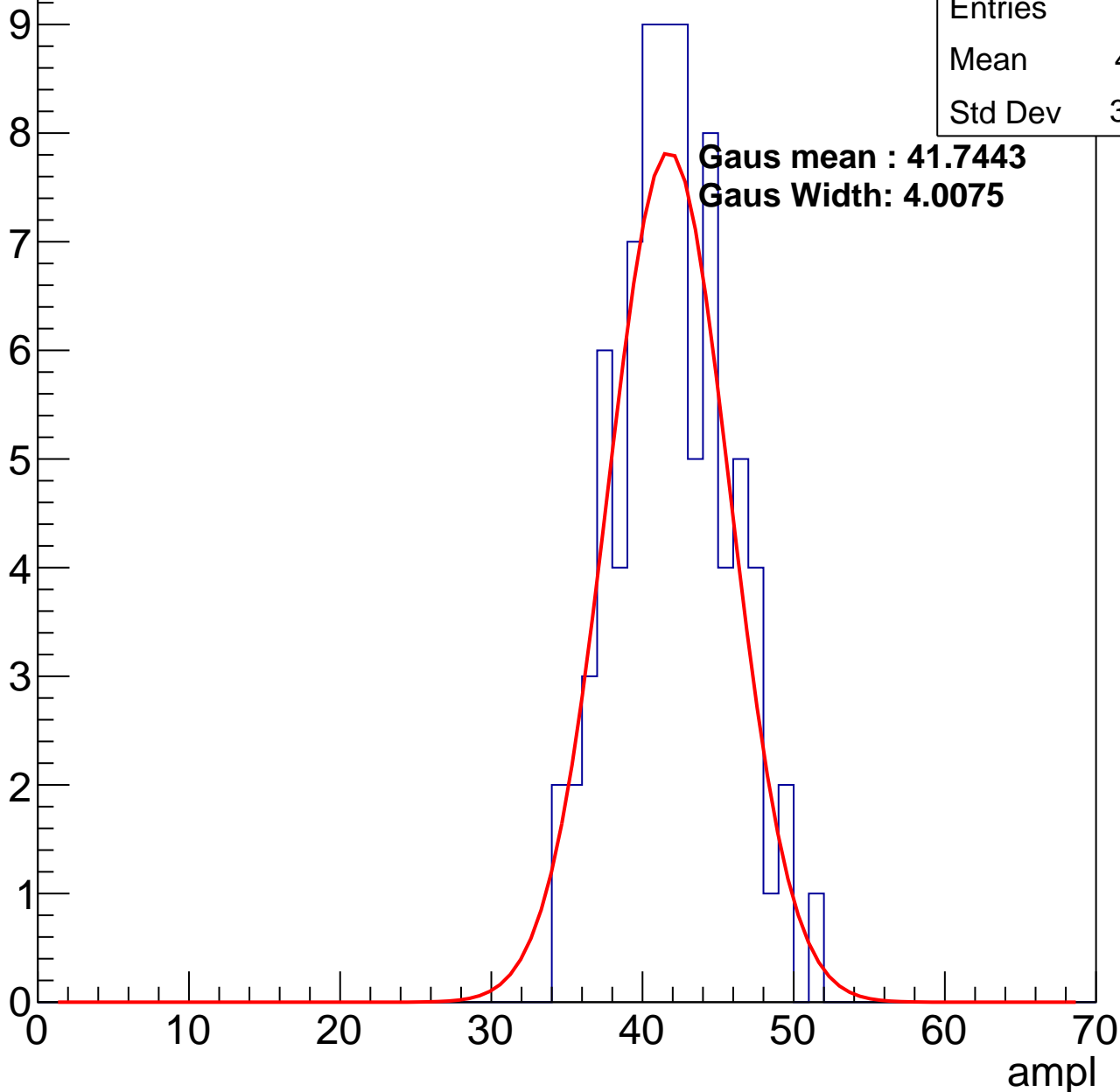
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	41.51
Std Dev	3.692

Gaus mean : 41.7443

Gaus Width: 4.0075

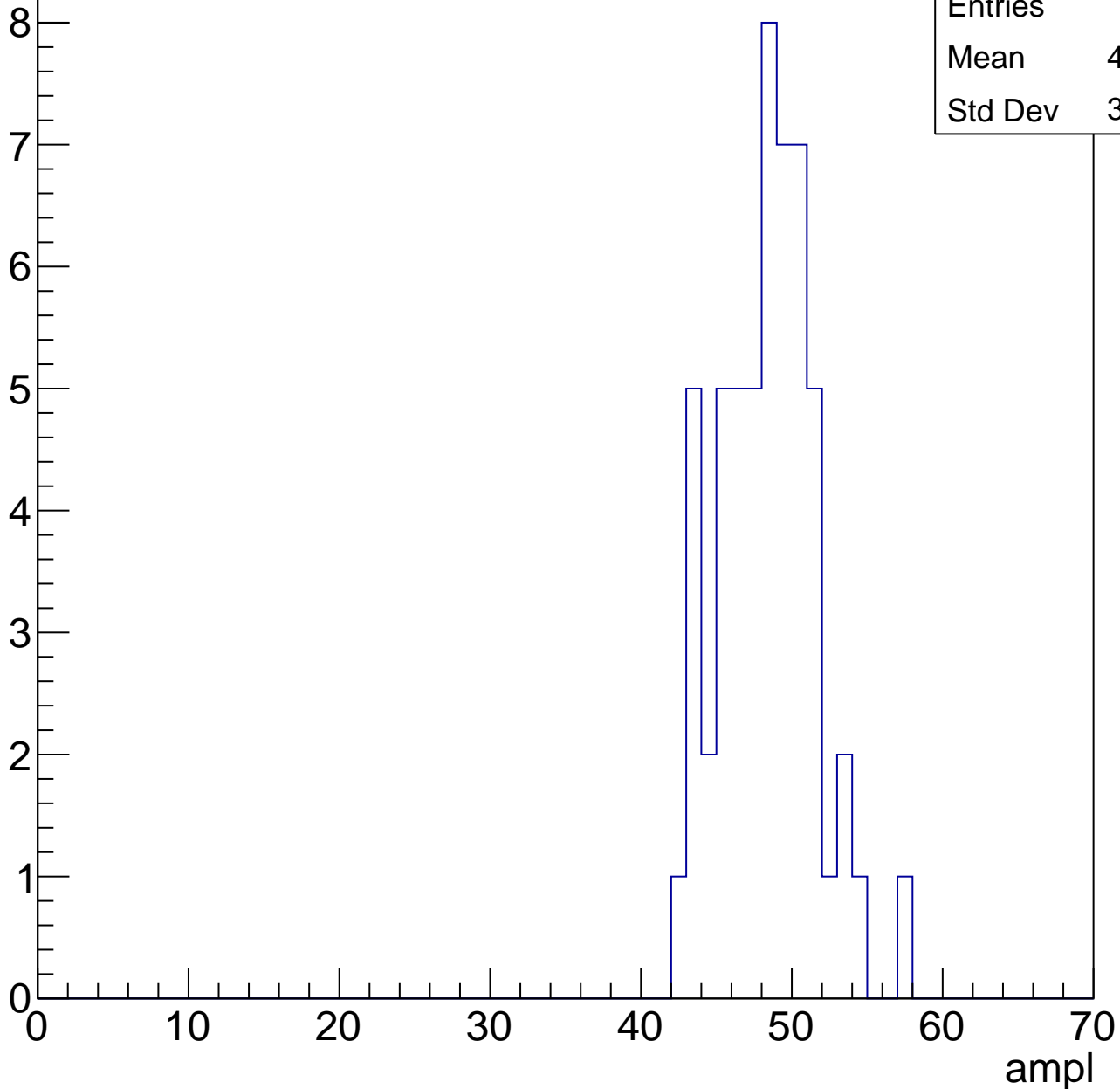


B1L100S, U10-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	47.93
Std Dev	3.092

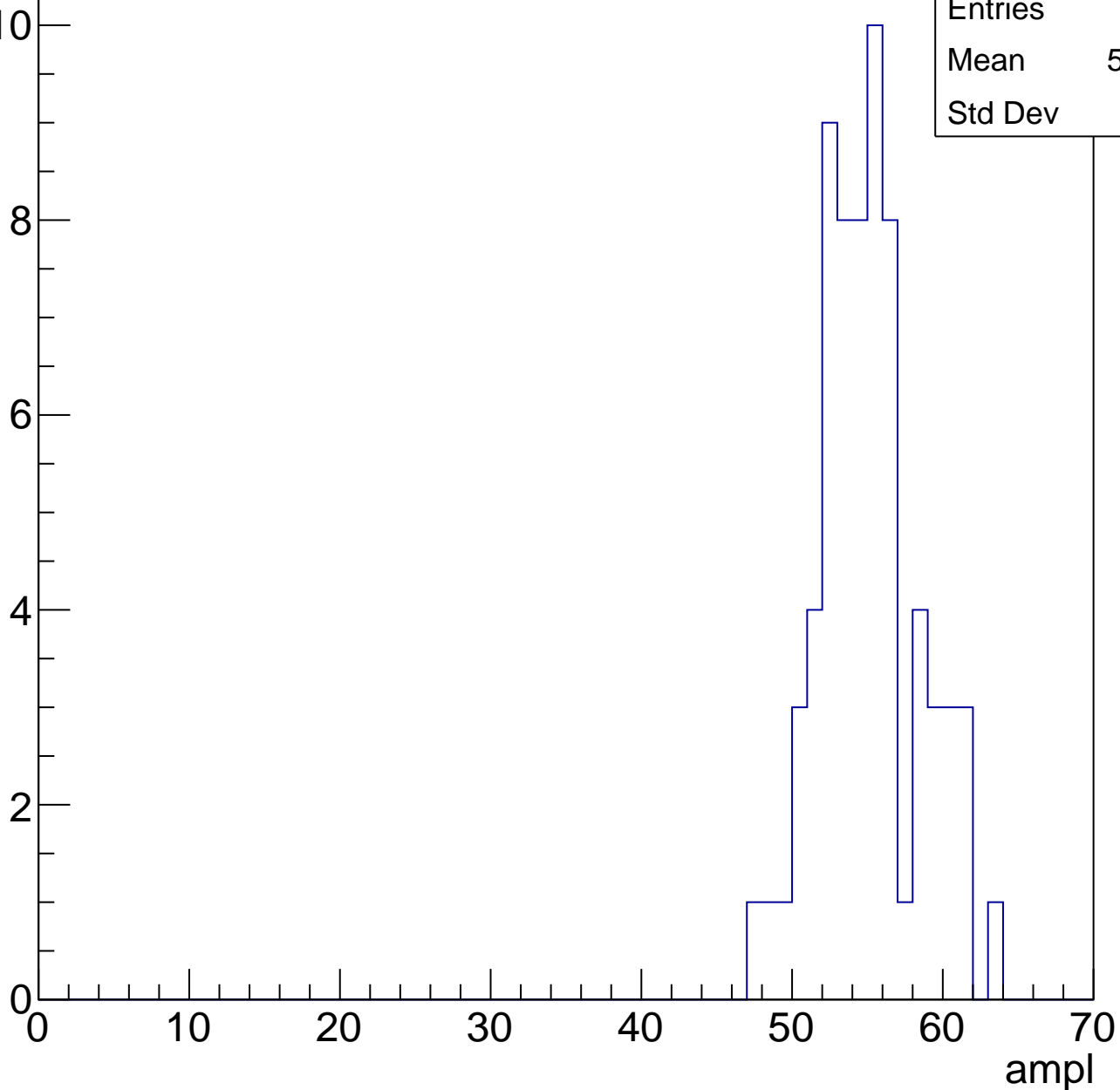


B1L100S, U10-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	54.59
Std Dev	3.3

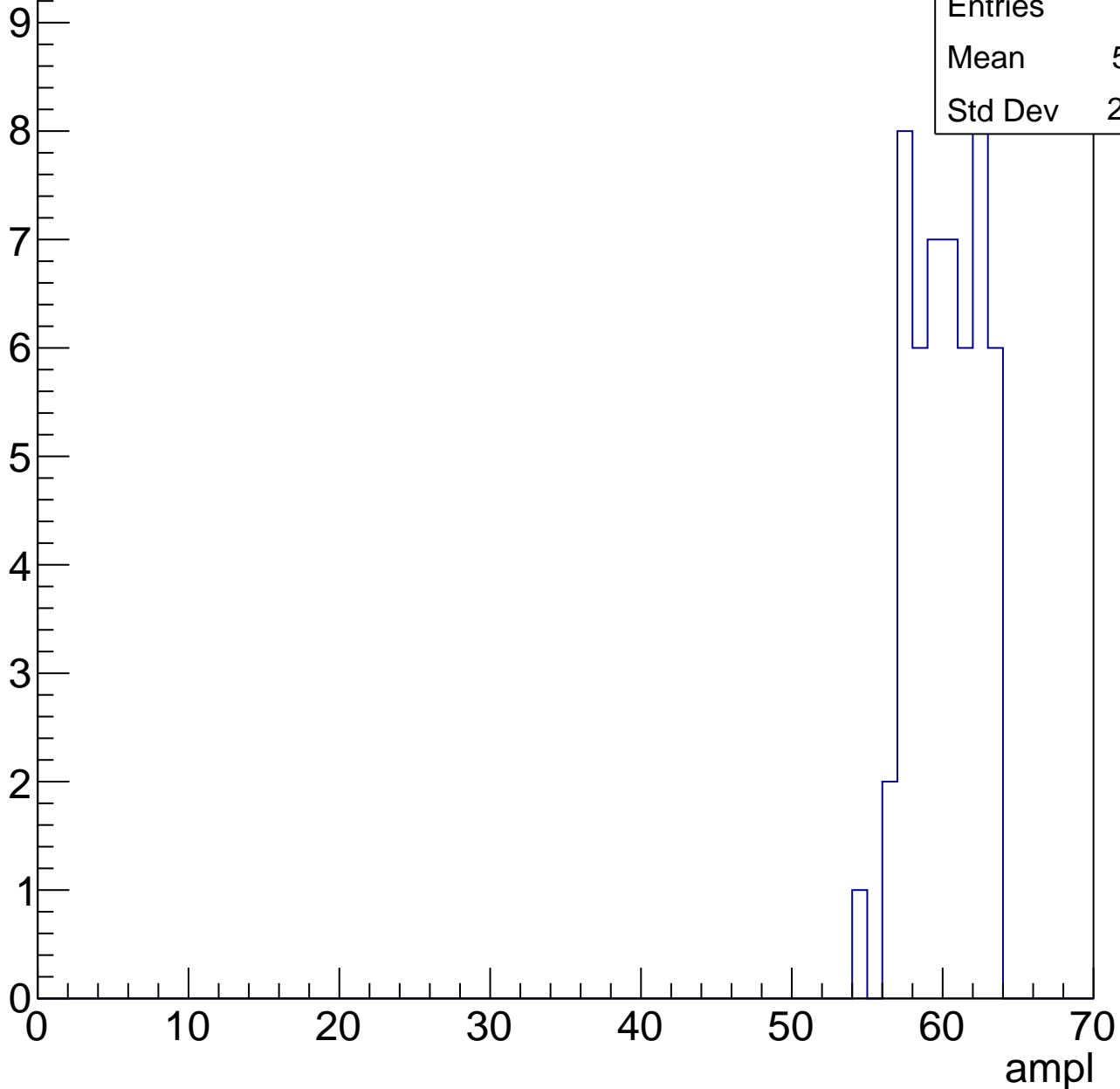


B1L100S, U10-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	59.71
Std Dev	2.248



B1L100S, U10-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch57, adc0

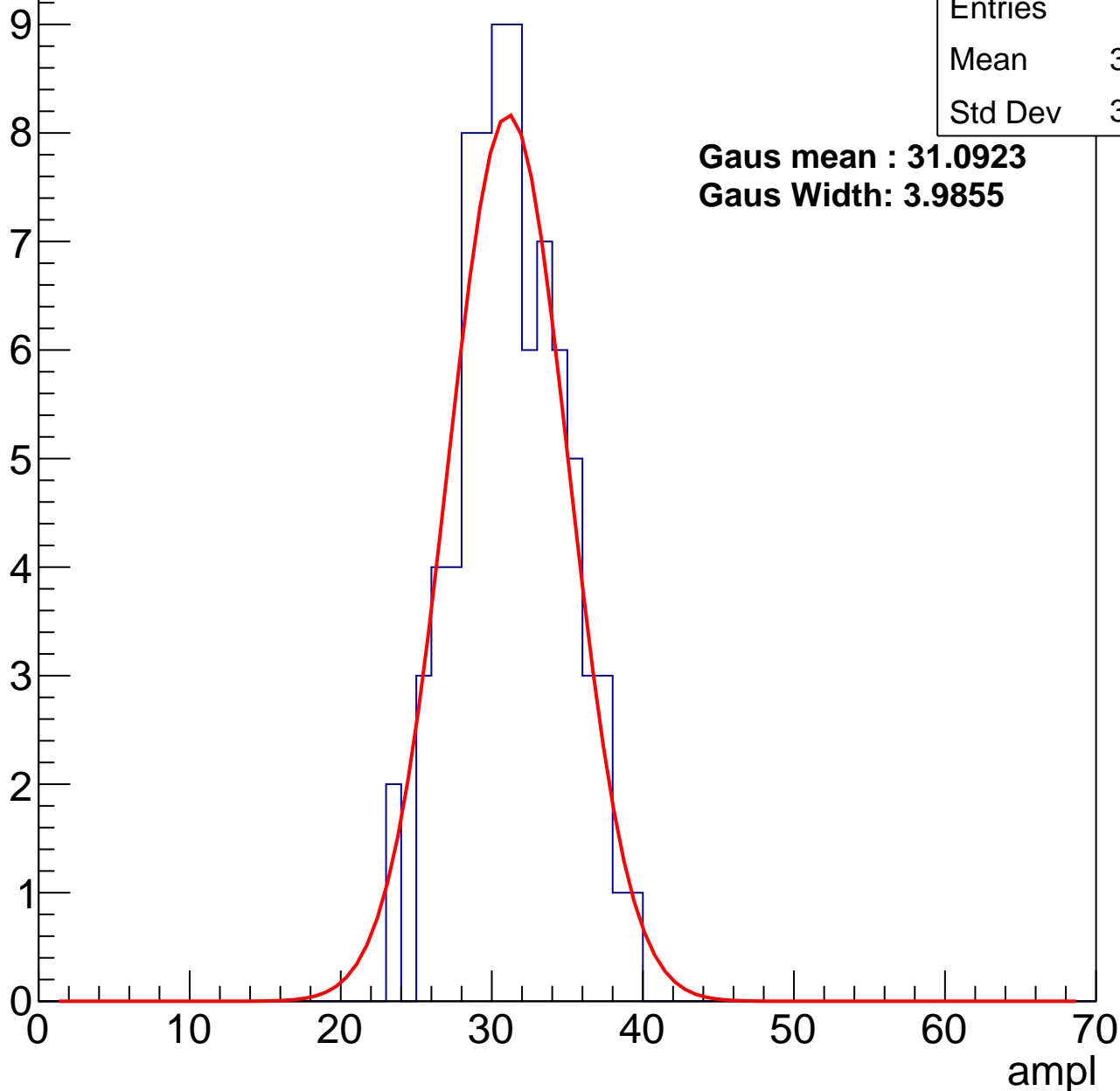
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.84
Std Dev	3.513

Gaus mean : 31.0923

Gaus Width: 3.9855



B1L100S, U10-ch57, adc1

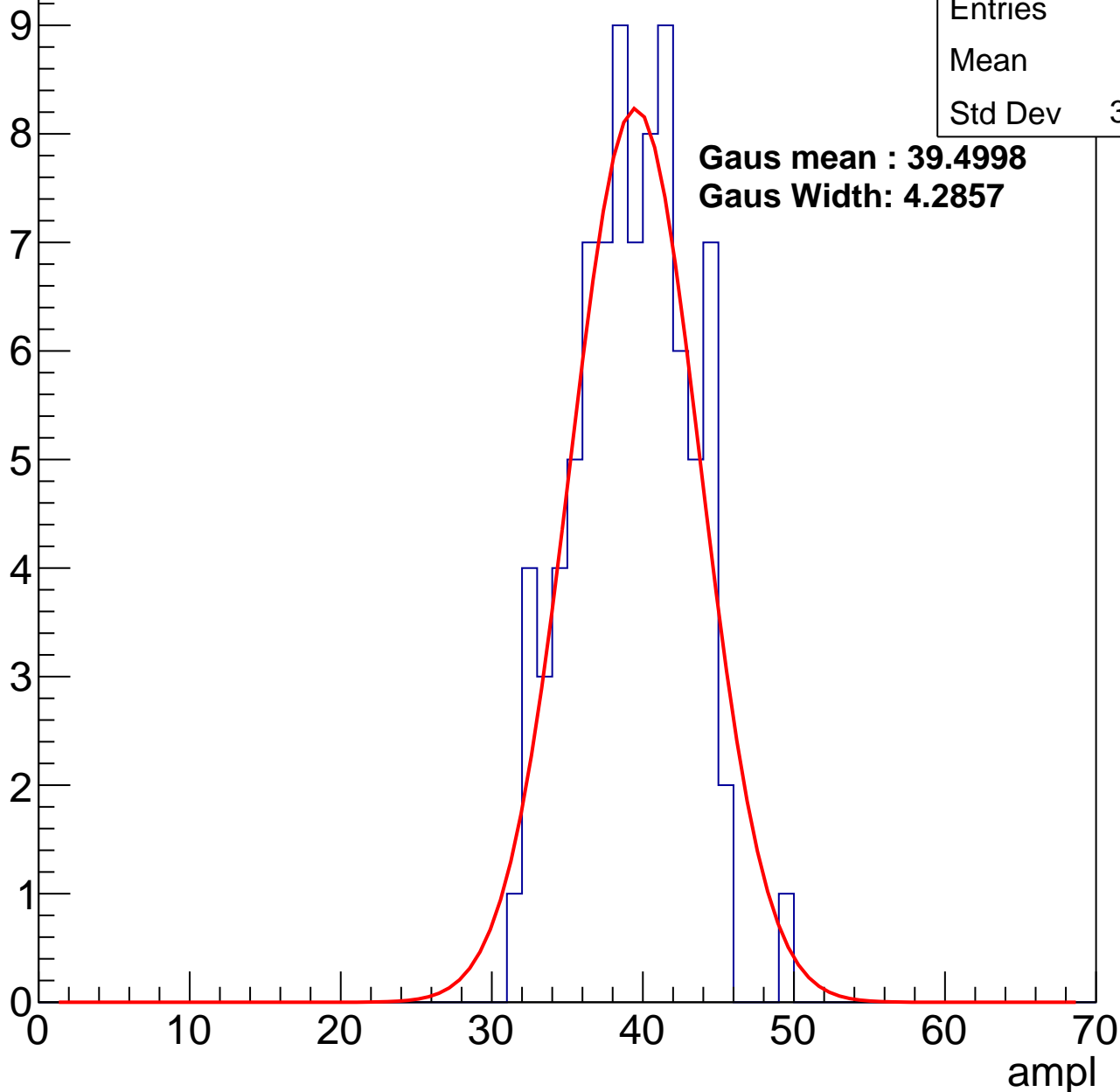
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	38.8
Std Dev	3.709

Gaus mean : 39.4998

Gaus Width: 4.2857



B1L100S, U10-ch57, adc2

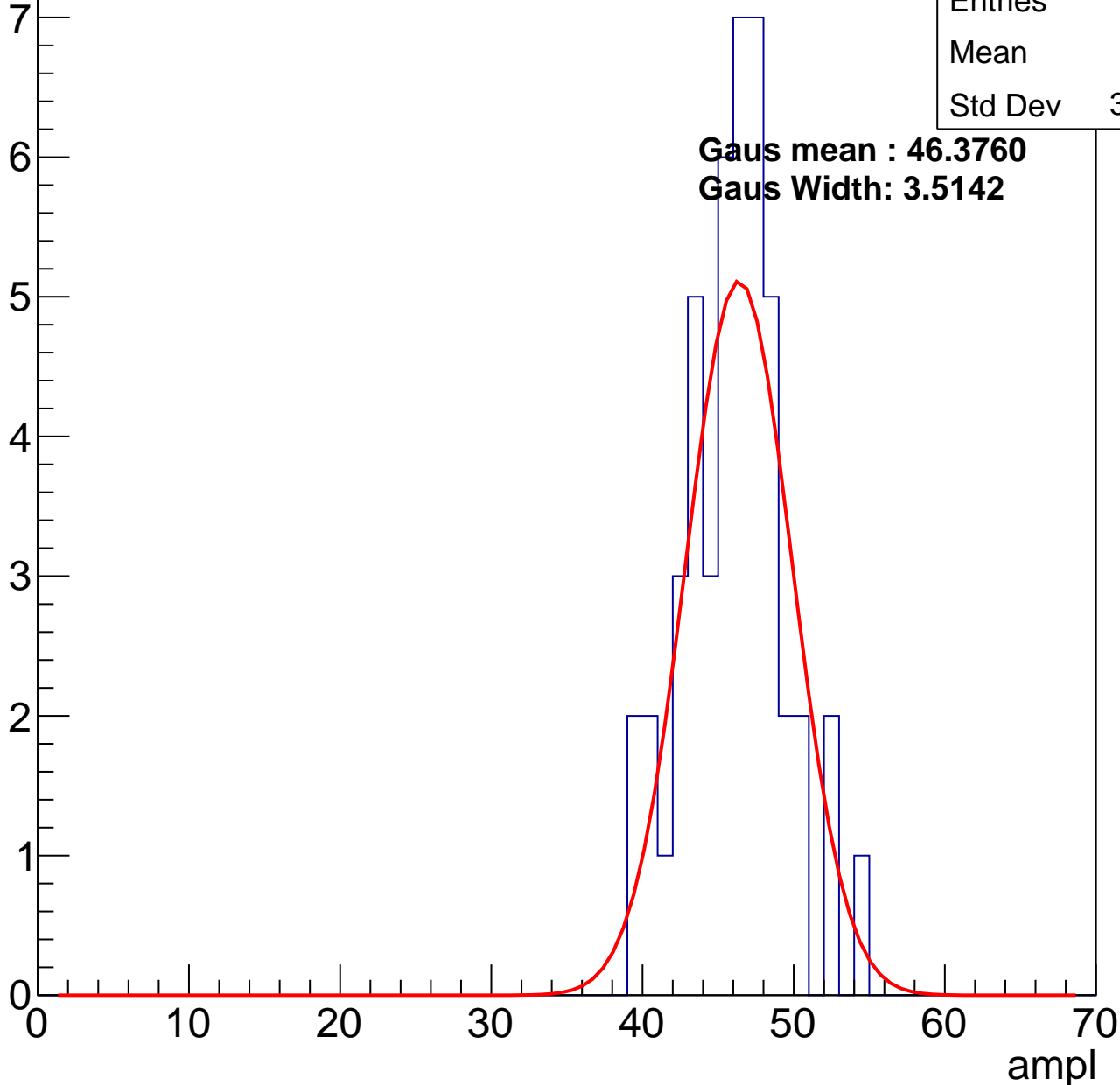
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	45.6
Std Dev	3.264

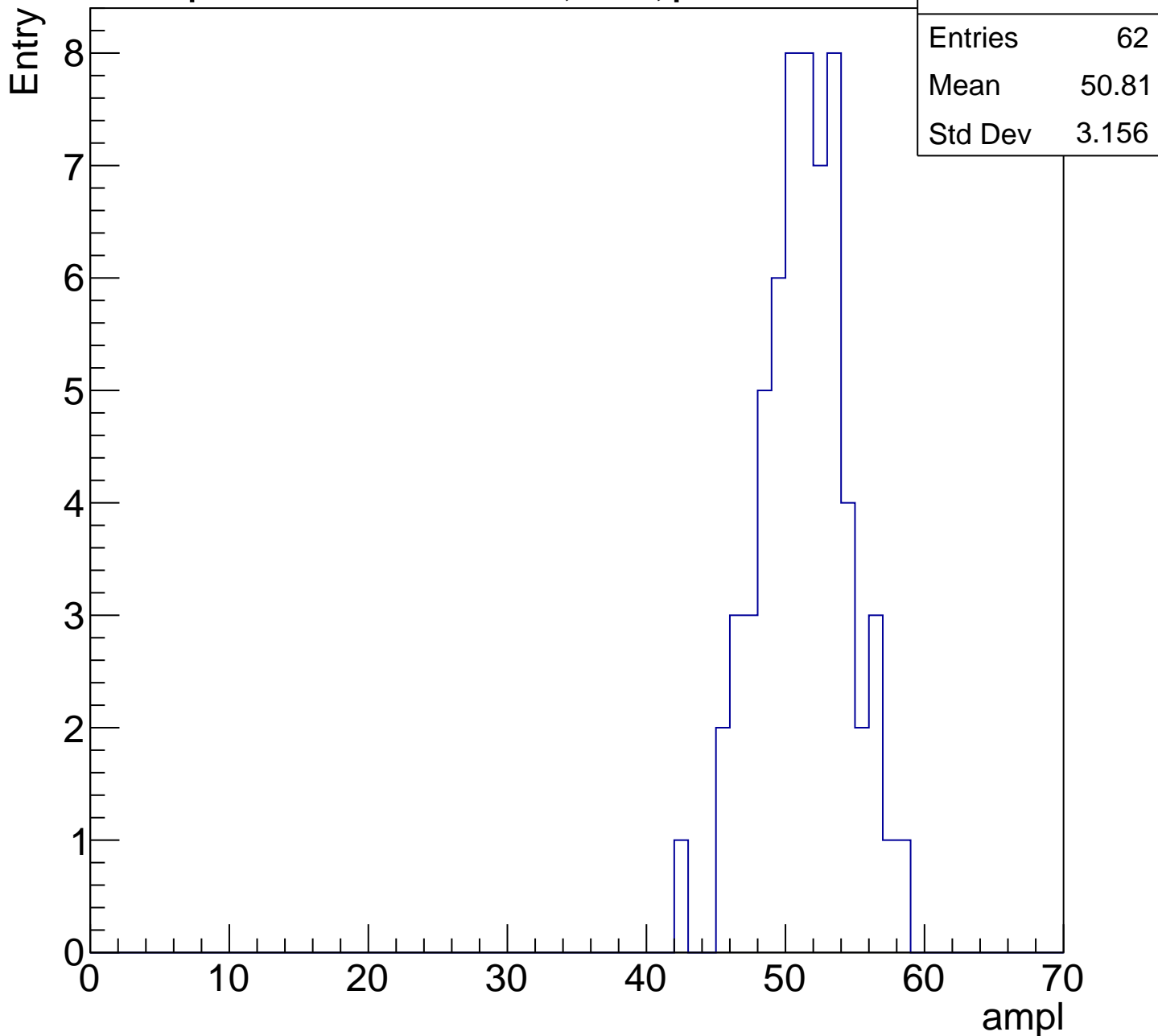
Gaus mean : 46.3760

Gaus Width: 3.5142



B1L100S, U10-ch57, adc3

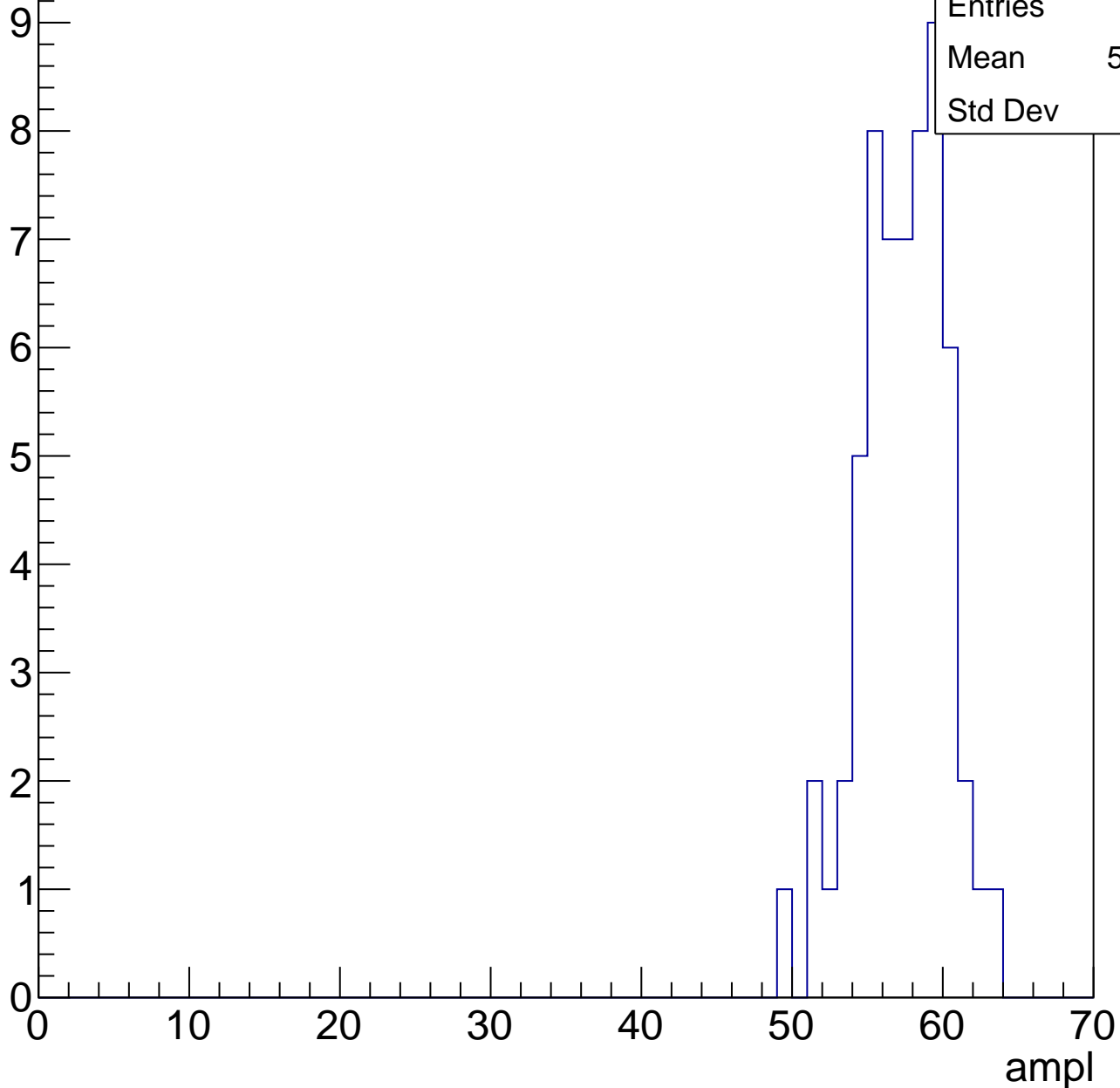
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

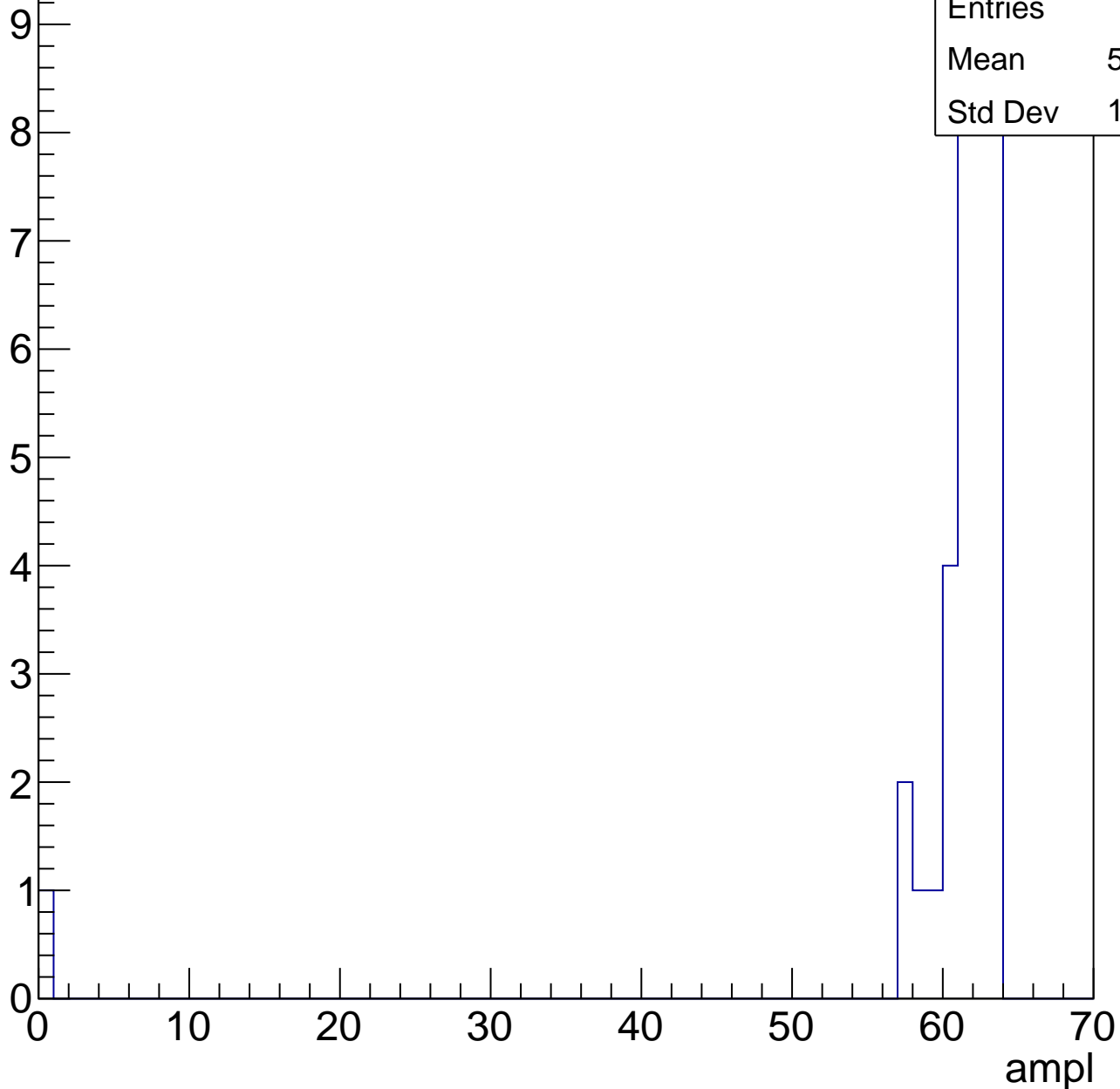
Entry



B1L100S, U10-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch58, adc0

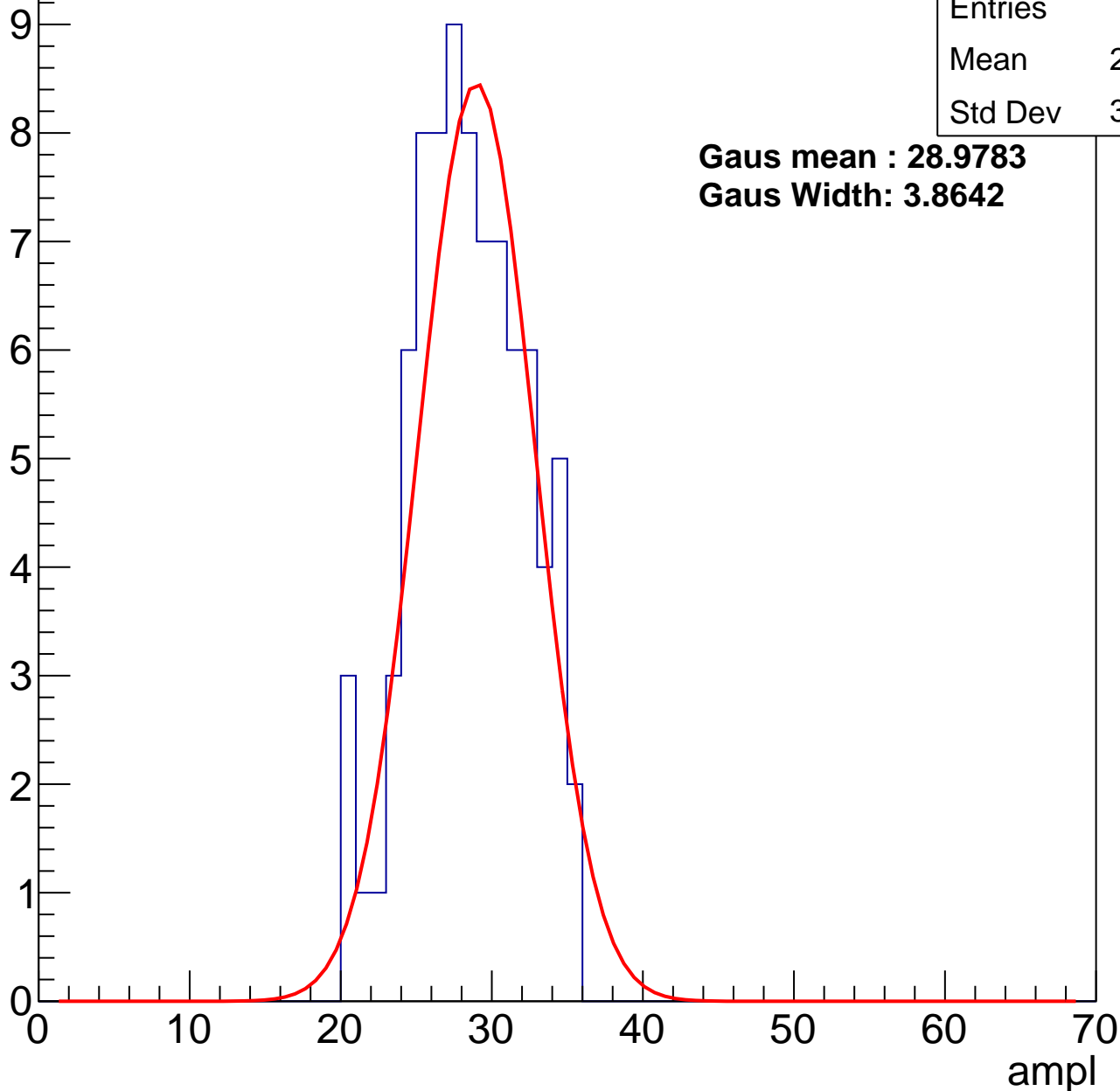
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	28.02
Std Dev	3.658

Gaus mean : 28.9783

Gaus Width: 3.8642



B1L100S, U10-ch58, adc1

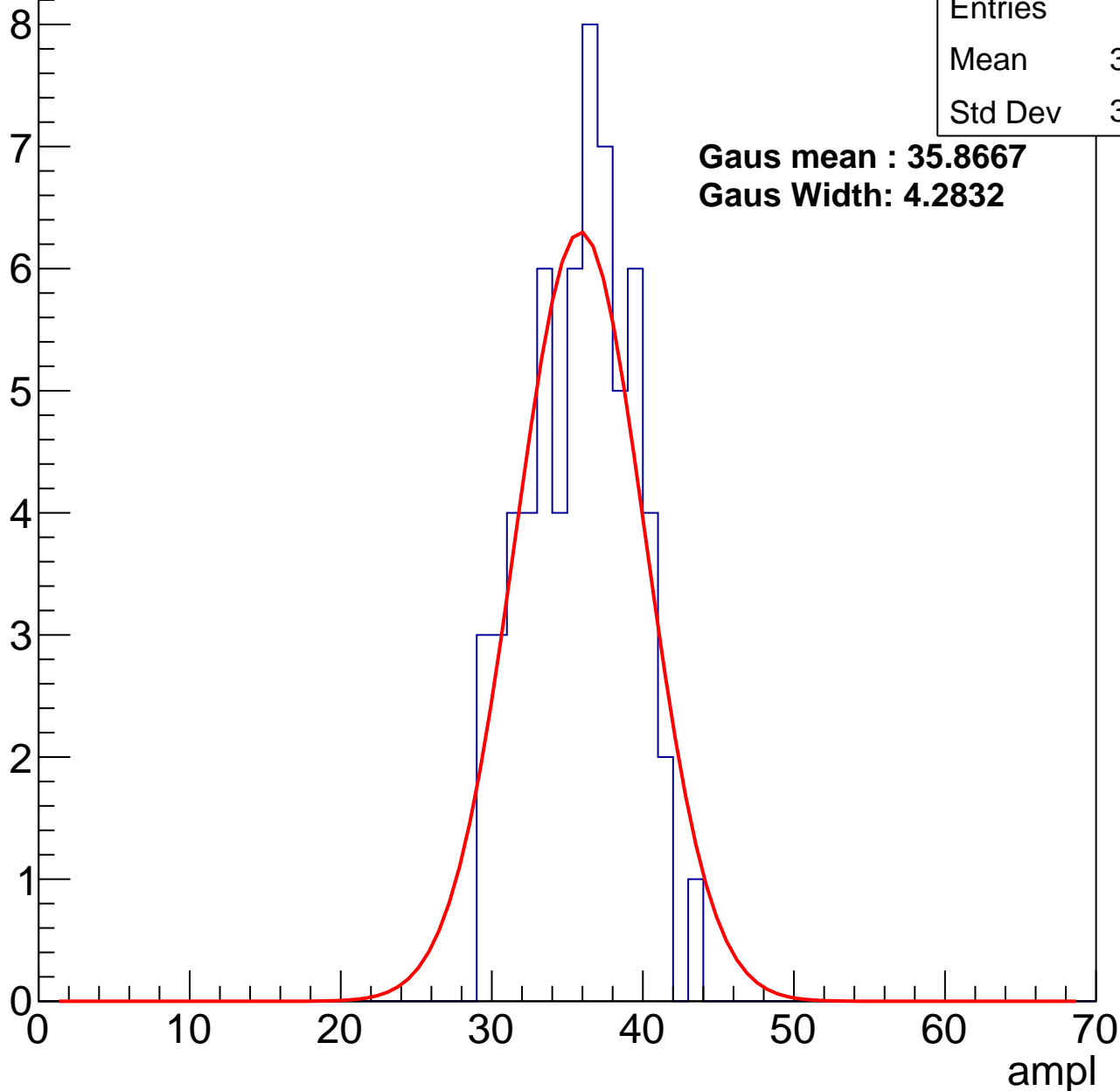
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	35.38
Std Dev	3.369

Gaus mean : 35.8667

Gaus Width: 4.2832



B1L100S, U10-ch58, adc2

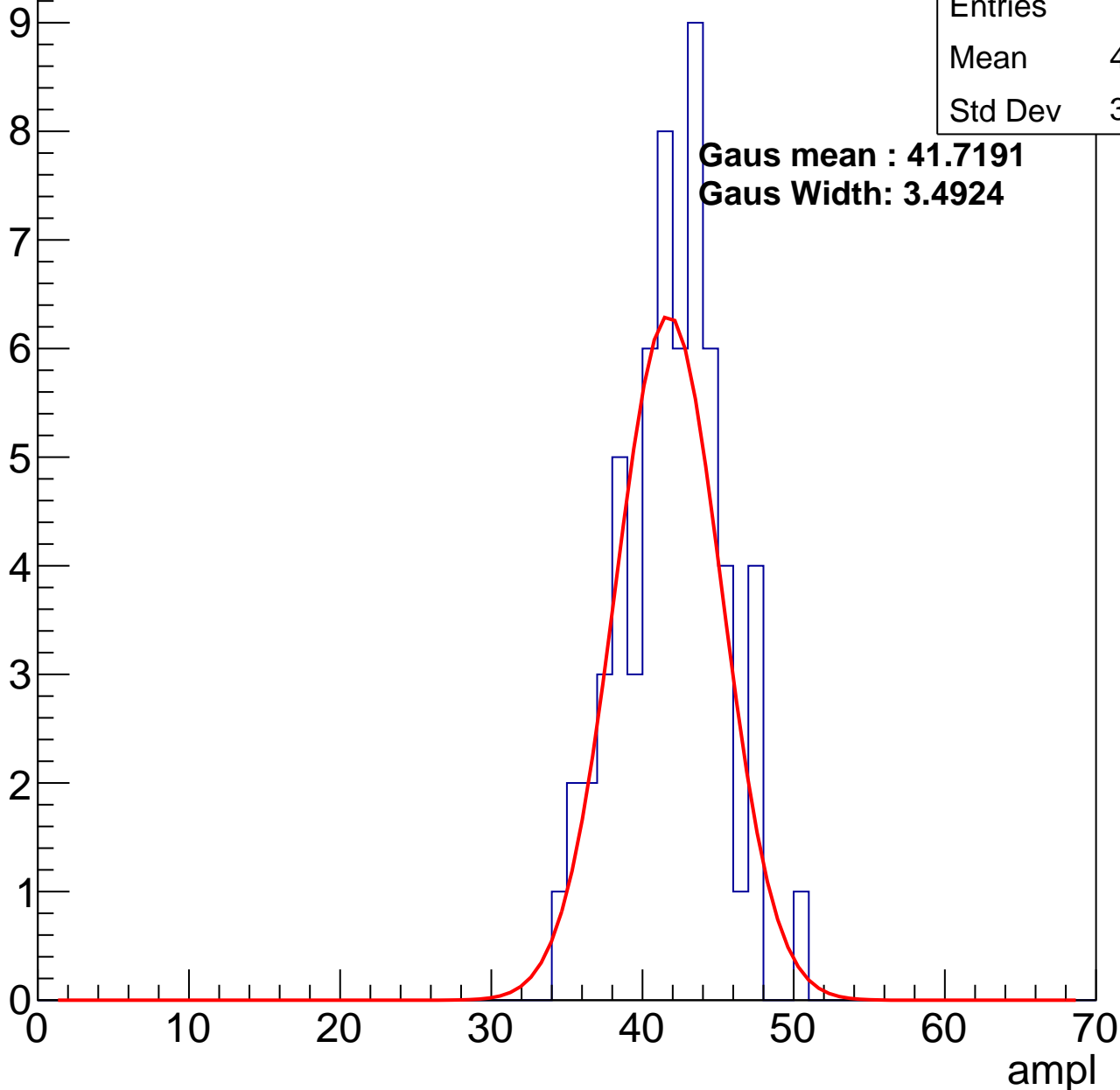
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.46
Std Dev	3.337

Gaus mean : 41.7191

Gaus Width: 3.4924



B1L100S, U10-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

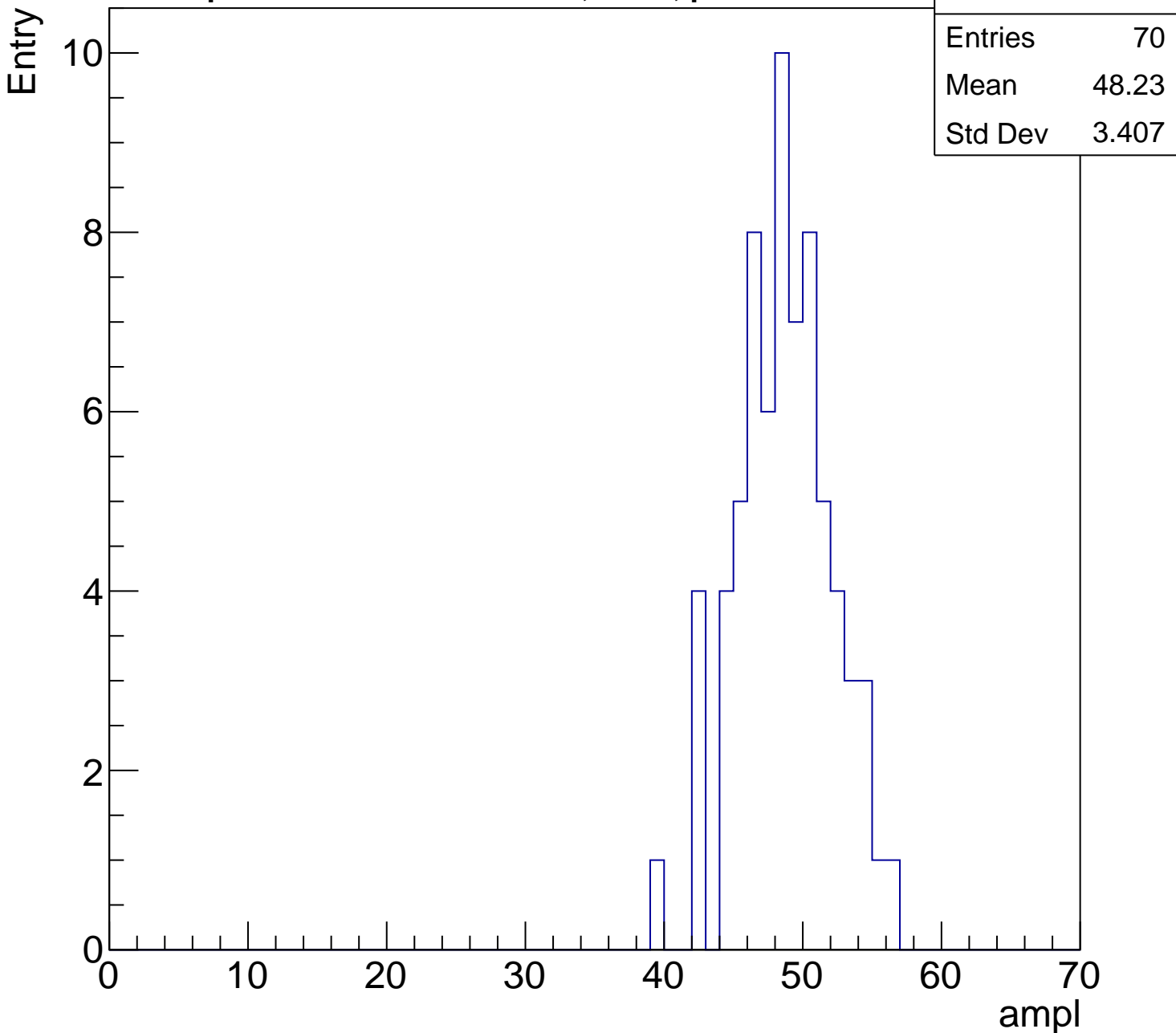
Entries	70
Mean	48.23
Std Dev	3.407

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

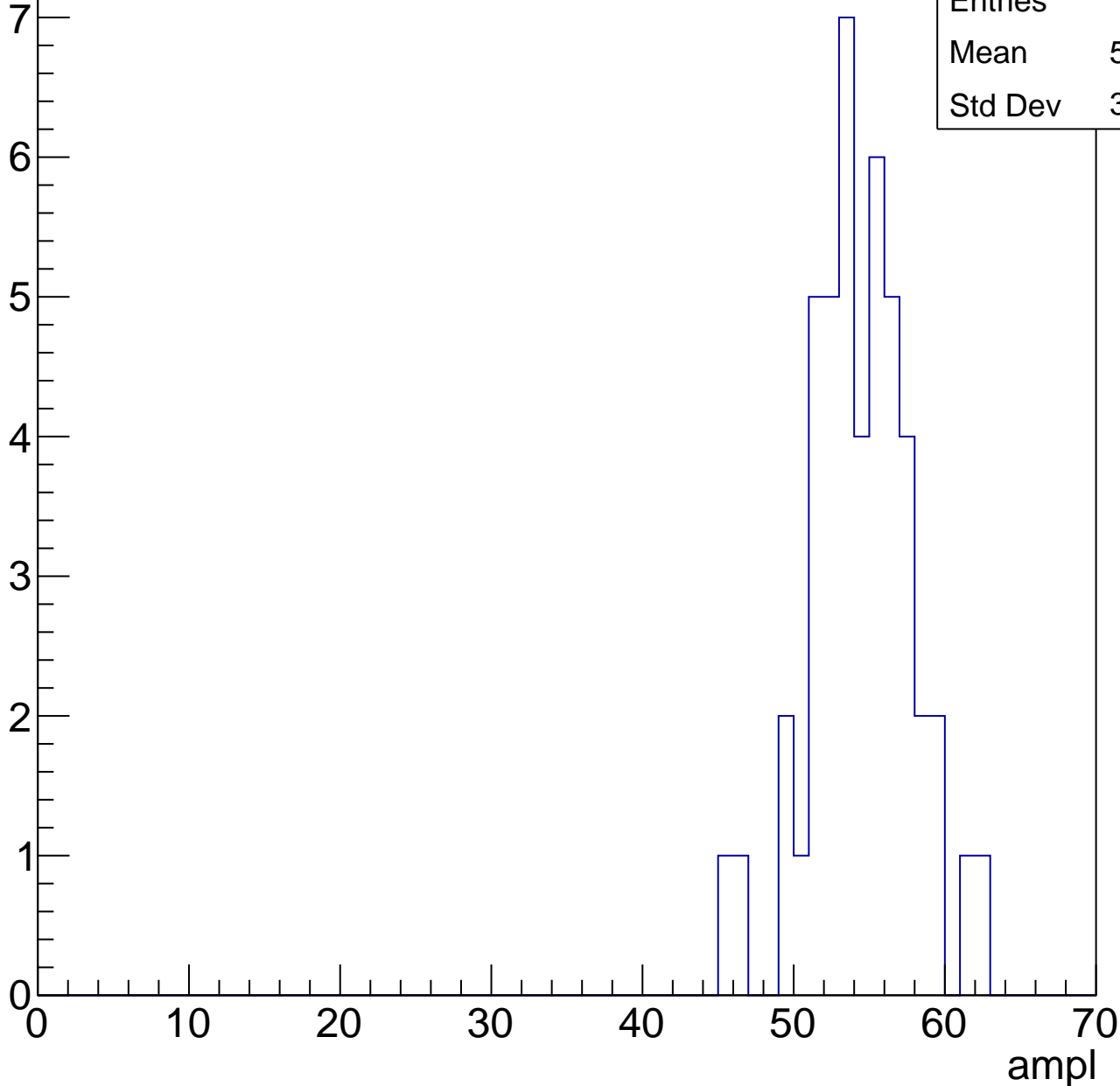


B1L100S, U10-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	53.96
Std Dev	3.389



B1L100S, U10-ch58, adc5

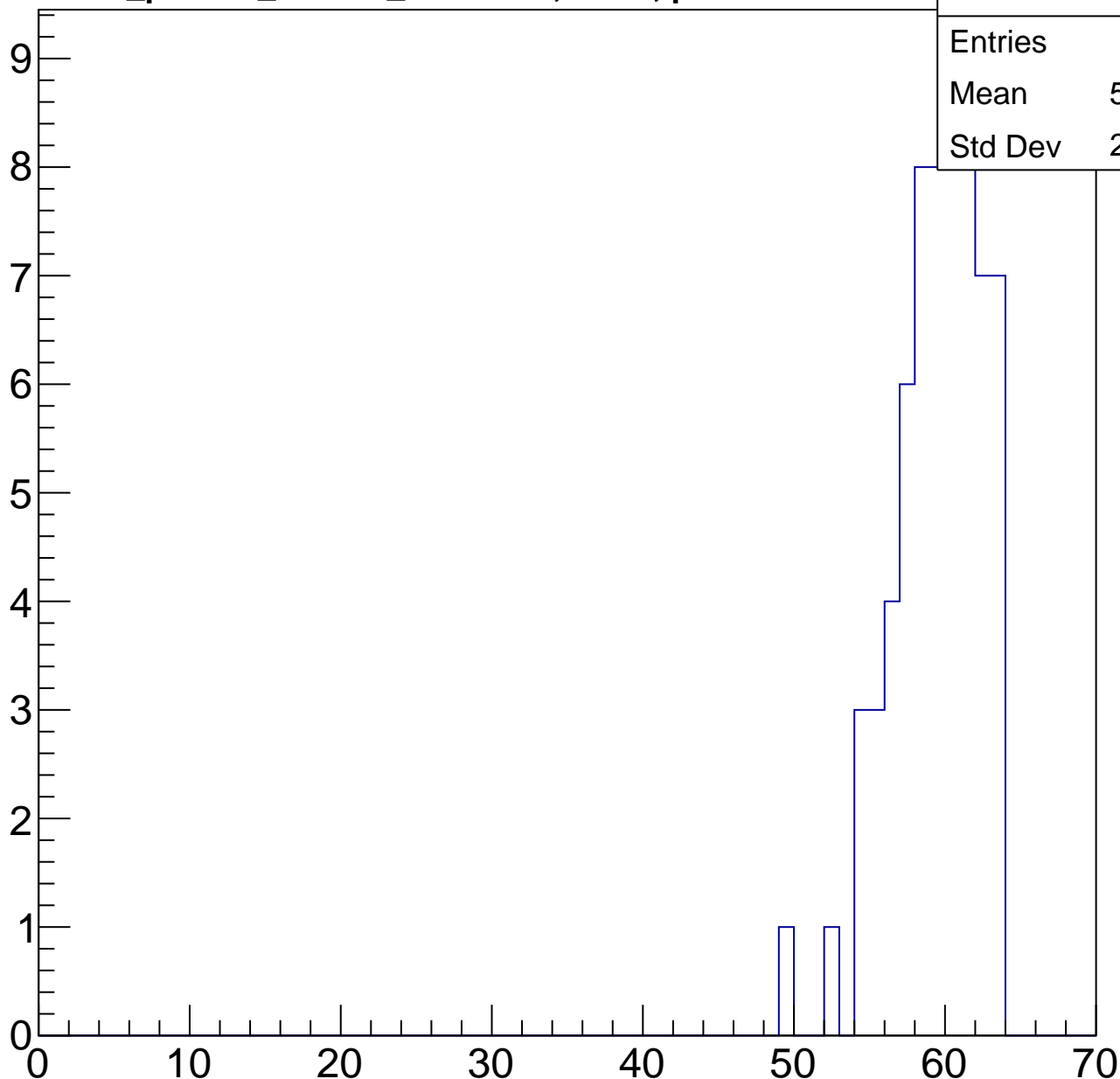
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	58.98
Std Dev	2.933

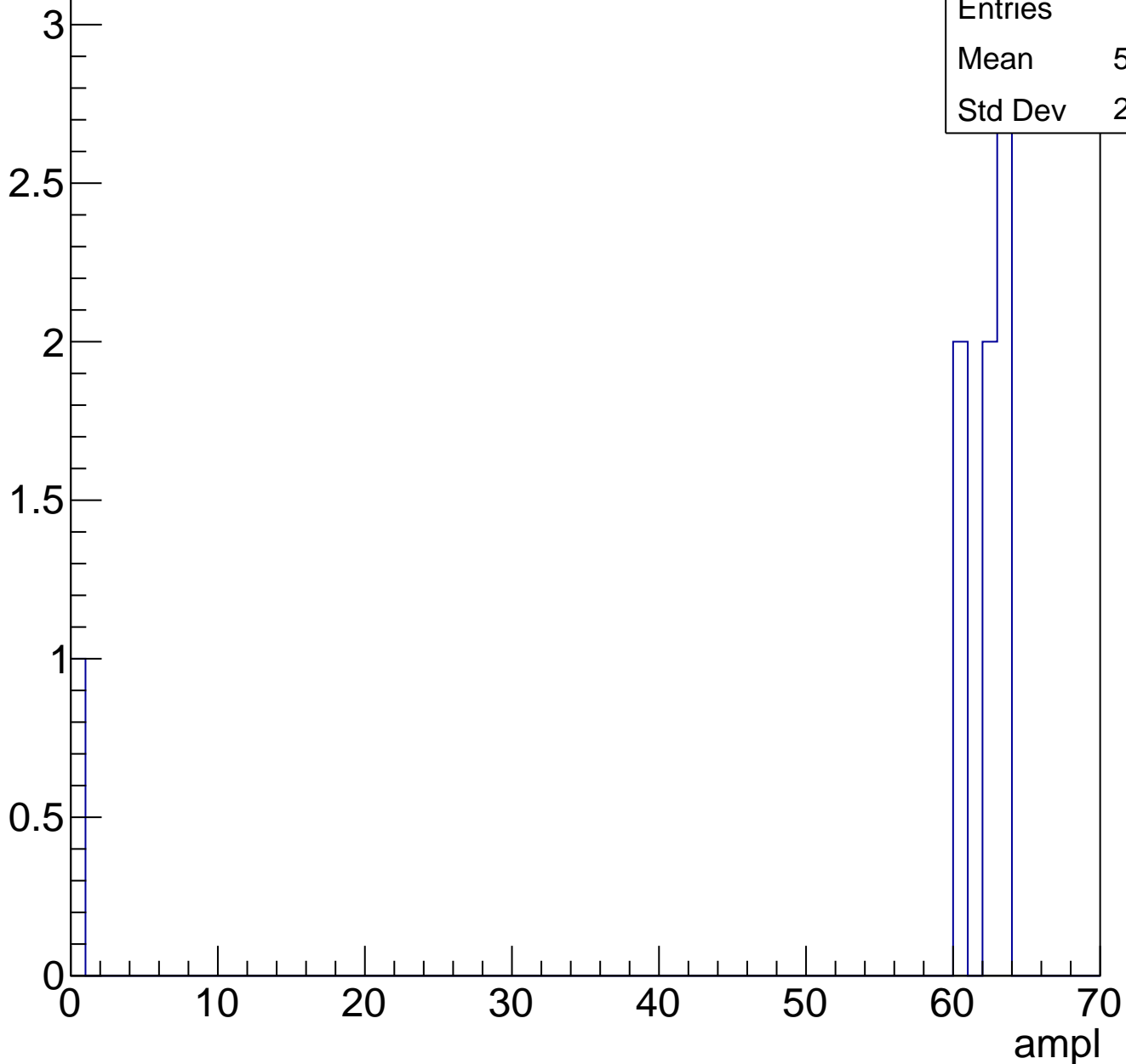
ampl



B1L100S, U10-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	8
Mean	54.12
Std Dev	20.49

B1L100S, U10-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch59, adc0

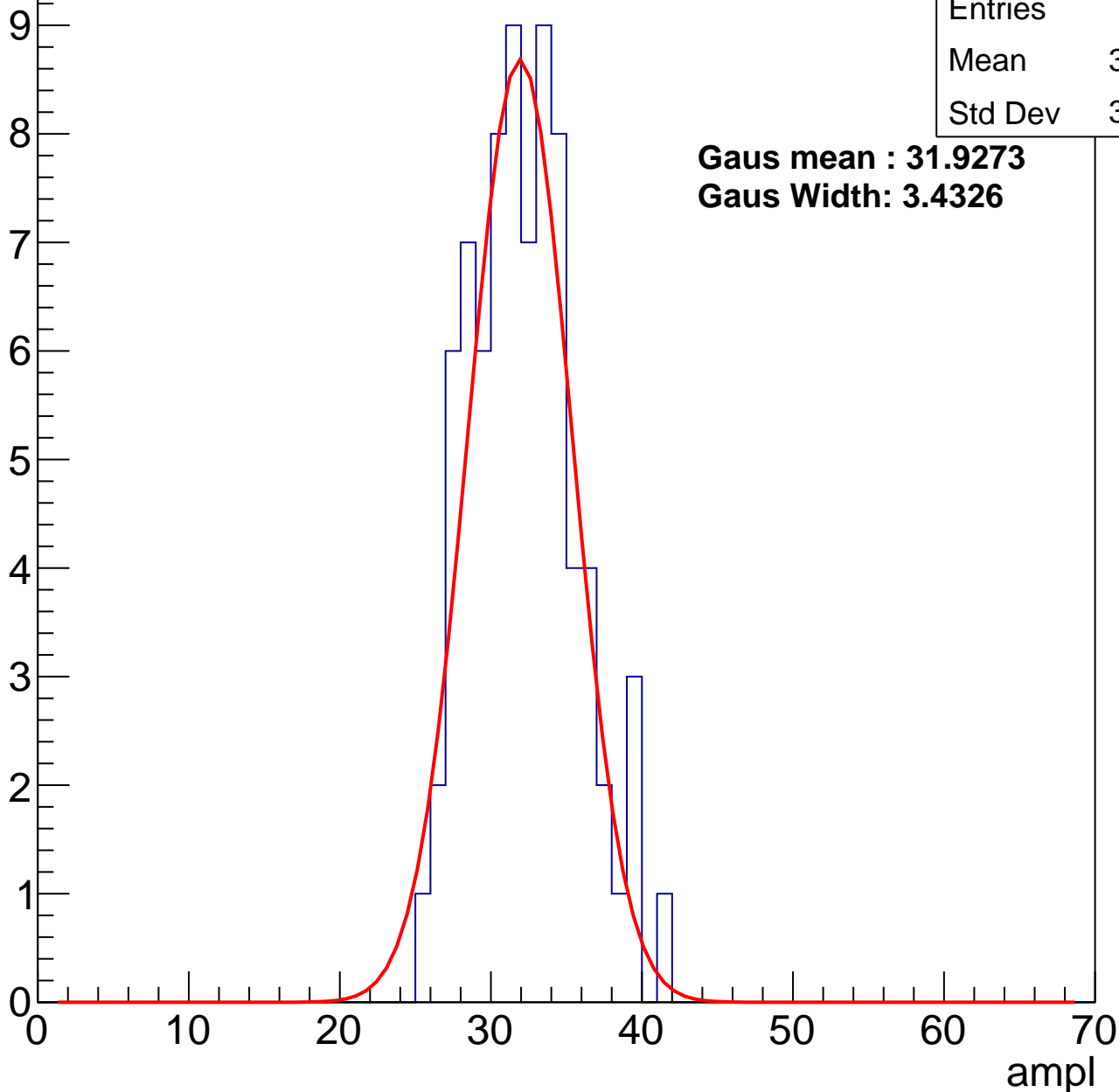
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	31.73
Std Dev	3.448

Gaus mean : 31.9273

Gaus Width: 3.4326



B1L100S, U10-ch59, adc1

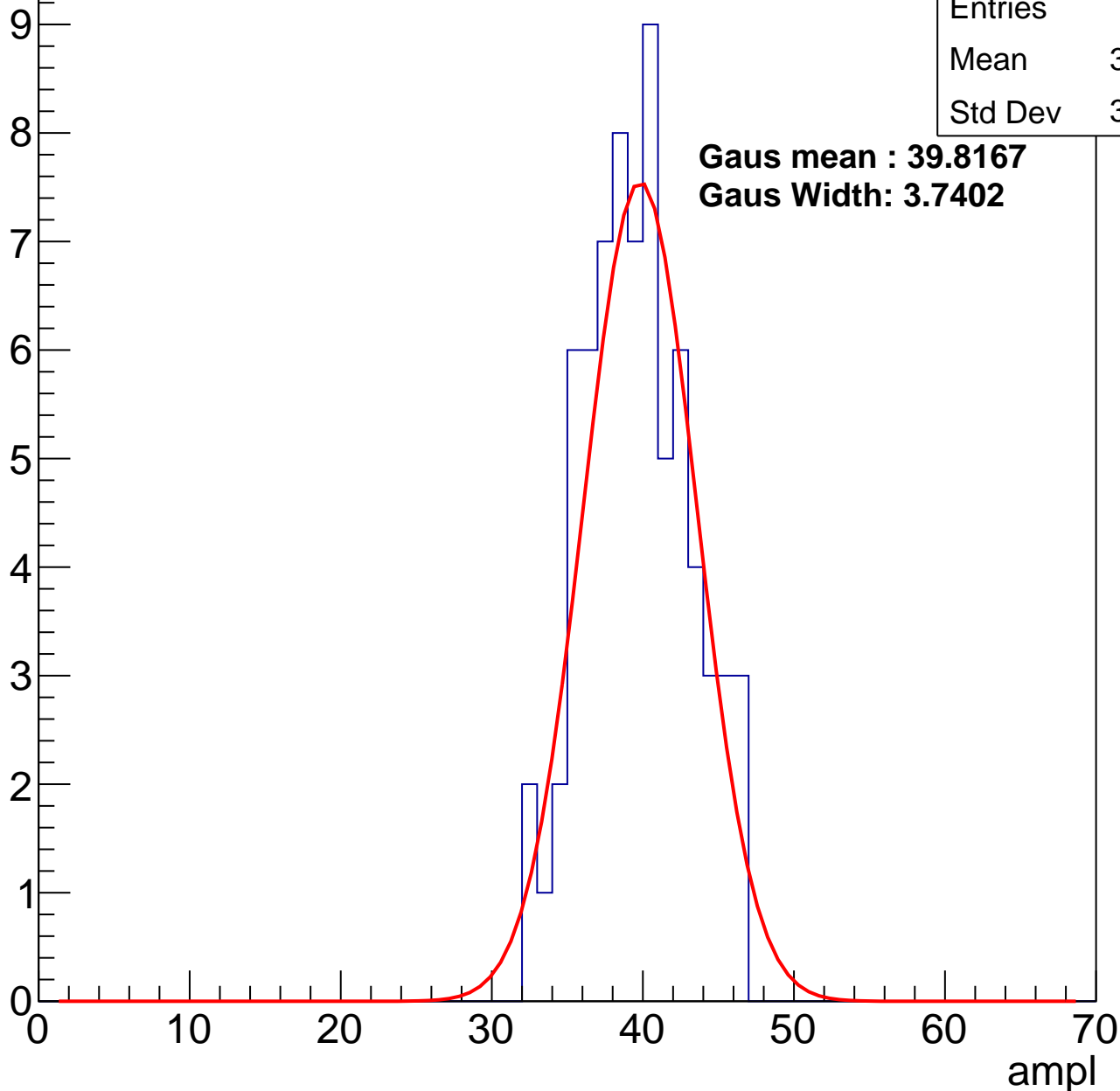
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	39.18
Std Dev	3.425

Gaus mean : 39.8167

Gaus Width: 3.7402



B1L100S, U10-ch59, adc2

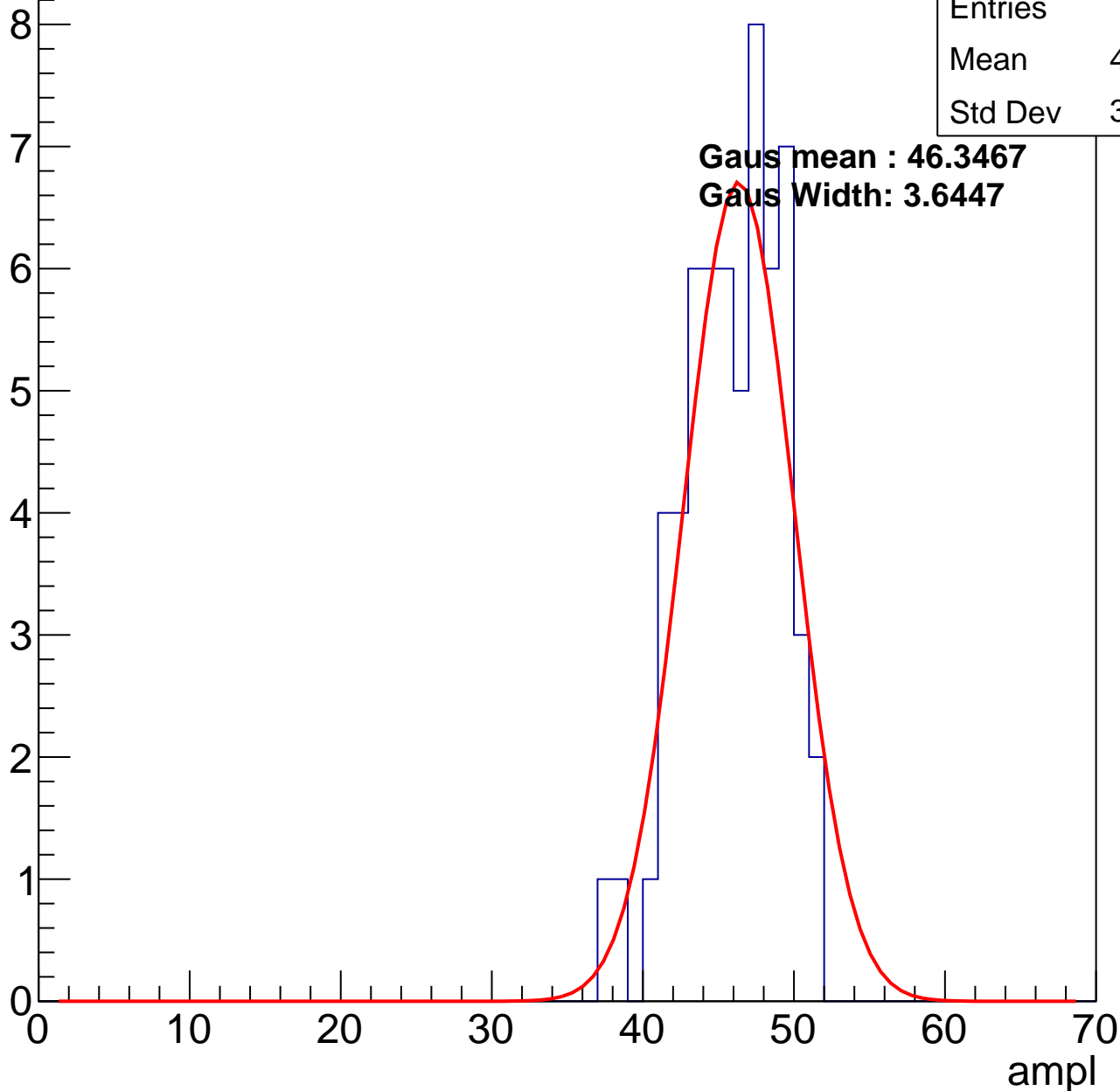
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	45.47
Std Dev	3.175

Gaus mean : 46.3467

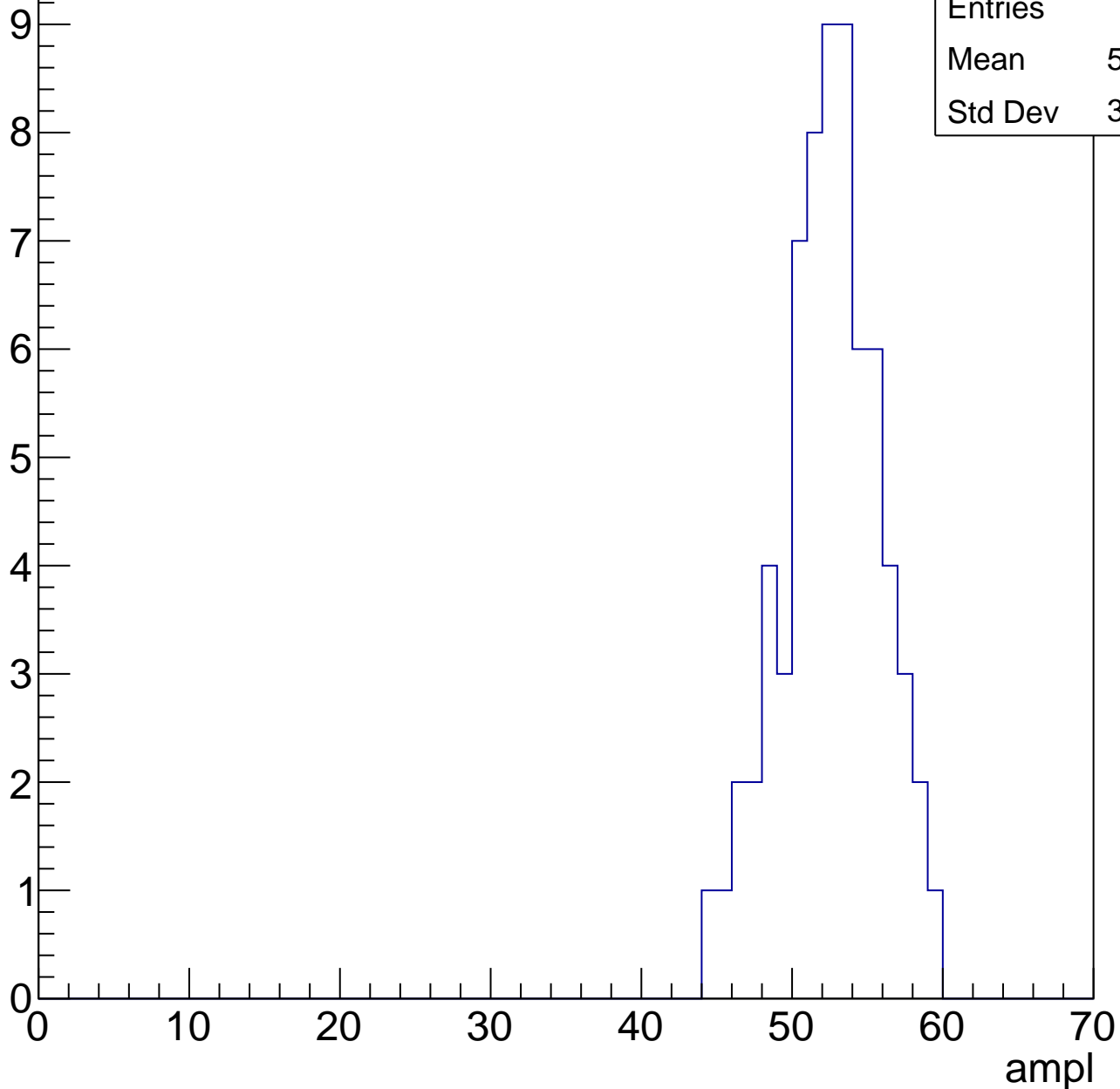
Gaus Width: 3.6447



B1L100S, U10-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

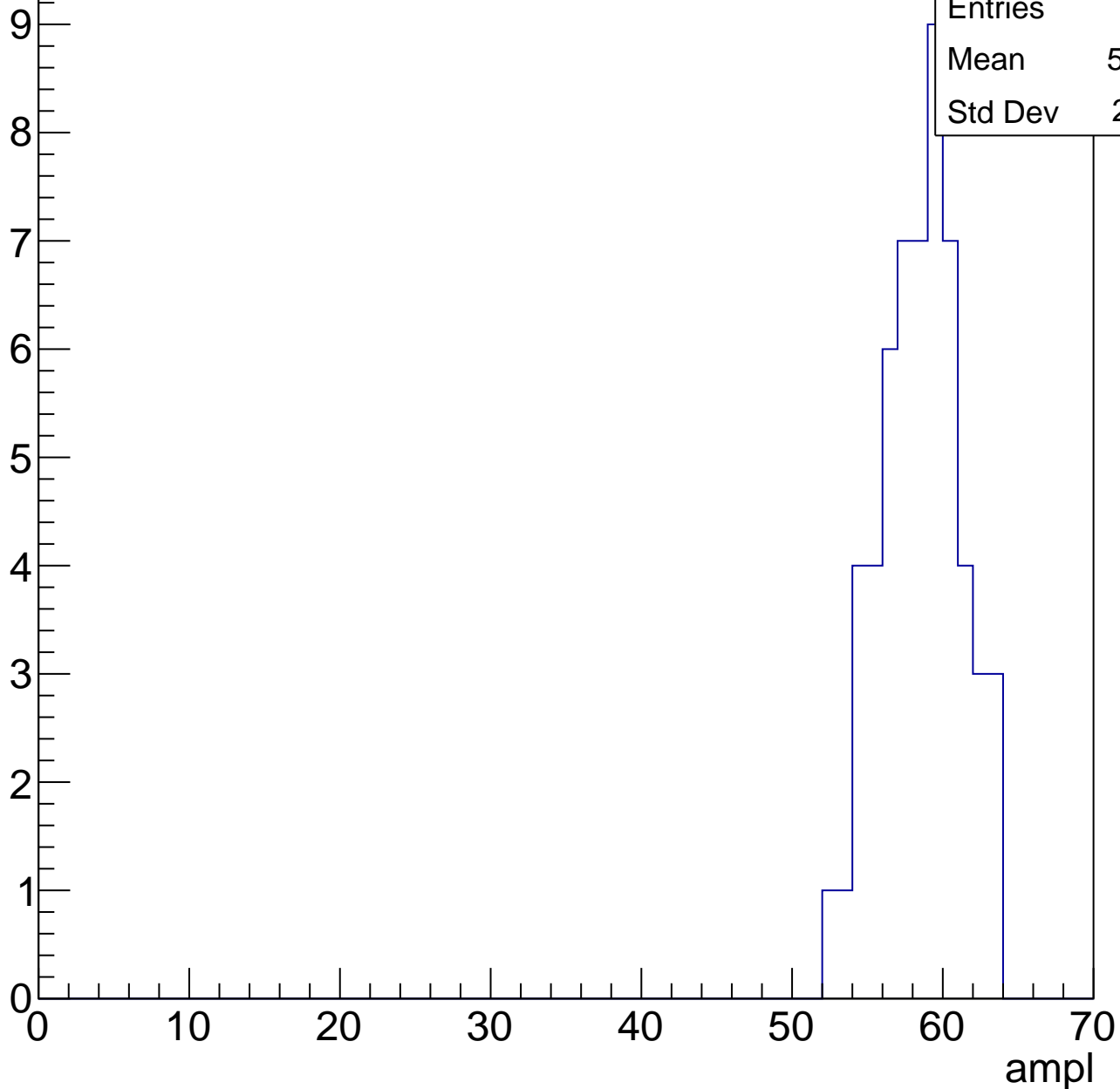


Entries	68
Mean	52.07
Std Dev	3.228

B1L100S, U10-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



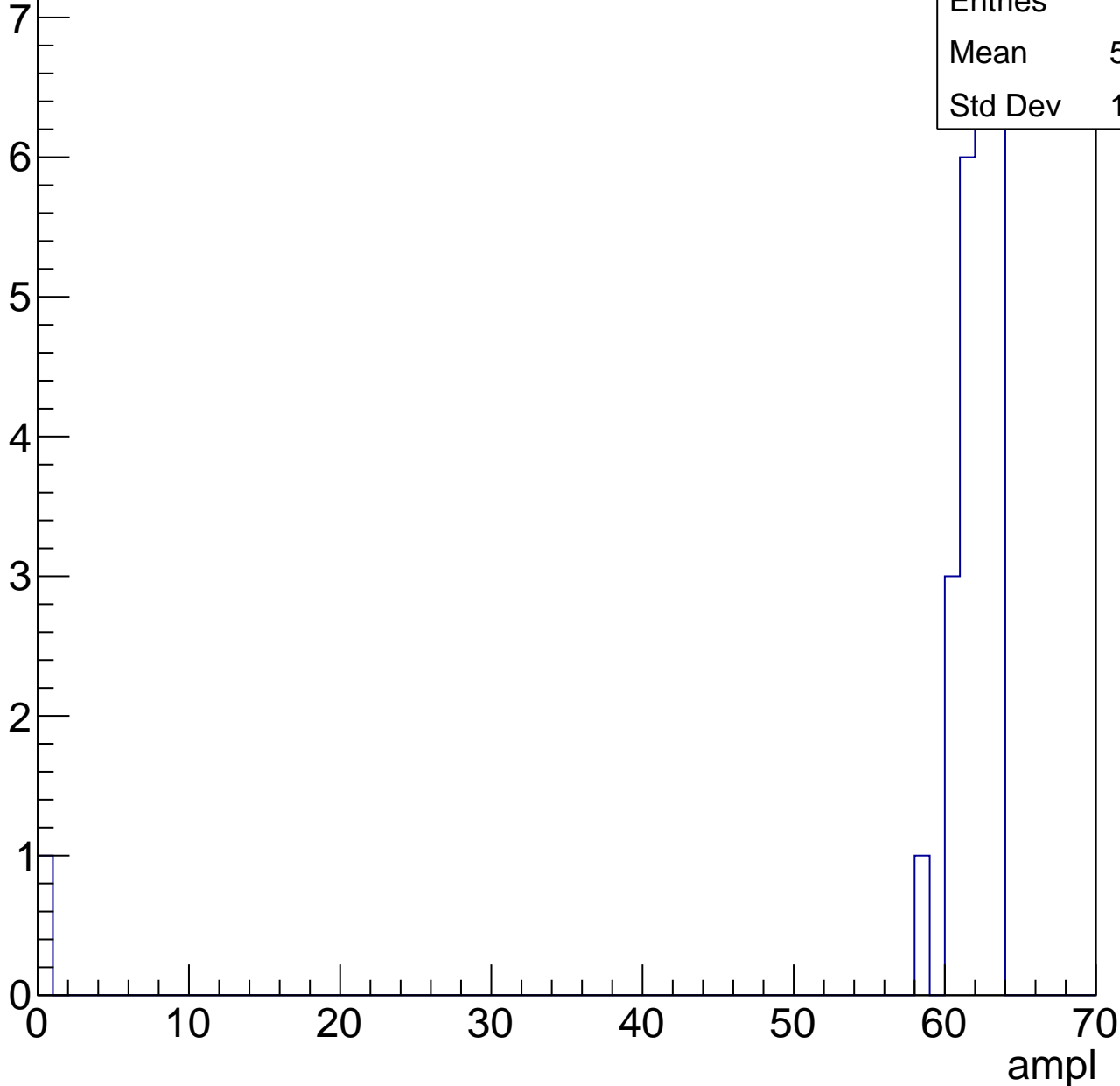
Entries	56
Mean	58.07
Std Dev	2.631

B1L100S, U10-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	59.16
Std Dev	12.14



B1L100S, U10-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch60, adc0

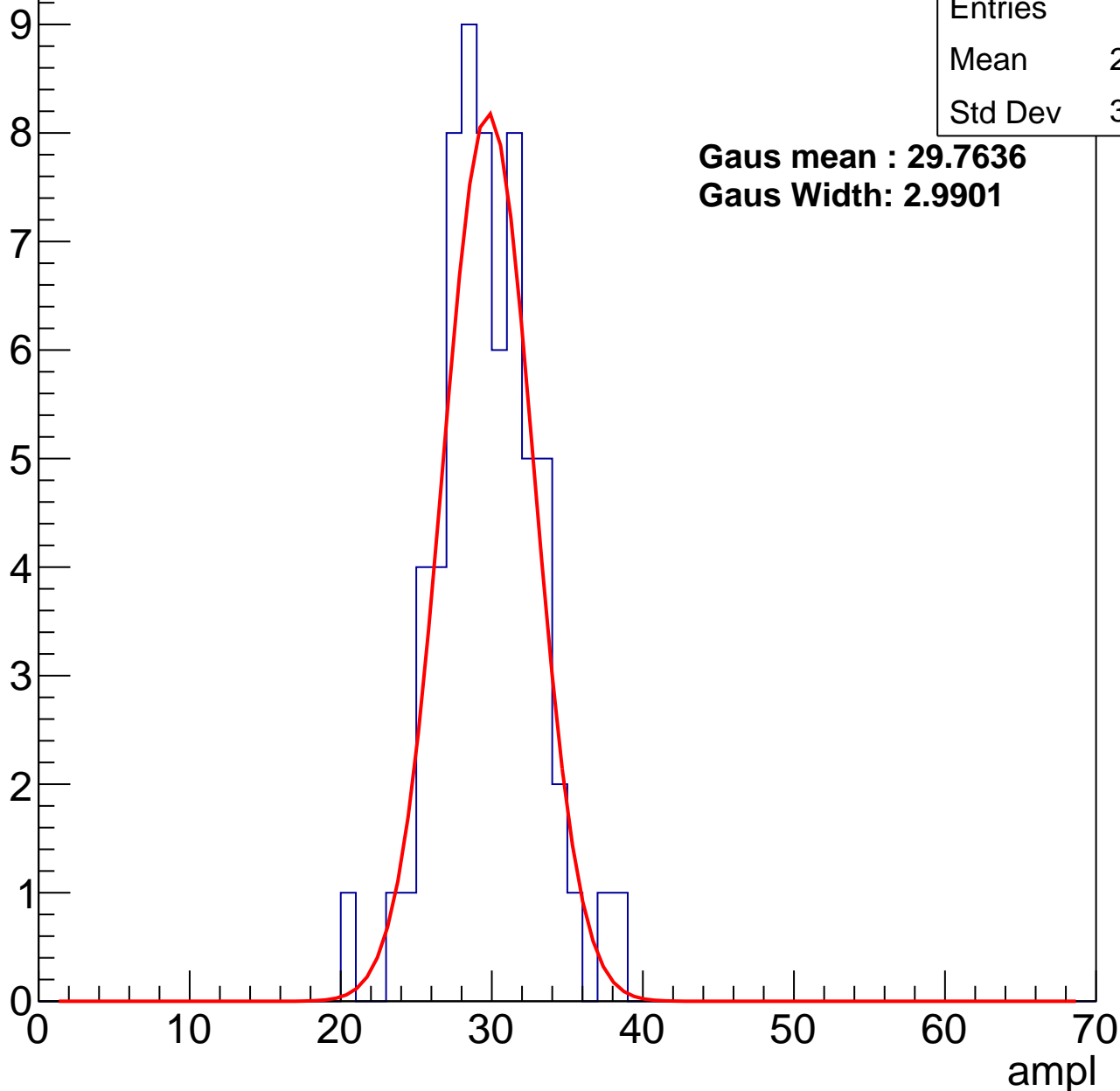
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.26
Std Dev	3.217

Gaus mean : 29.7636

Gaus Width: 2.9901



B1L100S, U10-ch60, adc1

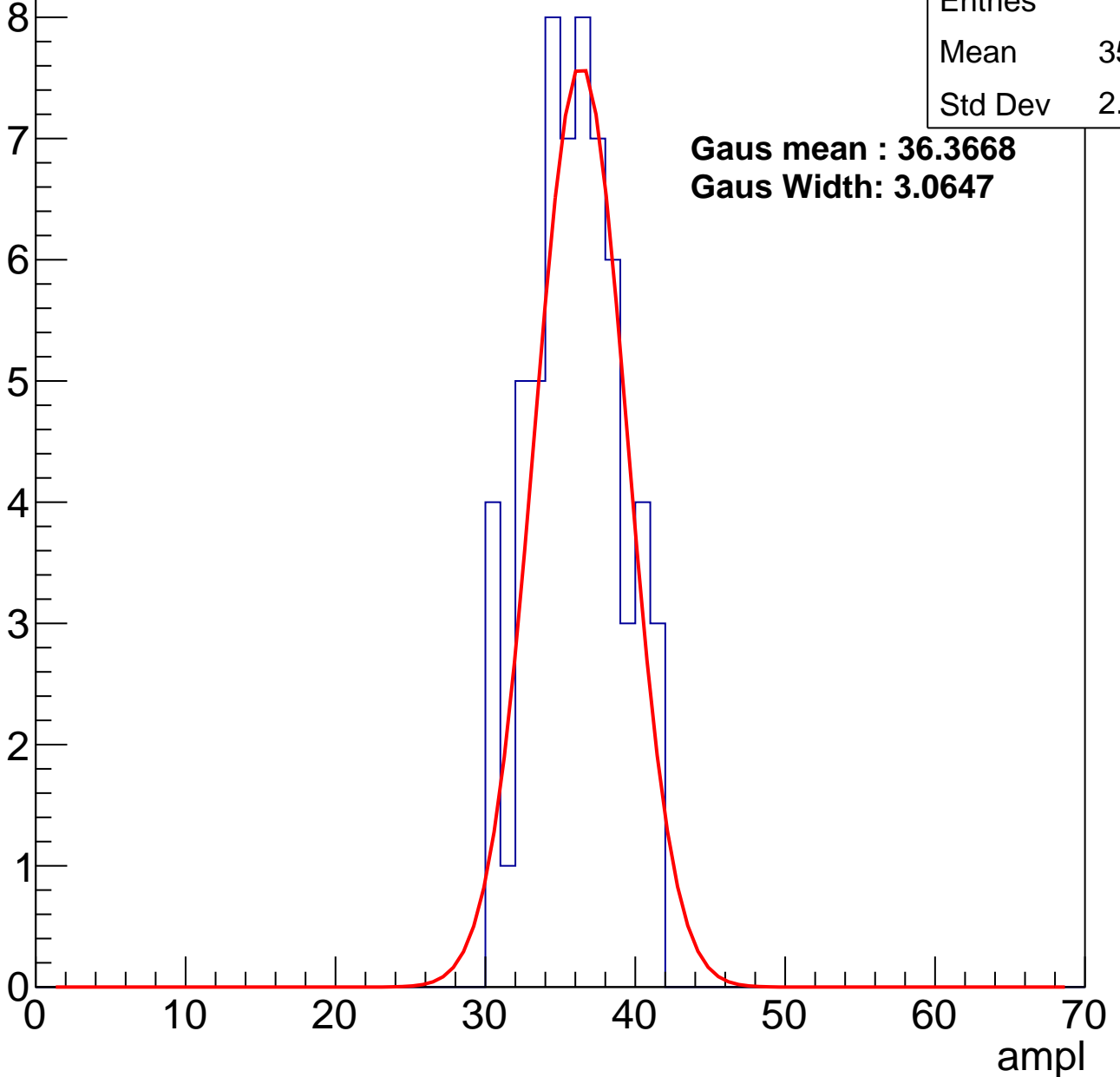
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.54
Std Dev	2.912

Gaus mean : 36.3668

Gaus Width: 3.0647



B1L100S, U10-ch60, adc2

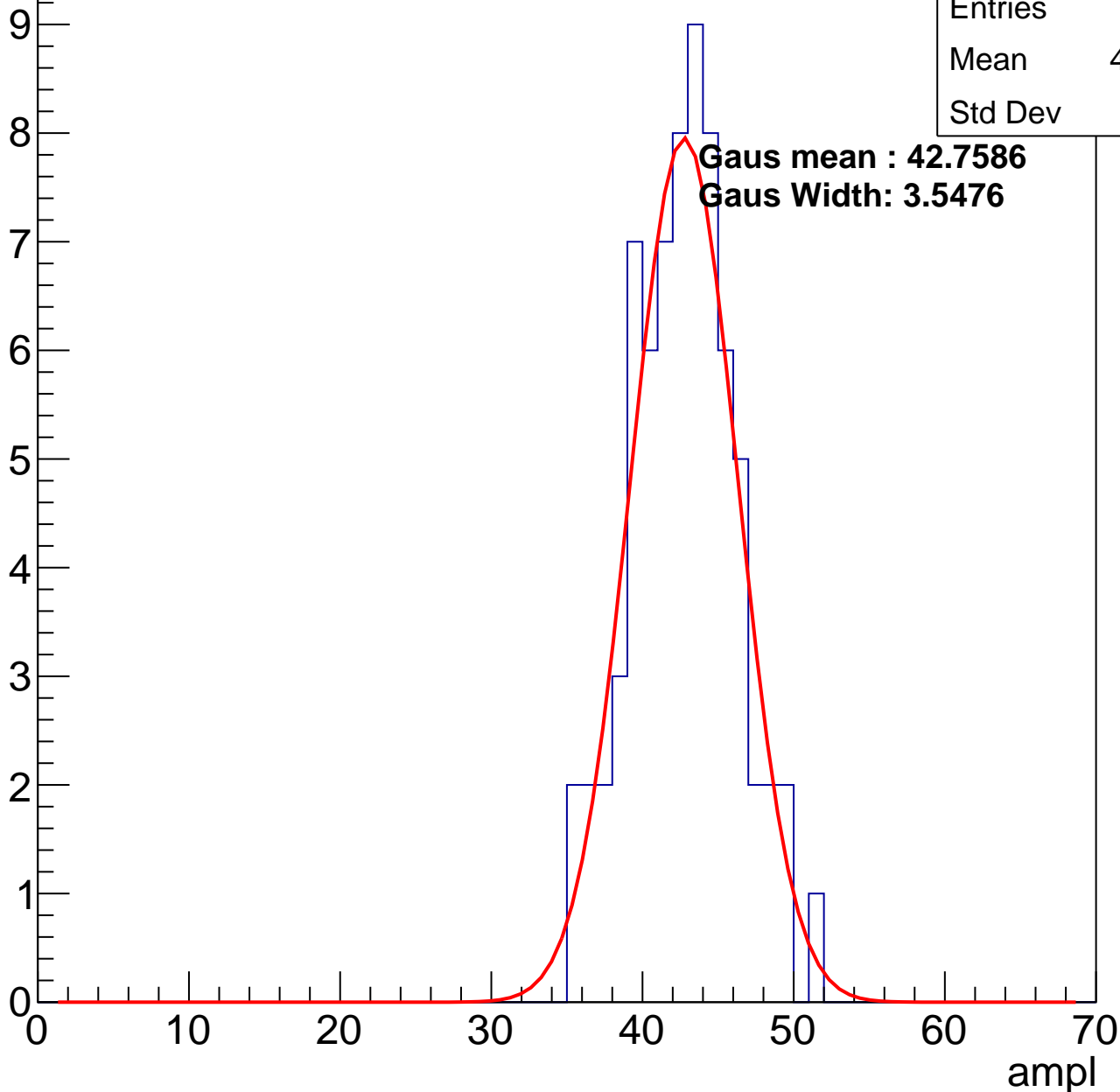
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.28
Std Dev	3.4

Gaus mean : 42.7586

Gaus Width: 3.5476

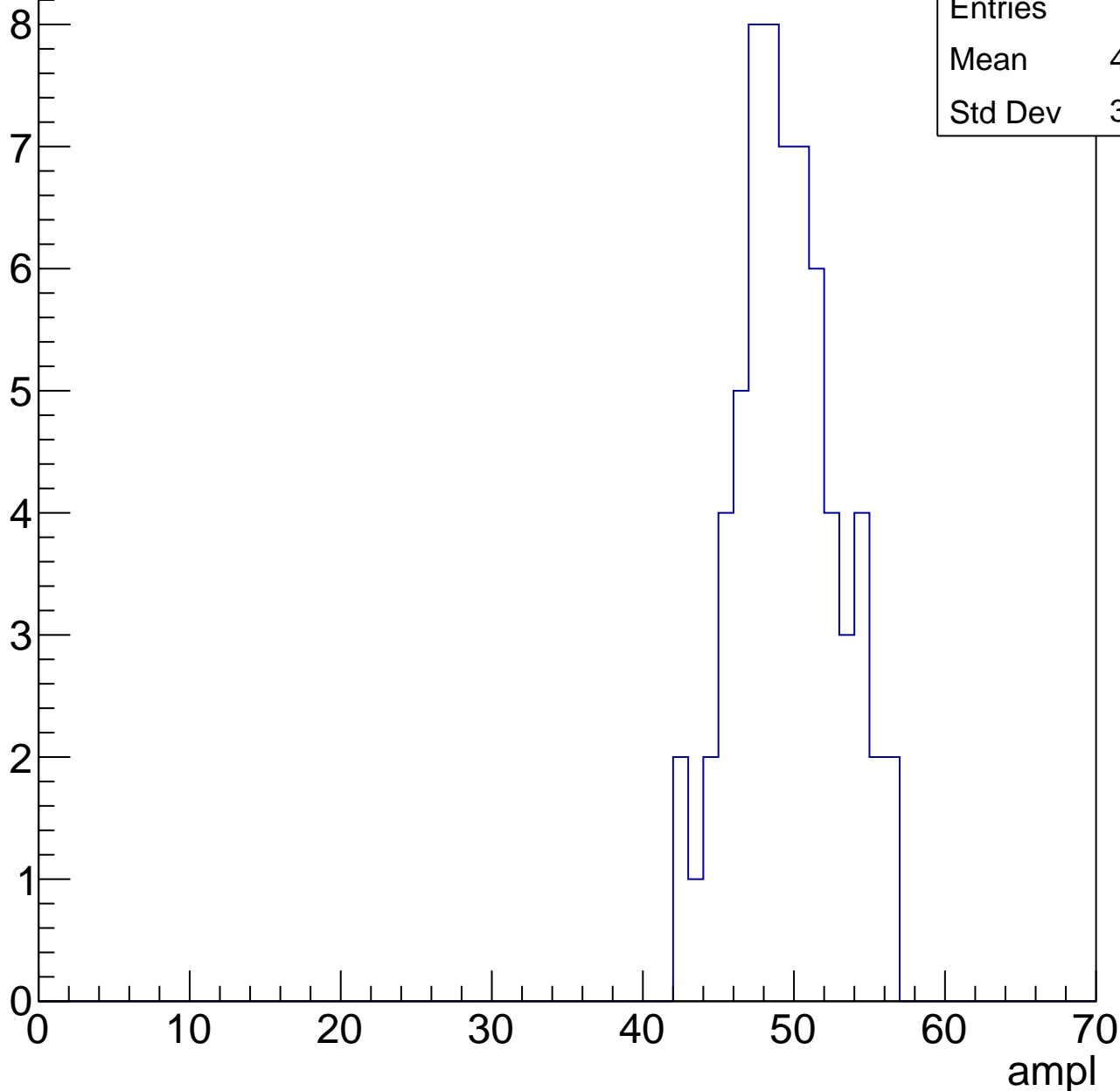


B1L100S, U10-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.06
Std Dev	3.323

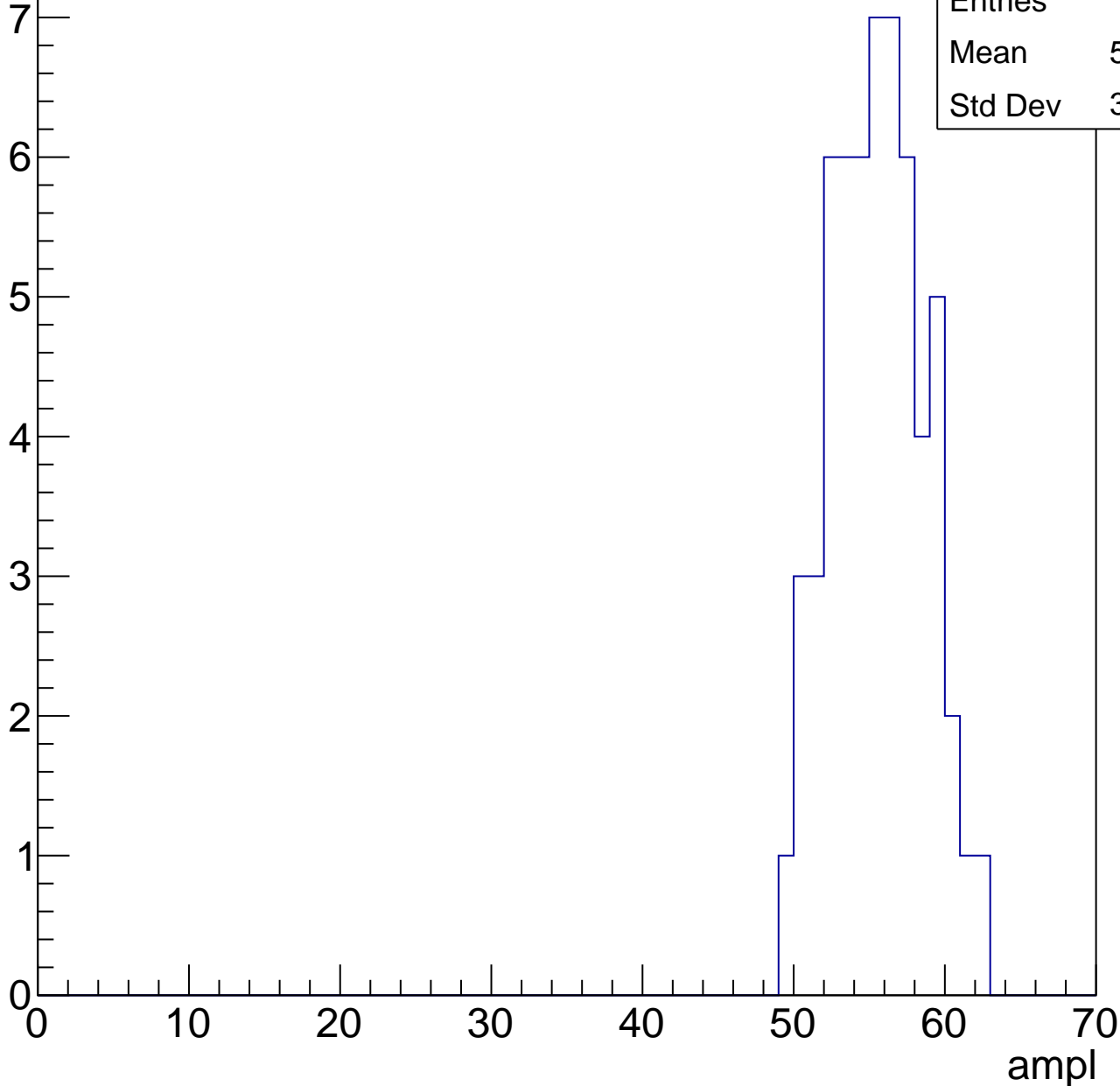


B1L100S, U10-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

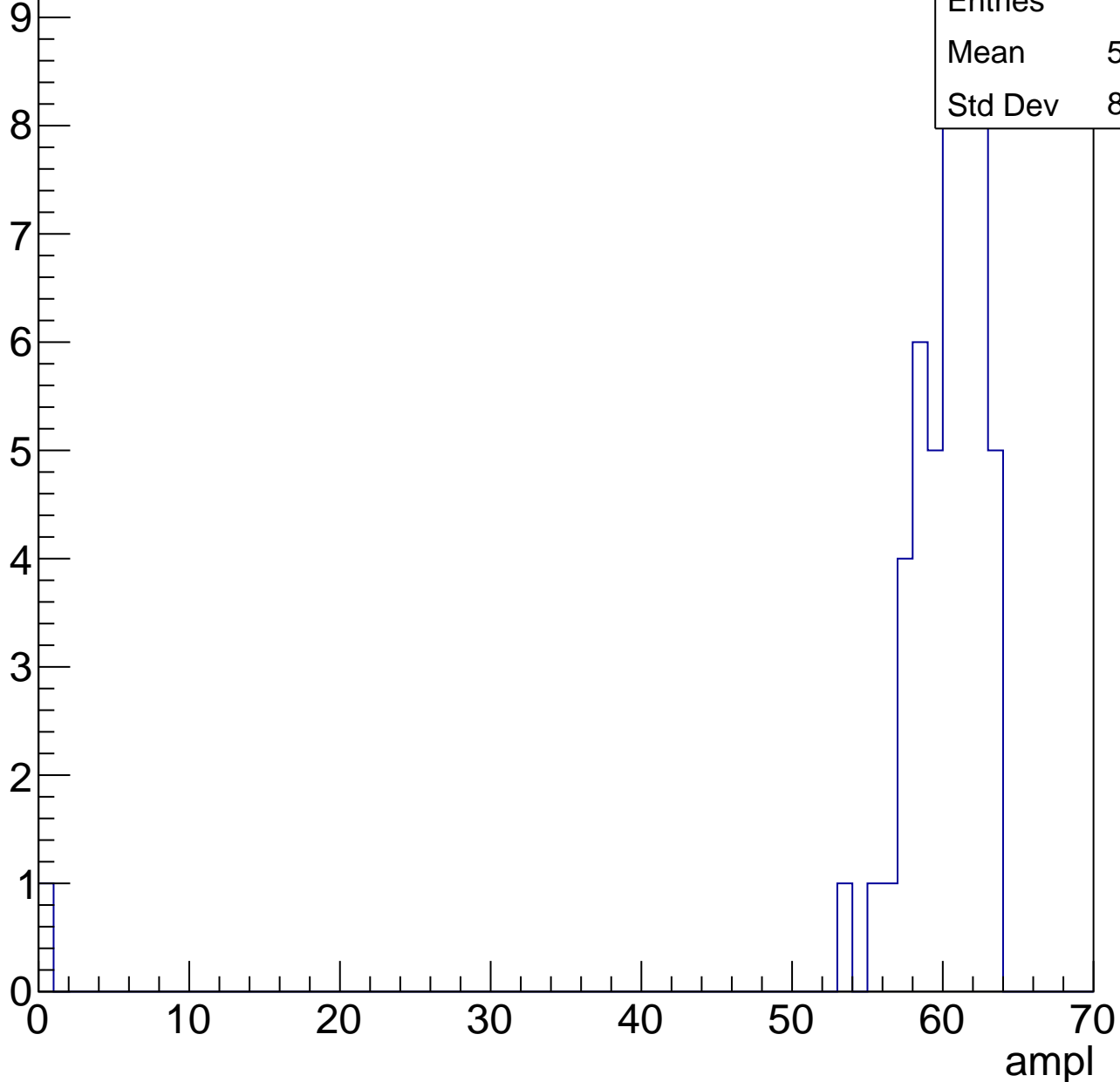
Entries	58
Mean	55.09
Std Dev	3.007



B1L100S, U10-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

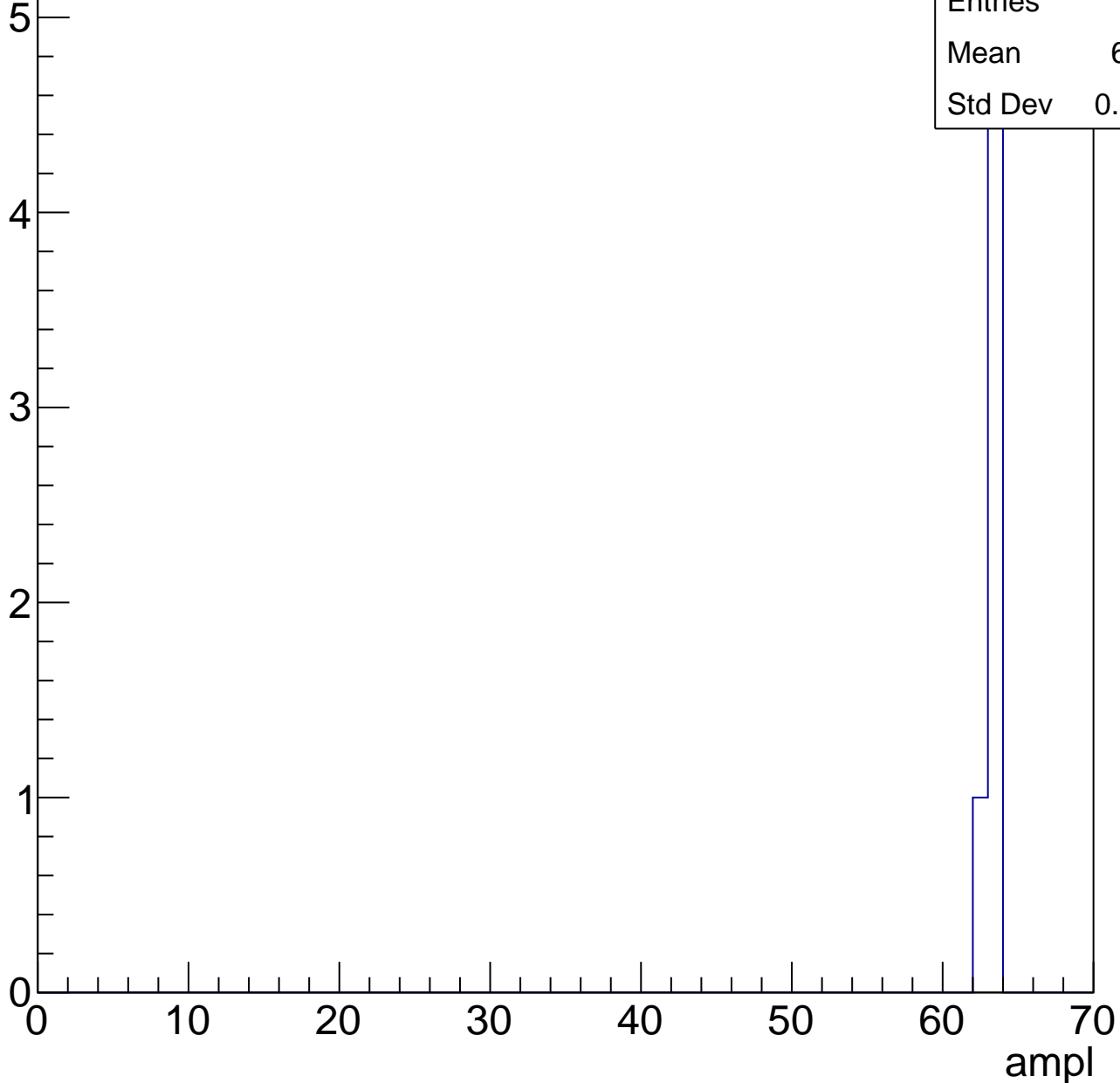


B1L100S, U10-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L100S, U10-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch61, adc0

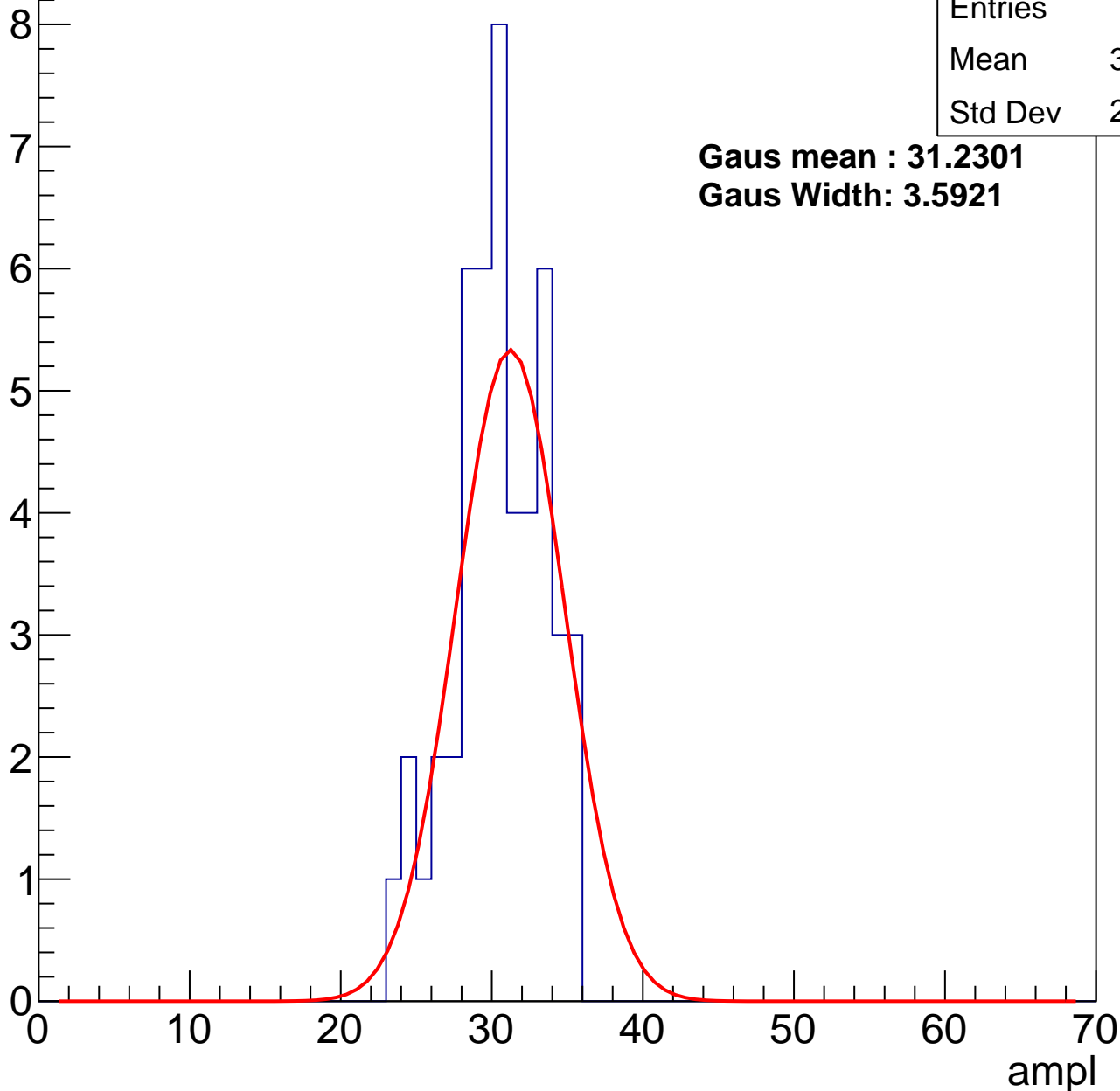
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	30.02
Std Dev	2.969

Gaus mean : 31.2301

Gaus Width: 3.5921

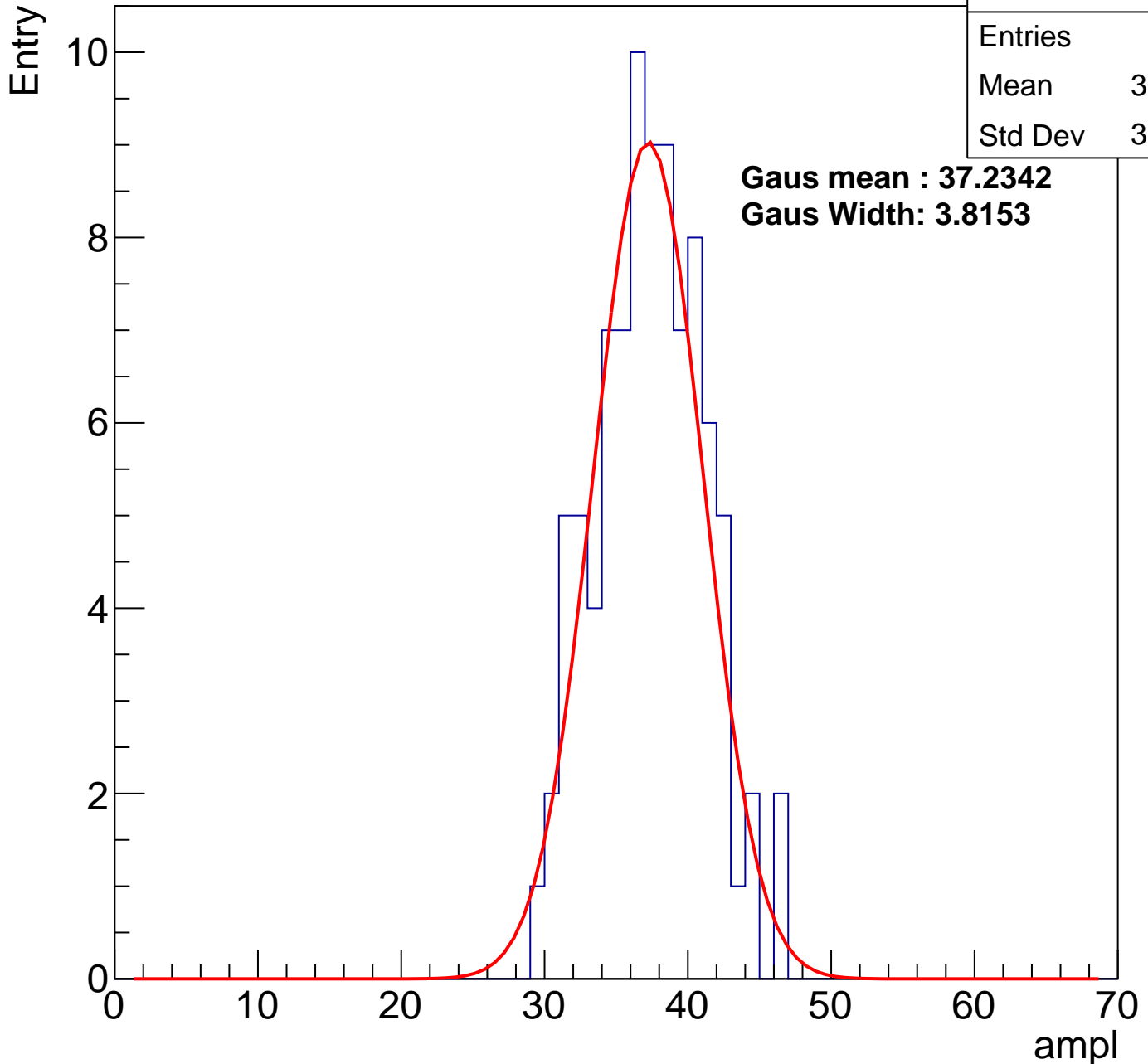


B1L100S, U10-ch61, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	36.96
Std Dev	3.738

Gaus mean : 37.2342
Gaus Width: 3.8153



B1L100S, U10-ch61, adc2

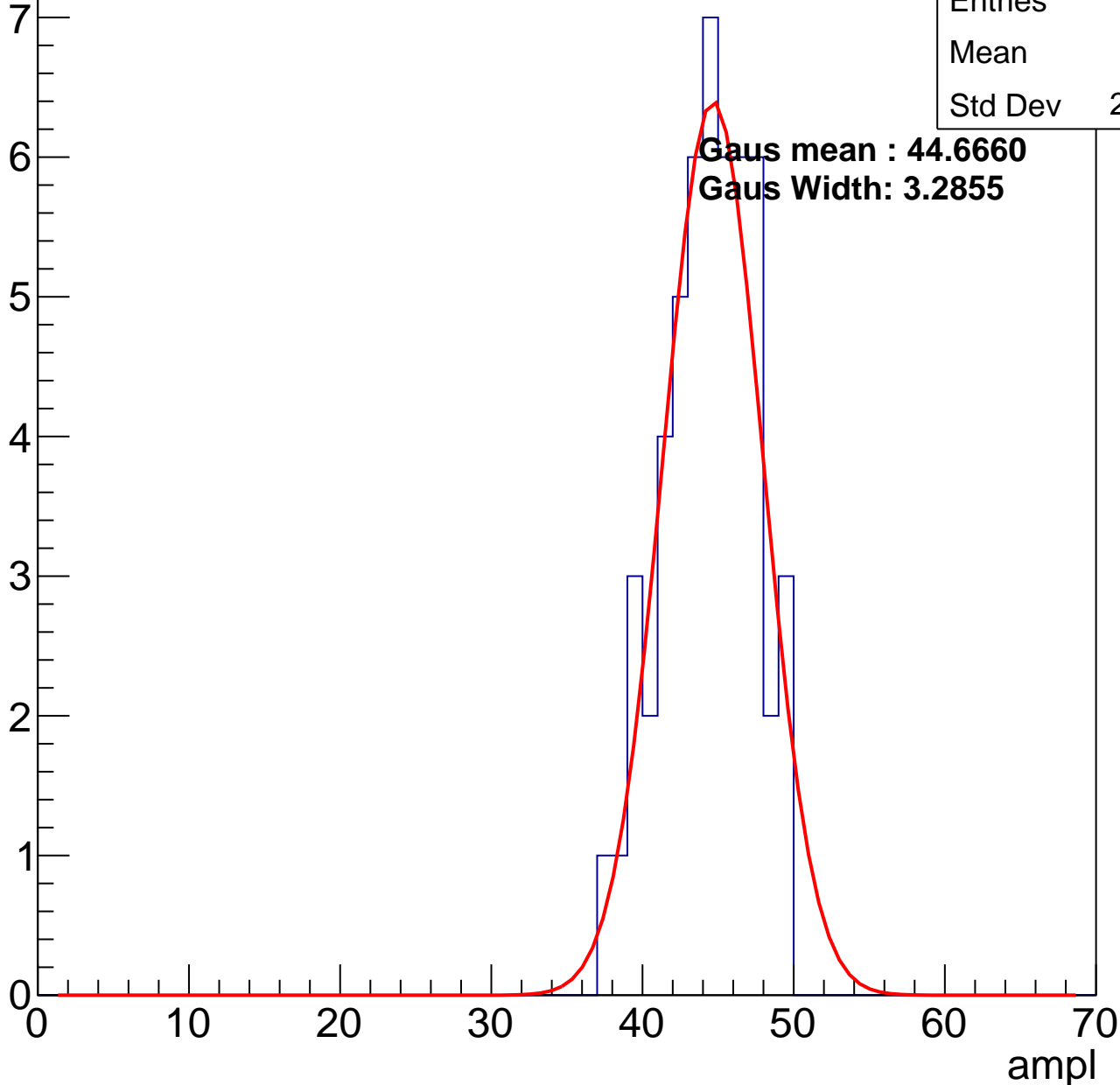
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.9
Std Dev	2.924

Gaus mean : 44.6660

Gaus Width: 3.2855

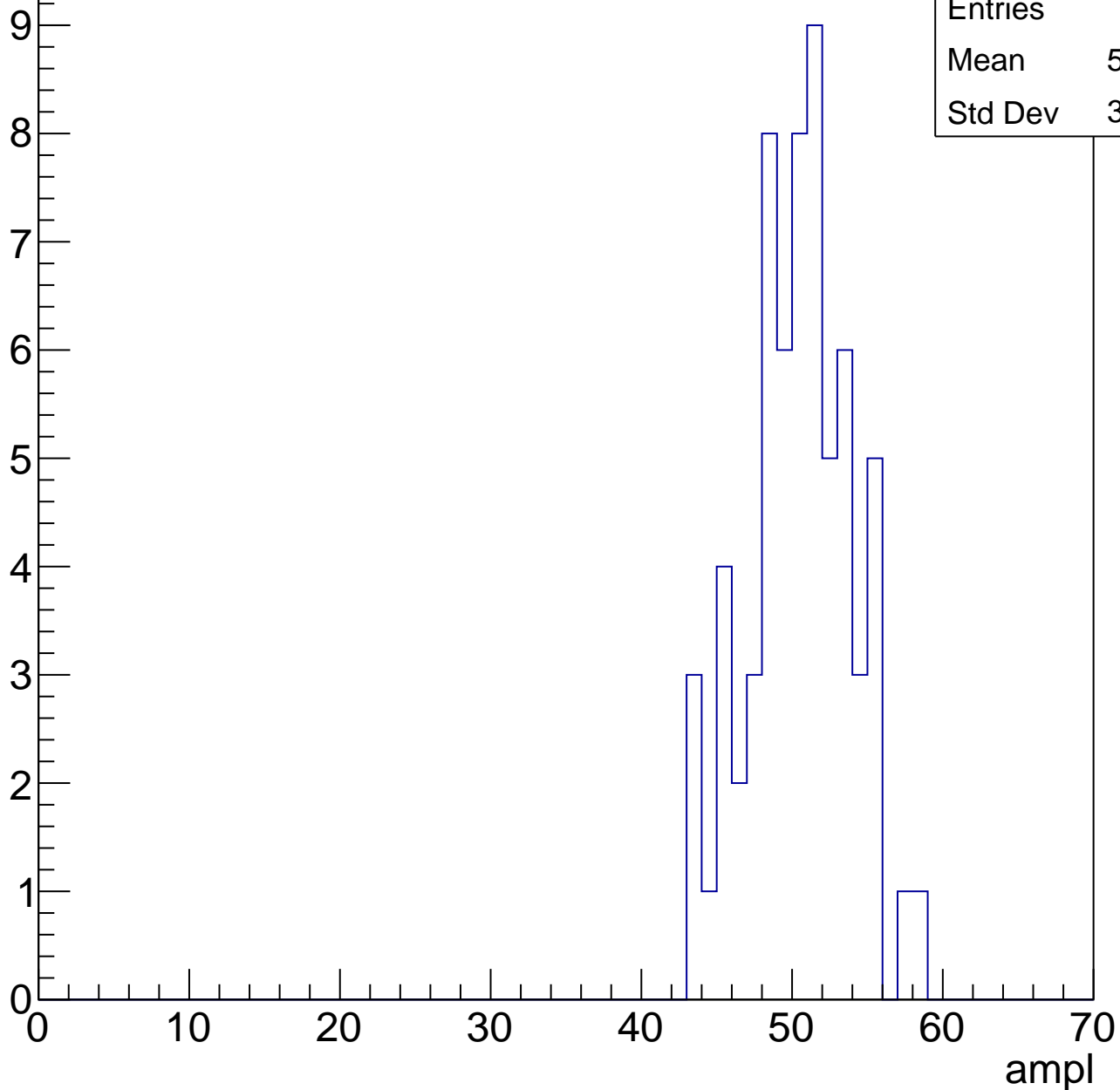


B1L100S, U10-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

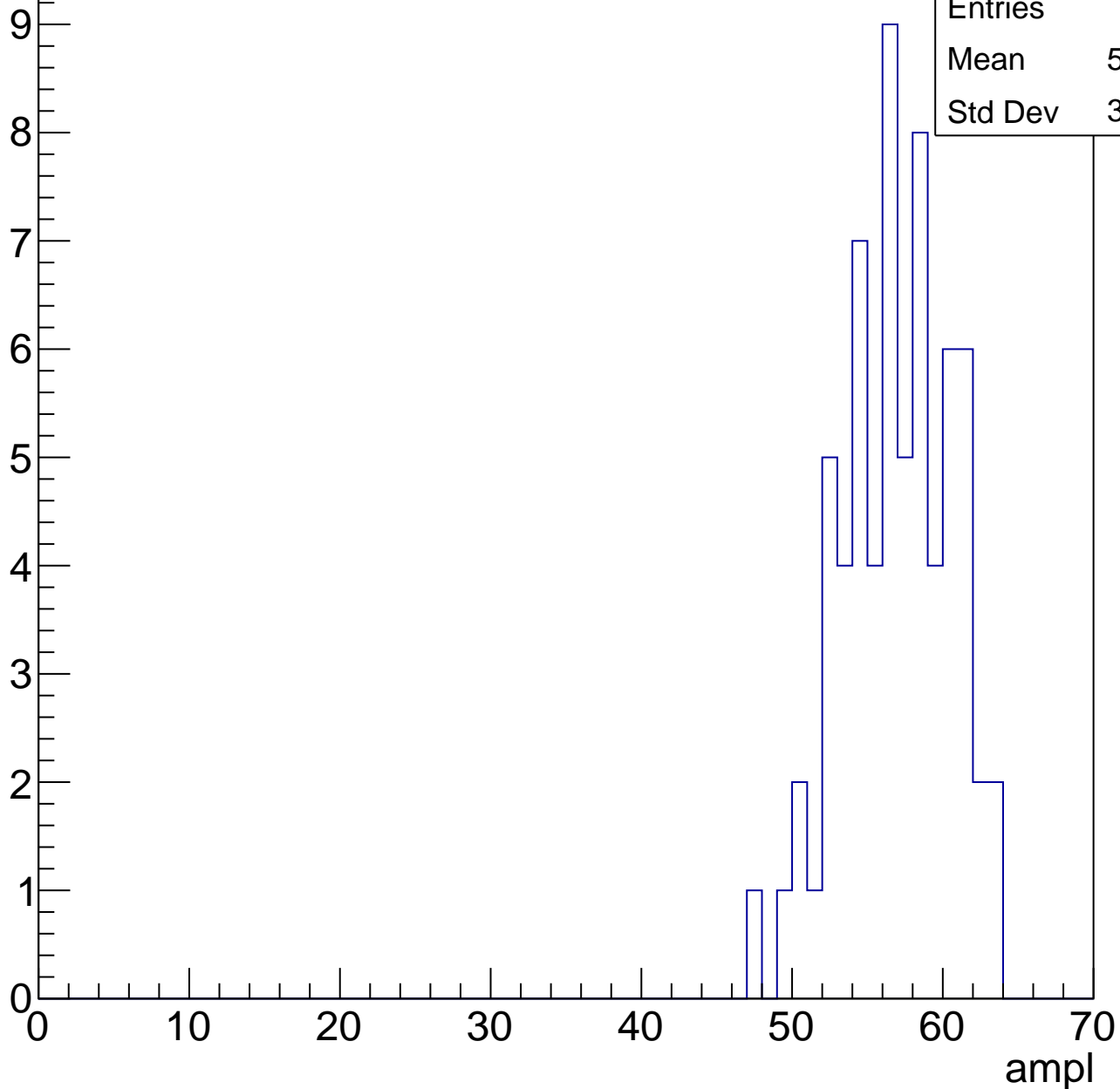
Entries	65
Mean	50.05
Std Dev	3.394



B1L100S, U10-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



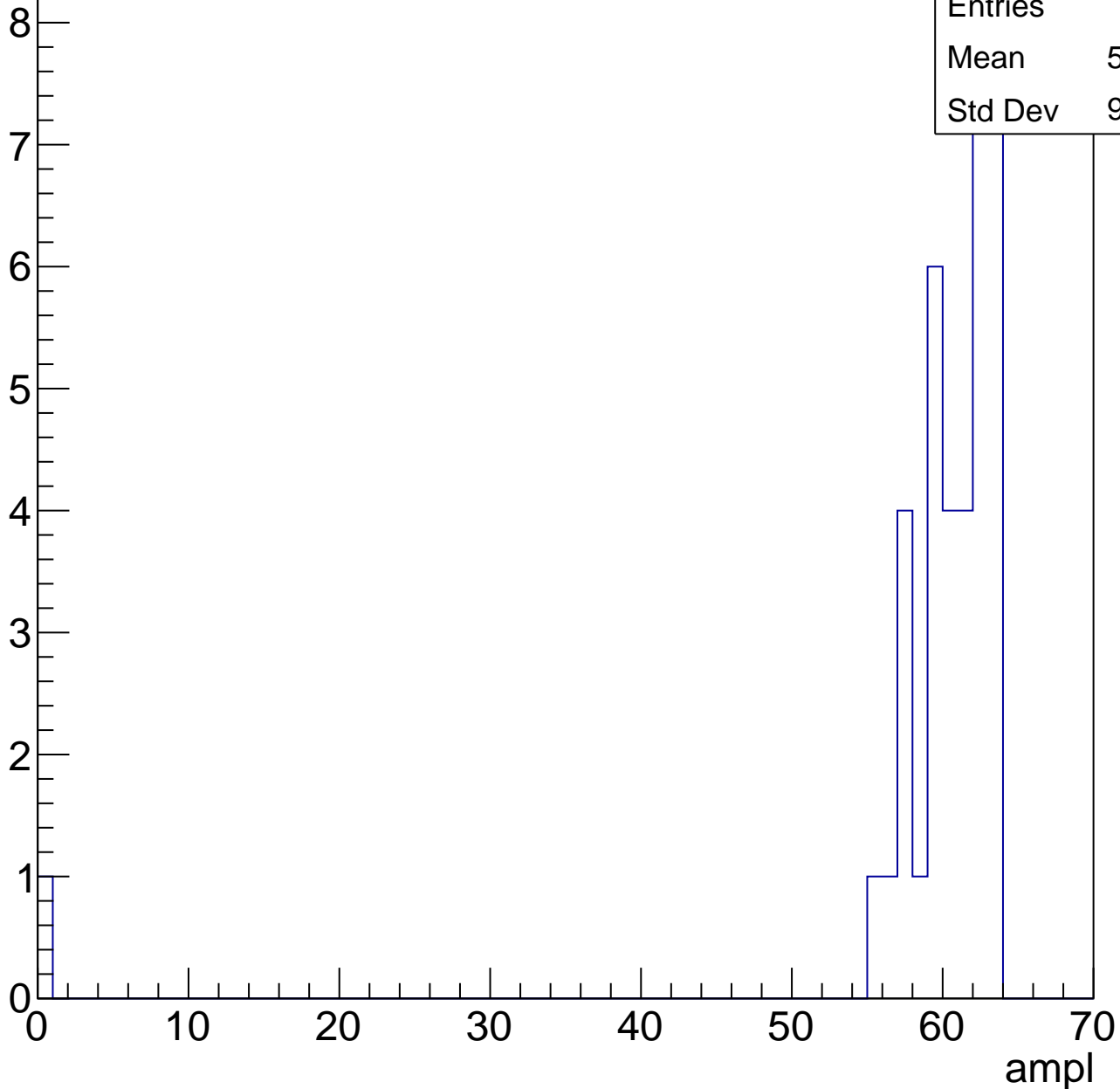
Entries	67
Mean	56.45
Std Dev	3.563

B1L100S, U10-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.82
Std Dev	9.923



B1L100S, U10-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch62, adc0

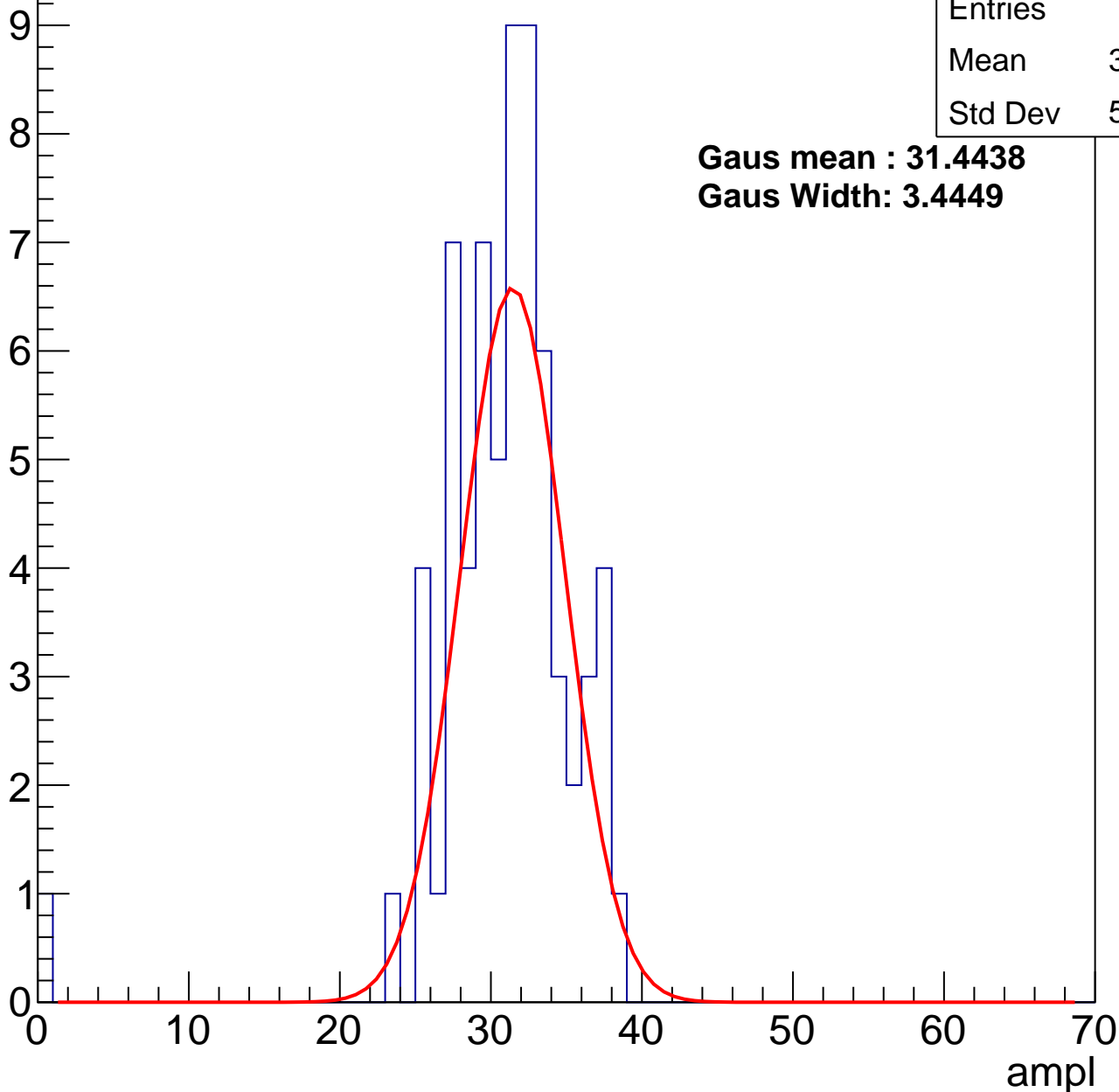
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.36
Std Dev	5.048

Gaus mean : 31.4438

Gaus Width: 3.4449



B1L100S, U10-ch62, adc1

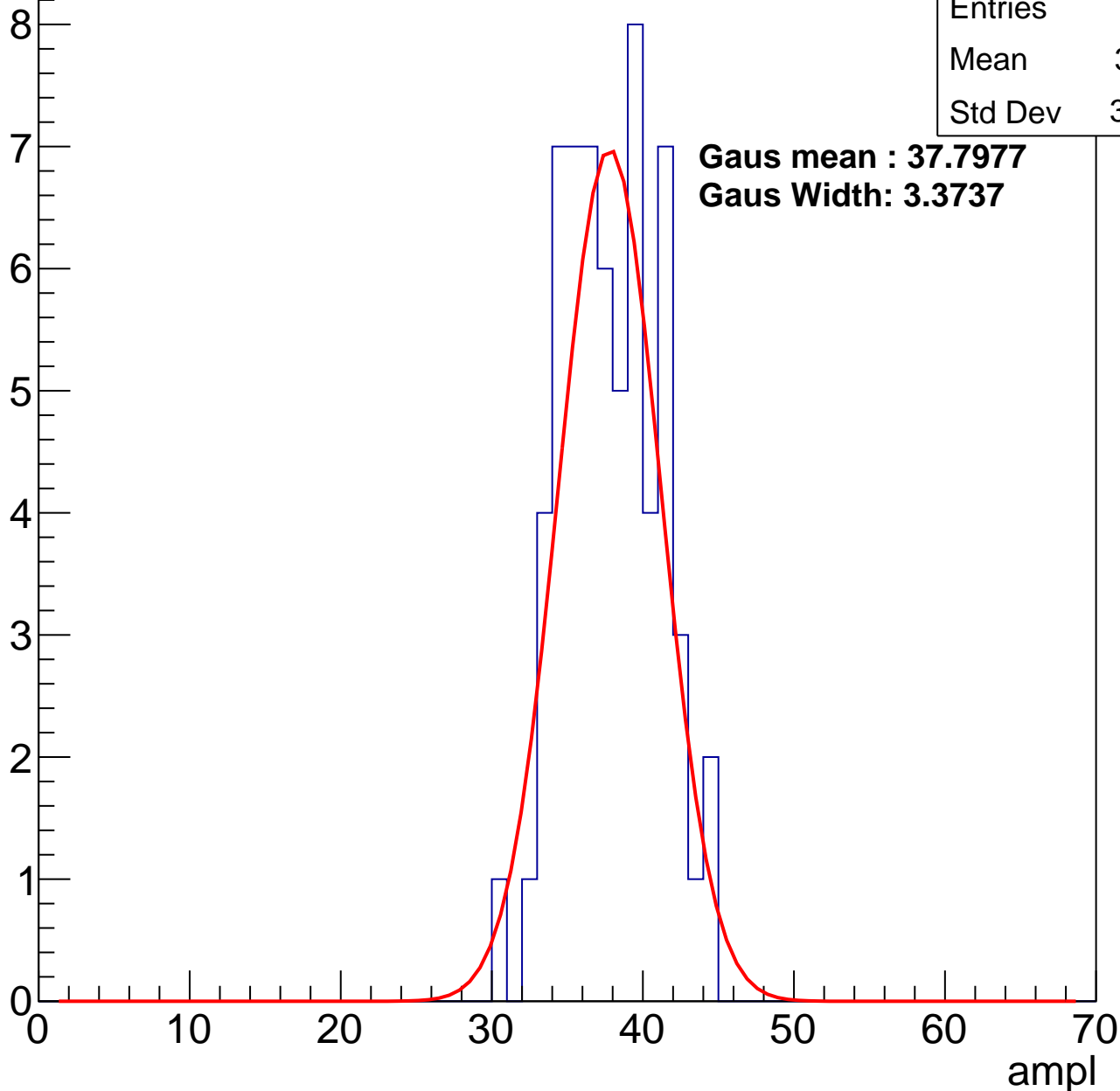
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.41
Std Dev	3.135

Gaus mean : 37.7977

Gaus Width: 3.3737



B1L100S, U10-ch62, adc2

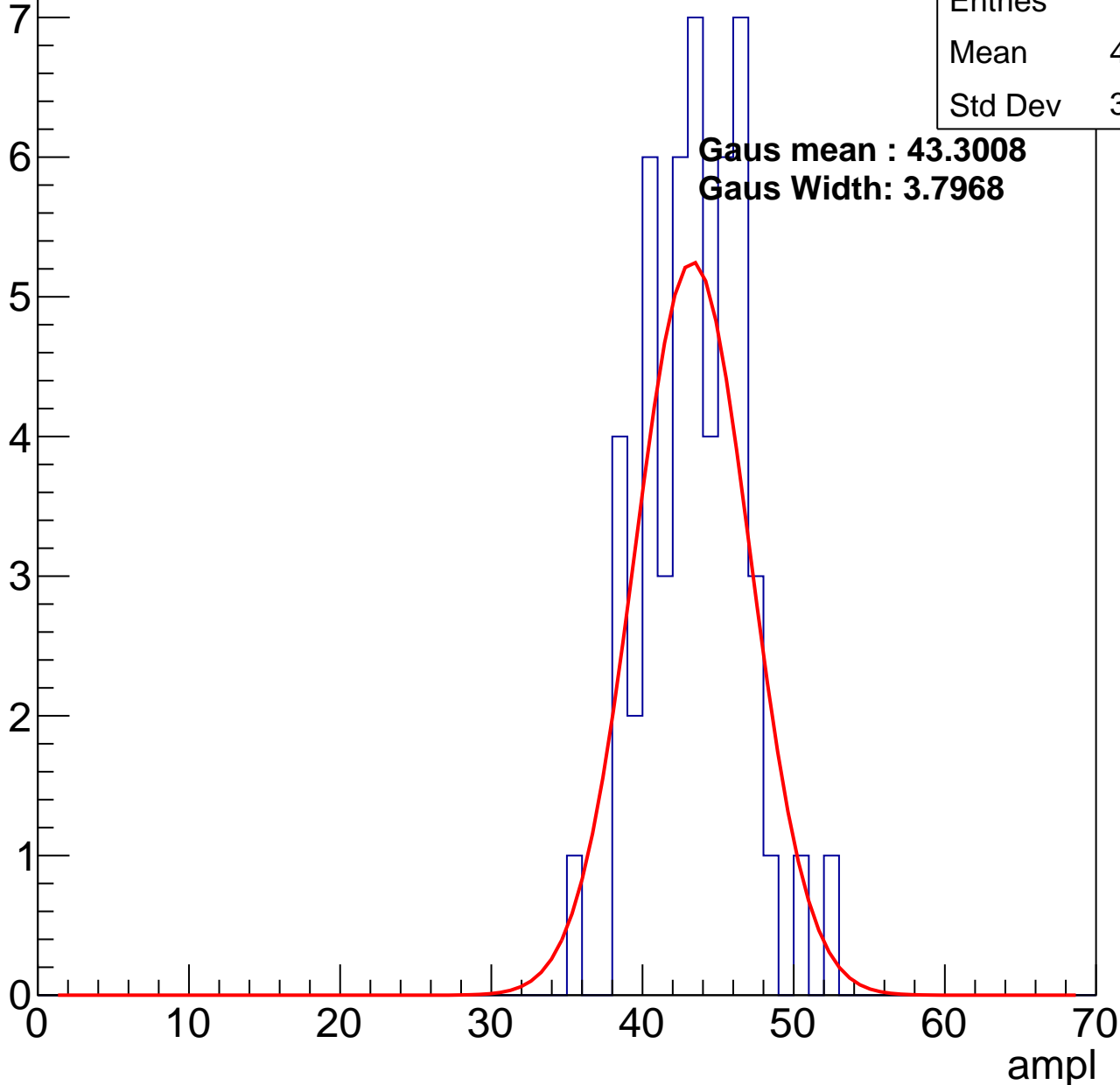
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	43.08
Std Dev	3.287

Gaus mean : 43.3008

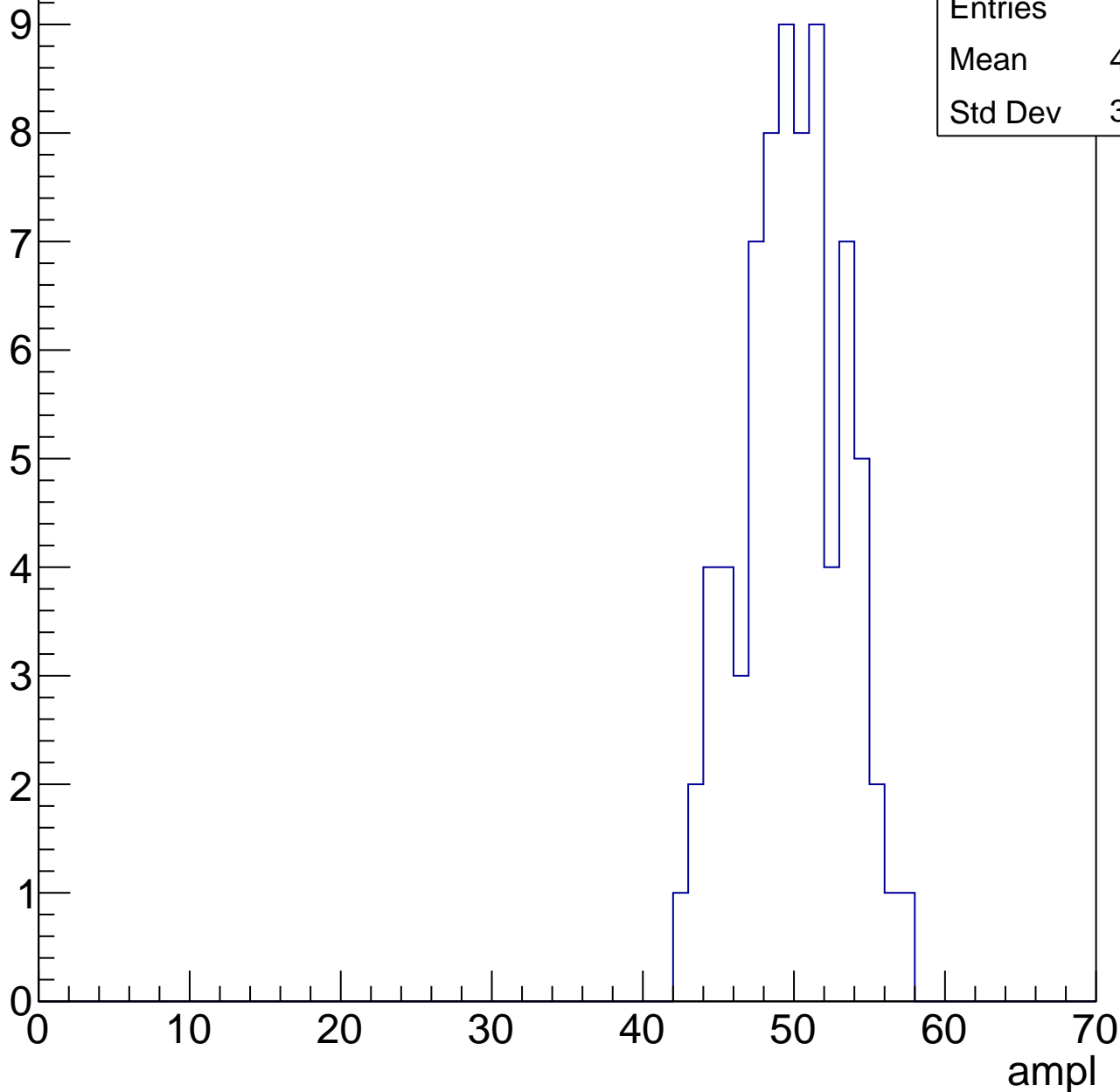
Gaus Width: 3.7968



B1L100S, U10-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

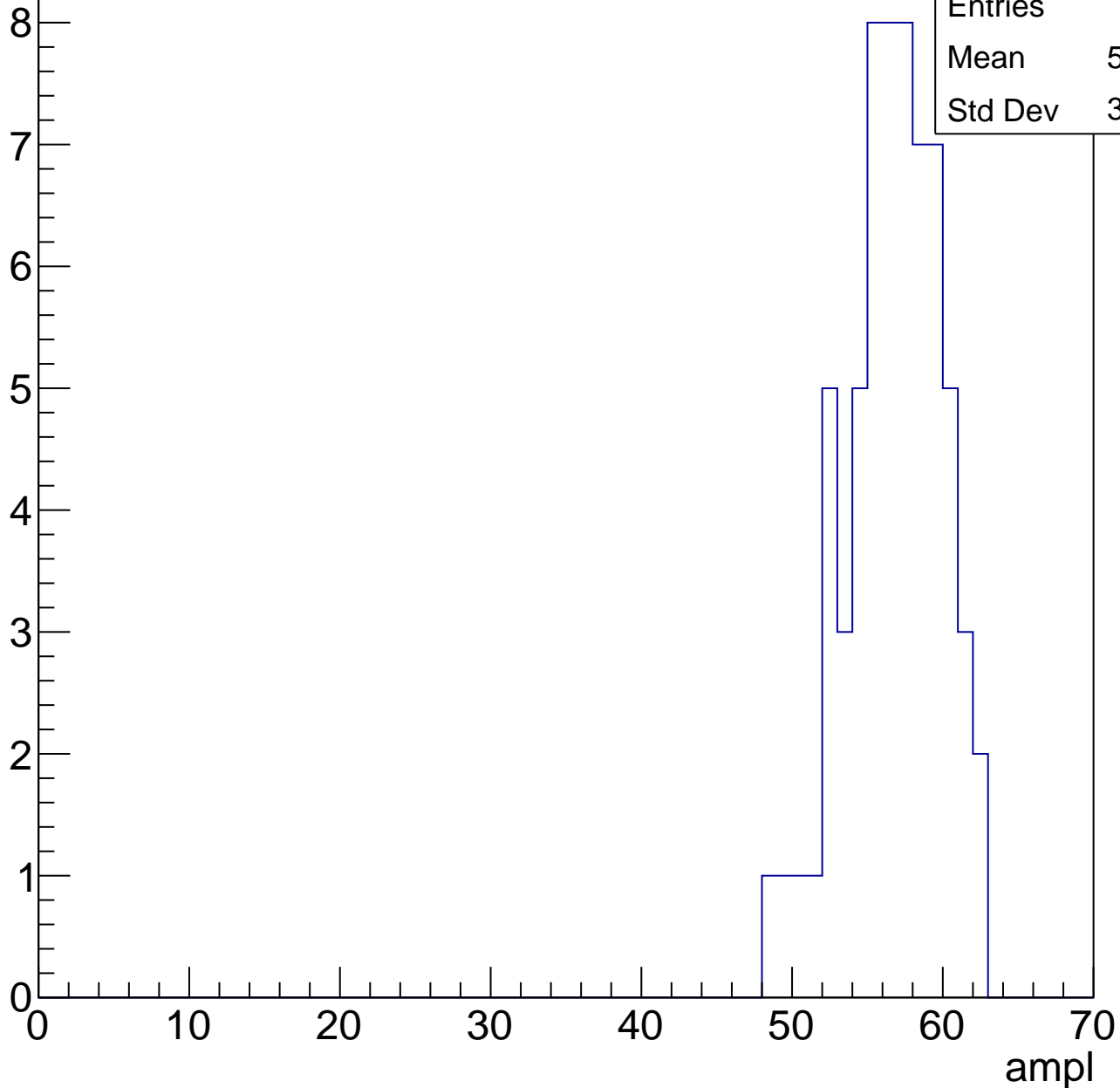
Entry



B1L100S, U10-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



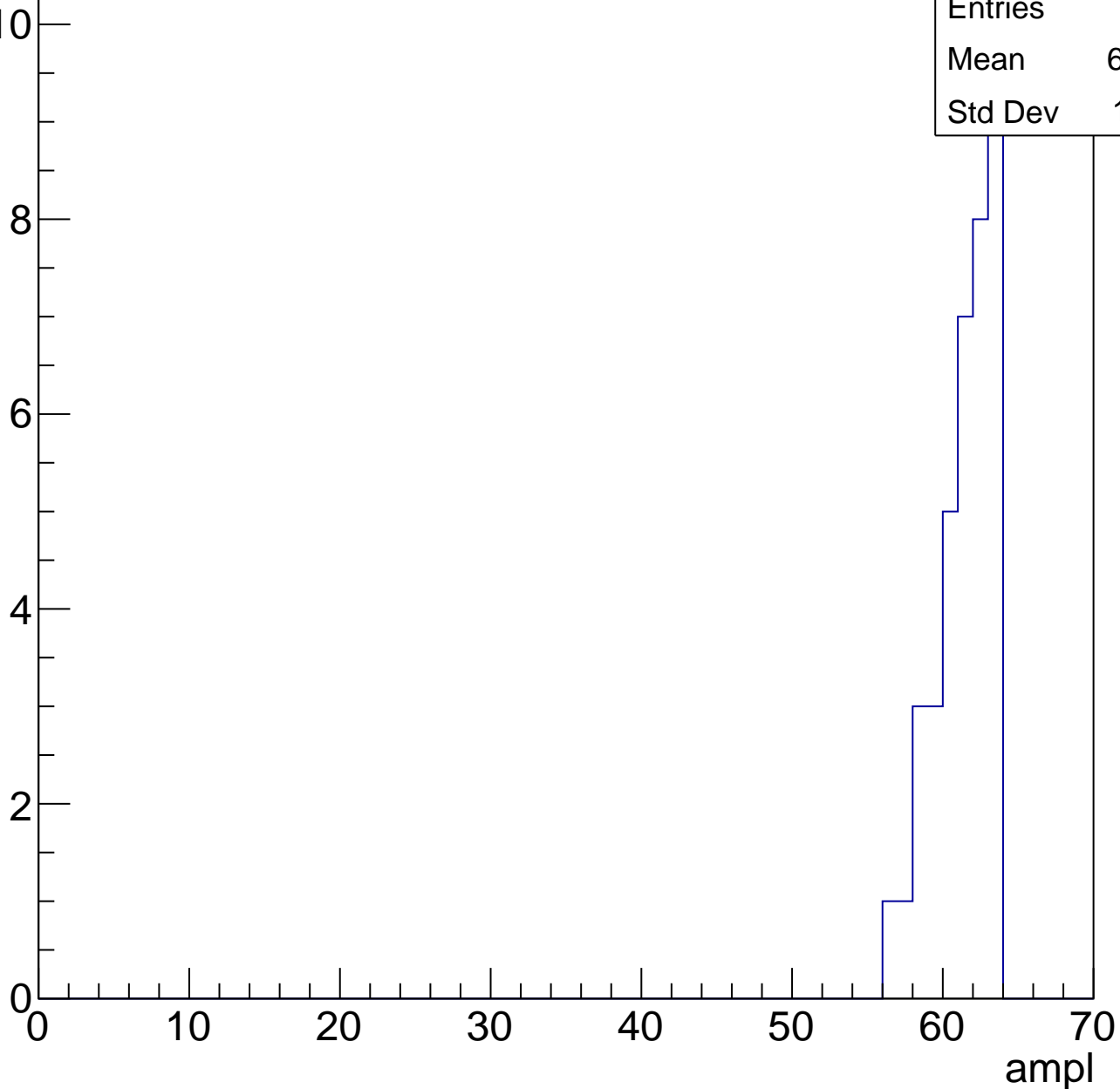
Entries	65
Mean	56.26
Std Dev	3.115

B1L100S, U10-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.97
Std Dev	1.871



B1L100S, U10-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch63, adc0

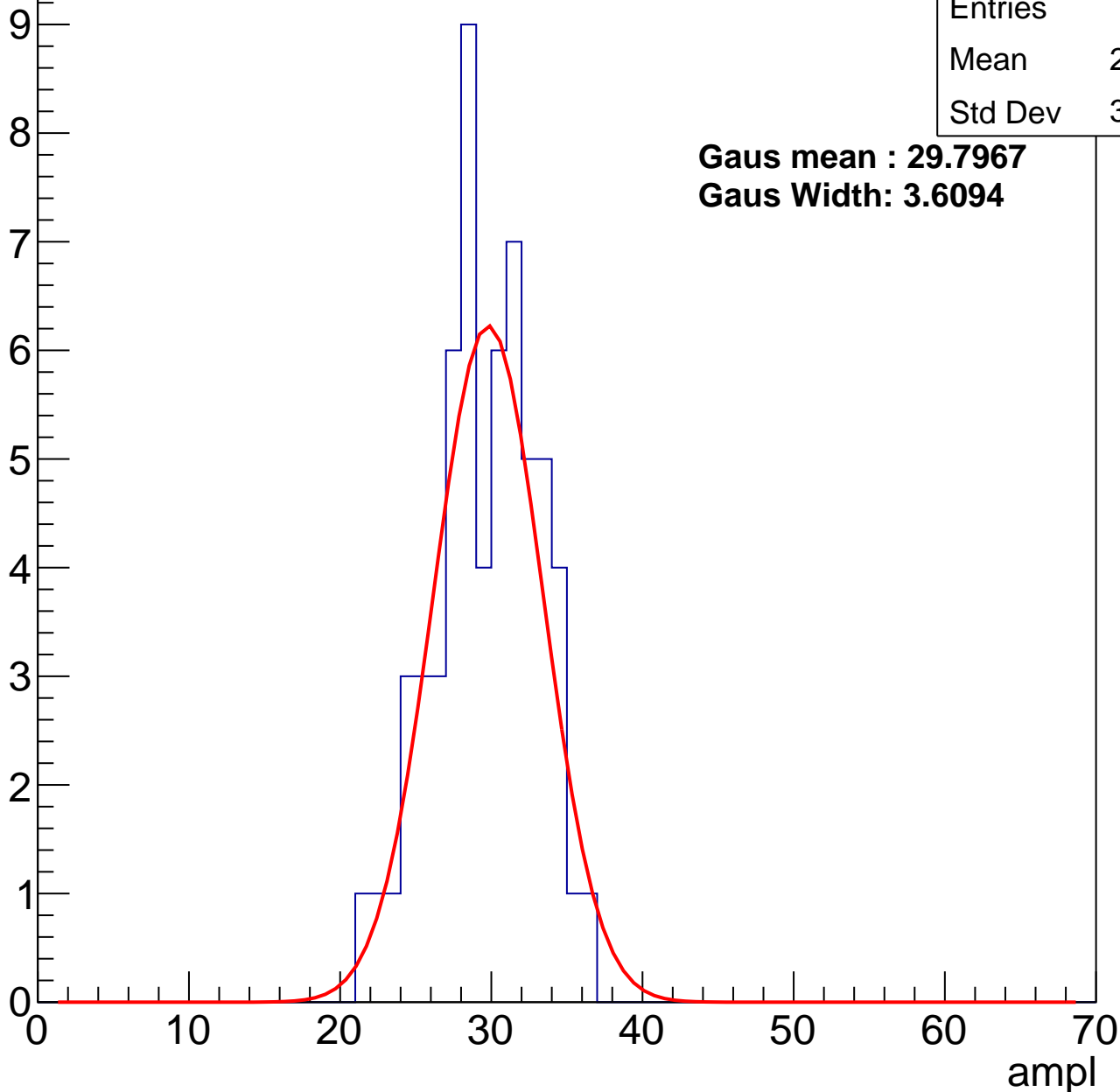
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.17
Std Dev	3.352

Gaus mean : 29.7967

Gaus Width: 3.6094

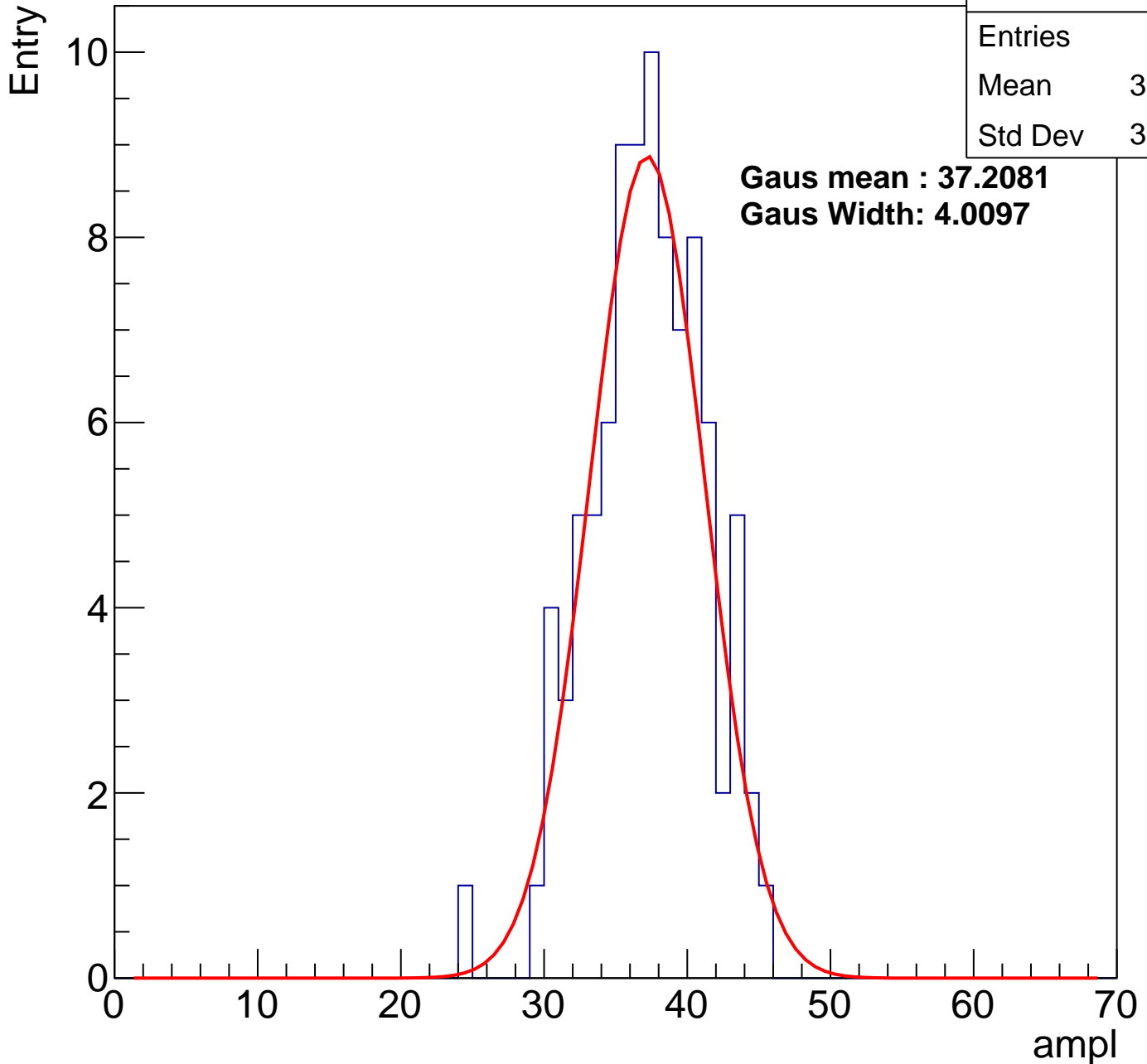


B1L100S, U10-ch63, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	36.73
Std Dev	3.943

Gaus mean : 37.2081
Gaus Width: 4.0097



B1L100S, U10-ch63, adc2

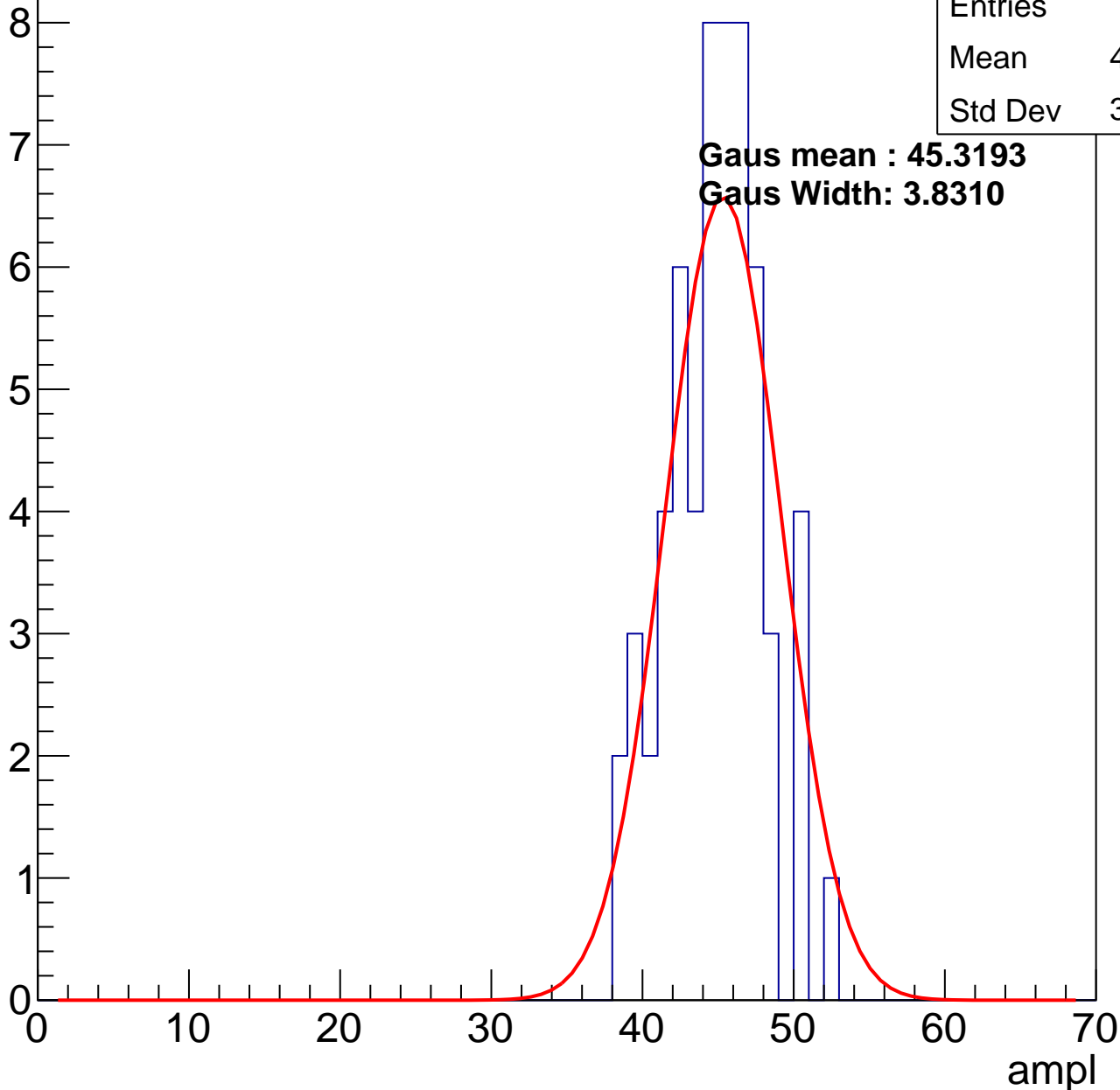
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.39
Std Dev	3.146

Gaus mean : 45.3193

Gaus Width: 3.8310



B1L100S, U10-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

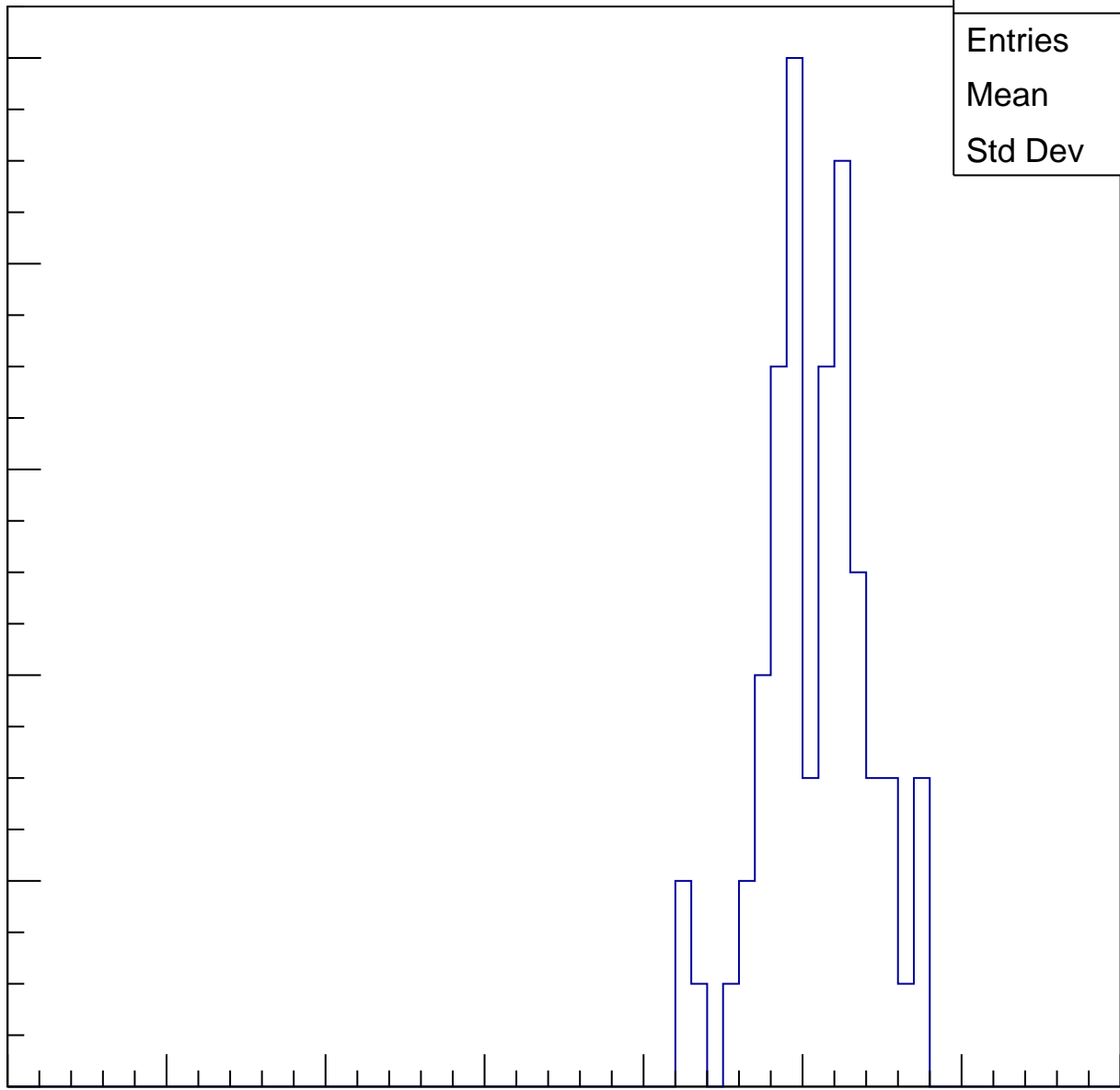
Entries	61
Mean	50.36
Std Dev	3.373

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

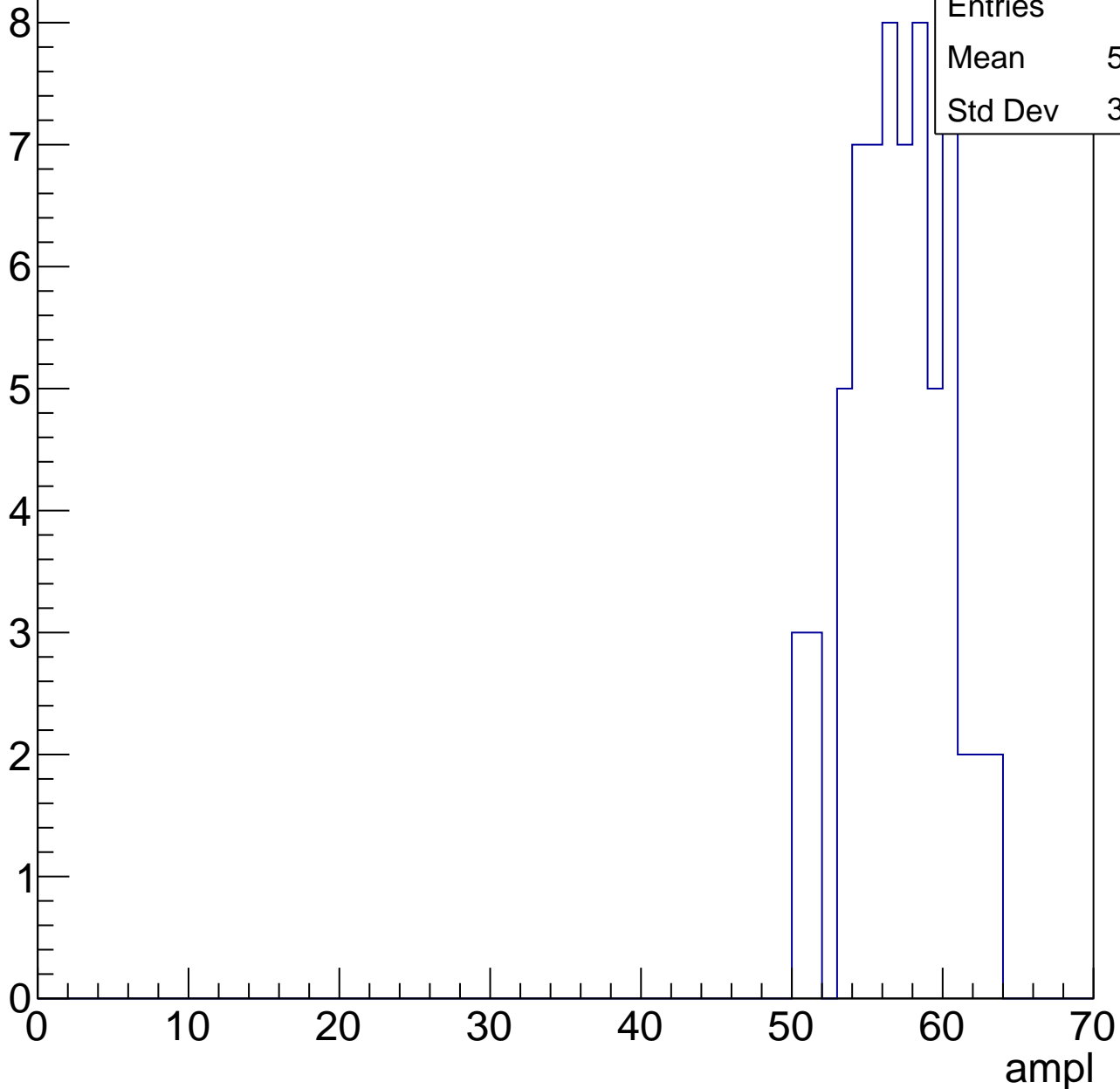


B1L100S, U10-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	56.55
Std Dev	3.173

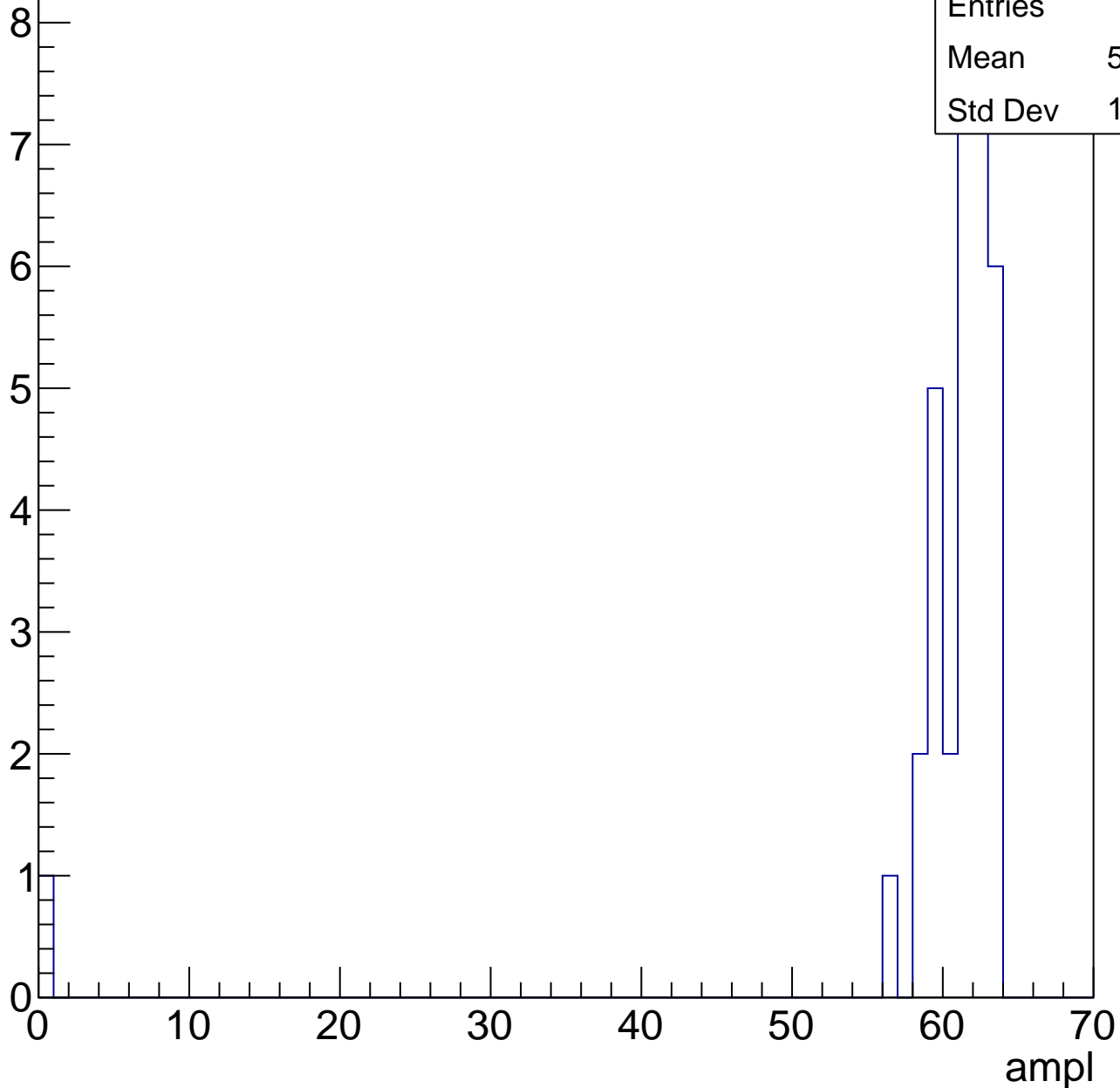


B1L100S, U10-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	59.06
Std Dev	10.58



B1L100S, U10-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	63
Std Dev	0

B1L100S, U10-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch64, adc0

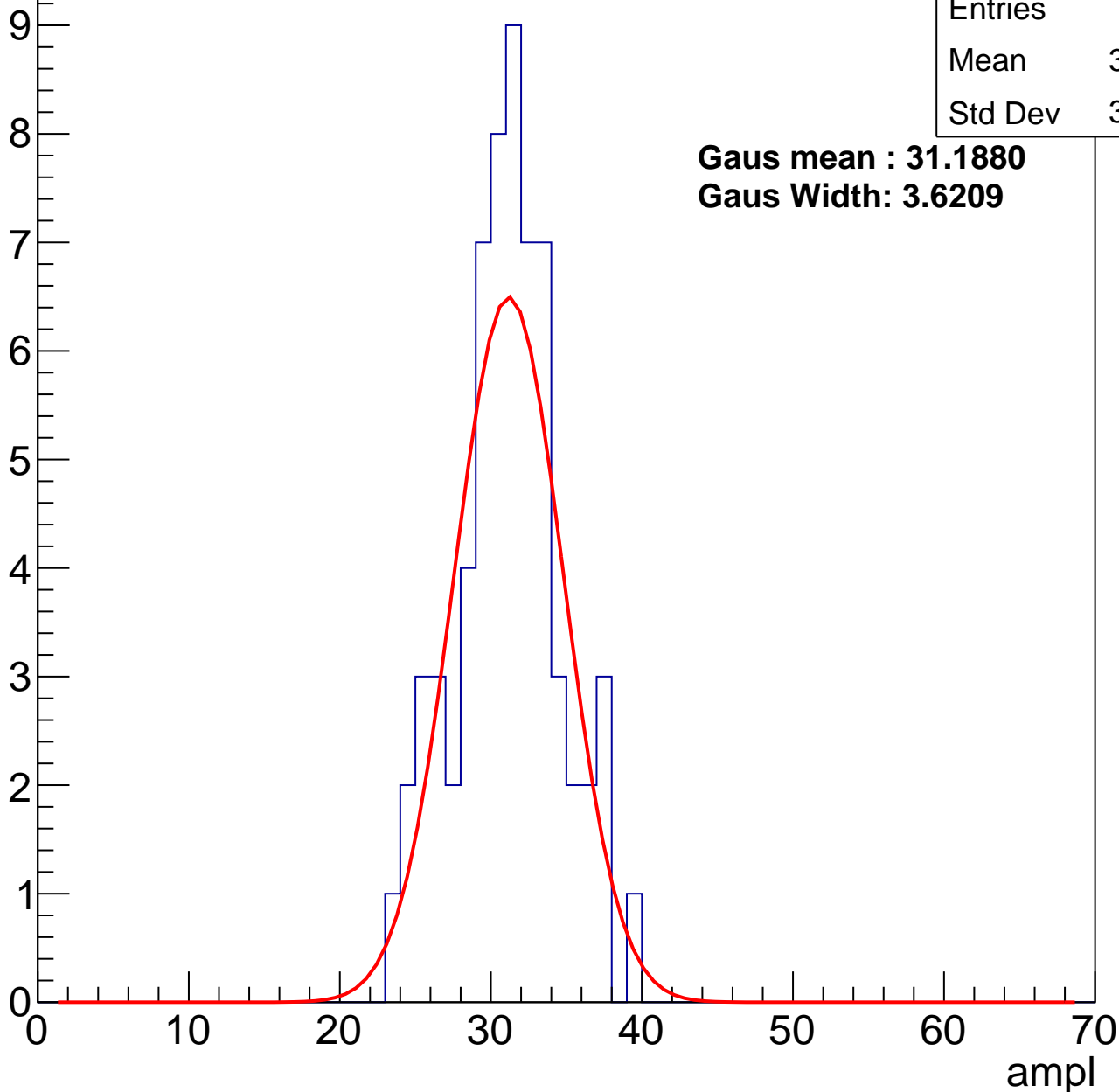
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.64
Std Dev	3.439

Gaus mean : 31.1880

Gaus Width: 3.6209



B1L100S, U10-ch64, adc1

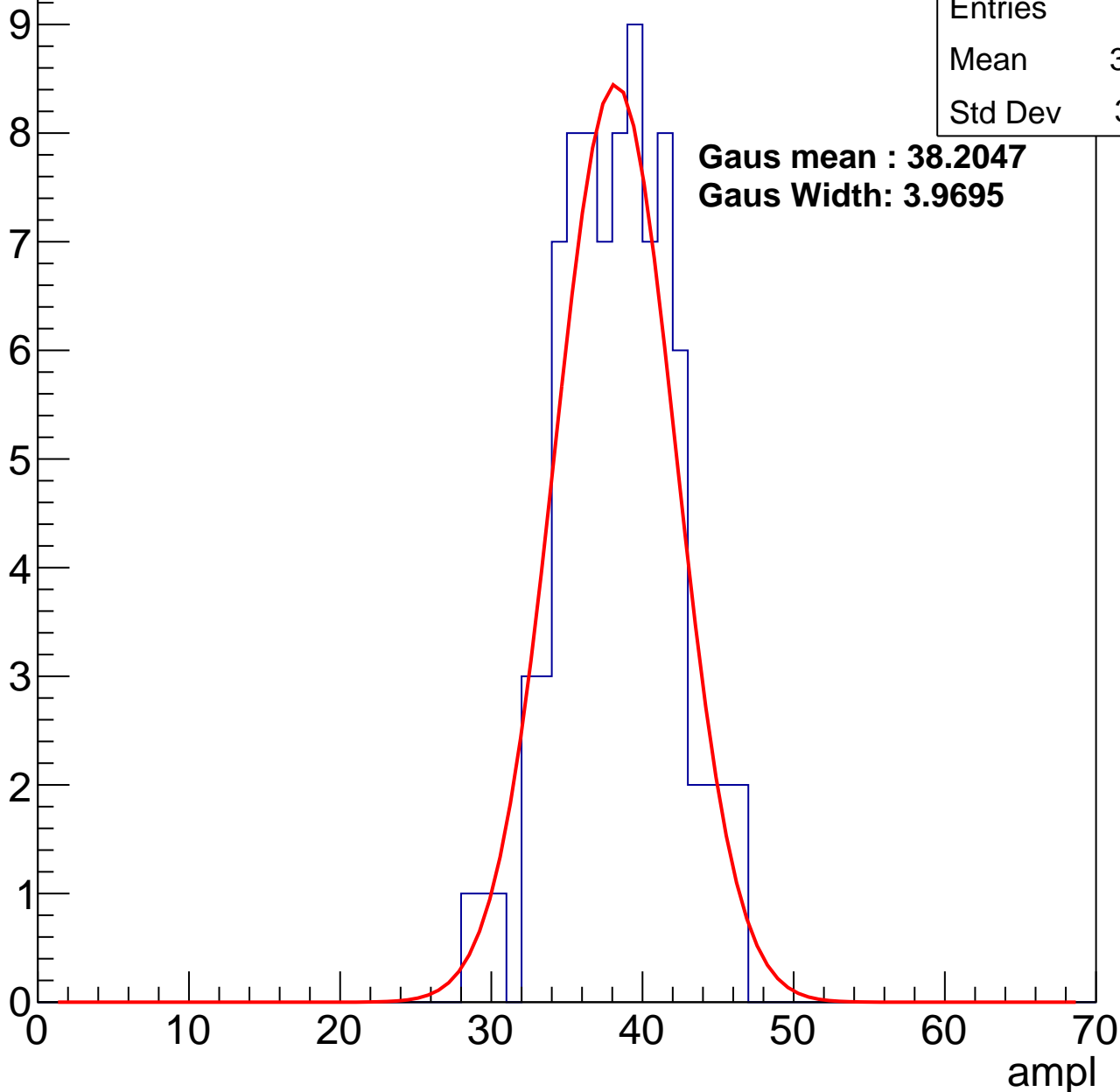
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	37.86
Std Dev	3.761

Gaus mean : 38.2047

Gaus Width: 3.9695



B1L100S, U10-ch64, adc2

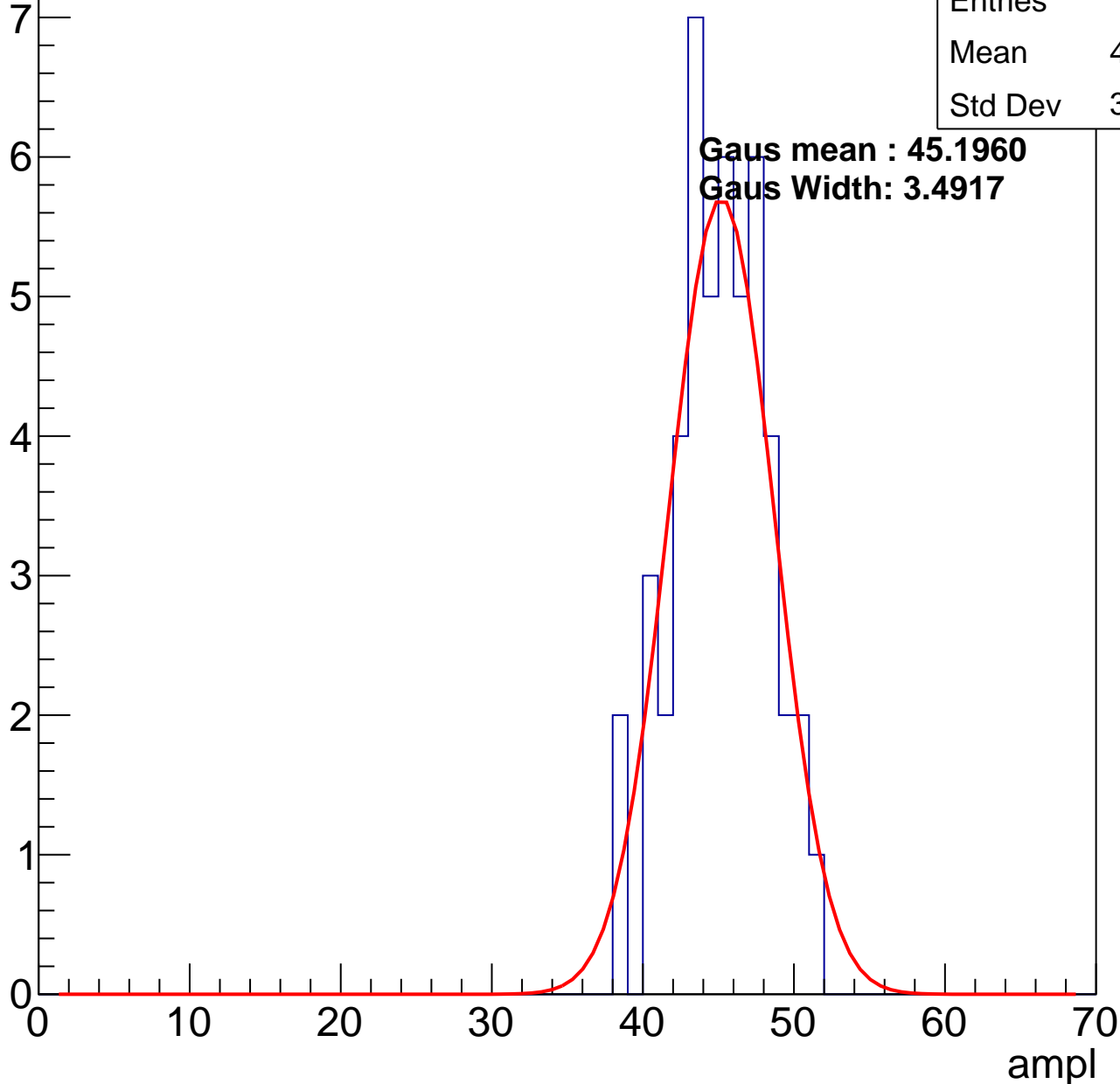
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	44.69
Std Dev	3.039

Gaus mean : 45.1960

Gaus Width: 3.4917

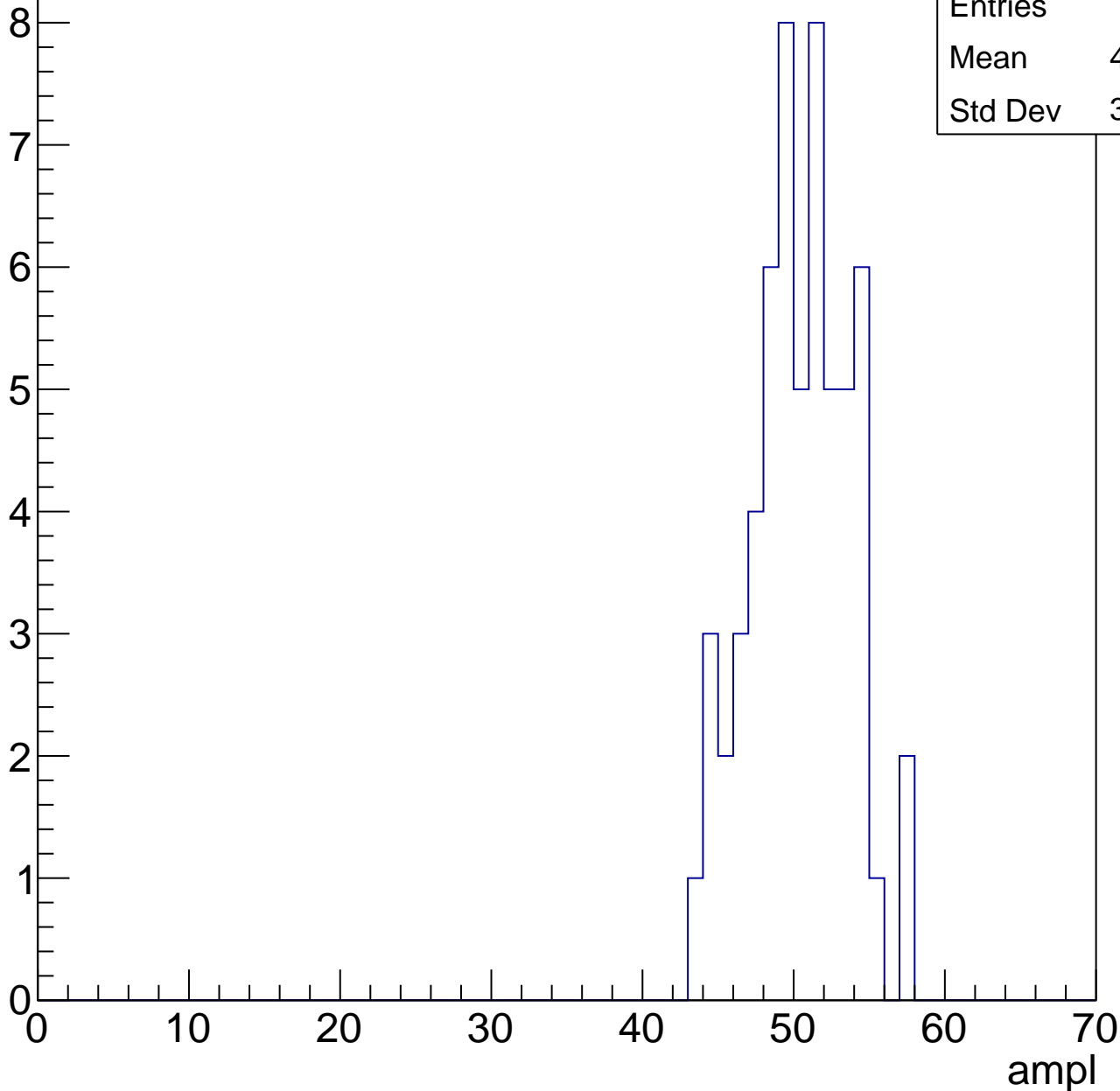


B1L100S, U10-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.95
Std Dev	3.228

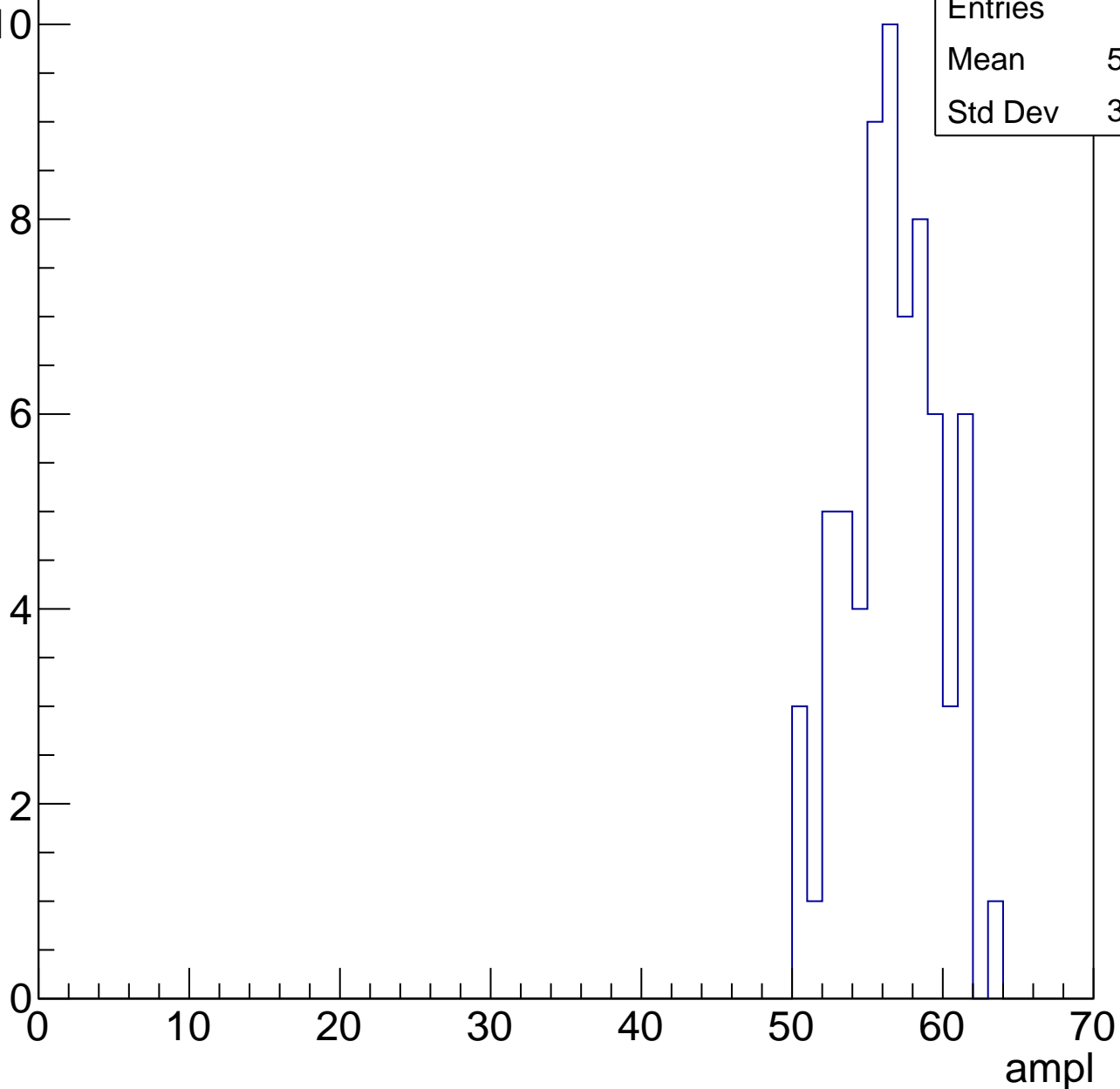


B1L100S, U10-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	56.22
Std Dev	3.019



B1L100S, U10-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 37

Mean 59.49

Std Dev 10.05

8

6

4

2

0

0

10

20

30

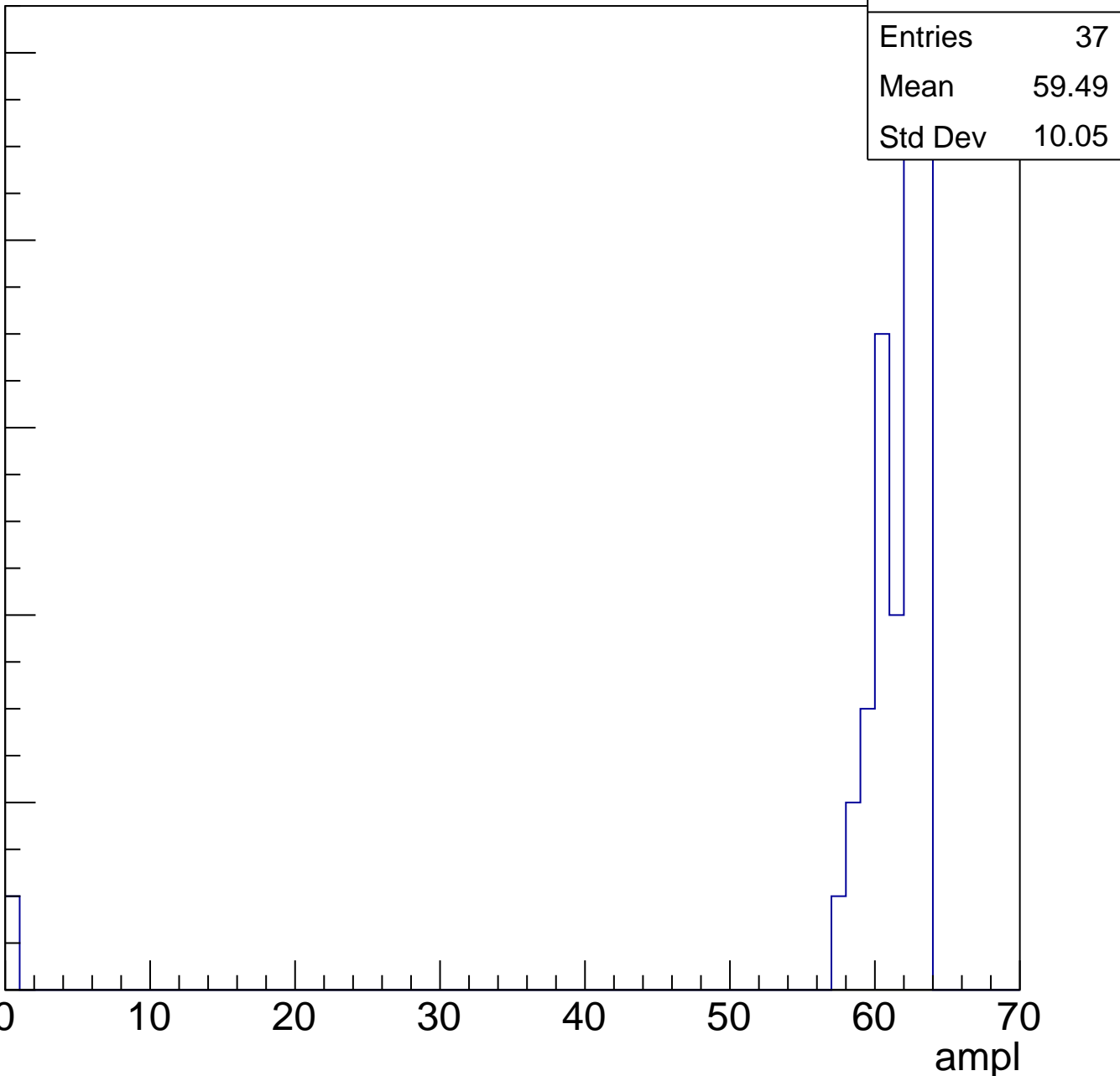
40

50

60

70

ampl



B1L100S, U10-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries

1

Mean

59

Std Dev

0

B1L100S, U10-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch65, adc0

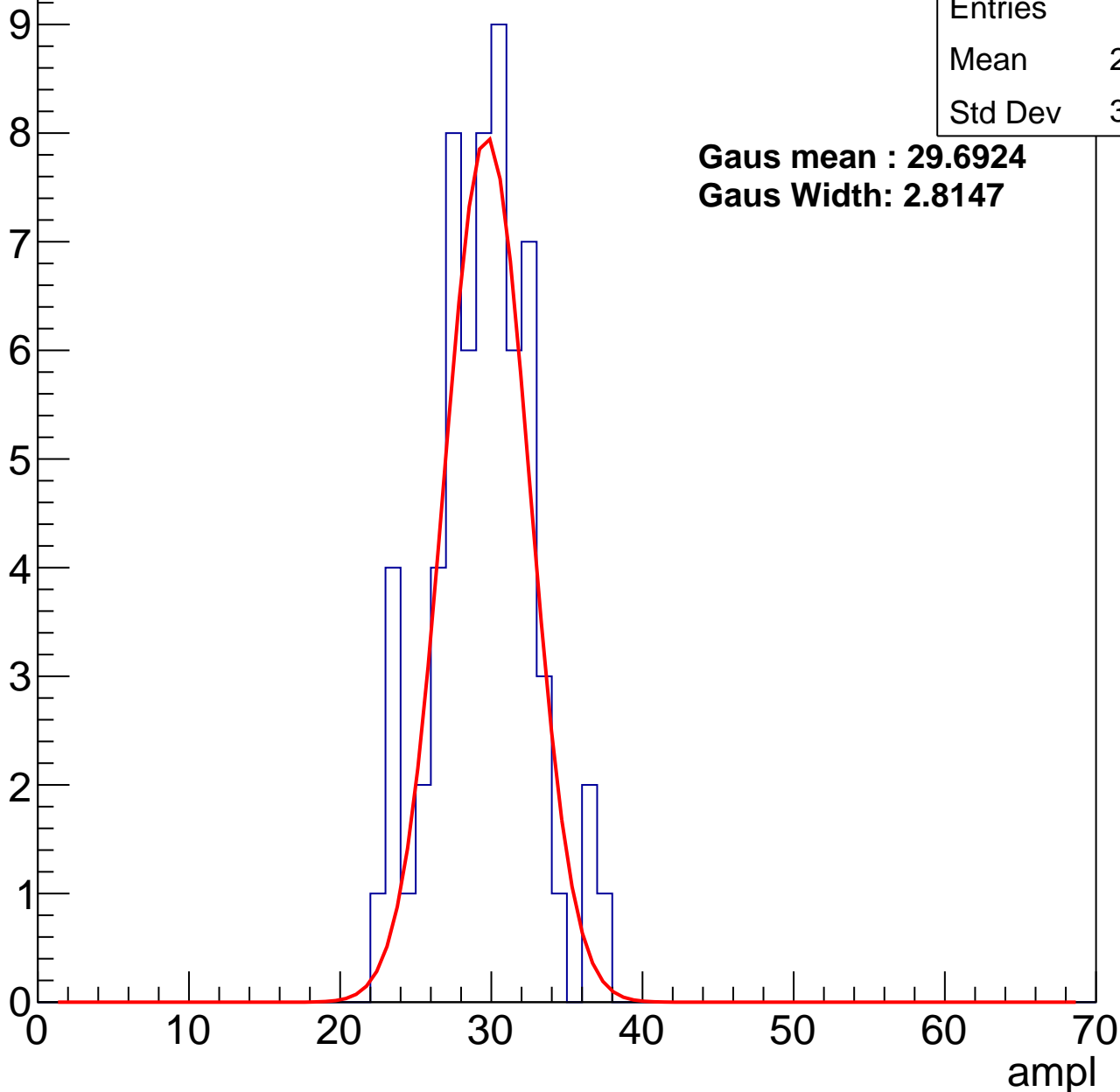
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.05
Std Dev	3.224

Gaus mean : 29.6924

Gaus Width: 2.8147



B1L100S, U10-ch65, adc1

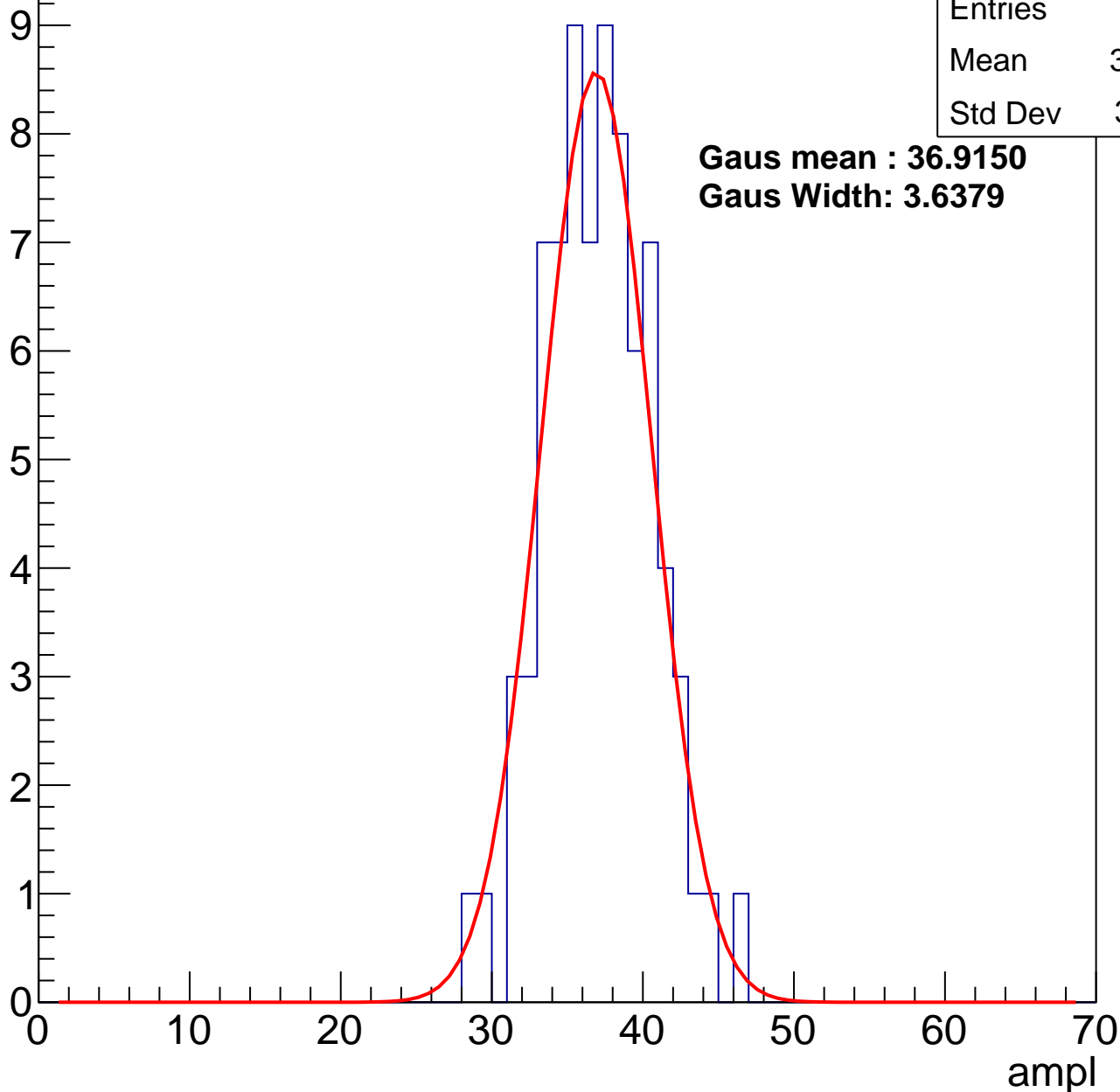
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.62
Std Dev	3.461

Gaus mean : 36.9150

Gaus Width: 3.6379



B1L100S, U10-ch65, adc2

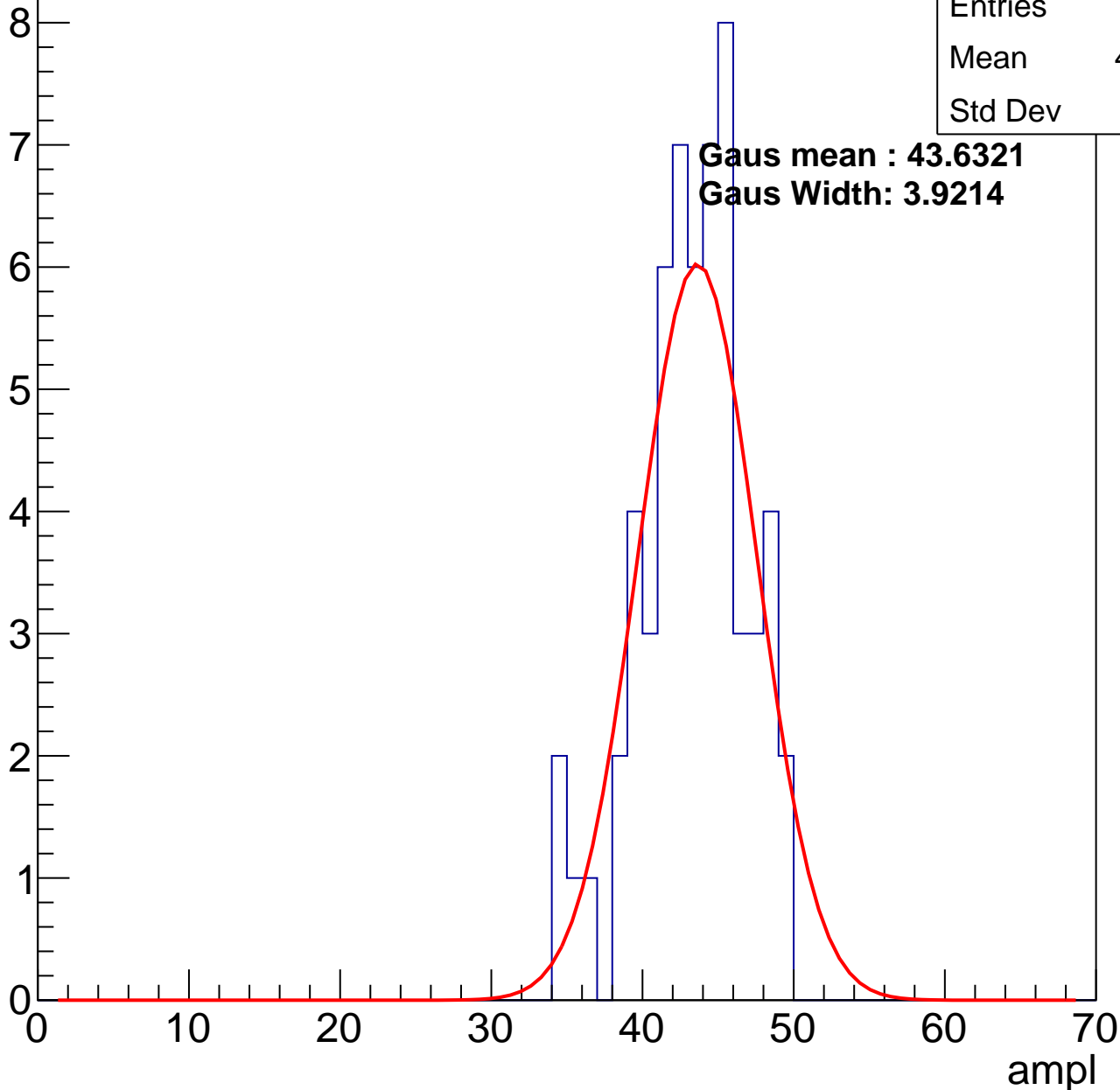
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.81
Std Dev	3.52

Gaus mean : 43.6321

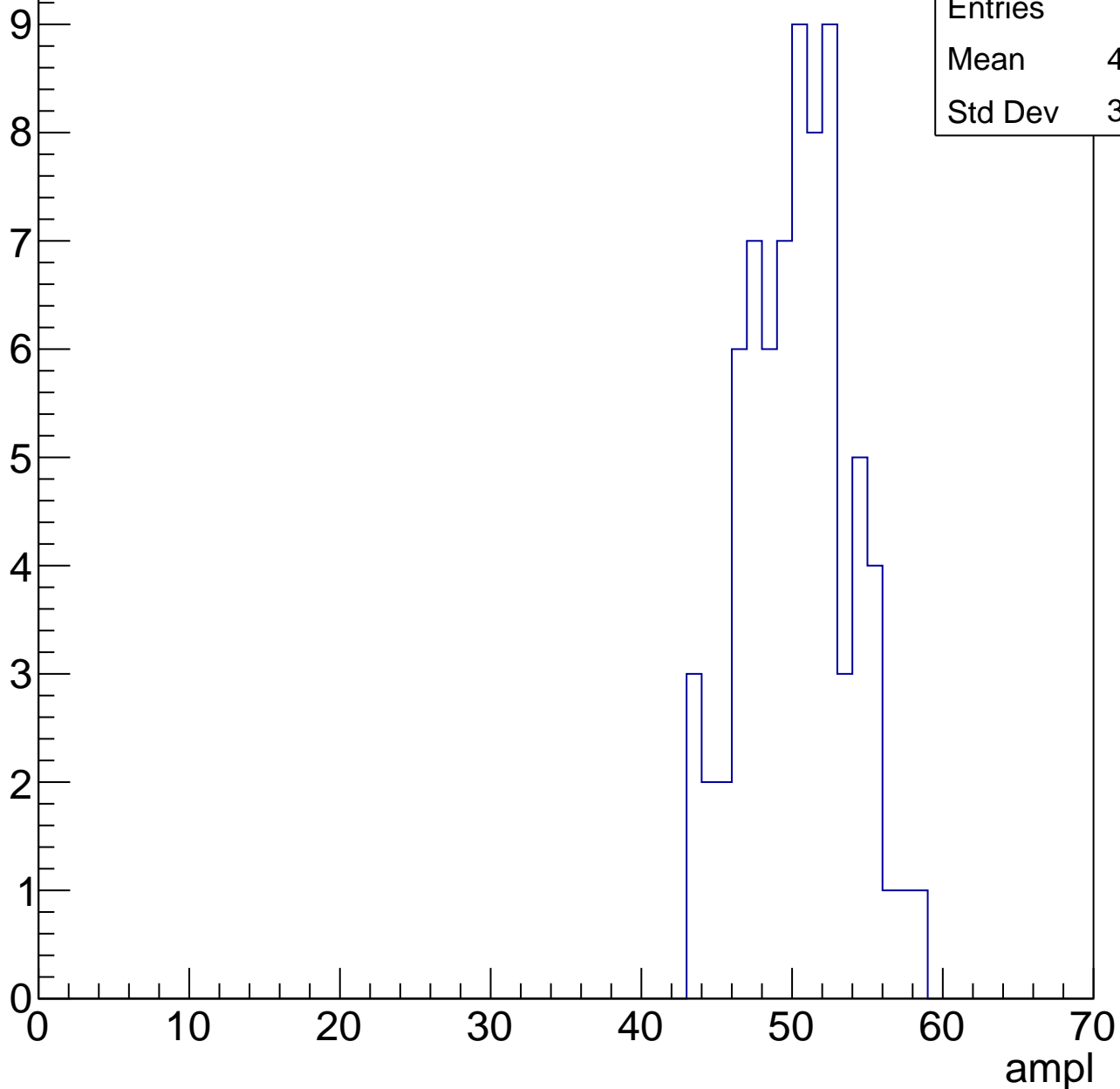
Gaus Width: 3.9214



B1L100S, U10-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

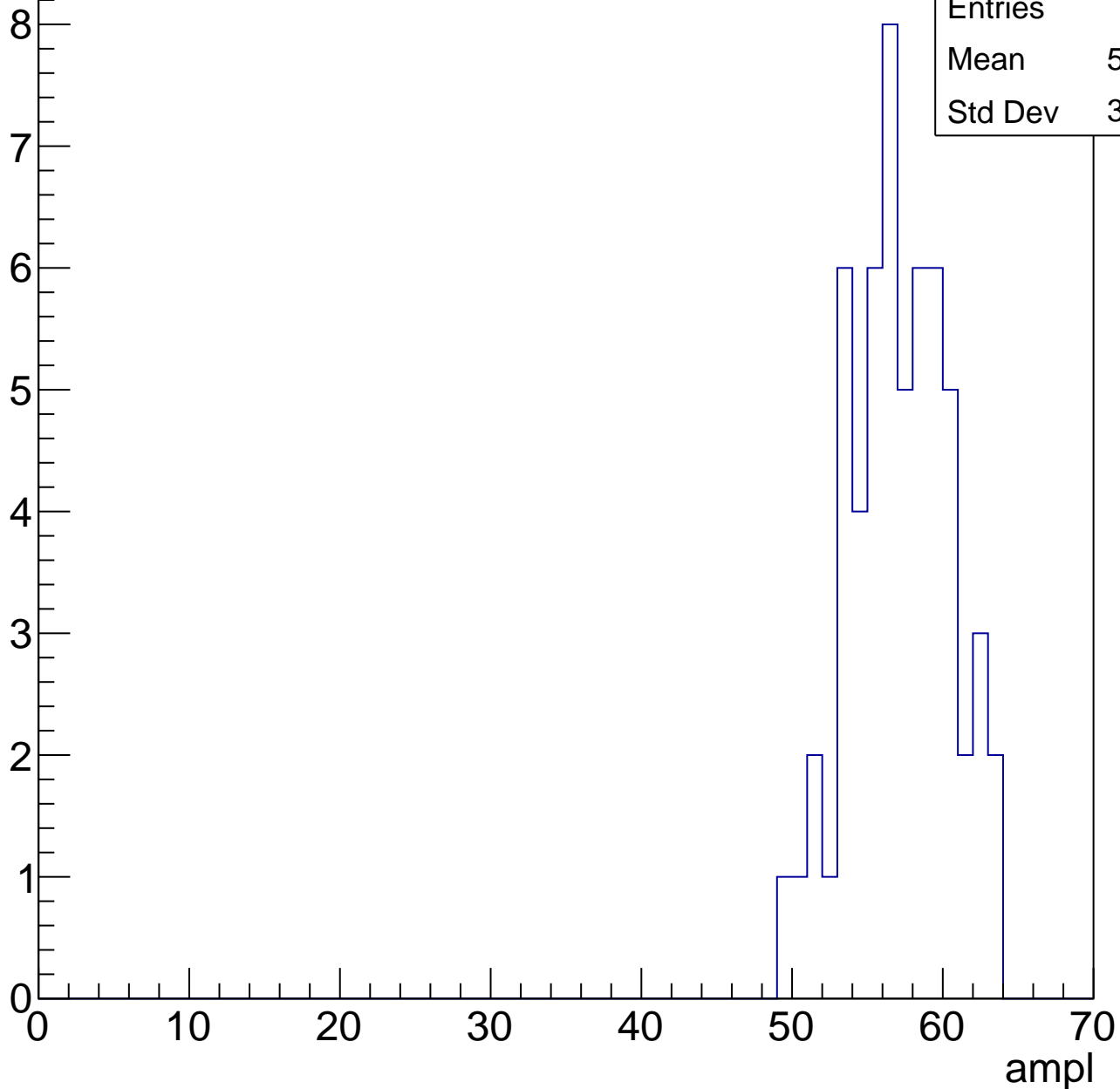


Entries	74
Mean	49.85
Std Dev	3.404

B1L100S, U10-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



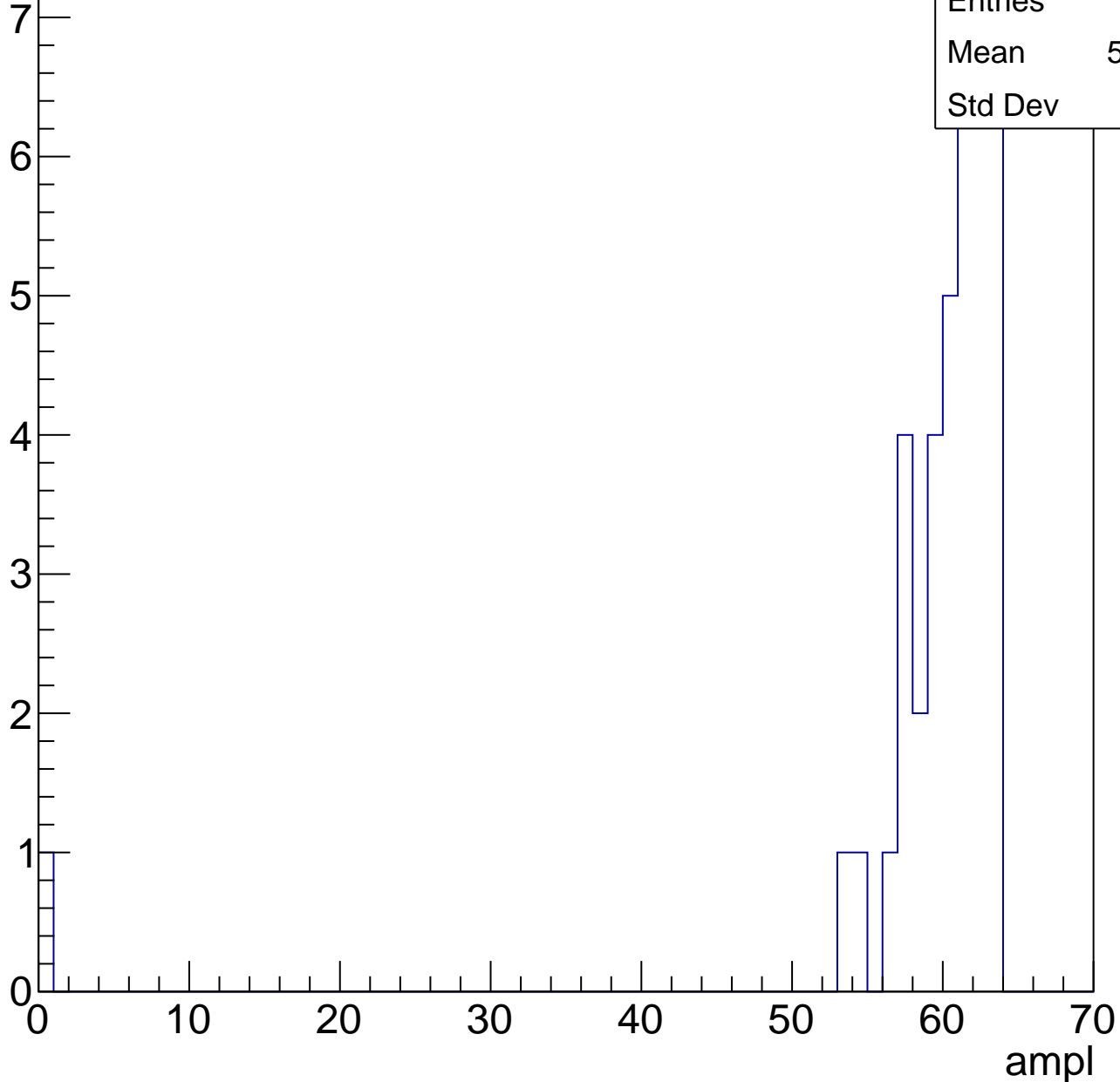
Entries	58
Mean	56.66
Std Dev	3.272

B1L100S, U10-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

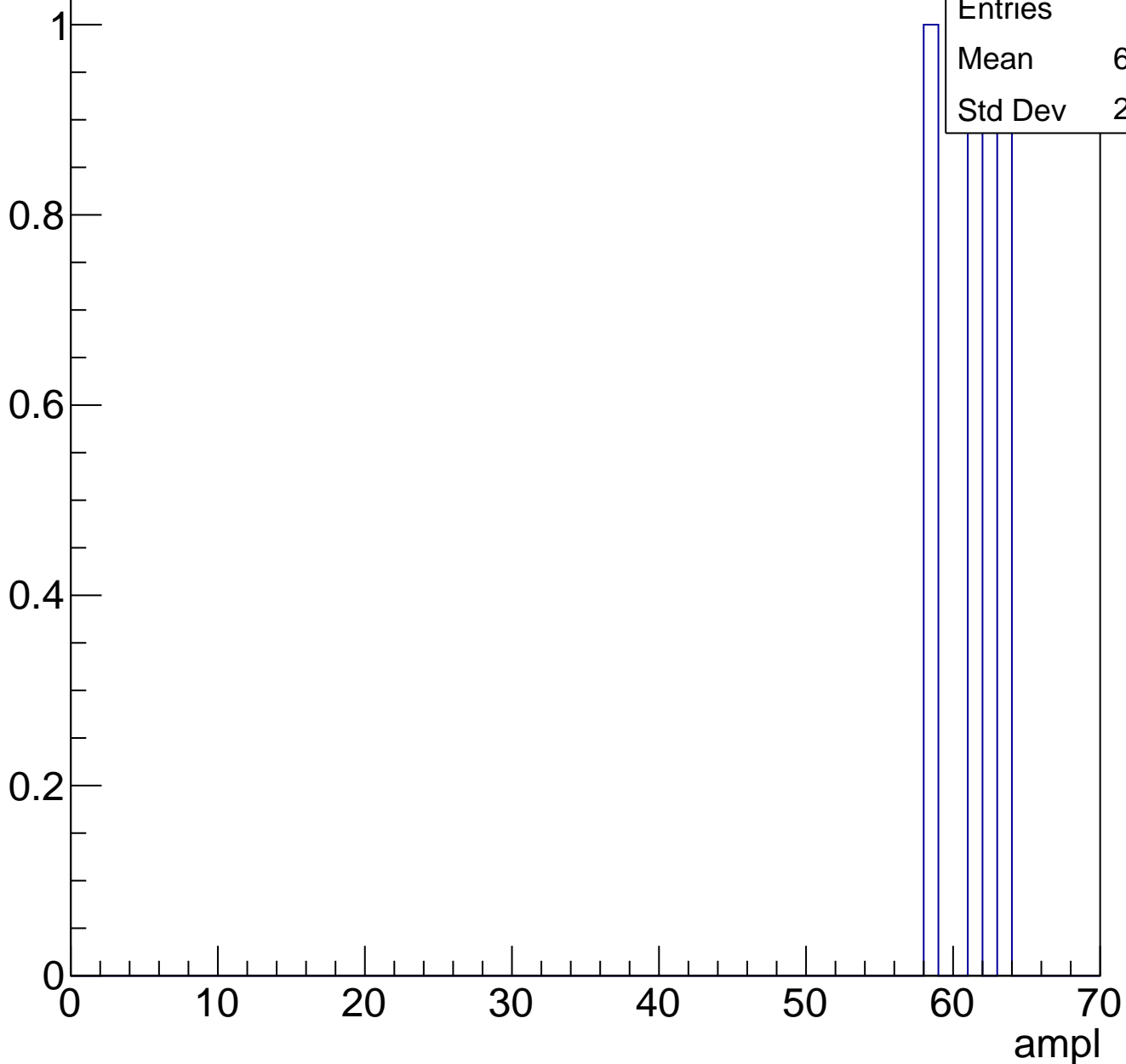
Entries	40
Mean	58.62
Std Dev	9.71



B1L100S, U10-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch66, adc0

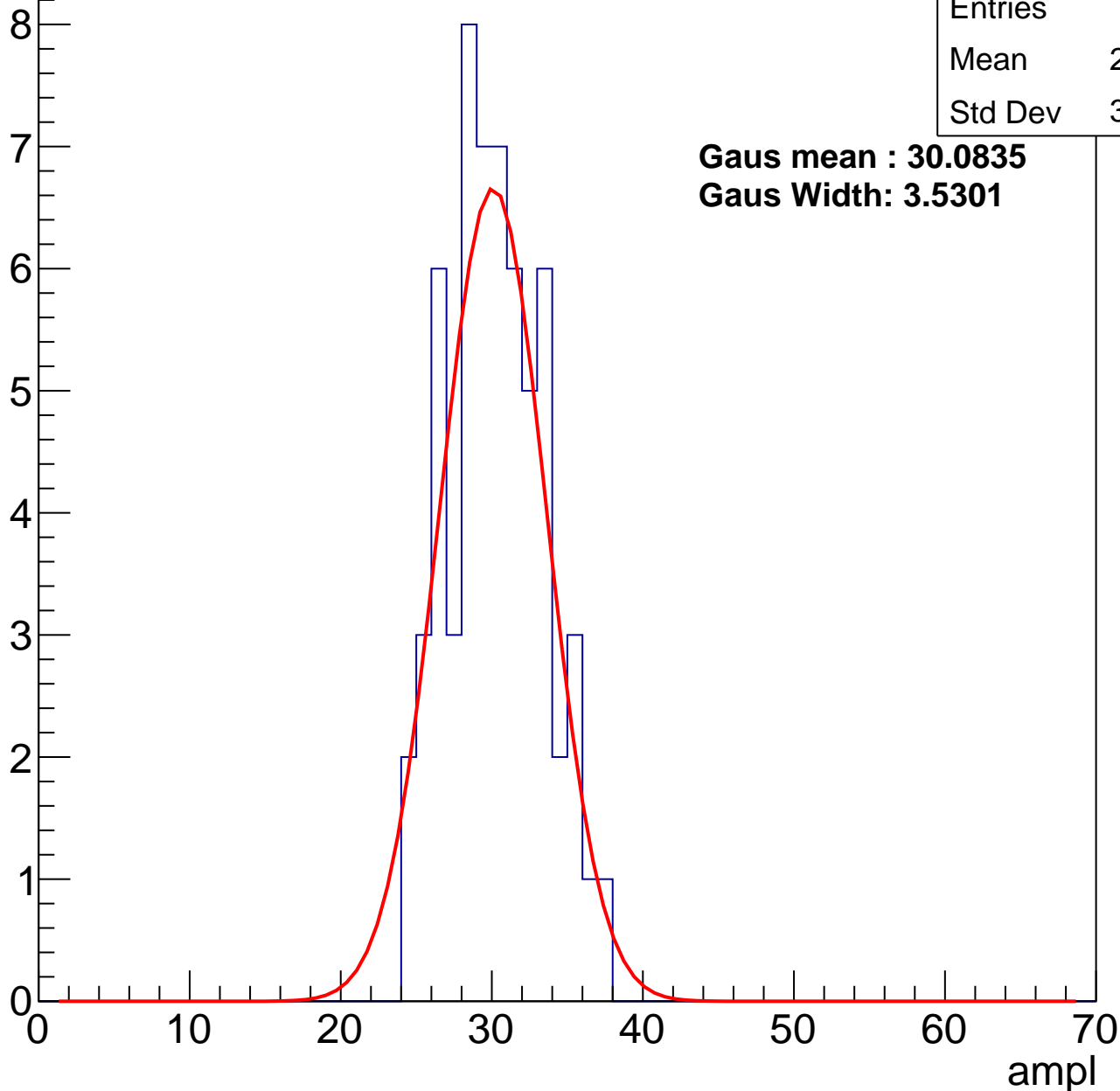
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.78
Std Dev	3.104

Gaus mean : 30.0835

Gaus Width: 3.5301



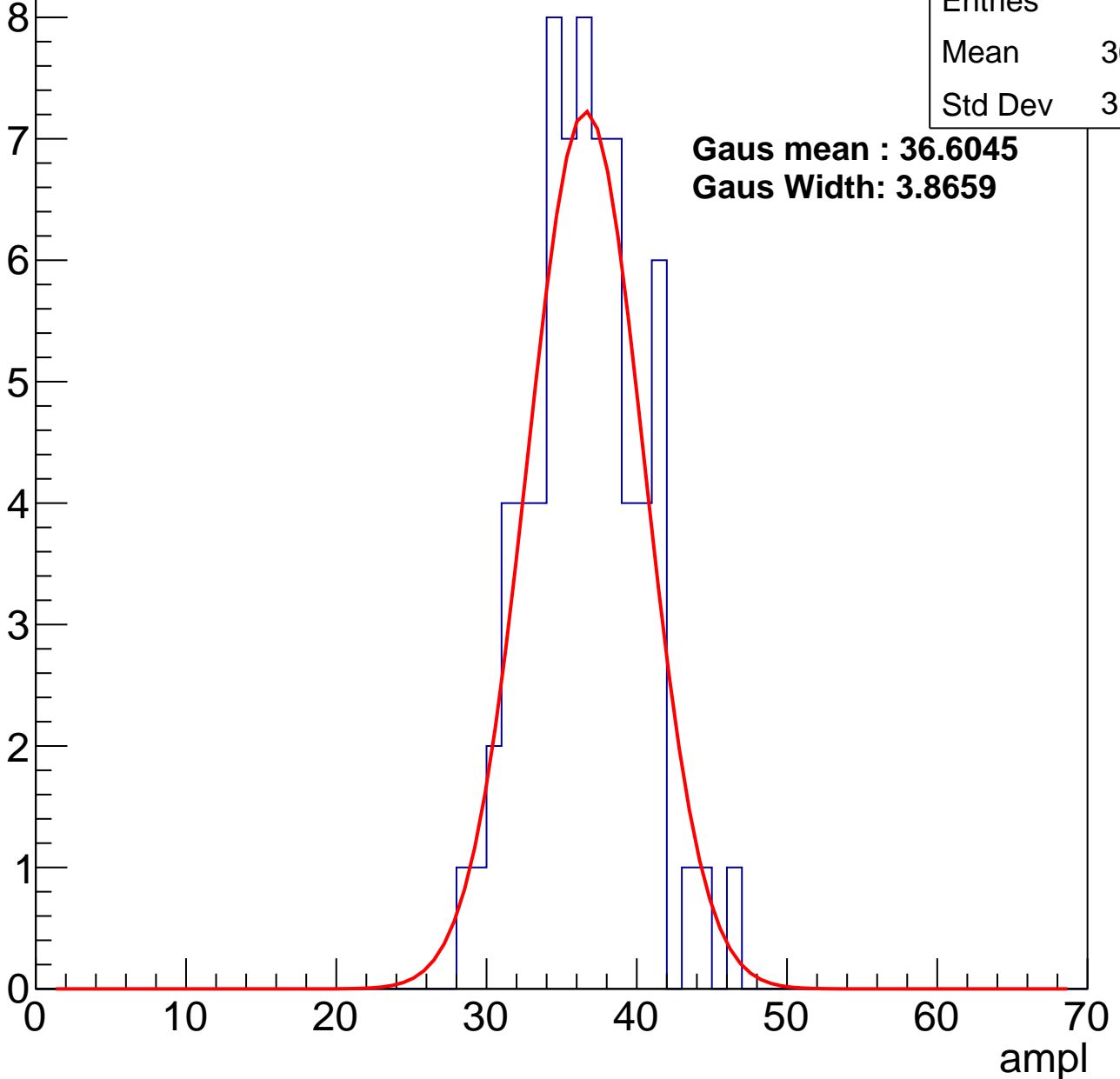
B1L100S, U10-ch66, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.09
Std Dev	3.628

Gaus mean : 36.6045
Gaus Width: 3.8659



B1L100S, U10-ch66, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	57
Mean	42.19
Std Dev	3

Gaus mean : 42.2011

Gaus Width: 3.0389

Entry

10

8

6

4

2

0

0

10

20

30

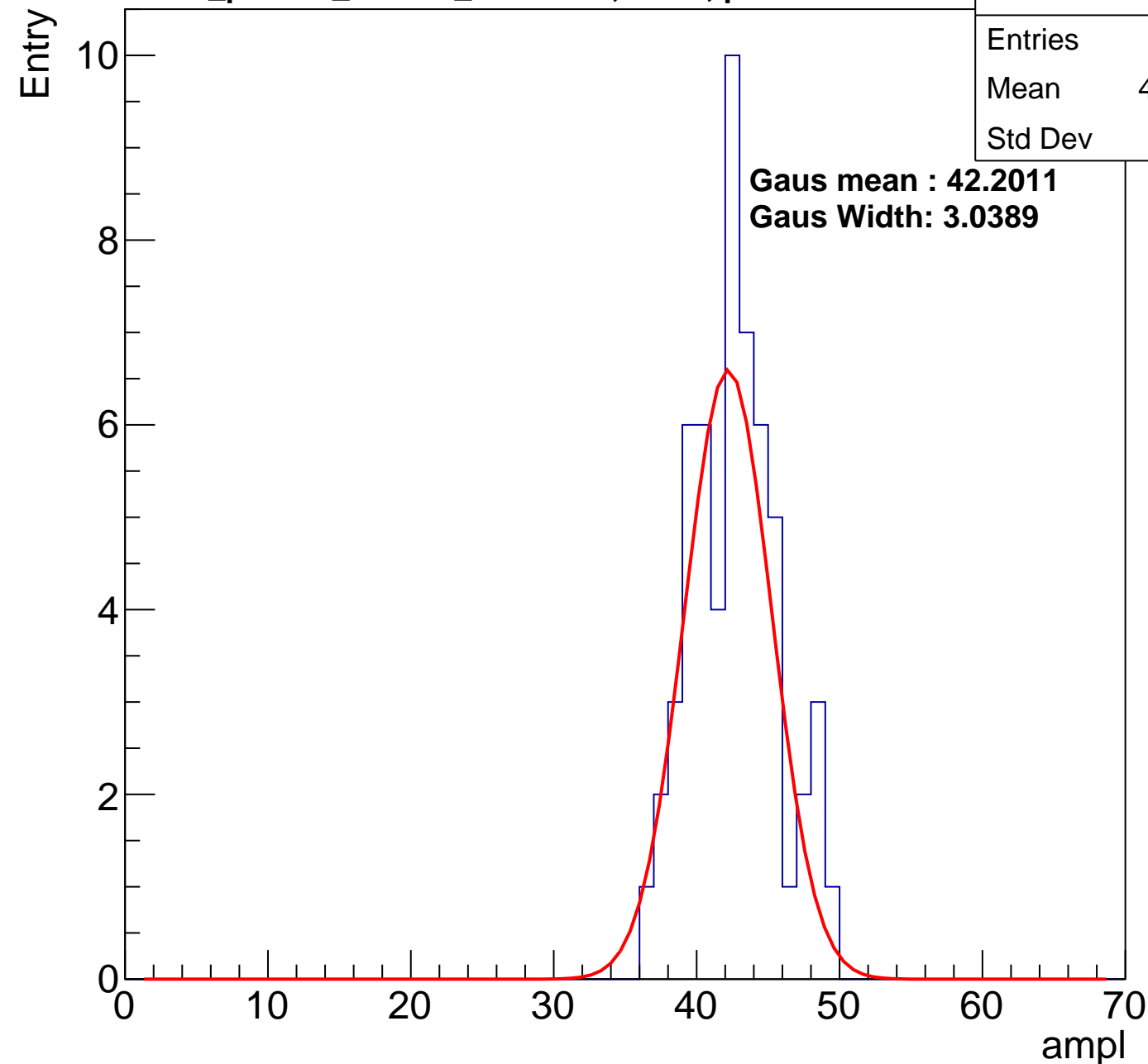
40

50

60

70

ampl

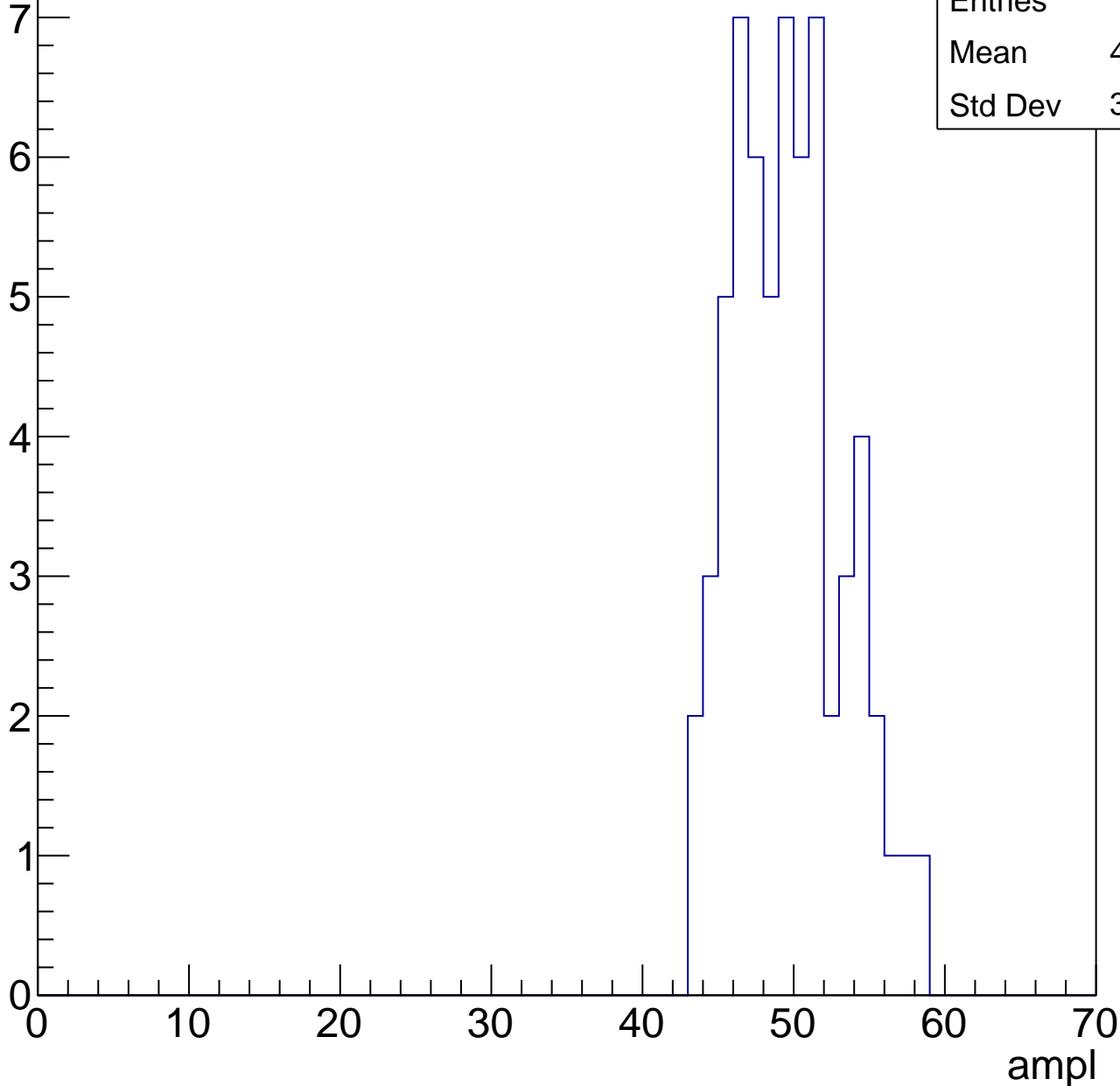


B1L100S, U10-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	49.15
Std Dev	3.555

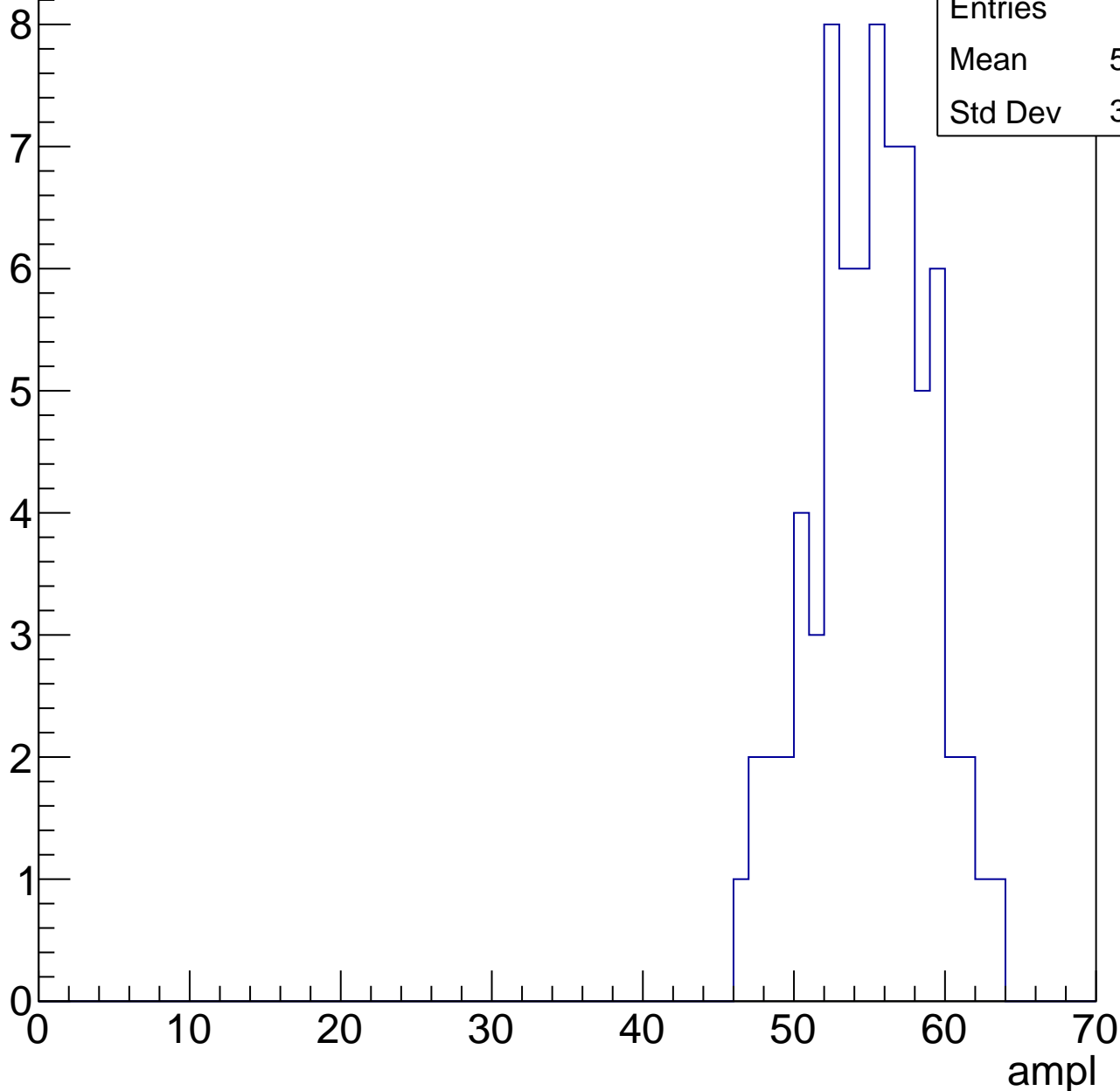


B1L100S, U10-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	54.62
Std Dev	3.755

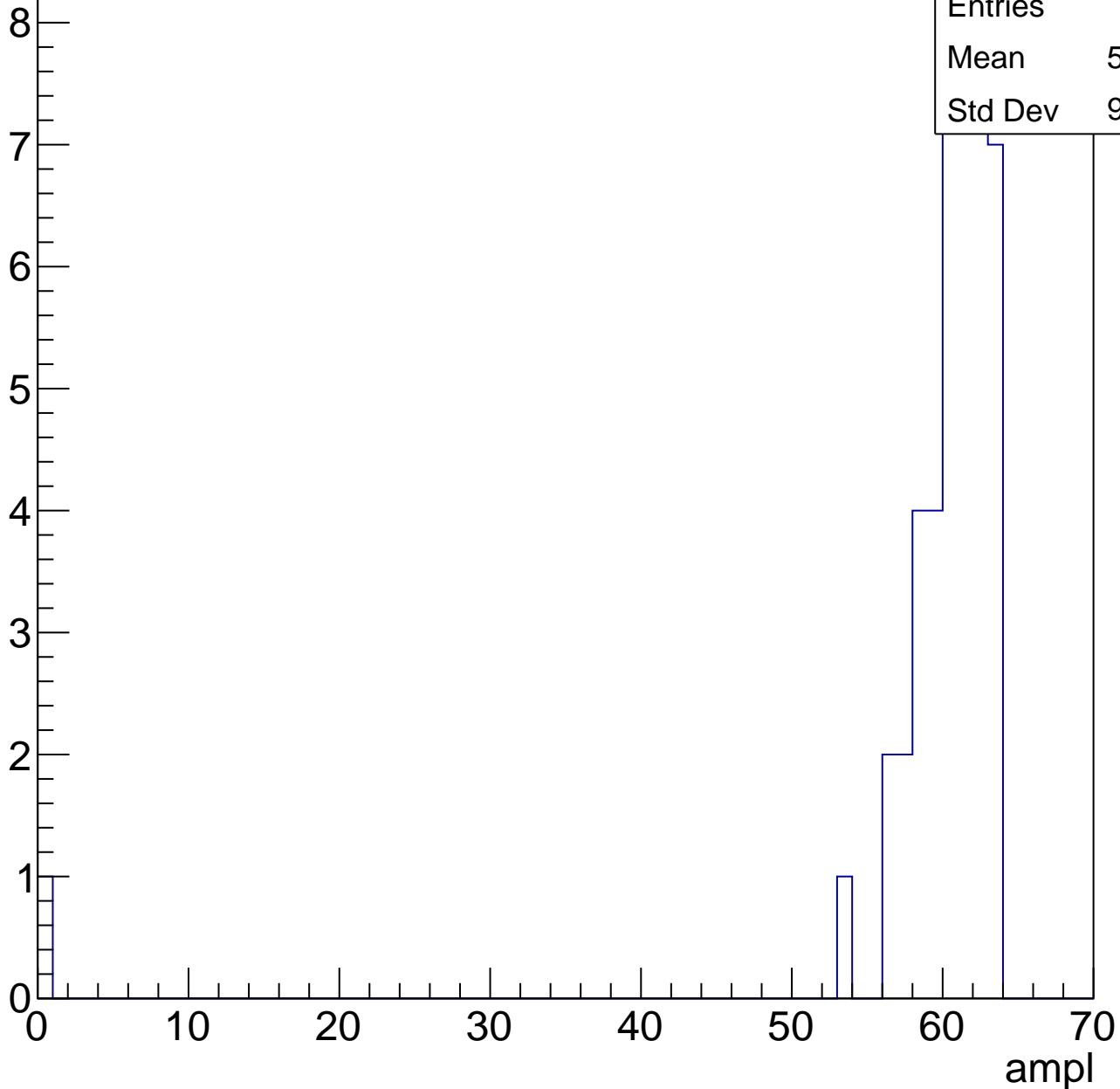


B1L100S, U10-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	58.93
Std Dev	9.154



B1L100S, U10-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch67, adc0

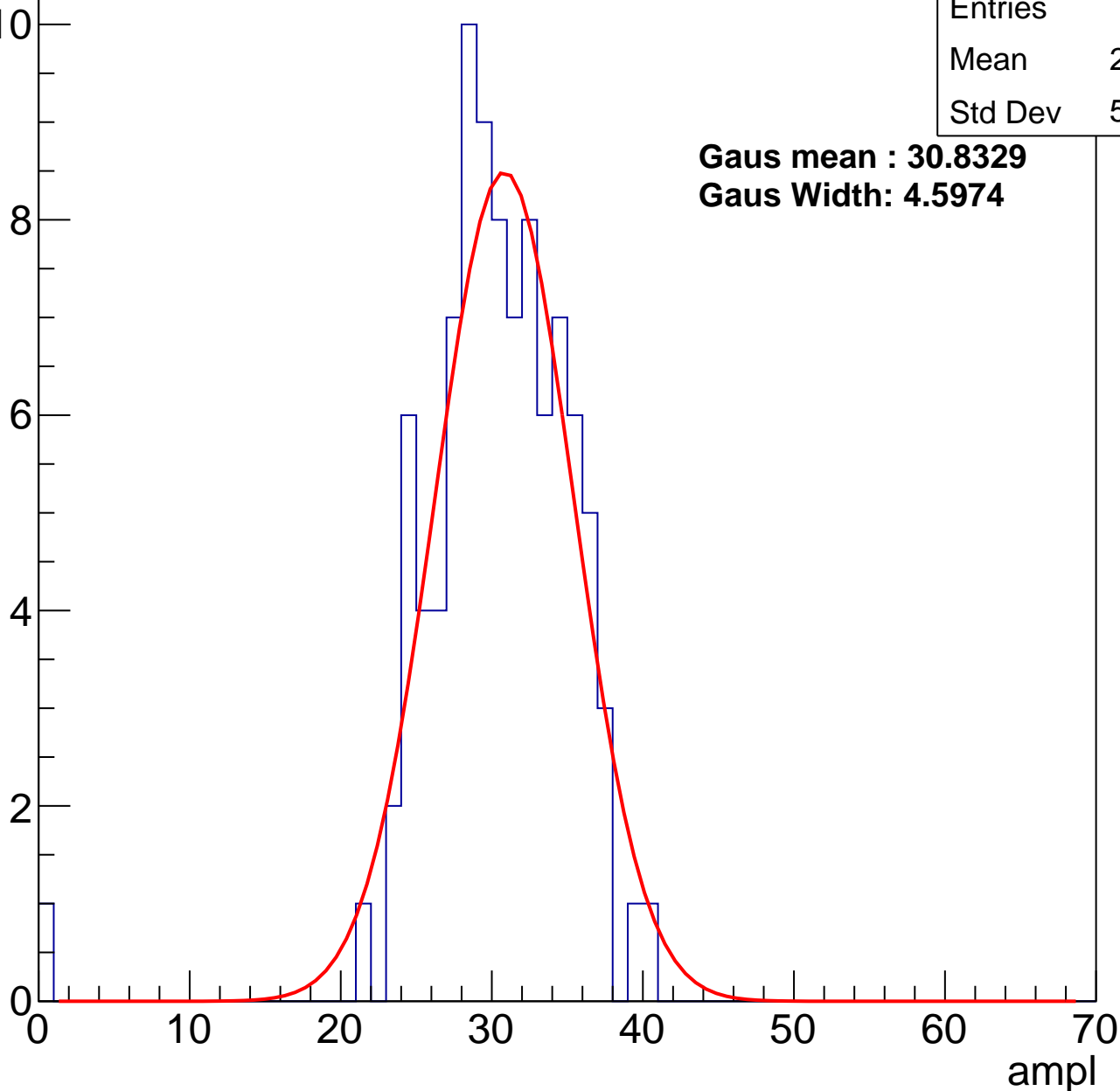
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	96
Mean	29.94
Std Dev	5.035

Gaus mean : 30.8329

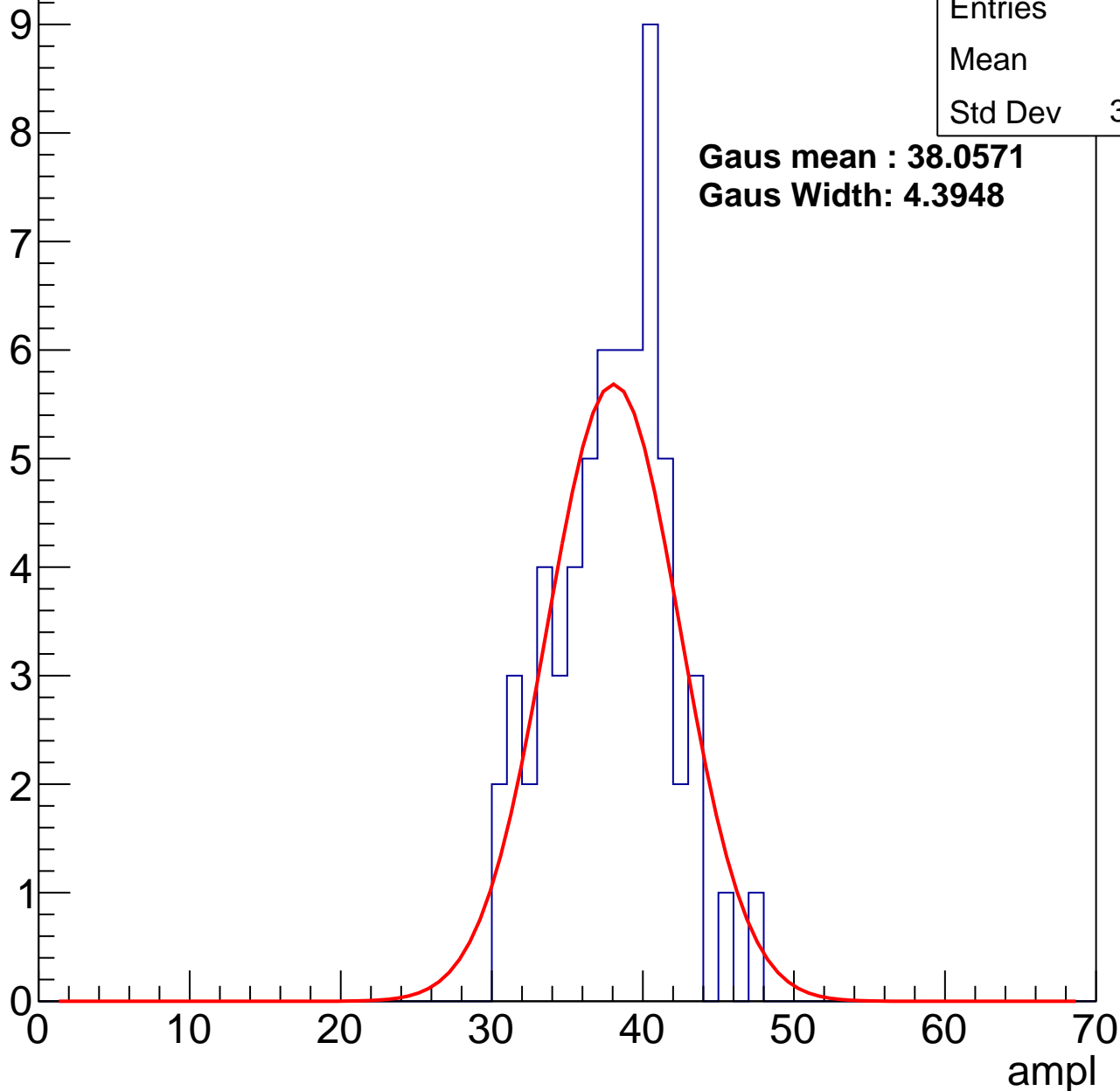
Gaus Width: 4.5974



B1L100S, U10-ch67, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch67, adc2

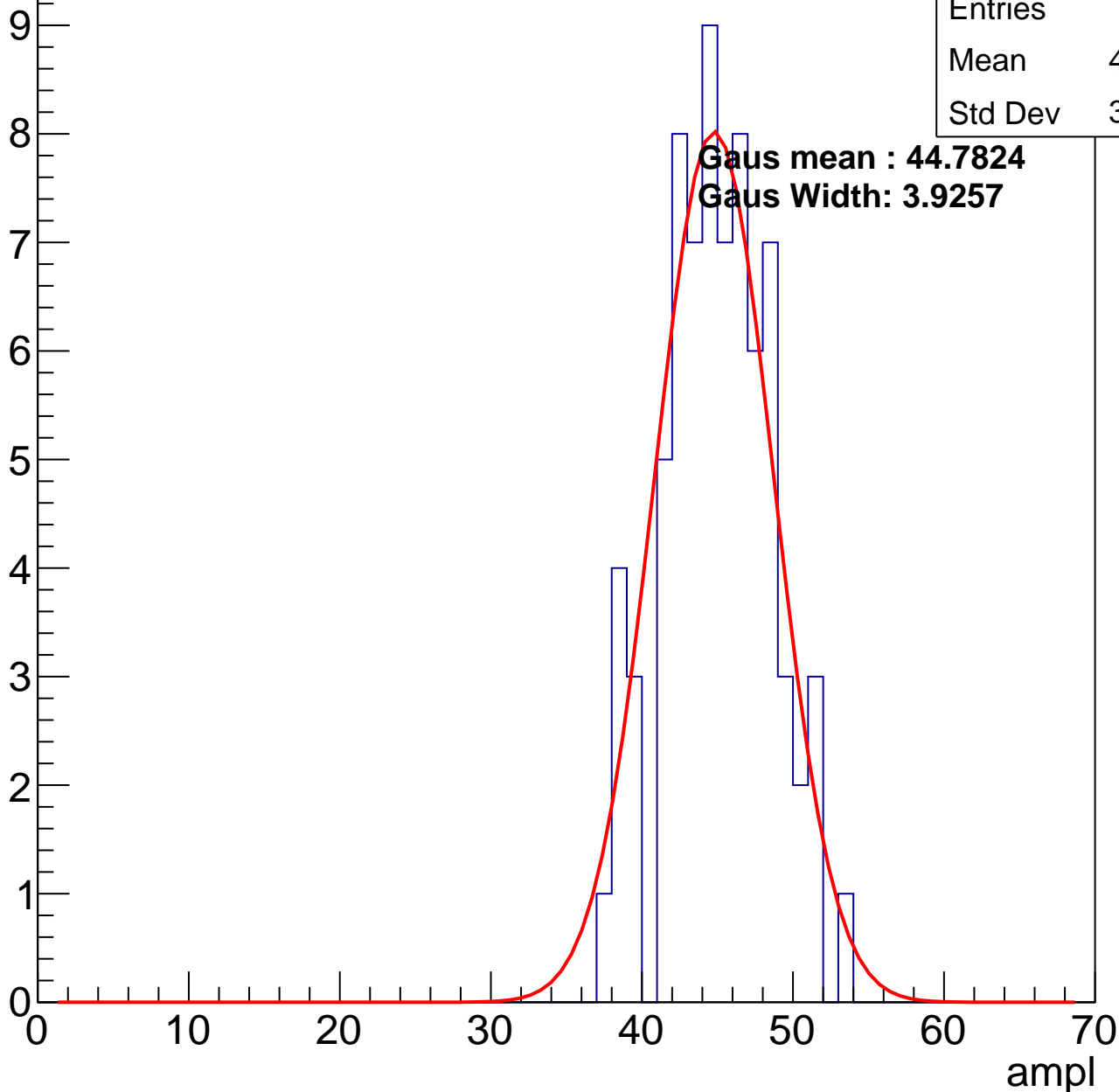
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	44.57
Std Dev	3.503

Gaus mean : 44.7824

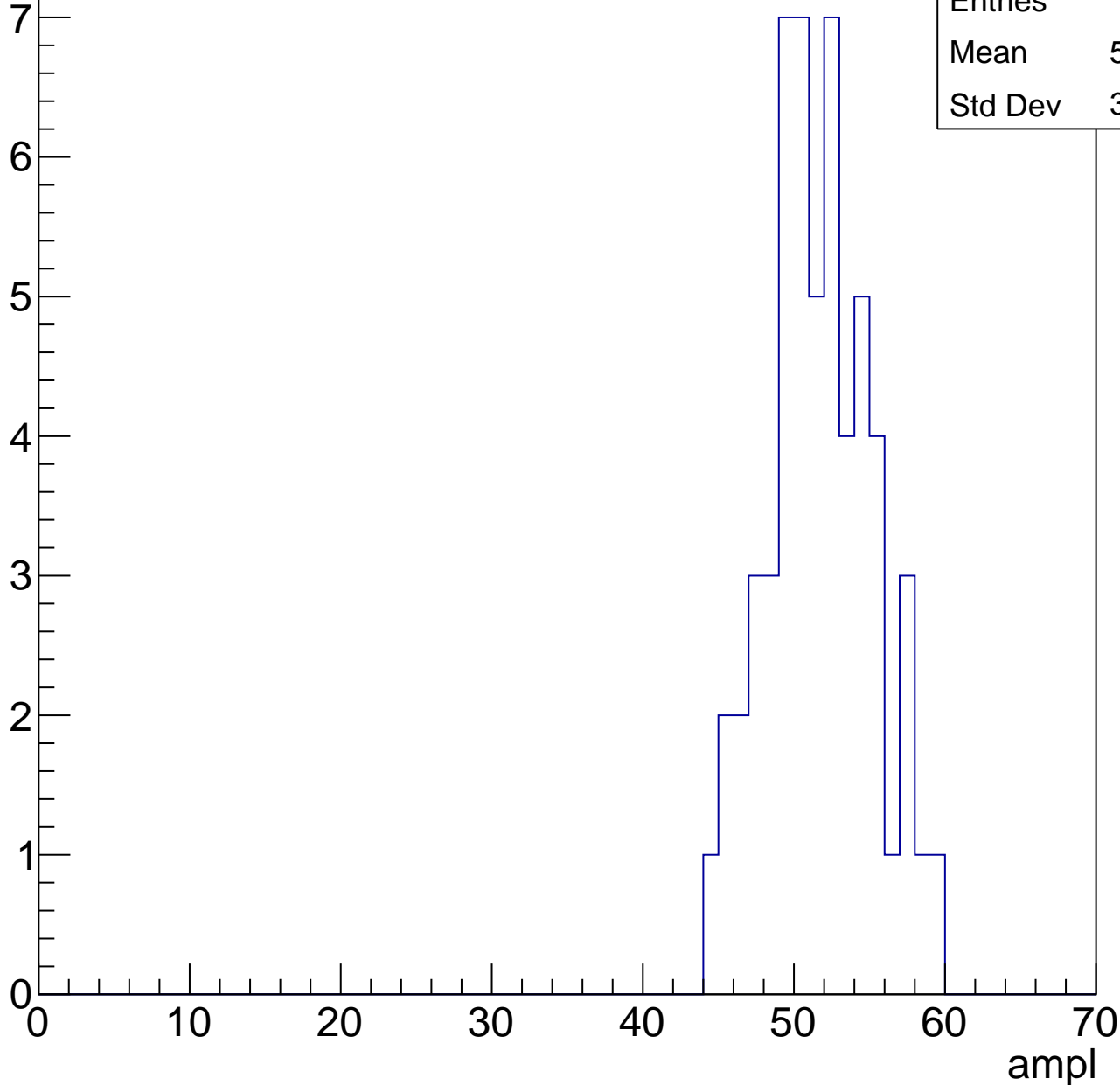
Gaus Width: 3.9257



B1L100S, U10-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

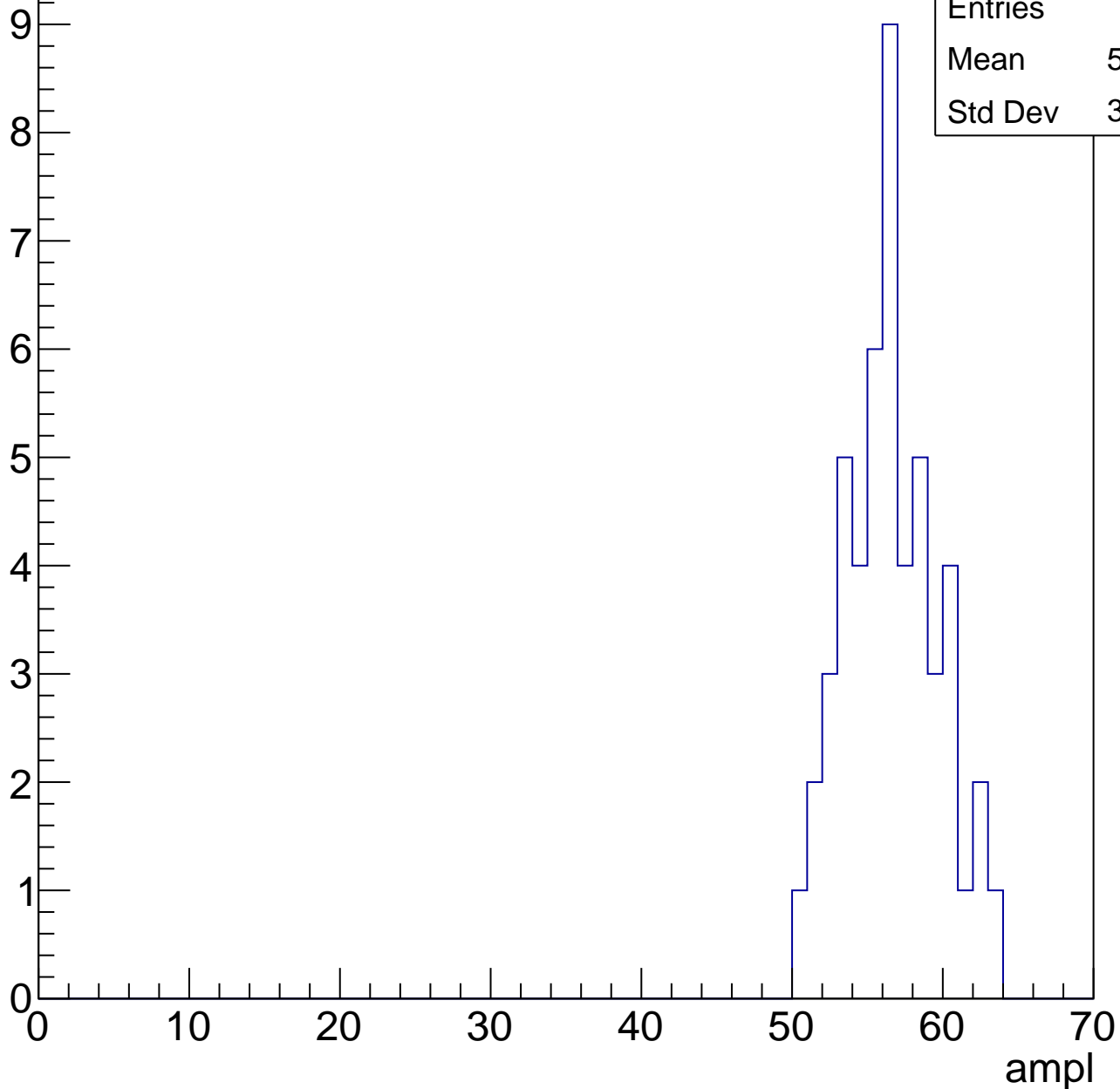
Entry



B1L100S, U10-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

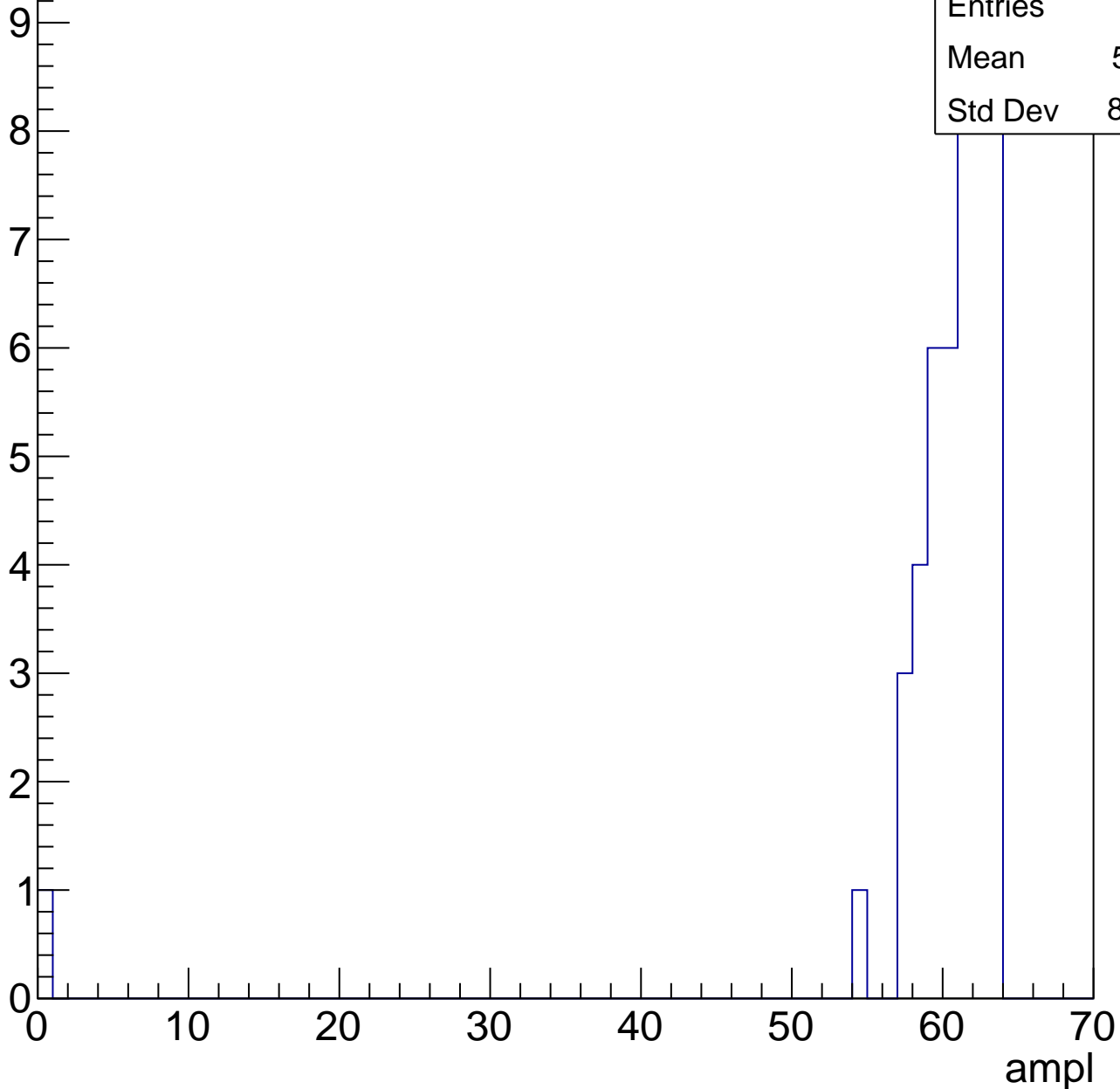


B1L100S, U10-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	59.21
Std Dev	8.966



B1L100S, U10-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch68, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	30.29
Std Dev	3.399

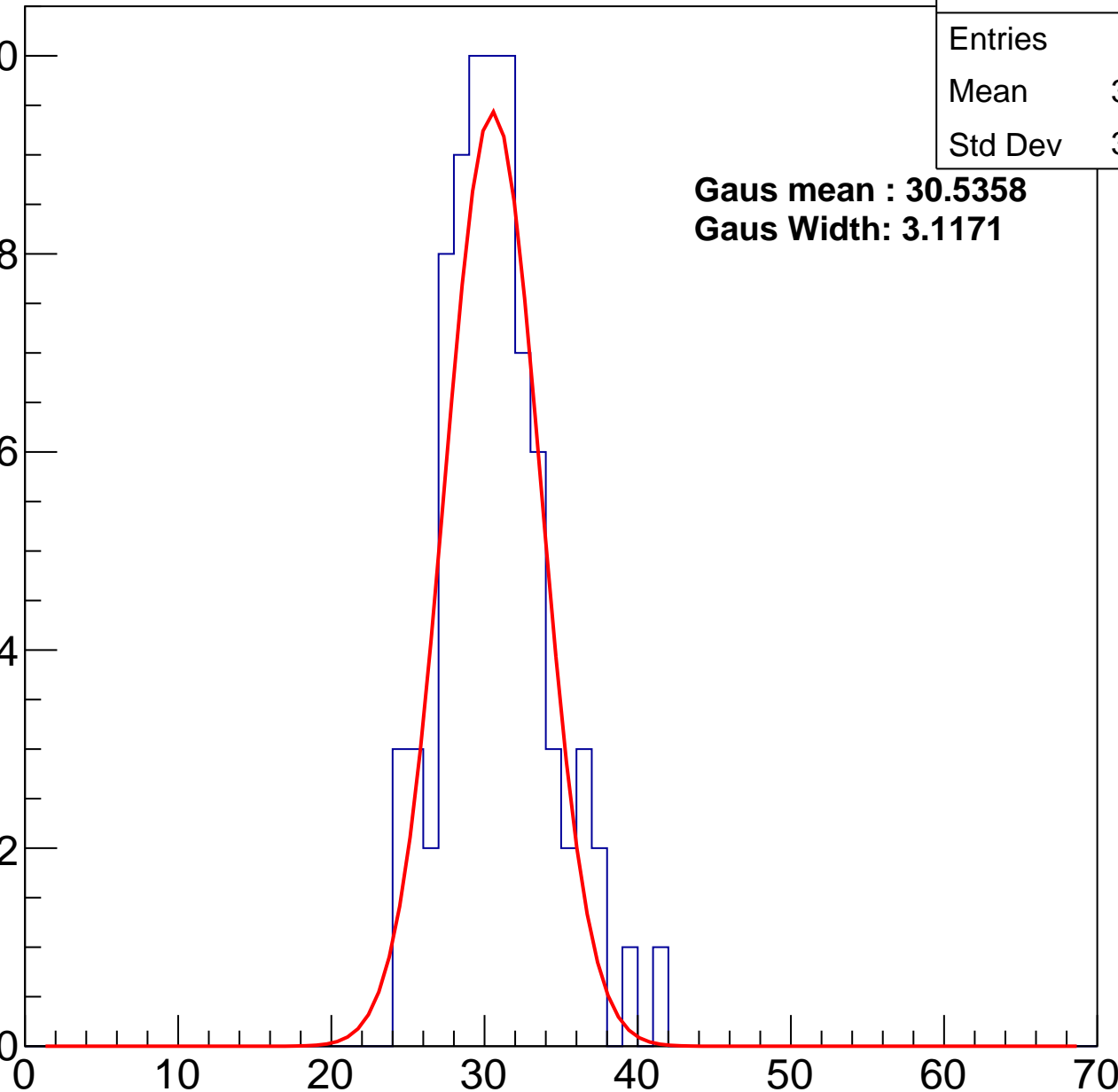
Gaus mean : 30.5358

Gaus Width: 3.1171

Entry

10
8
6
4
2
0

ampl



B1L100S, U10-ch68, adc1

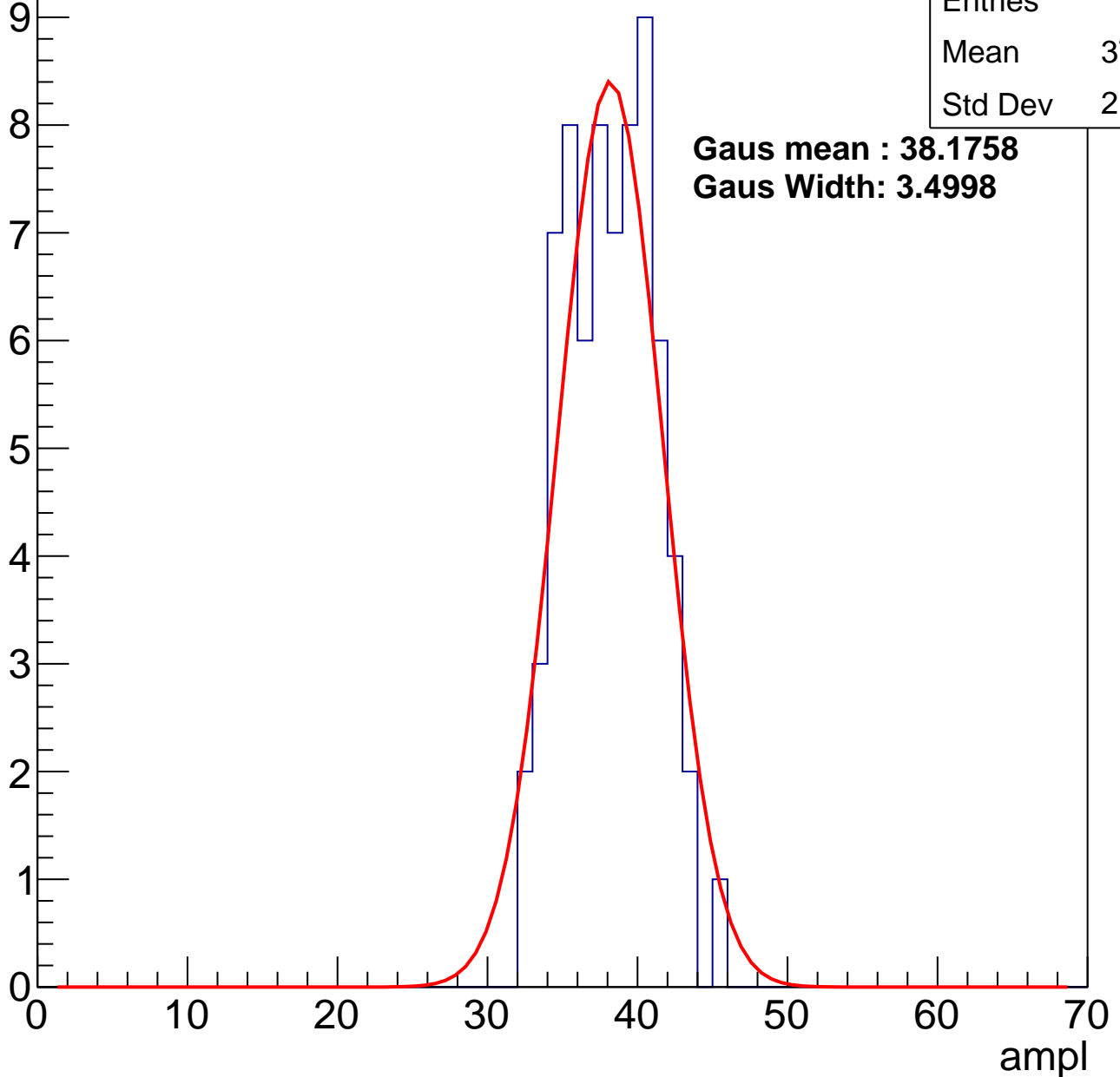
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.69
Std Dev	2.948

Gaus mean : 38.1758

Gaus Width: 3.4998



B1L100S, U10-ch68, adc2

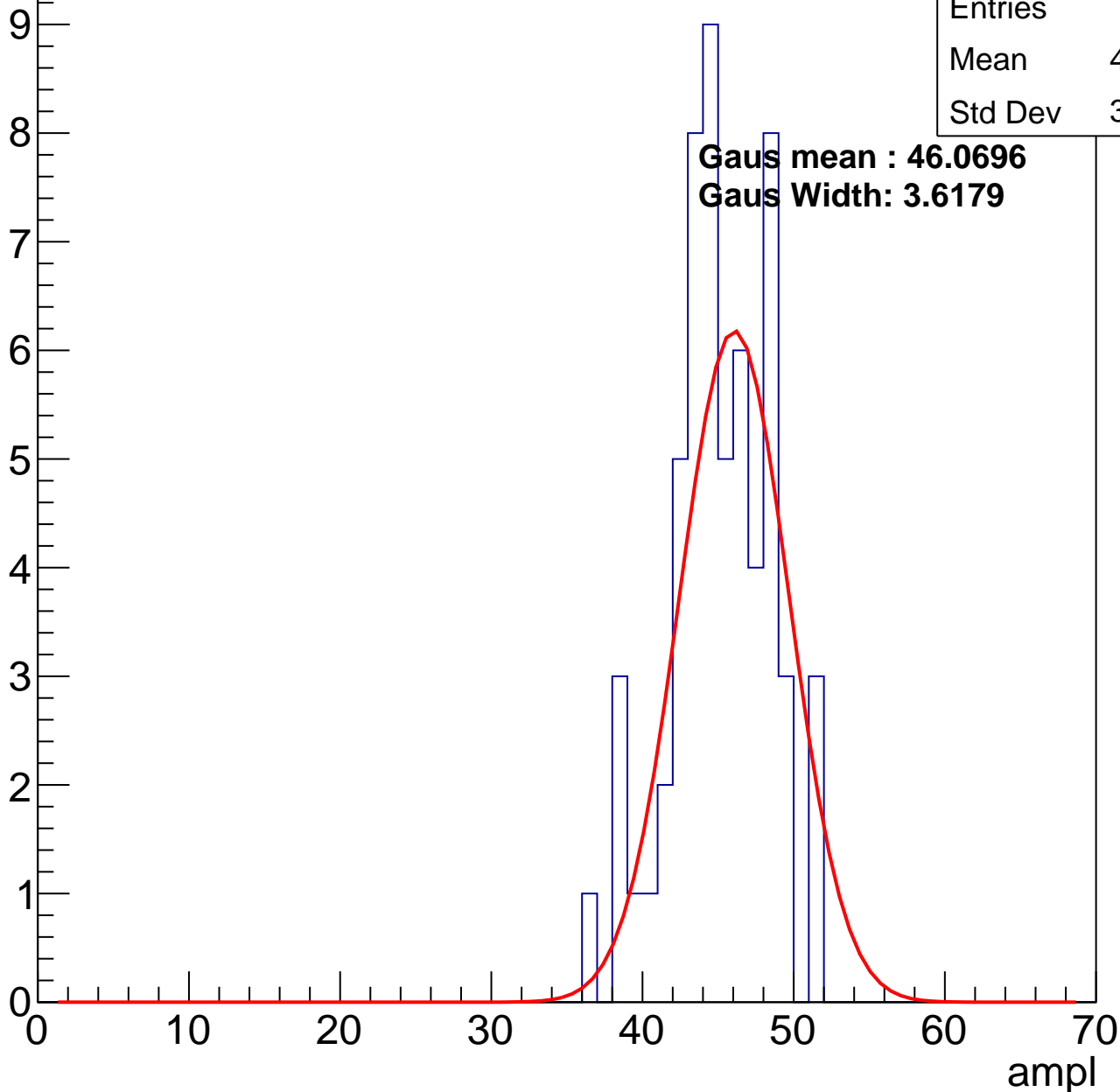
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.64
Std Dev	3.318

Gaus mean : 46.0696

Gaus Width: 3.6179

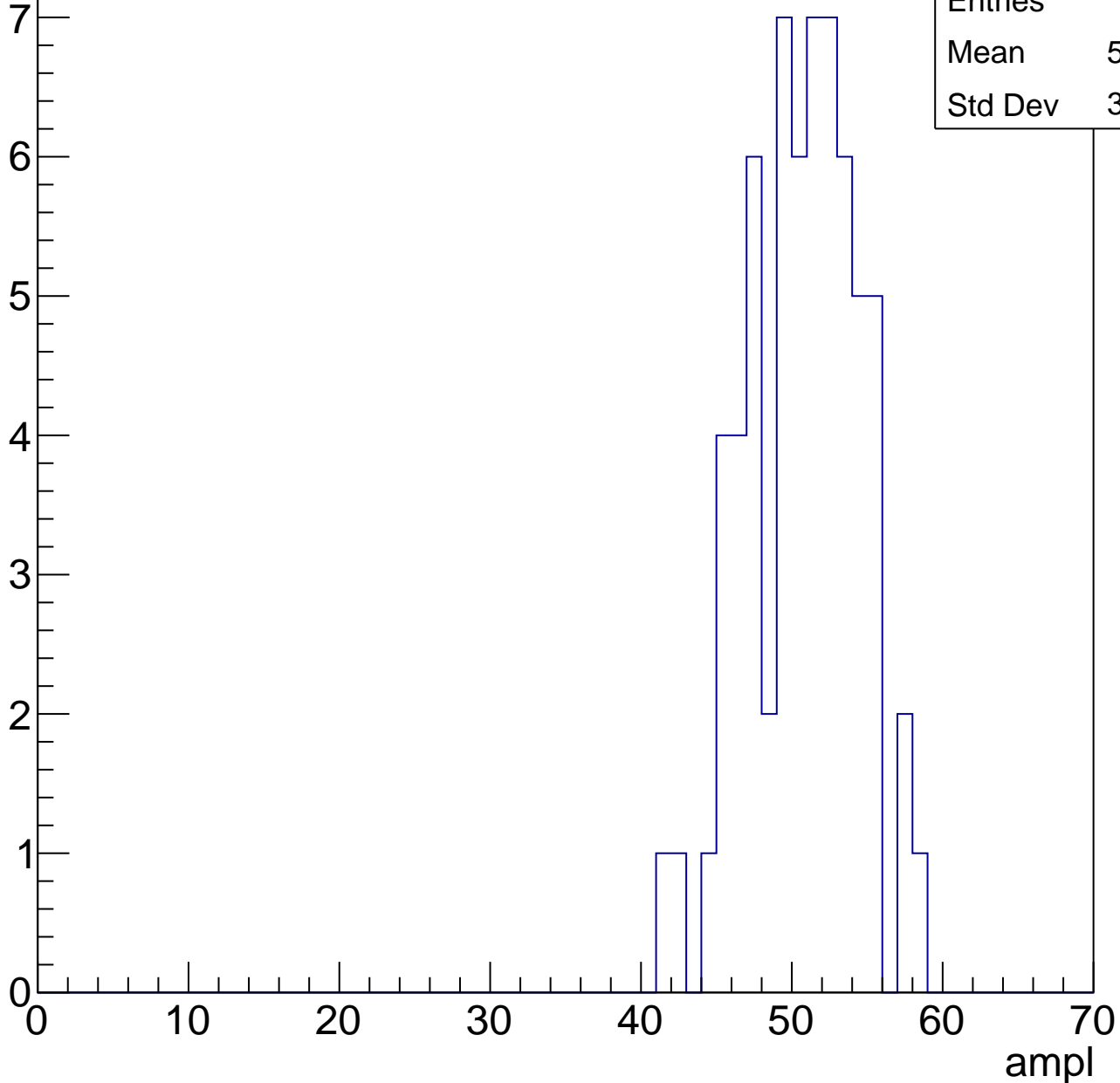


B1L100S, U10-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

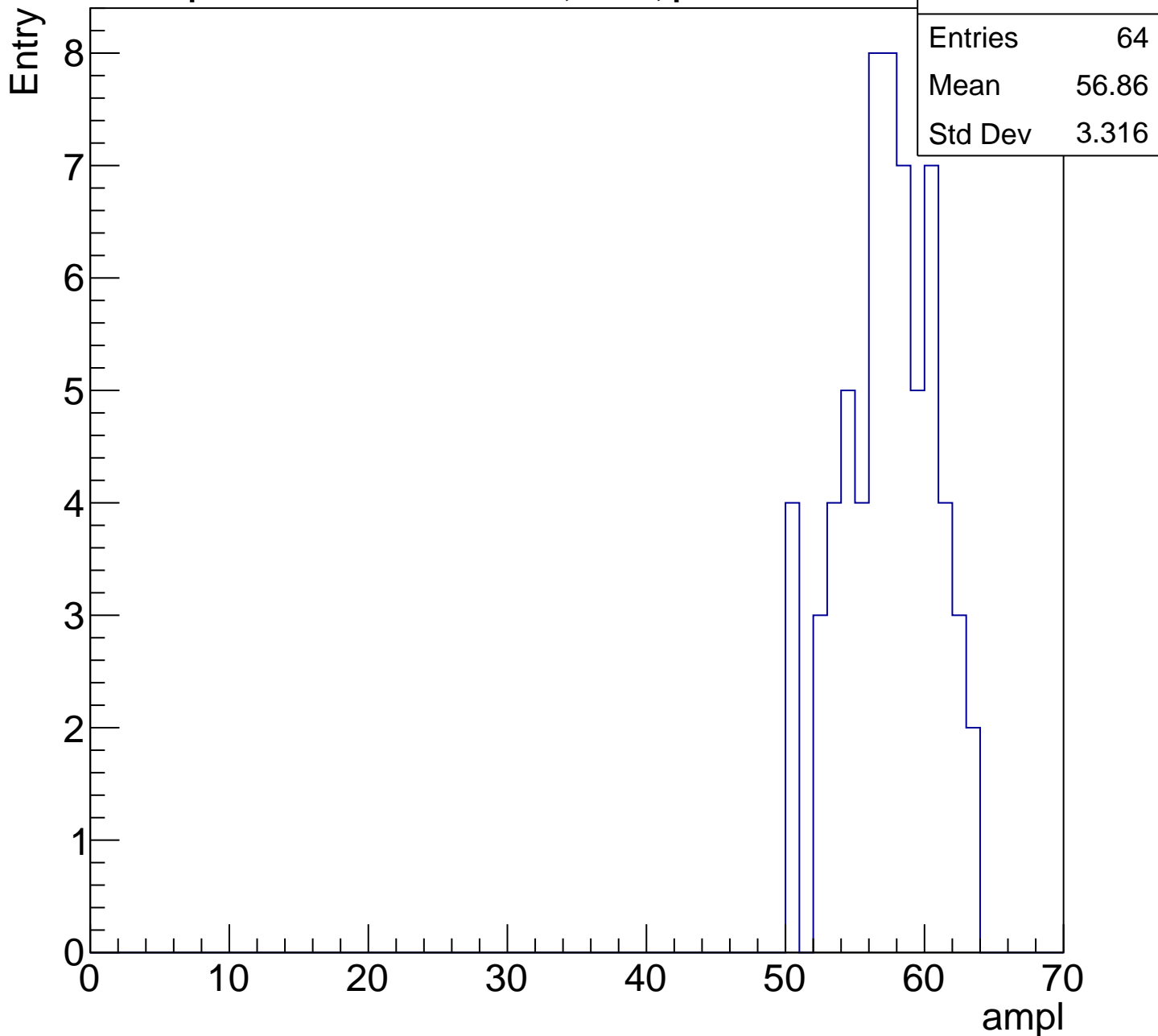
Entry

Entries	65
Mean	50.28
Std Dev	3.648



B1L100S, U10-ch68, adc4

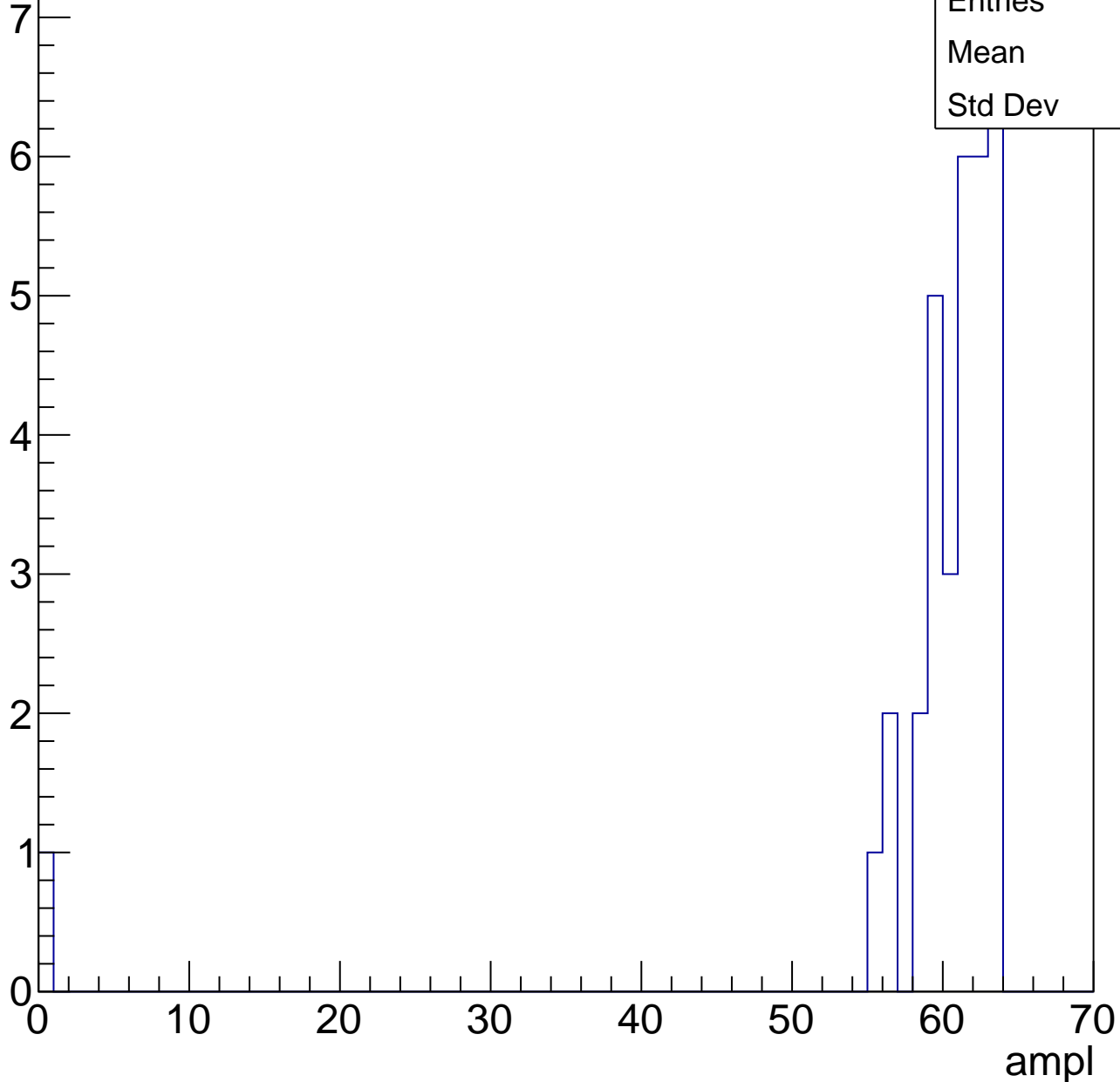
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch69, adc0

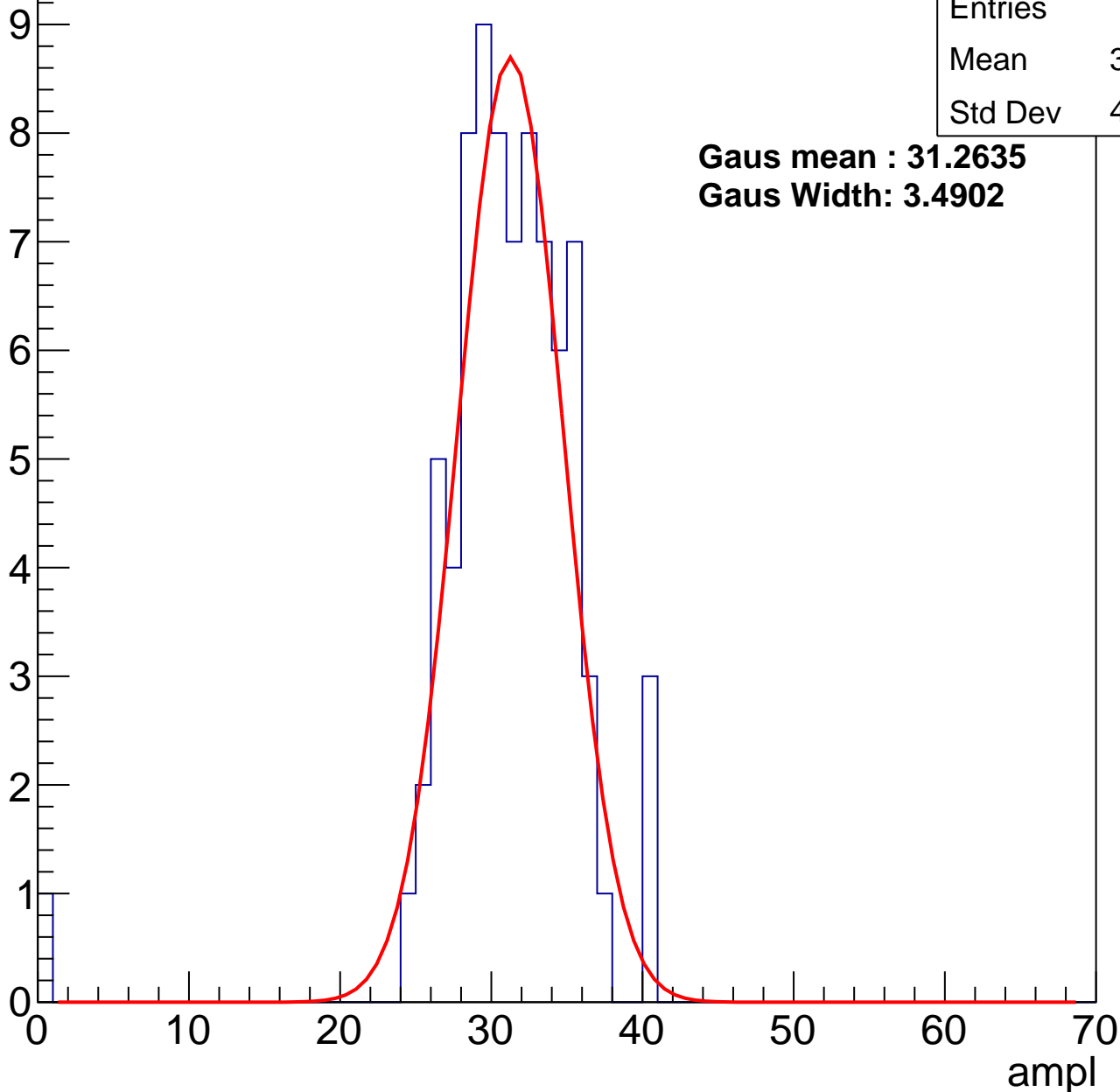
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	30.69
Std Dev	4.918

Gaus mean : 31.2635

Gaus Width: 3.4902



B1L100S, U10-ch69, adc1

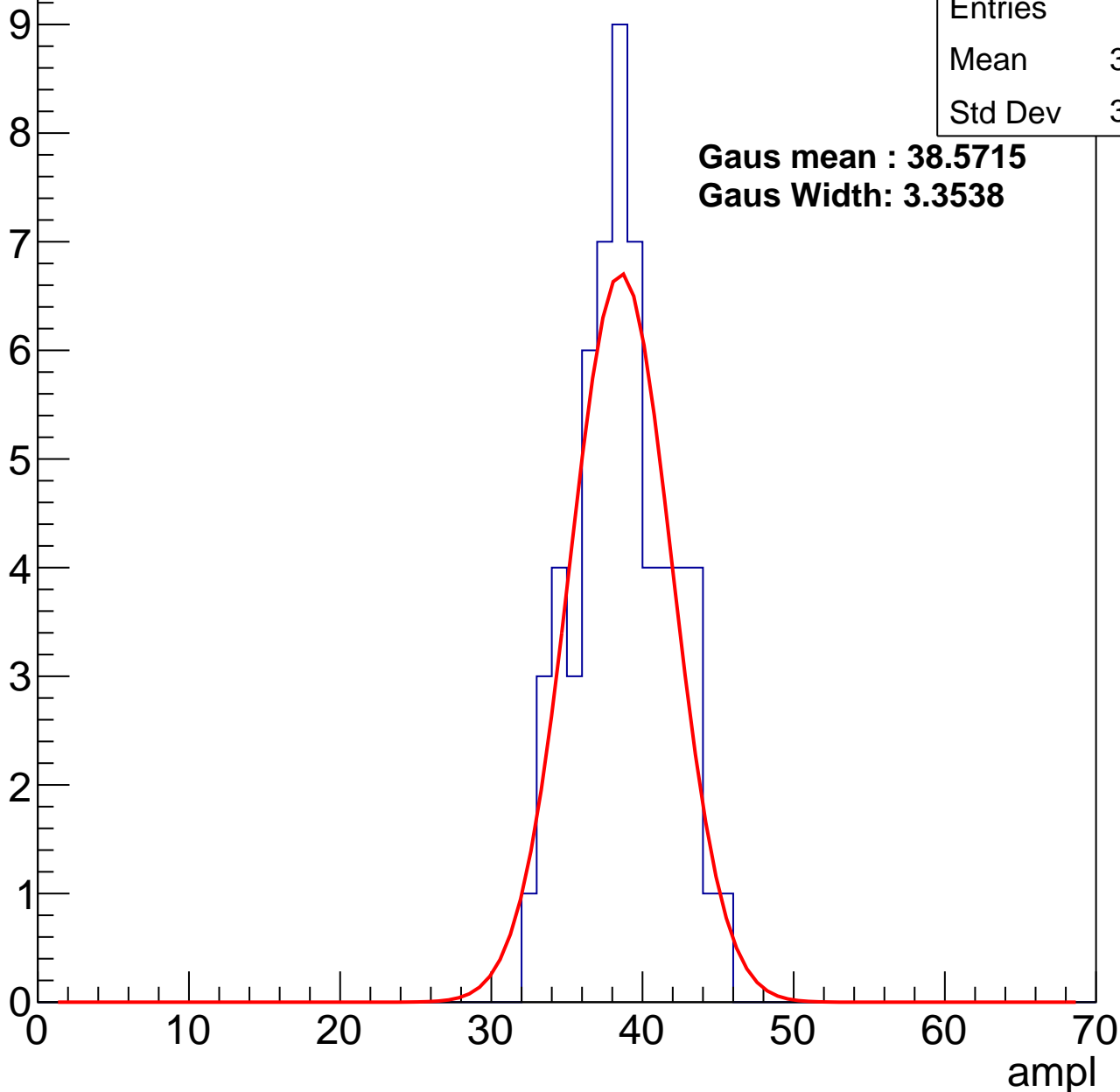
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.19
Std Dev	3.048

Gaus mean : 38.5715

Gaus Width: 3.3538



B1L100S, U10-ch69, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	44.75
Std Dev	3.74

Gaus mean : 45.4475

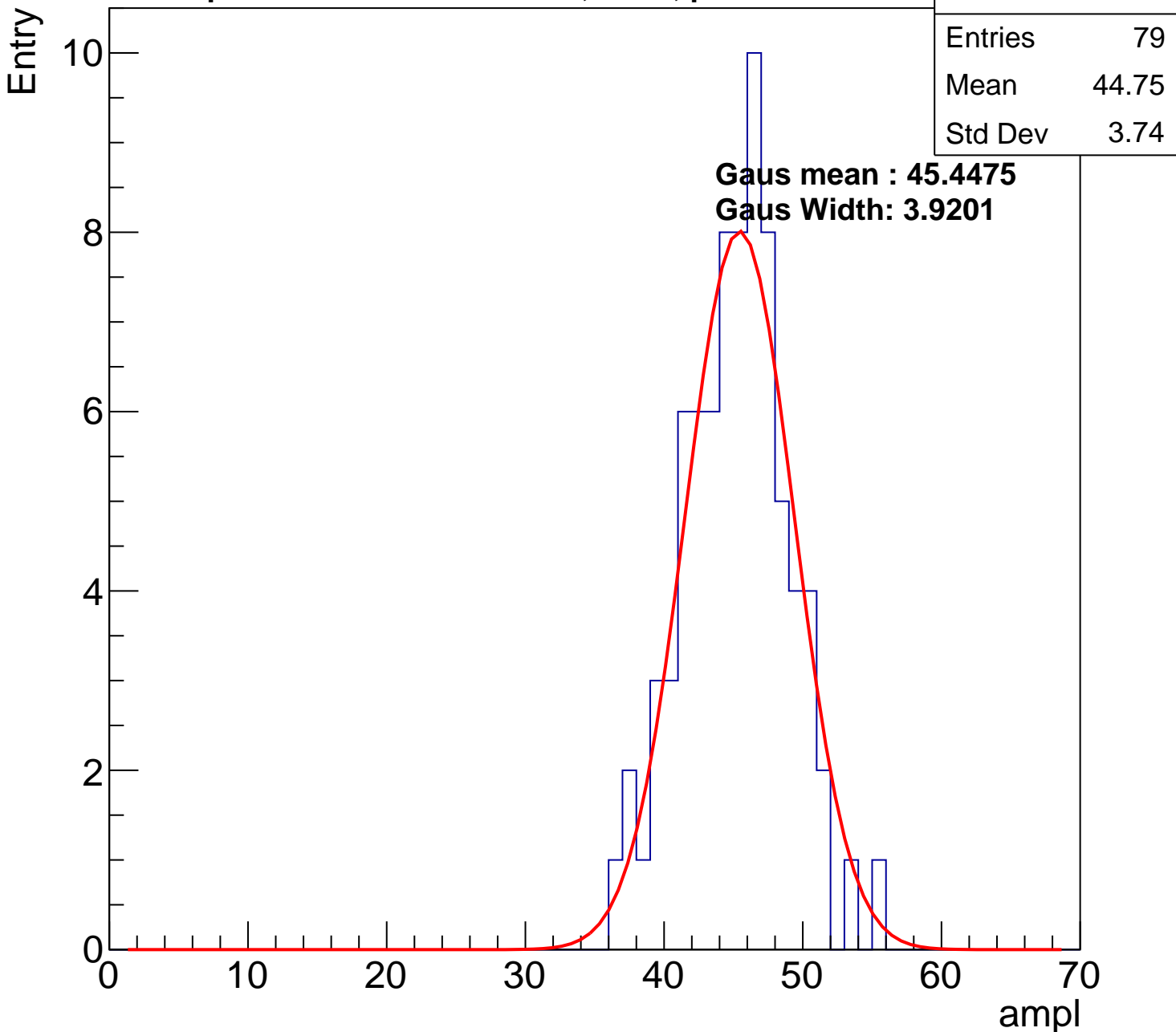
Gaus Width: 3.9201

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

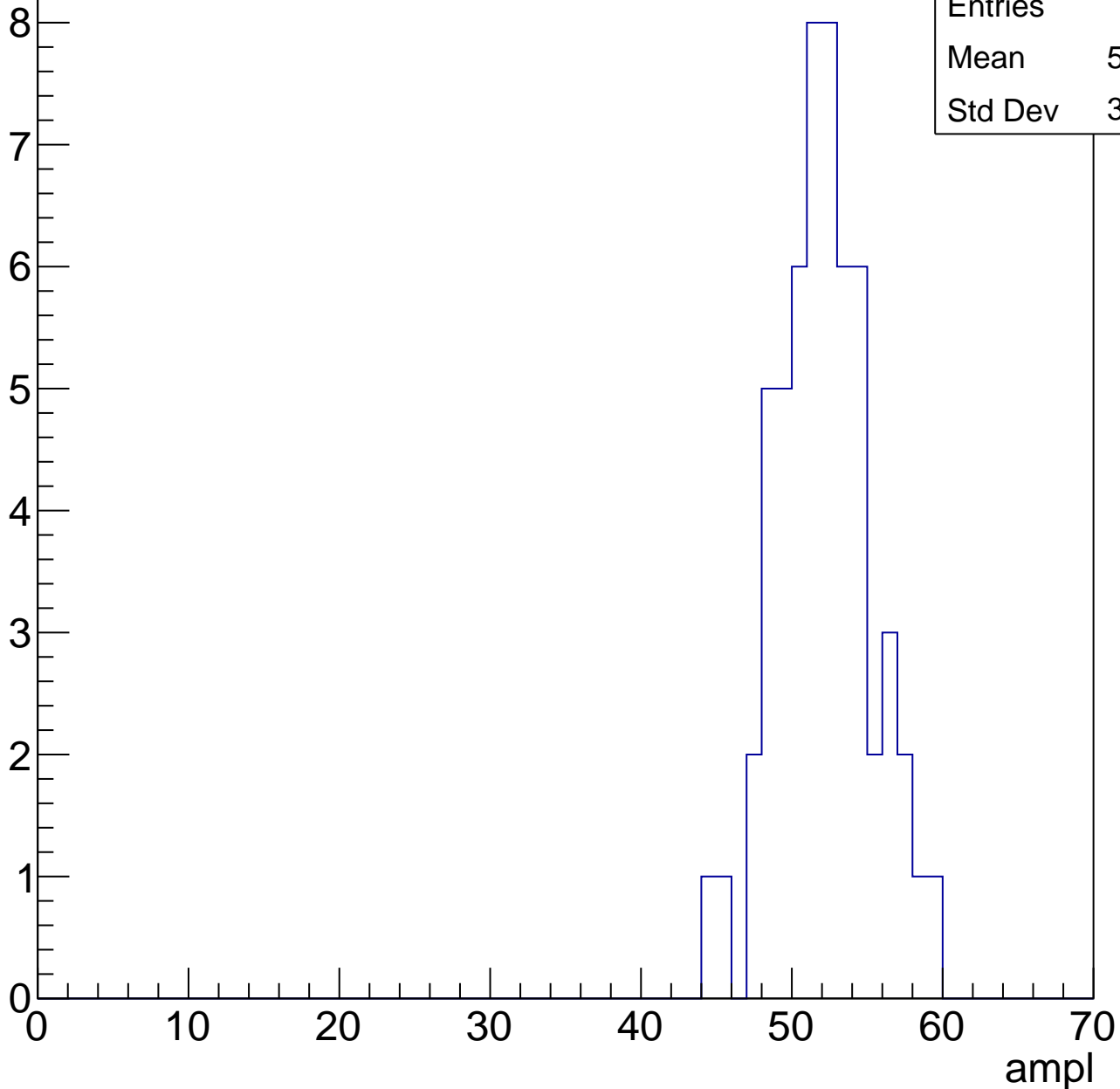


B1L100S, U10-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	51.63
Std Dev	3.087



B1L100S, U10-ch69, adc4

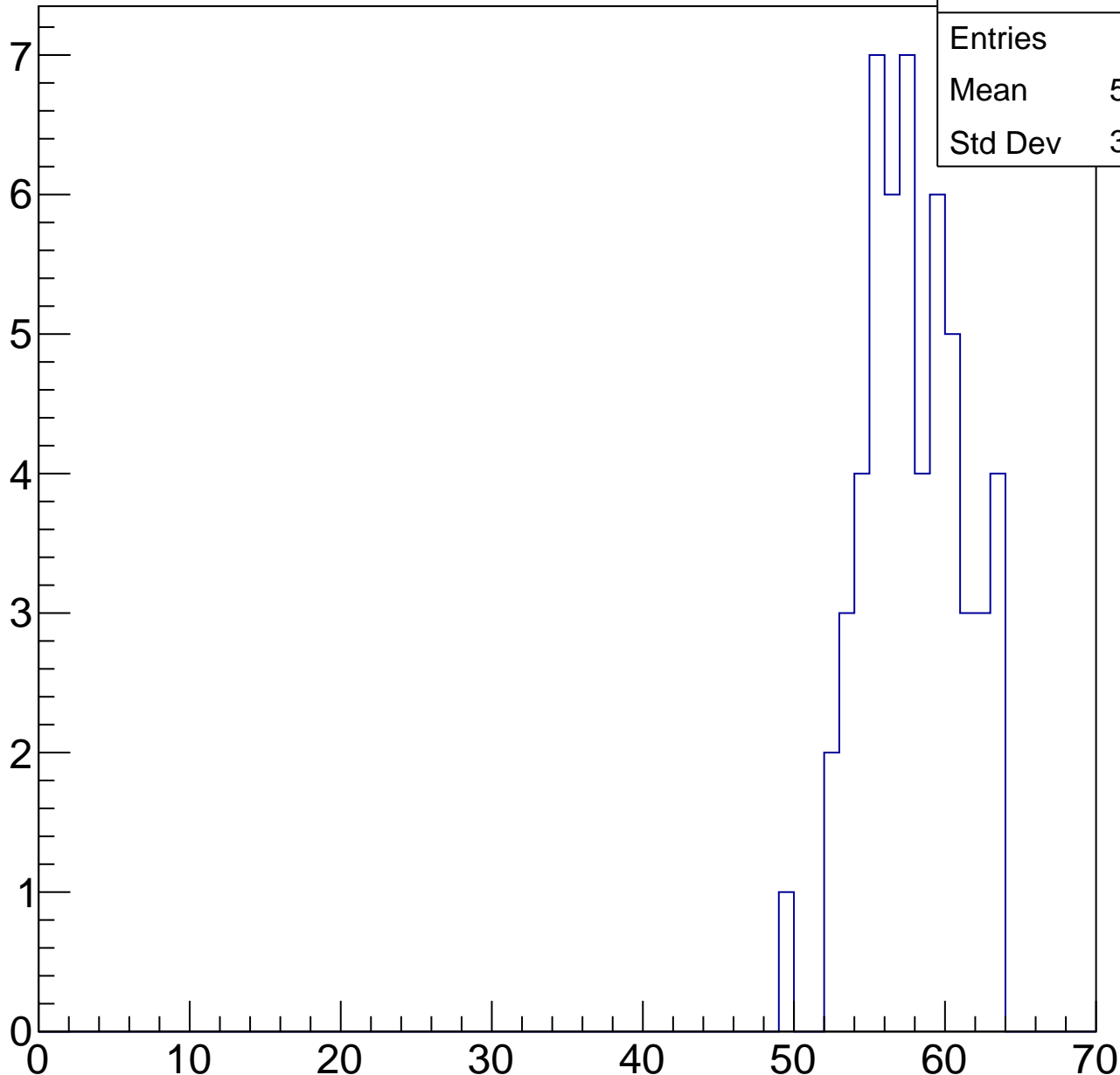
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	55
Mean	57.36
Std Dev	3.204

ampl

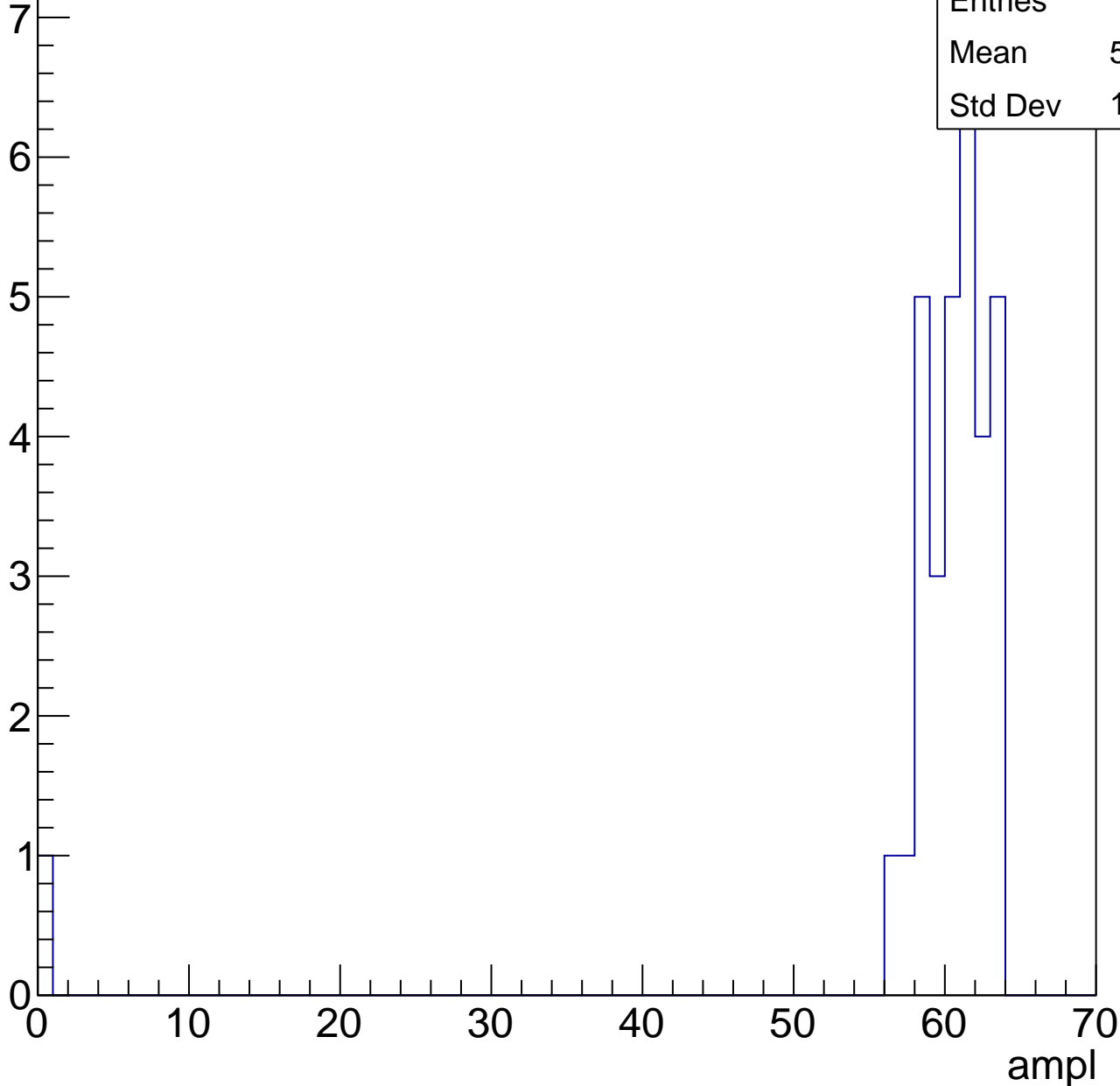


B1L100S, U10-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.44
Std Dev	10.66



B1L100S, U10-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch70, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	29.99
Std Dev	3.658

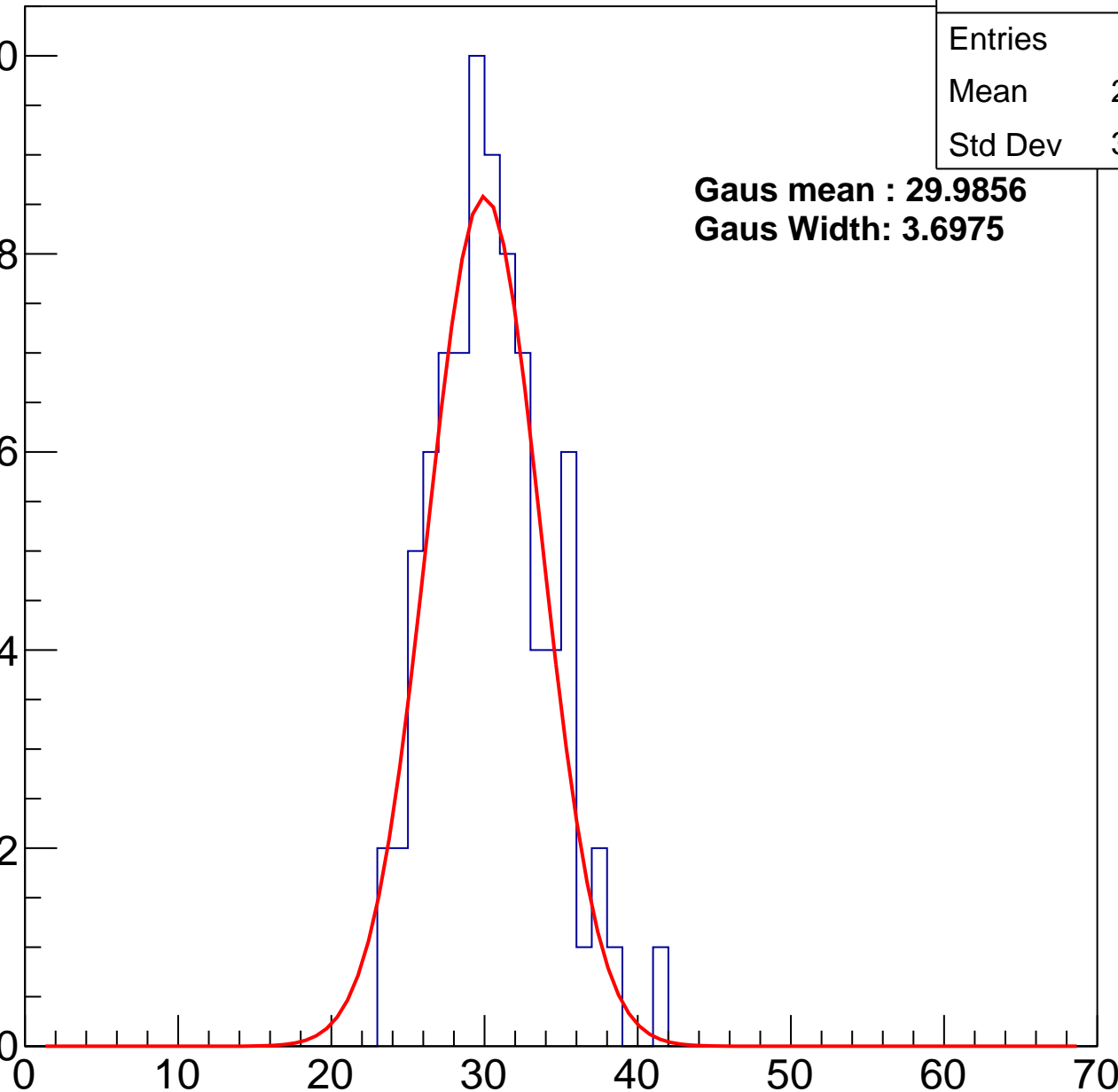
Gaus mean : 29.9856

Gaus Width: 3.6975

Entry

10
8
6
4
2
0

ampl



B1L100S, U10-ch70, adc1

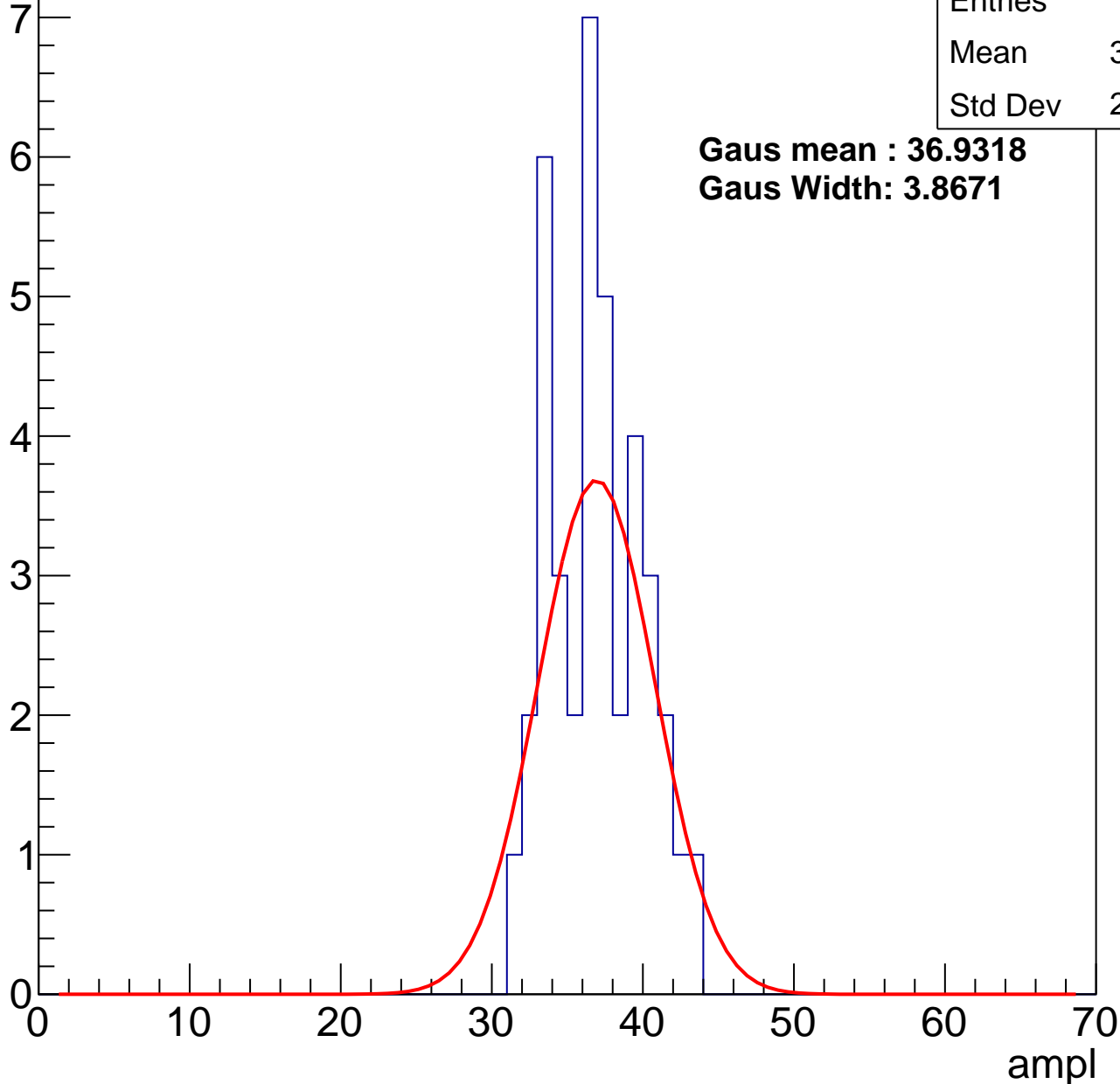
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	36.44
Std Dev	2.994

Gaus mean : 36.9318

Gaus Width: 3.8671



B1L100S, U10-ch70, adc2

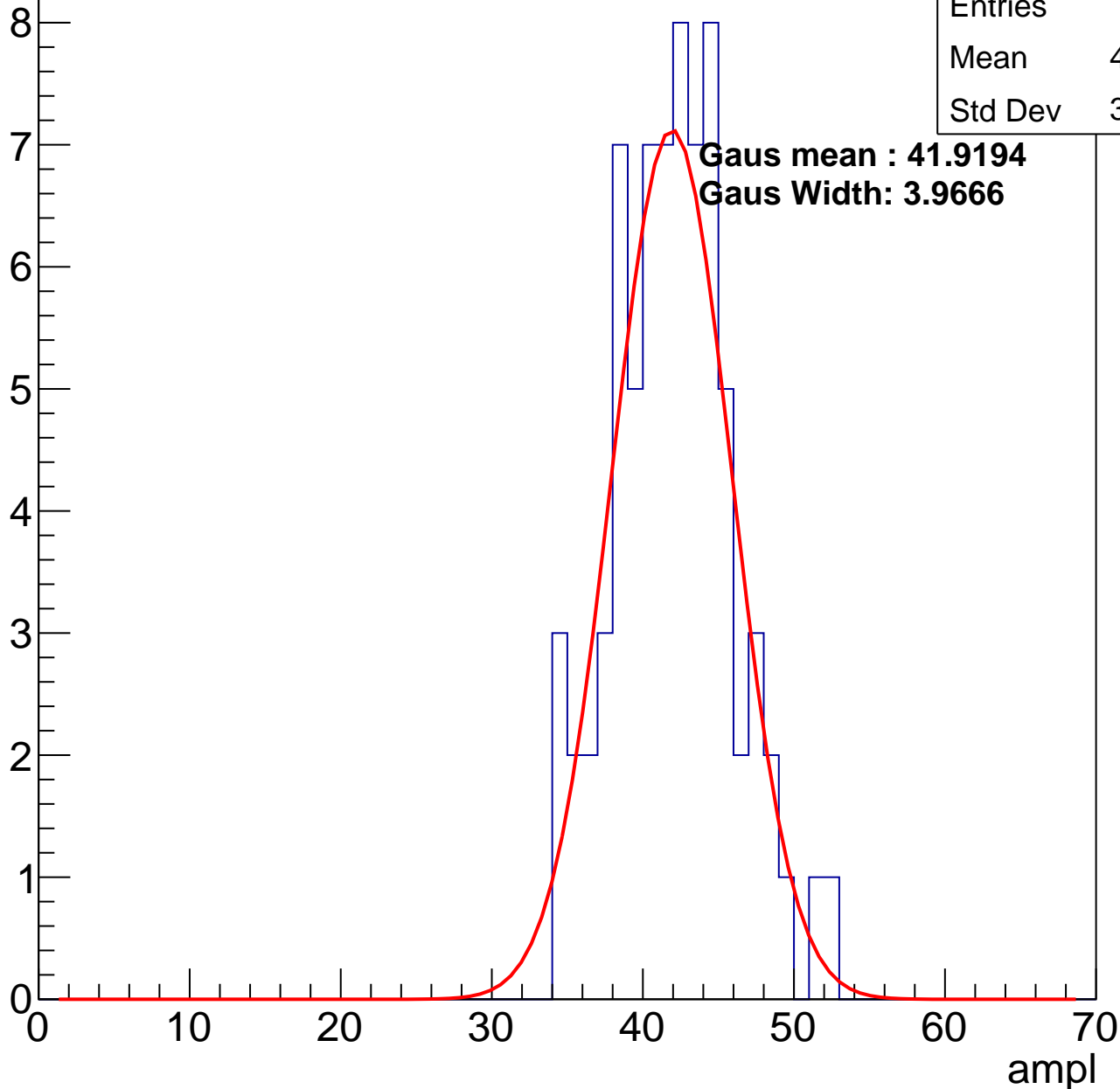
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	41.59
Std Dev	3.873

Gaus mean : 41.9194

Gaus Width: 3.9666

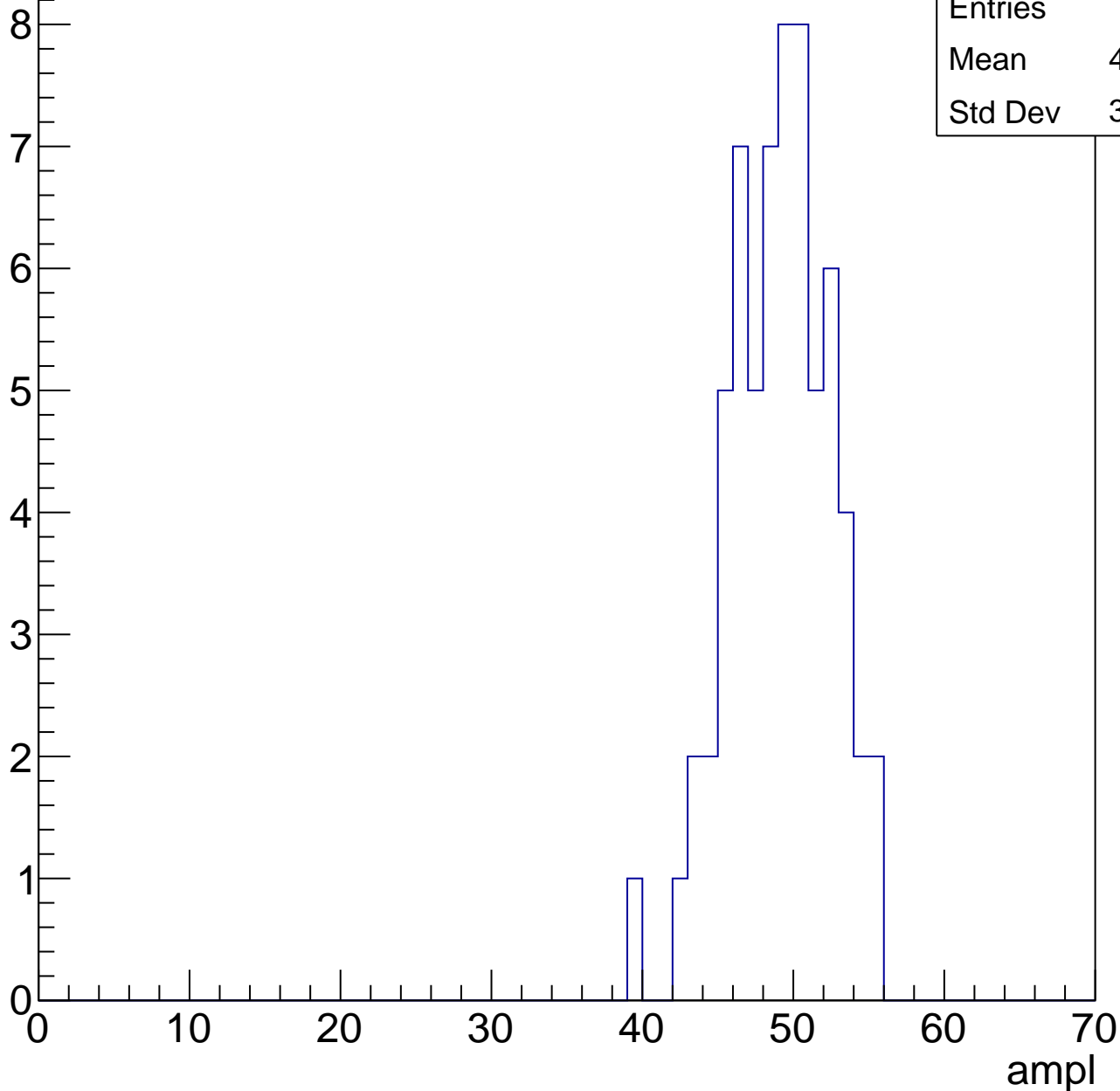


B1L100S, U10-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

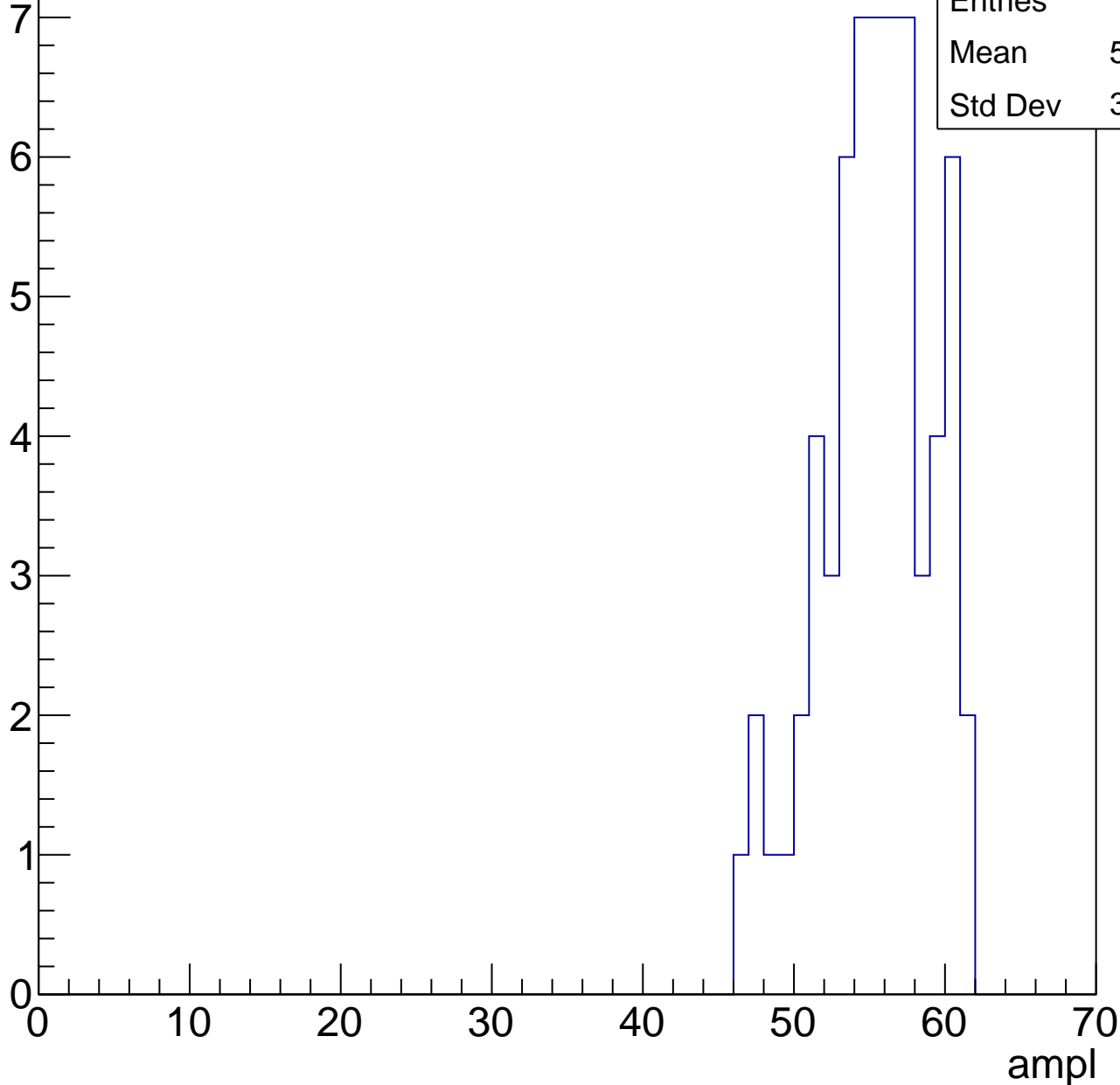
Entries	65
Mean	48.65
Std Dev	3.284



B1L100S, U10-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch70, adc5

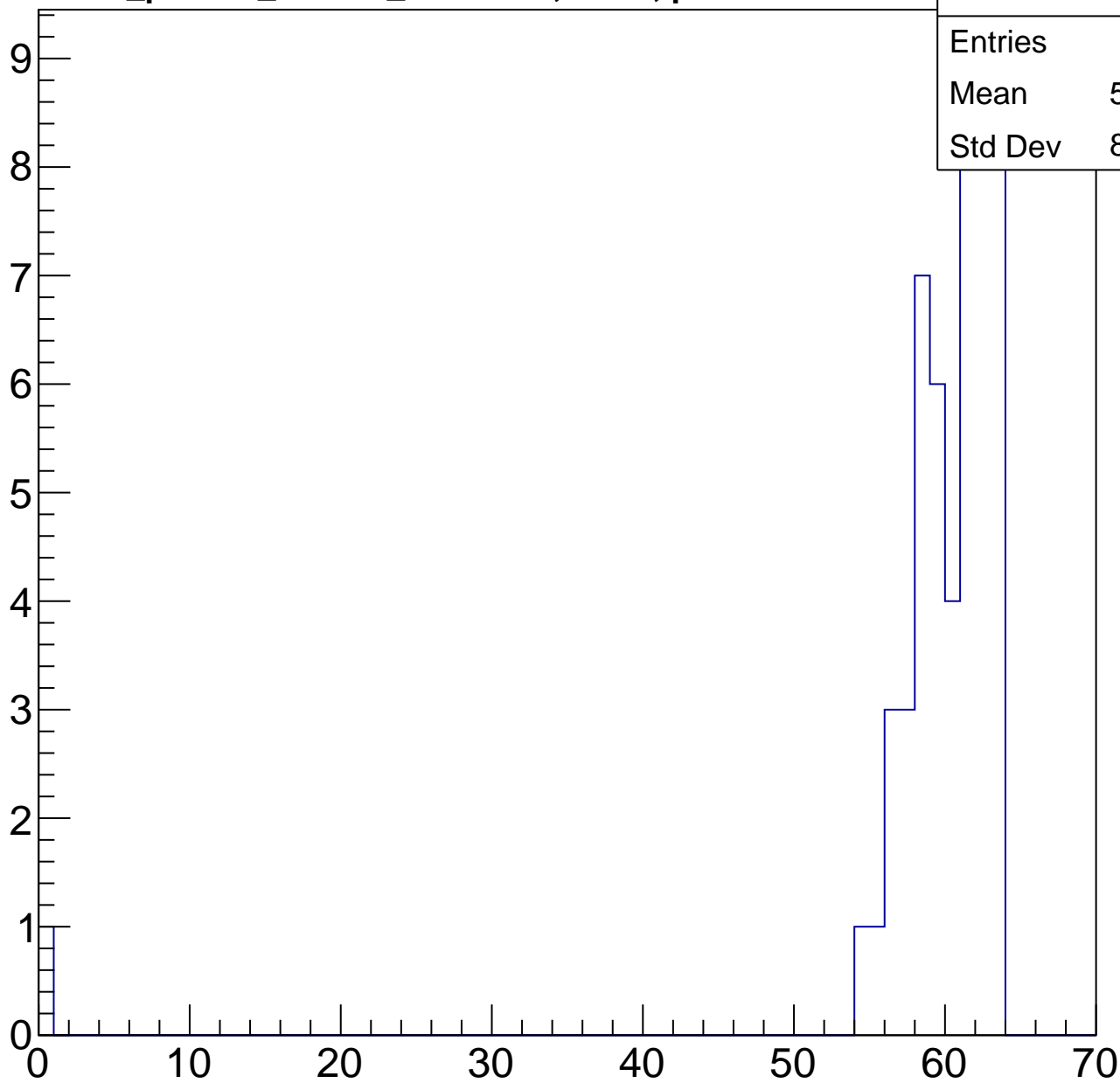
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.78
Std Dev	8.644

ampl



B1L100S, U10-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U10-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch71, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	31.29
Std Dev	3.422

Gaus mean : 32.0004

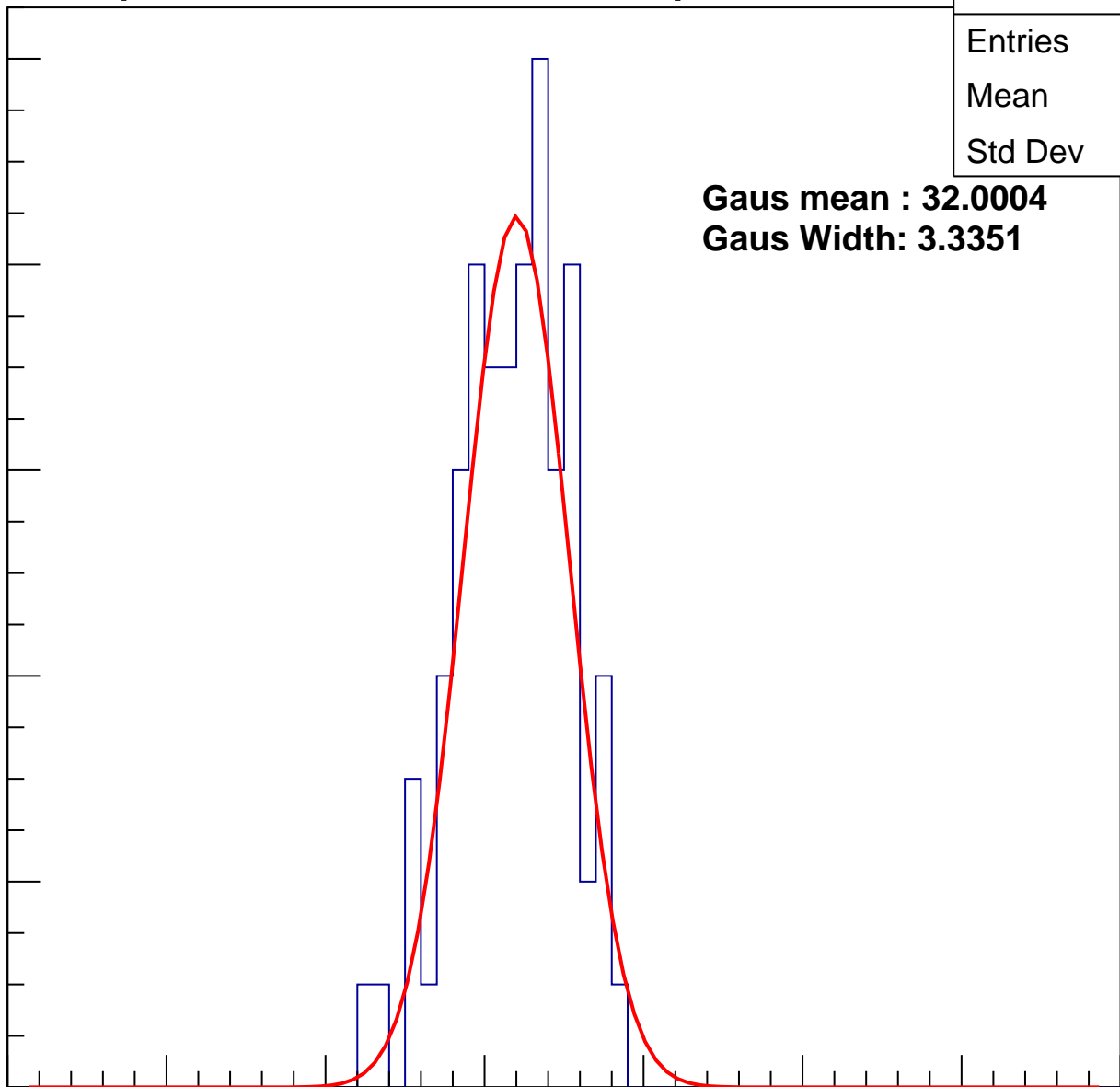
Gaus Width: 3.3351

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch71, adc1

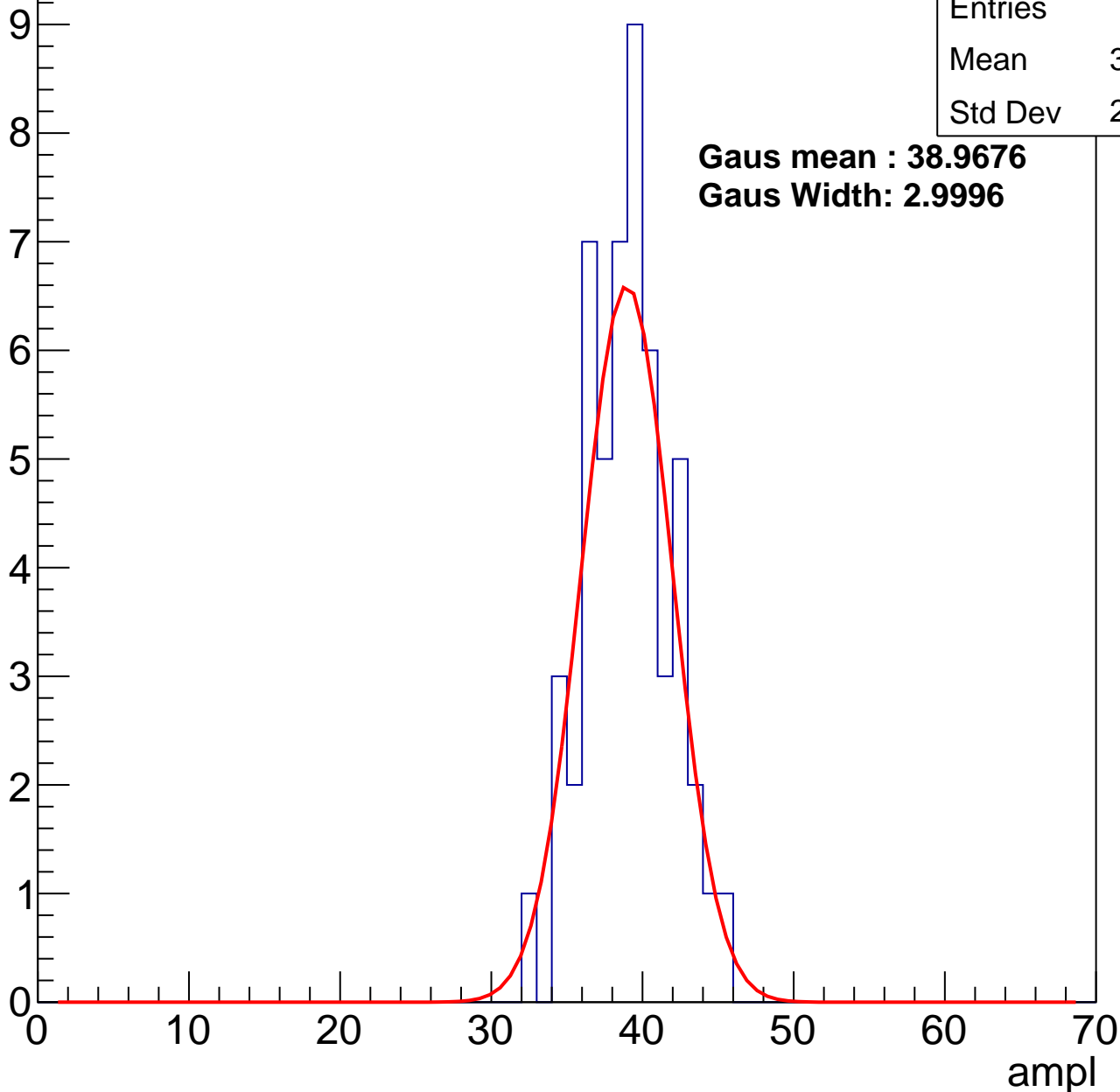
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	38.58
Std Dev	2.748

Gaus mean : 38.9676

Gaus Width: 2.9996



B1L100S, U10-ch71, adc2

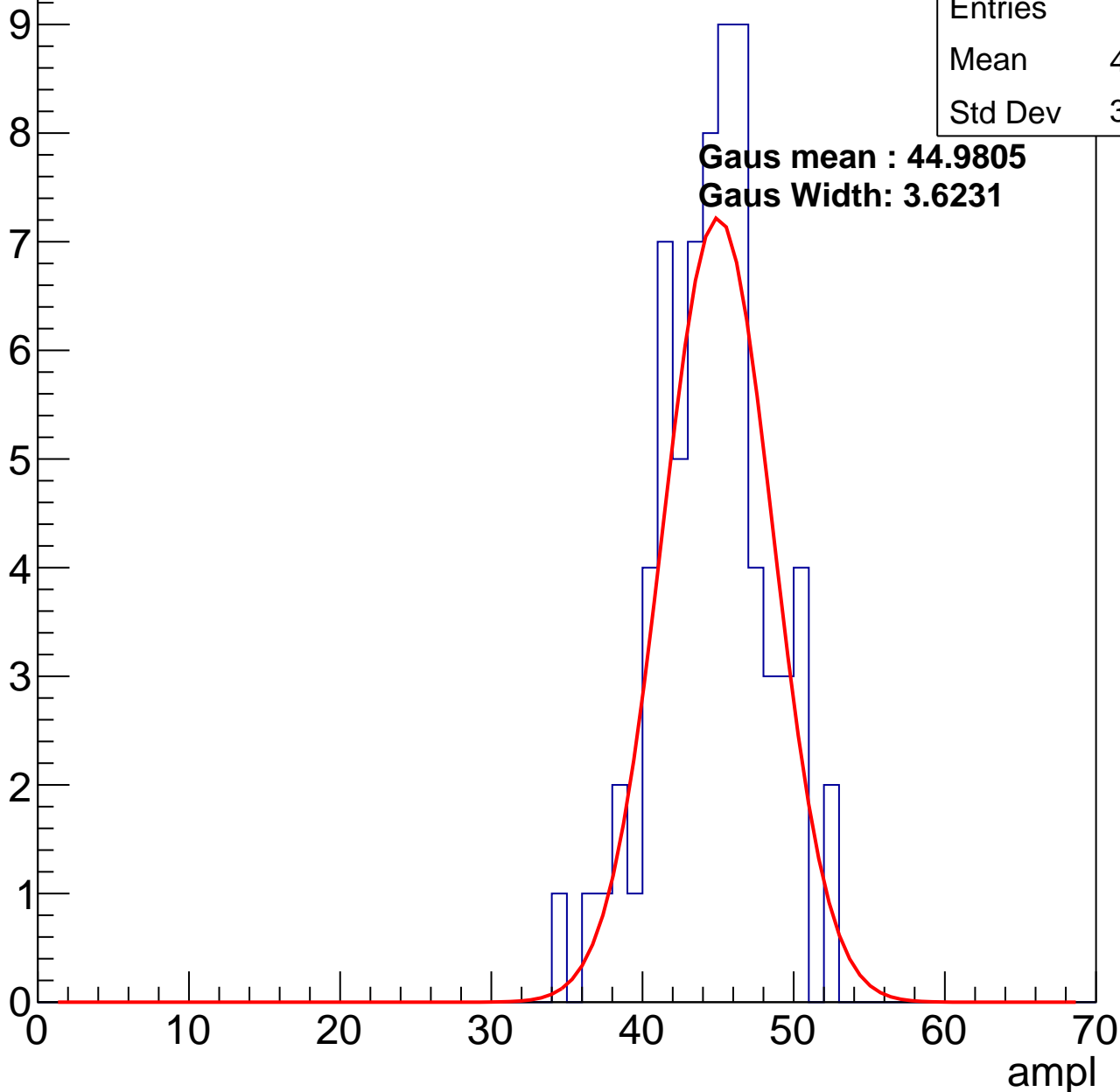
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.14
Std Dev	3.636

Gaus mean : 44.9805

Gaus Width: 3.6231



B1L100S, U10-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	51.03
Std Dev	3.112

Entry

10

8

6

4

2

0

0

10

20

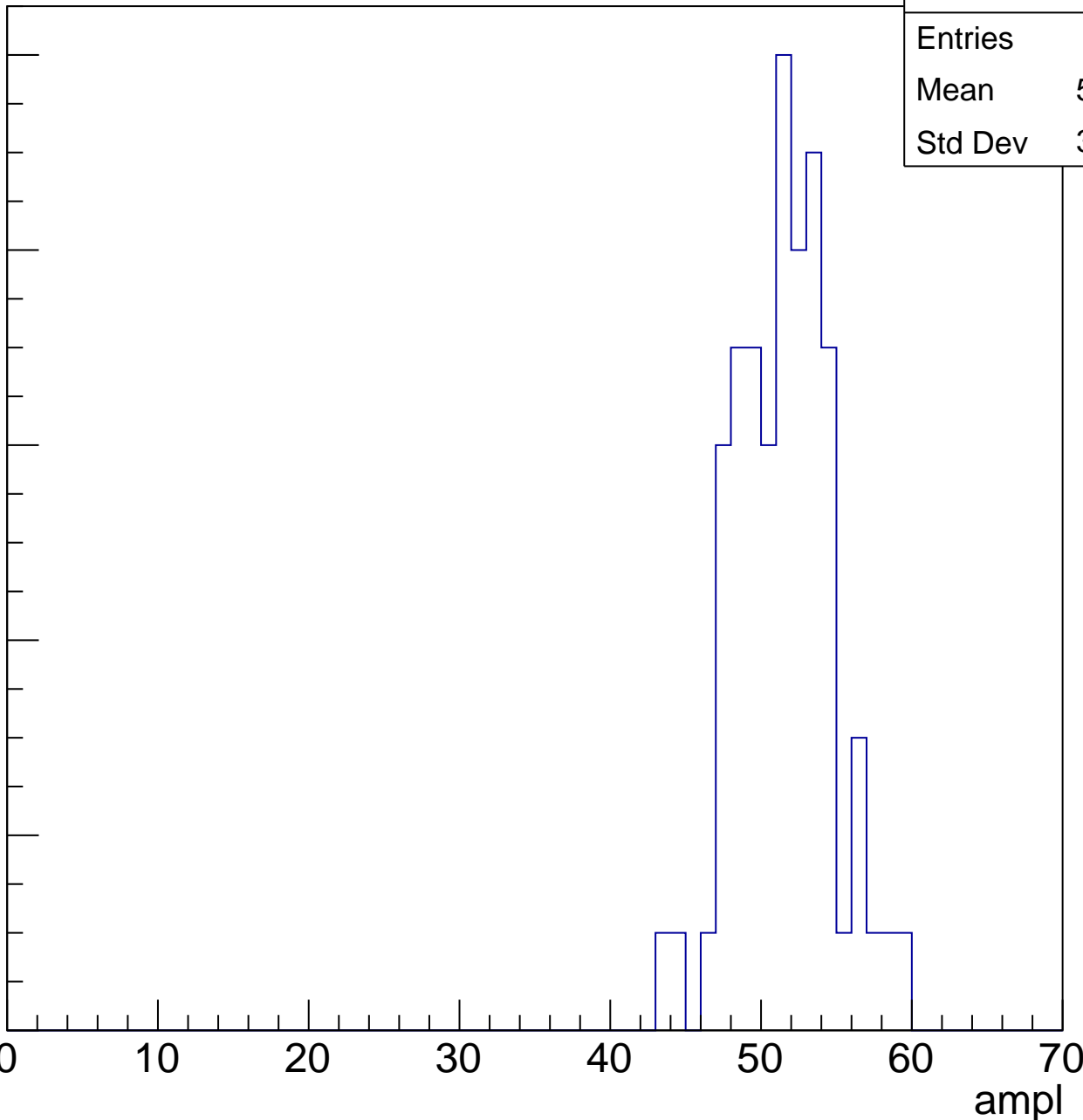
30

40

50

60

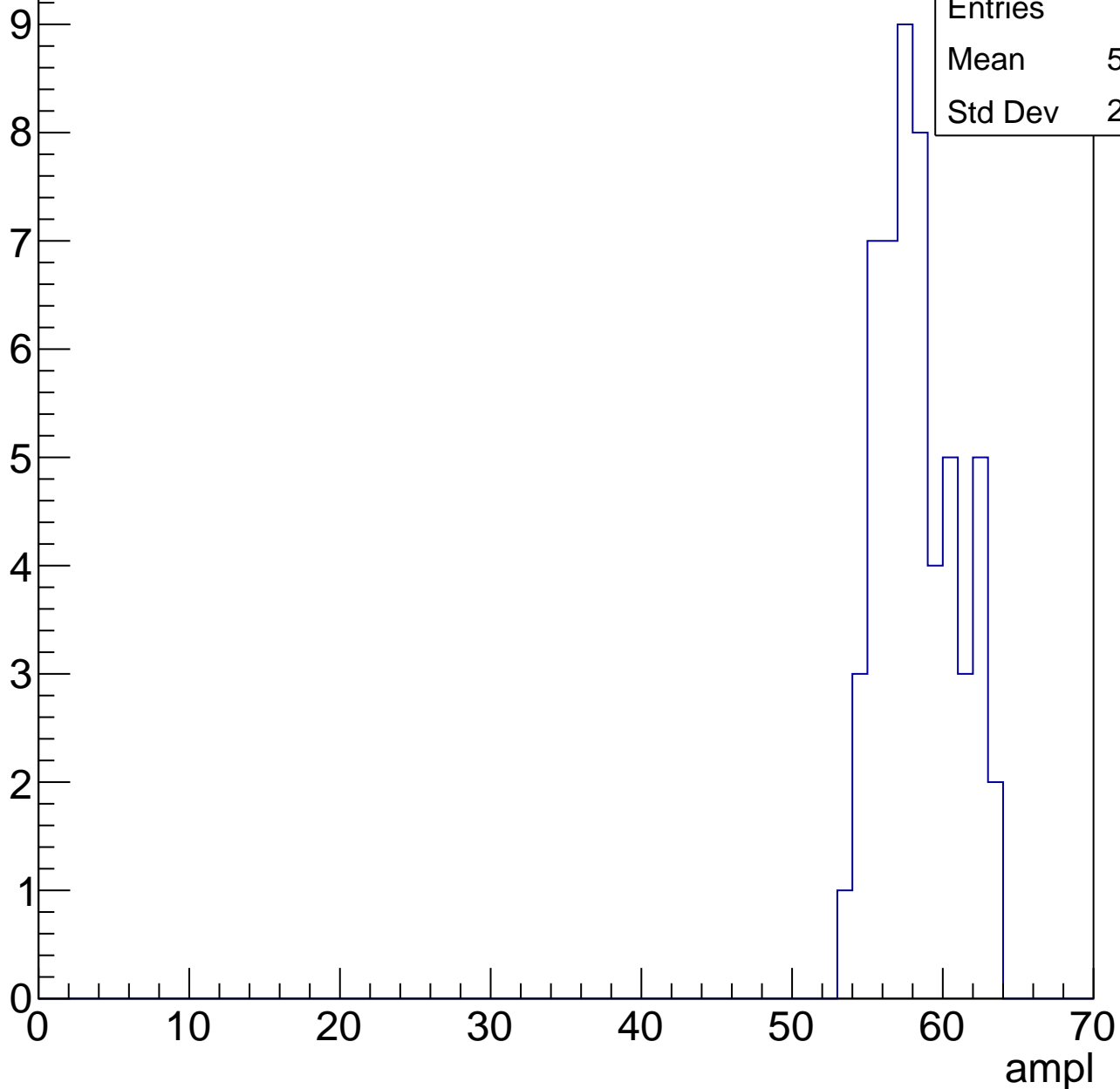
ampl



B1L100S, U10-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

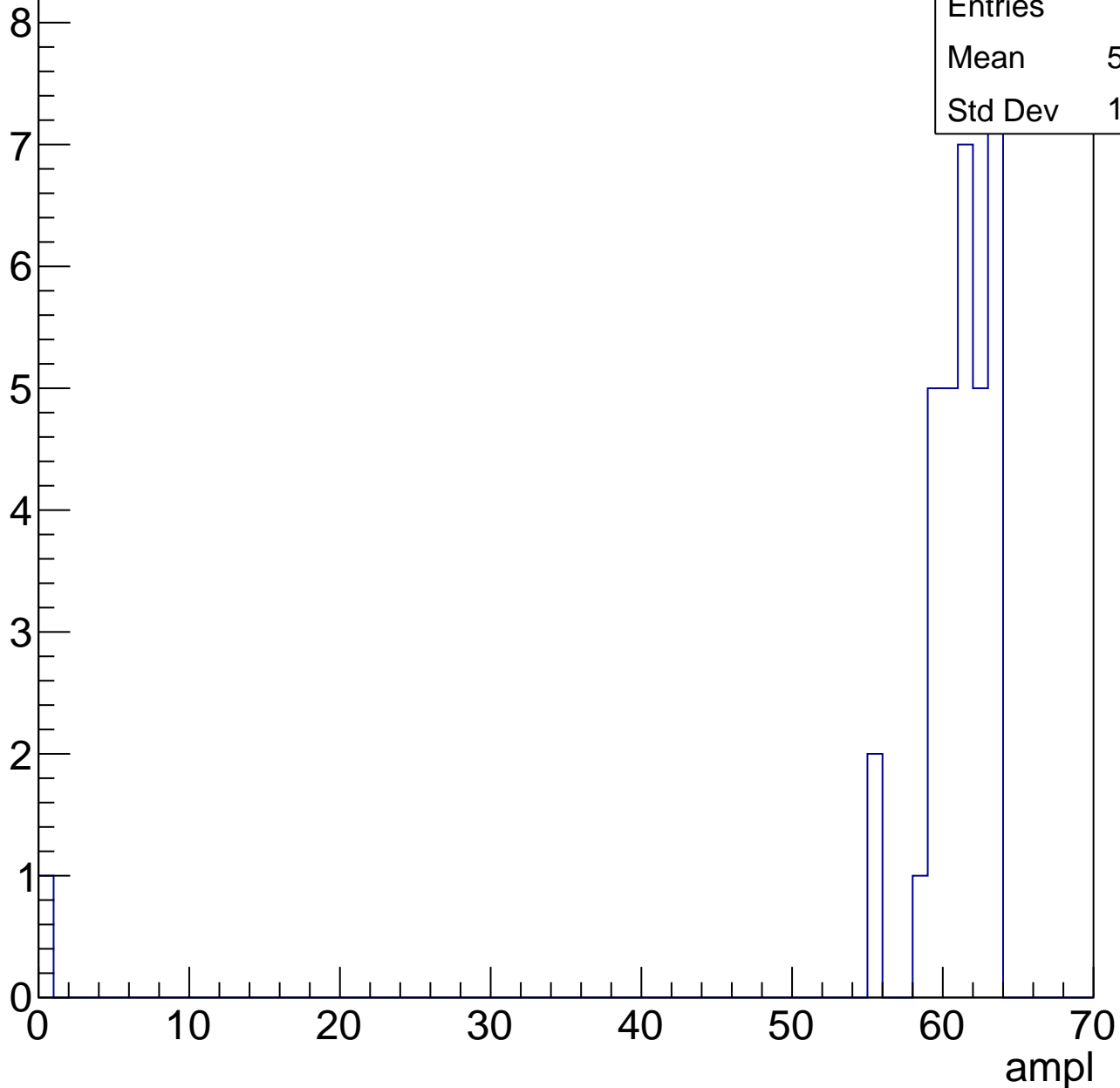


Entries	54
Mean	57.85
Std Dev	2.556

B1L100S, U10-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	34
Mean	58.94
Std Dev	10.46

B1L100S, U10-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch72, adc0

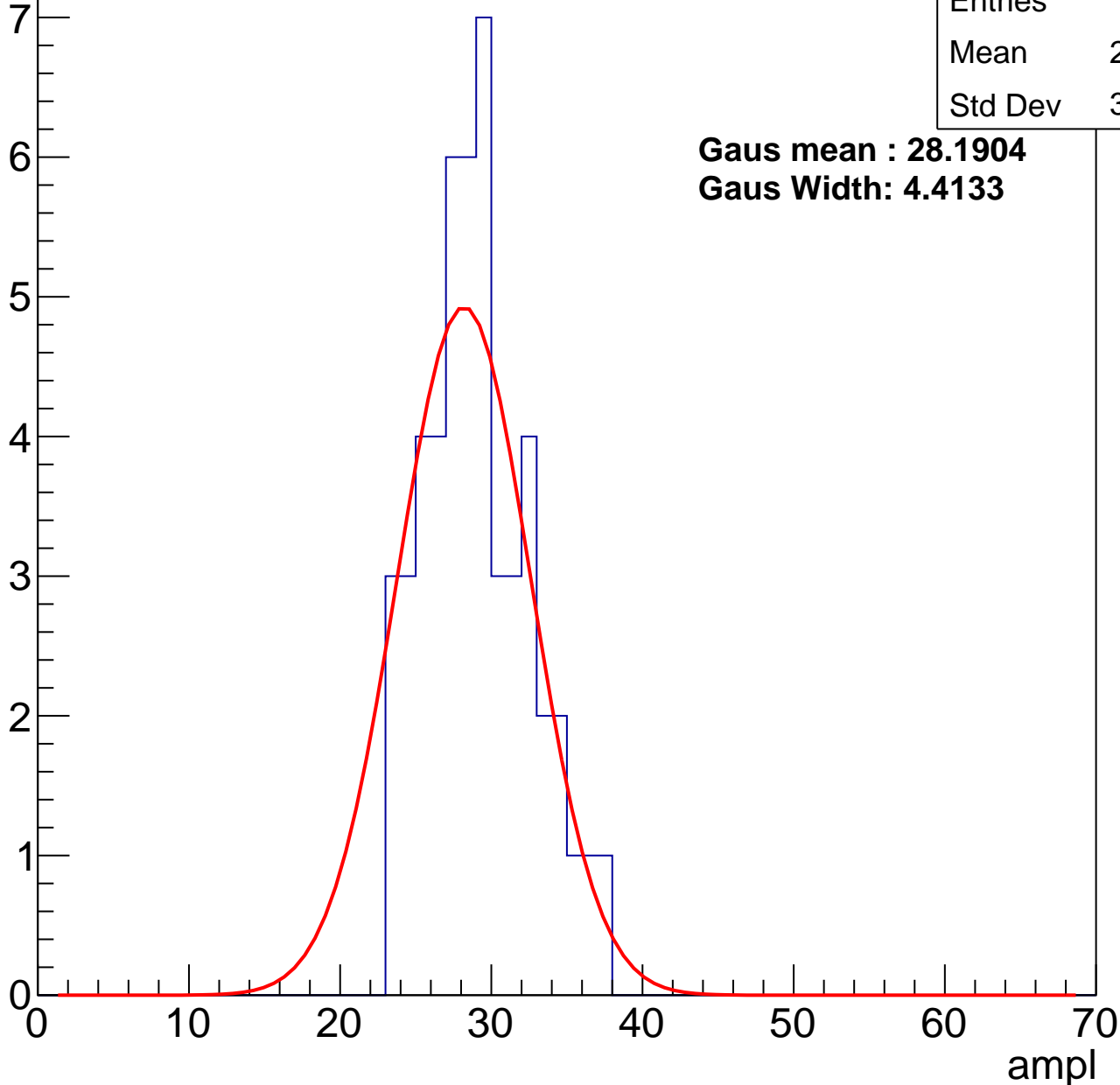
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	28.62
Std Dev	3.429

Gaus mean : 28.1904

Gaus Width: 4.4133



B1L100S, U10-ch72, adc1

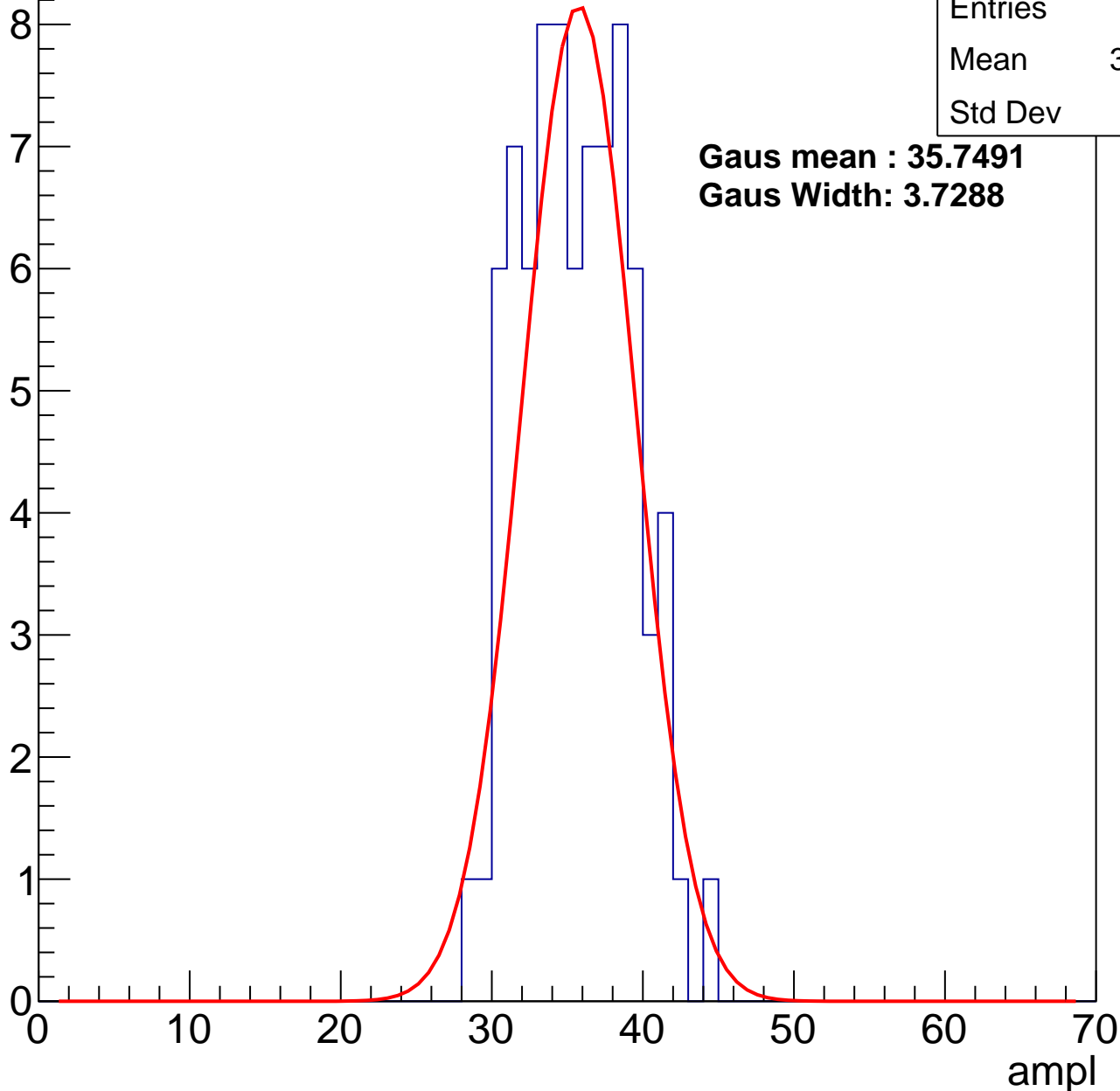
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	35.14
Std Dev	3.52

Gaus mean : 35.7491

Gaus Width: 3.7288



B1L100S, U10-ch72, adc2

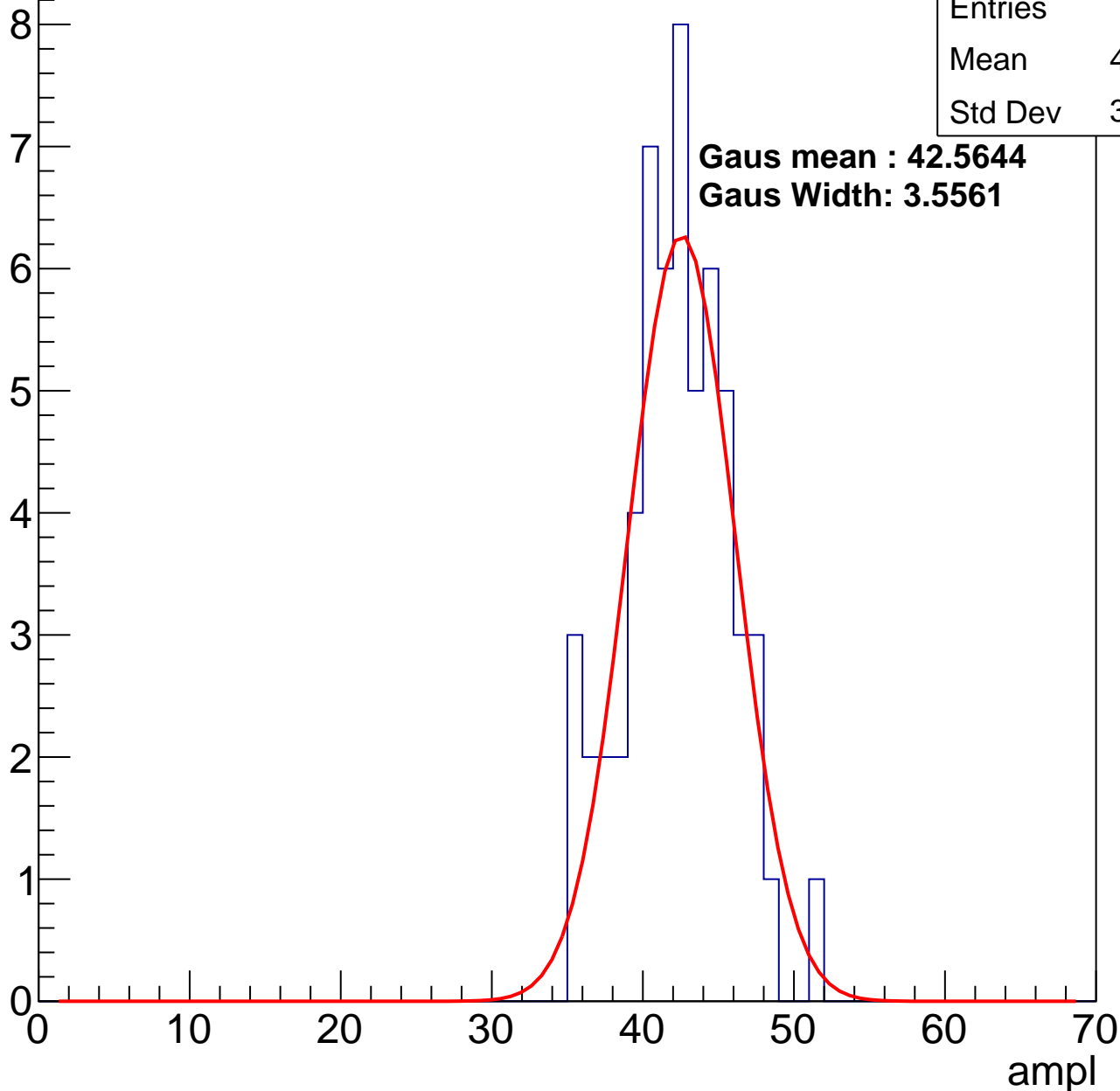
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	41.84
Std Dev	3.433

Gaus mean : 42.5644

Gaus Width: 3.5561

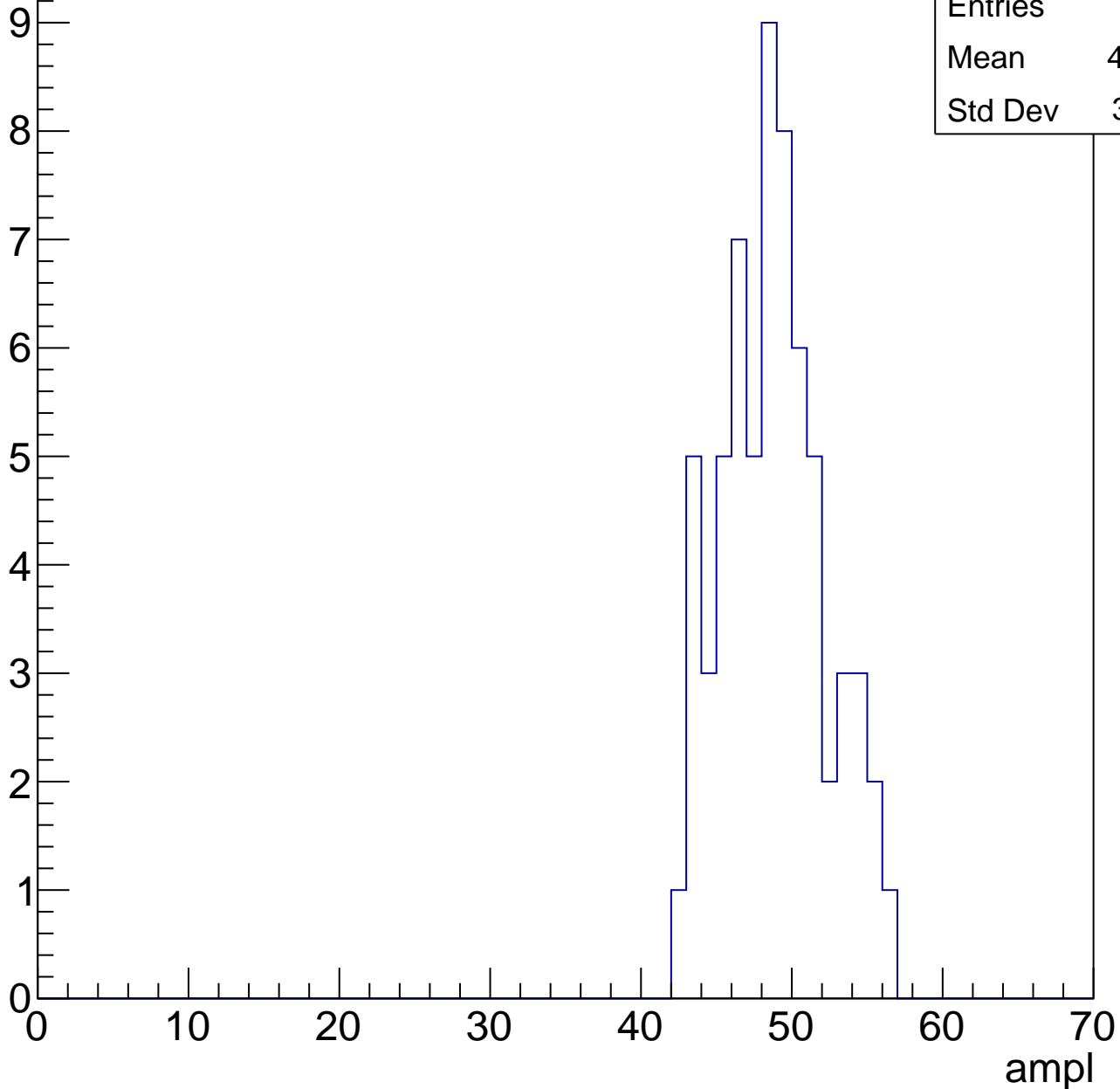


B1L100S, U10-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	48.32
Std Dev	3.361

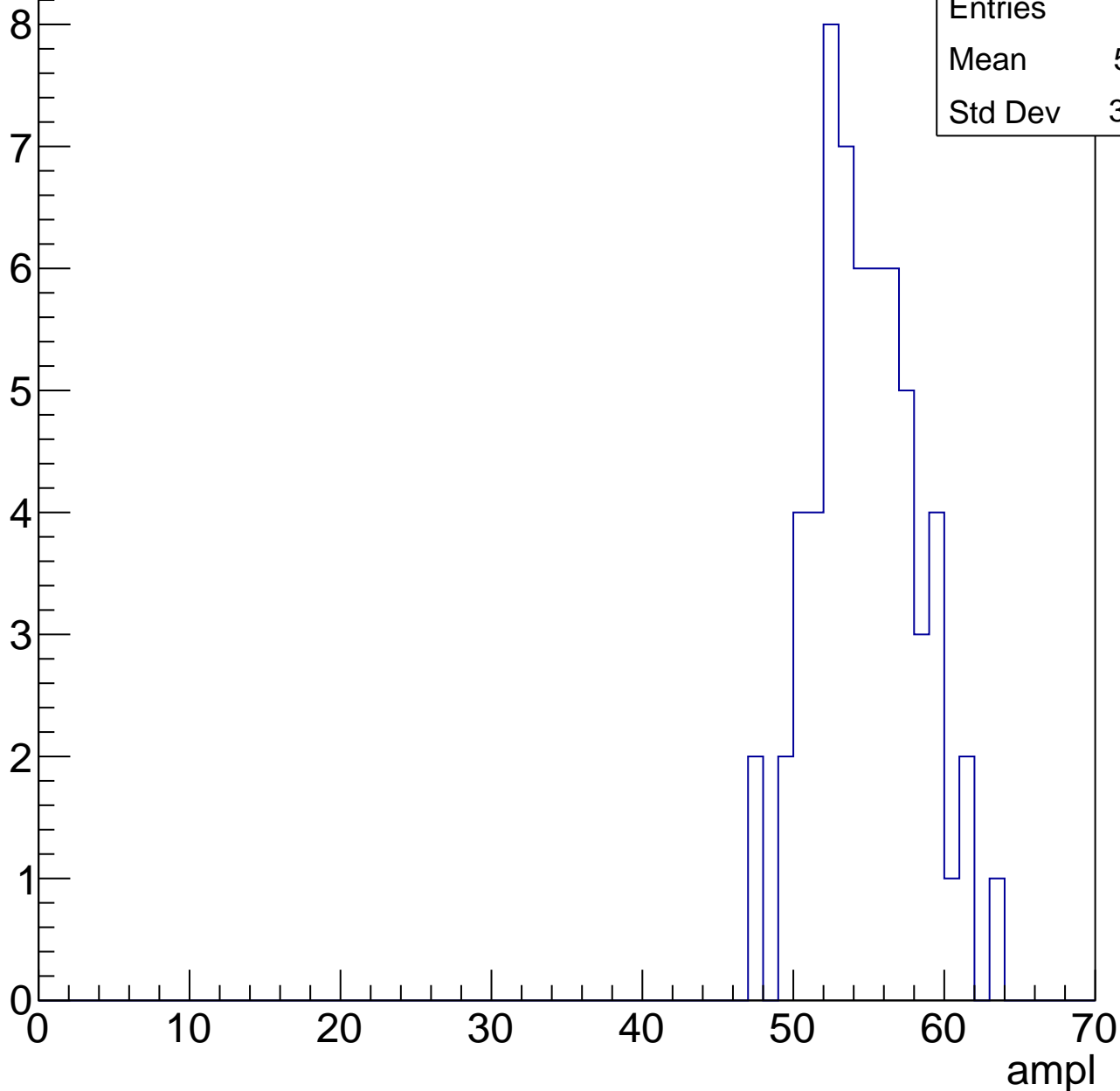


B1L100S, U10-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	54.31
Std Dev	3.433



B1L100S, U10-ch72, adc5

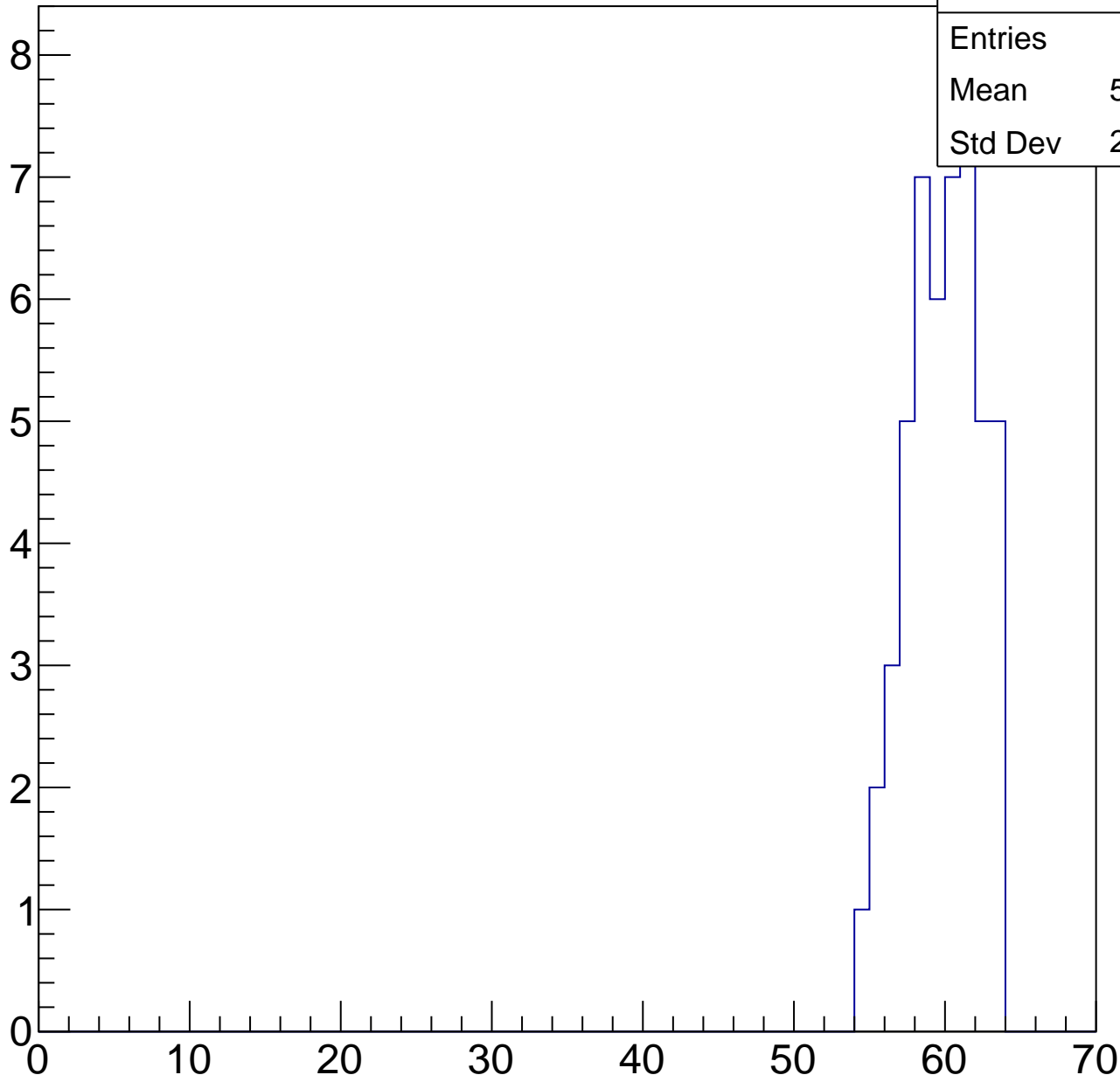
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.39
Std Dev	2.337

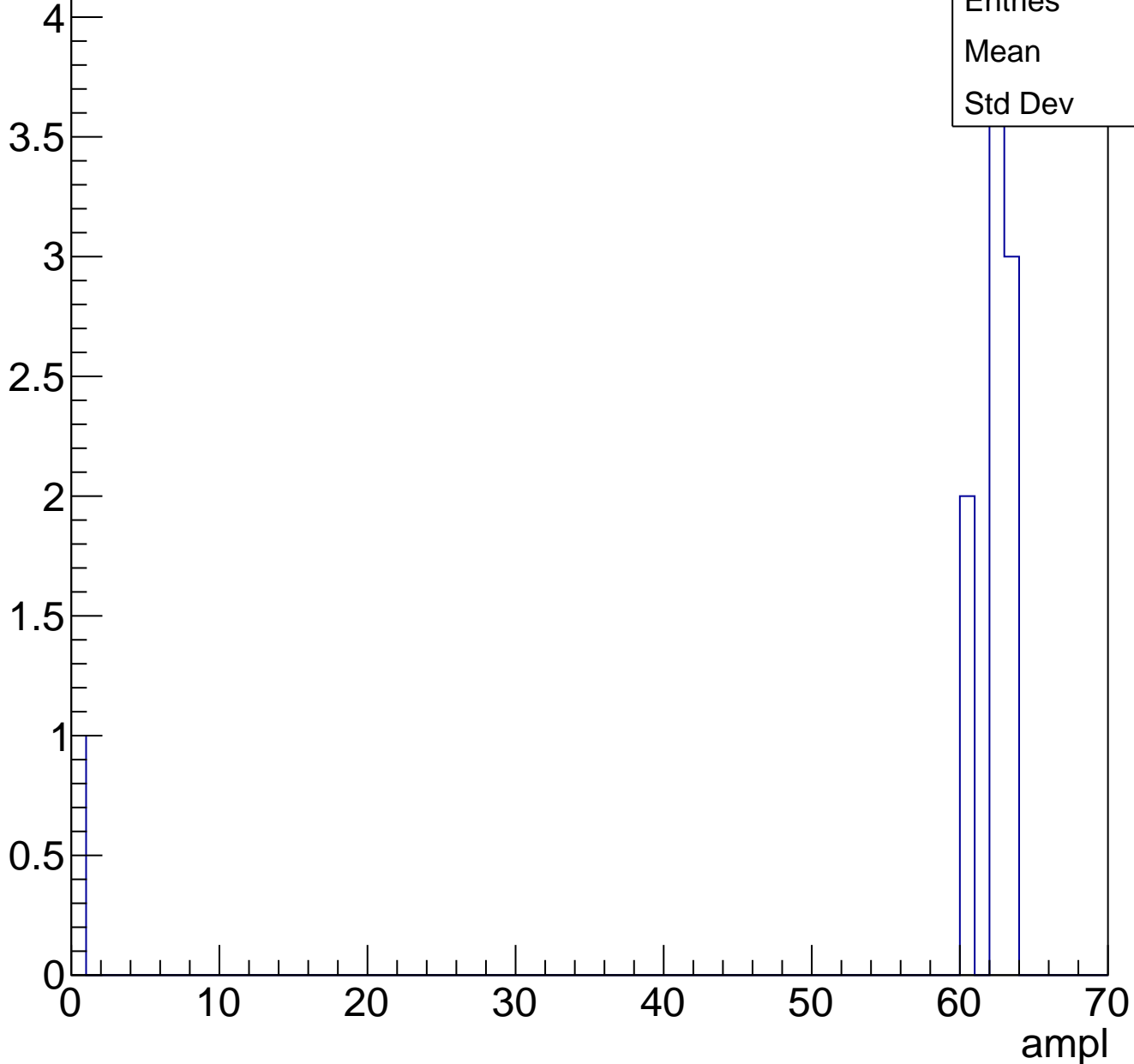
ampl



B1L100S, U10-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch73, adc0

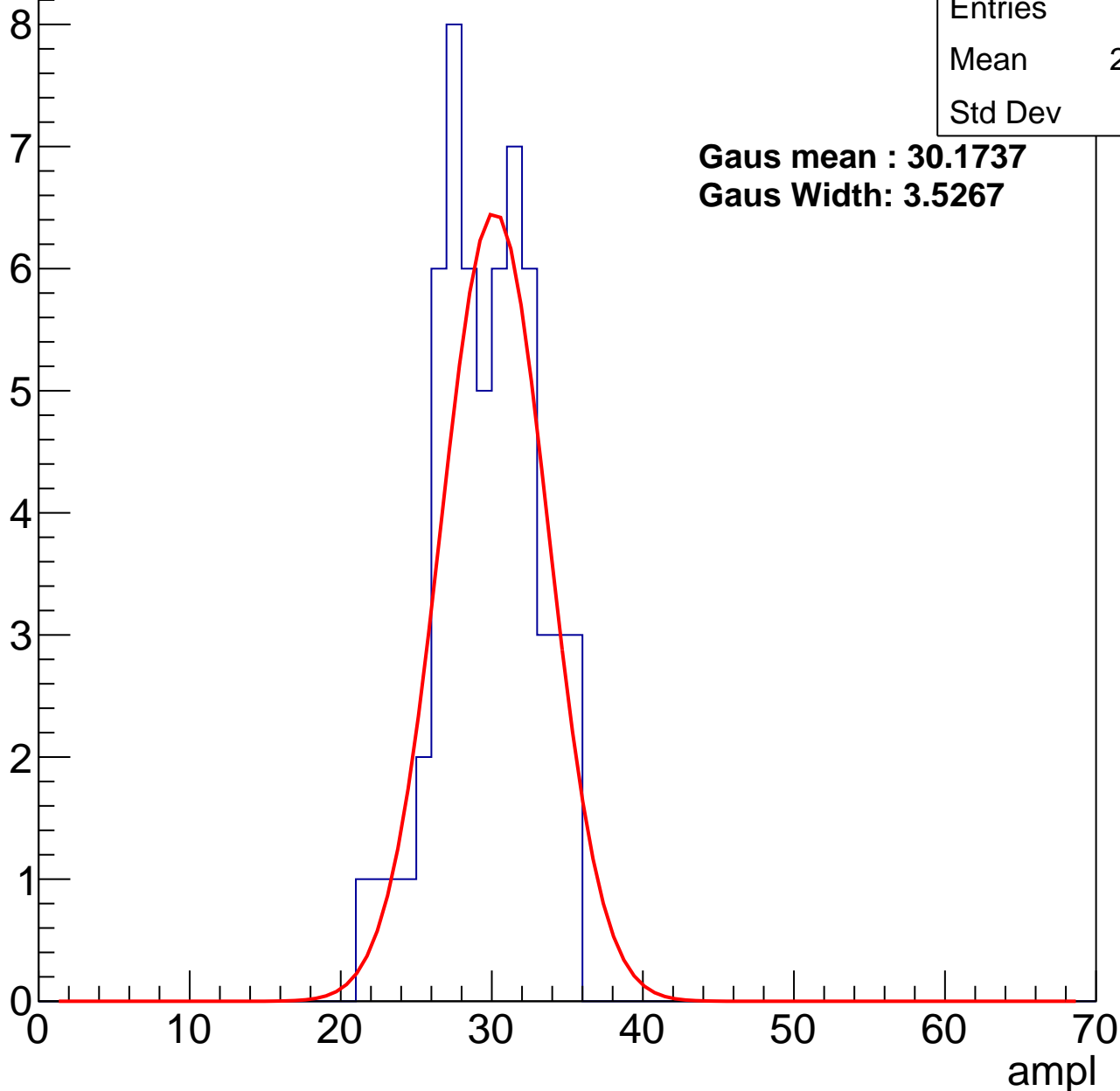
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.15
Std Dev	3.23

Gaus mean : 30.1737

Gaus Width: 3.5267



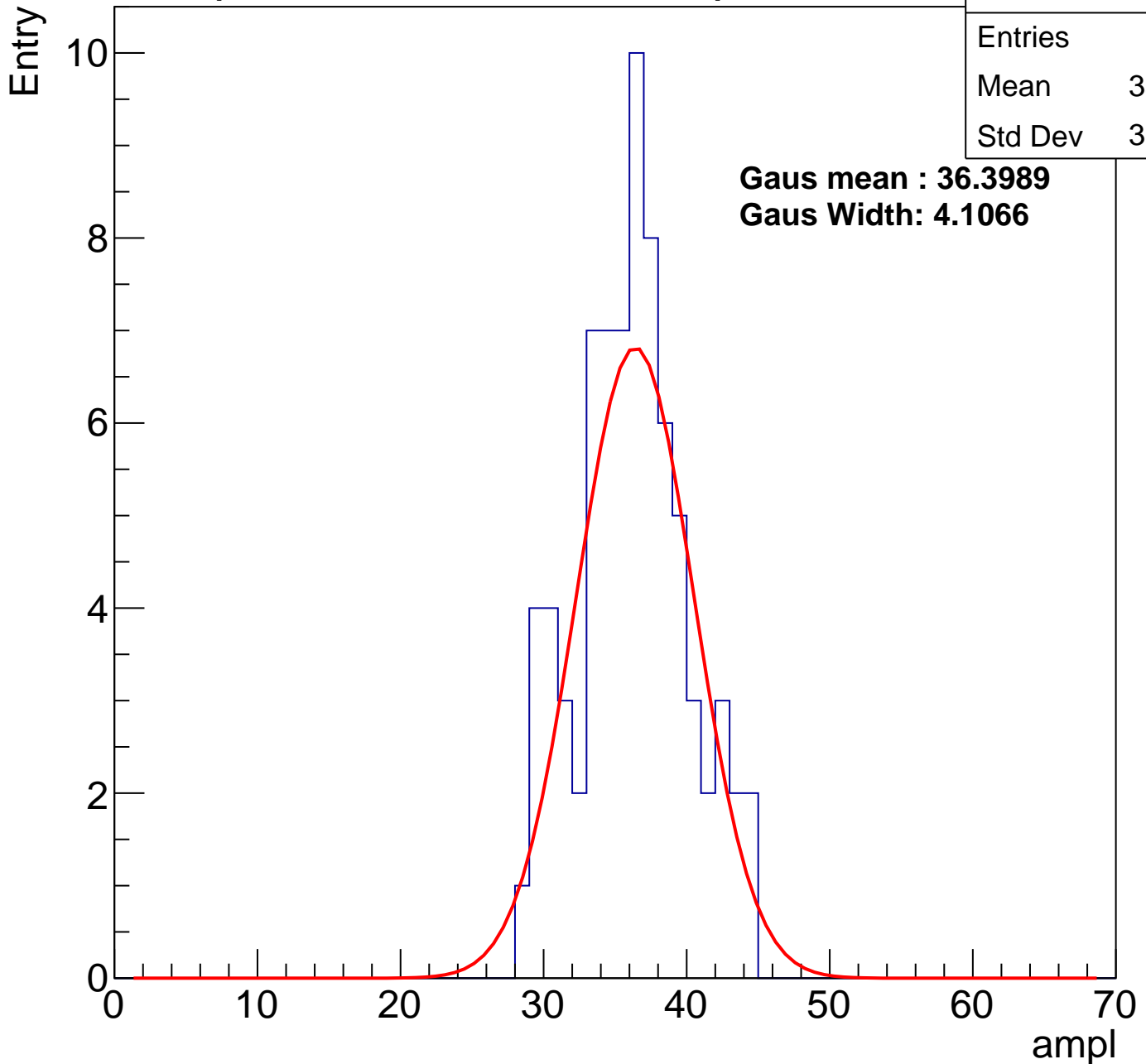
B1L100S, U10-ch73, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	35.74
Std Dev	3.826

Gaus mean : 36.3989

Gaus Width: 4.1066



B1L100S, U10-ch73, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	43.64
Std Dev	3.512

Gaus mean : 43.7786

Gaus Width: 4.3401

Entry

10

8

6

4

2

0

0

10

20

30

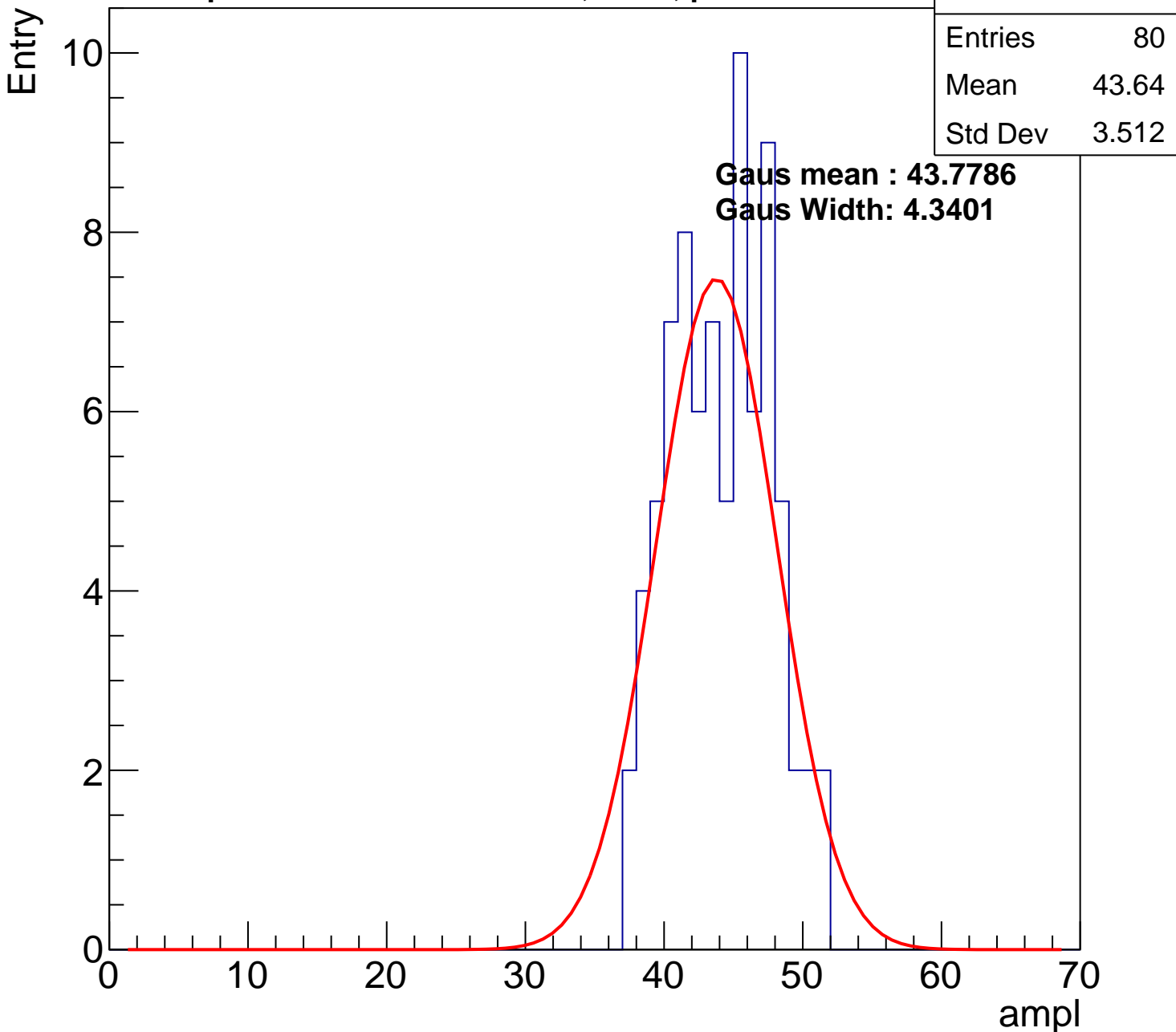
40

50

60

70

ampl



B1L100S, U10-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	50.39
Std Dev	3.461

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

10

8

6

4

2

0

0

10

20

30

40

50

60

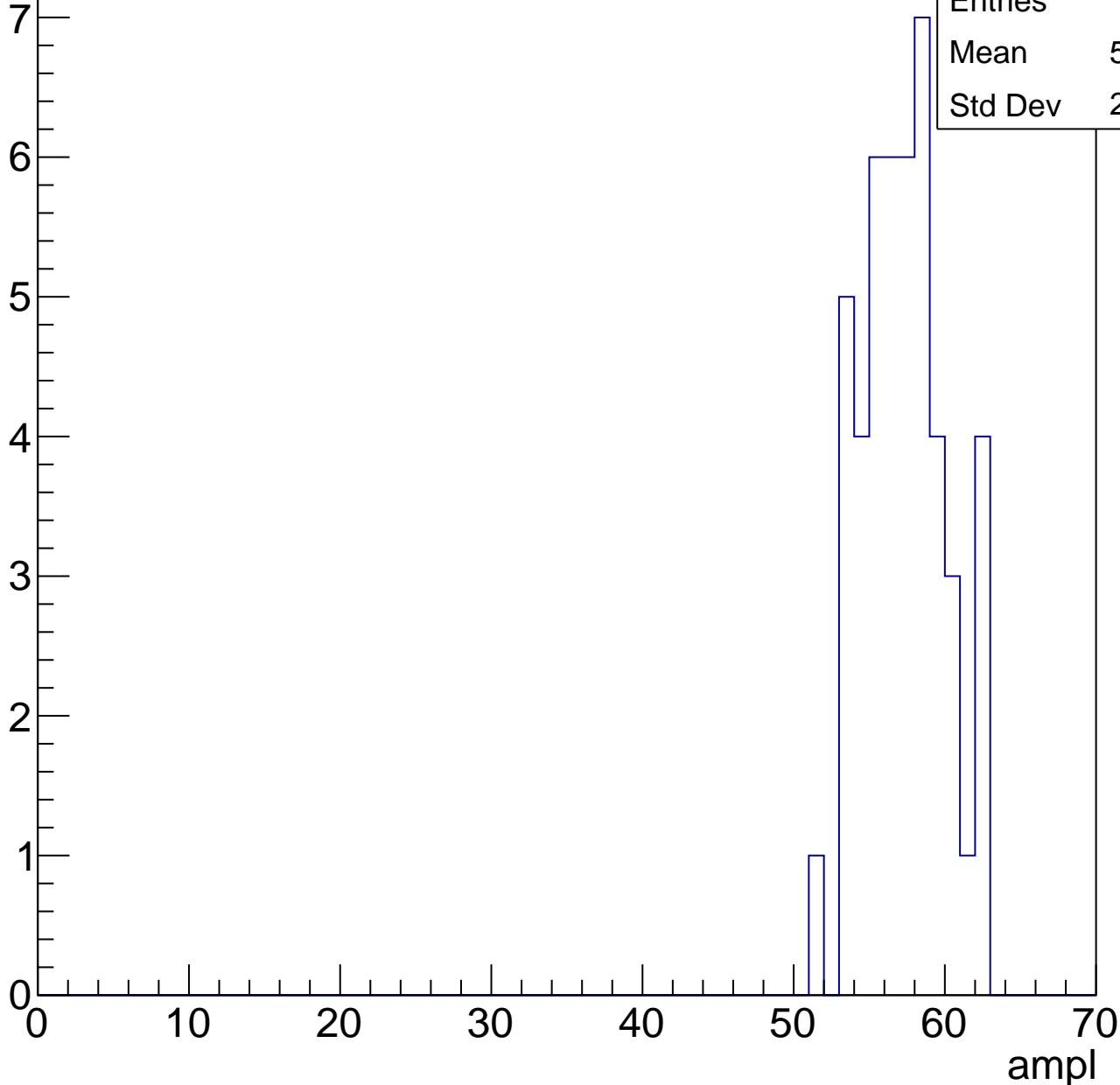
ampl

B1L100S, U10-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	56.83
Std Dev	2.716

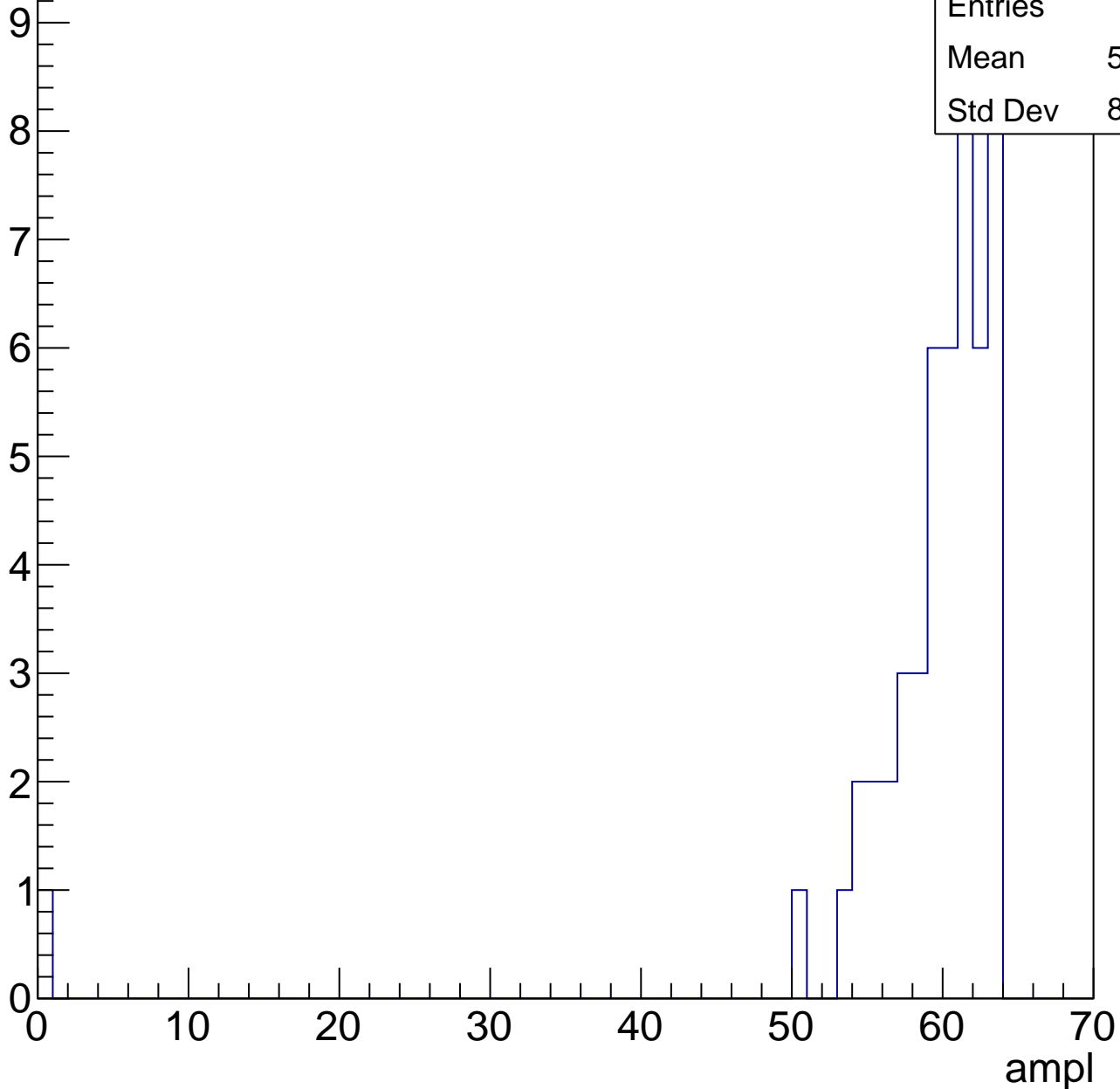


B1L100S, U10-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.34
Std Dev	8.849



B1L100S, U10-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch74, adc0

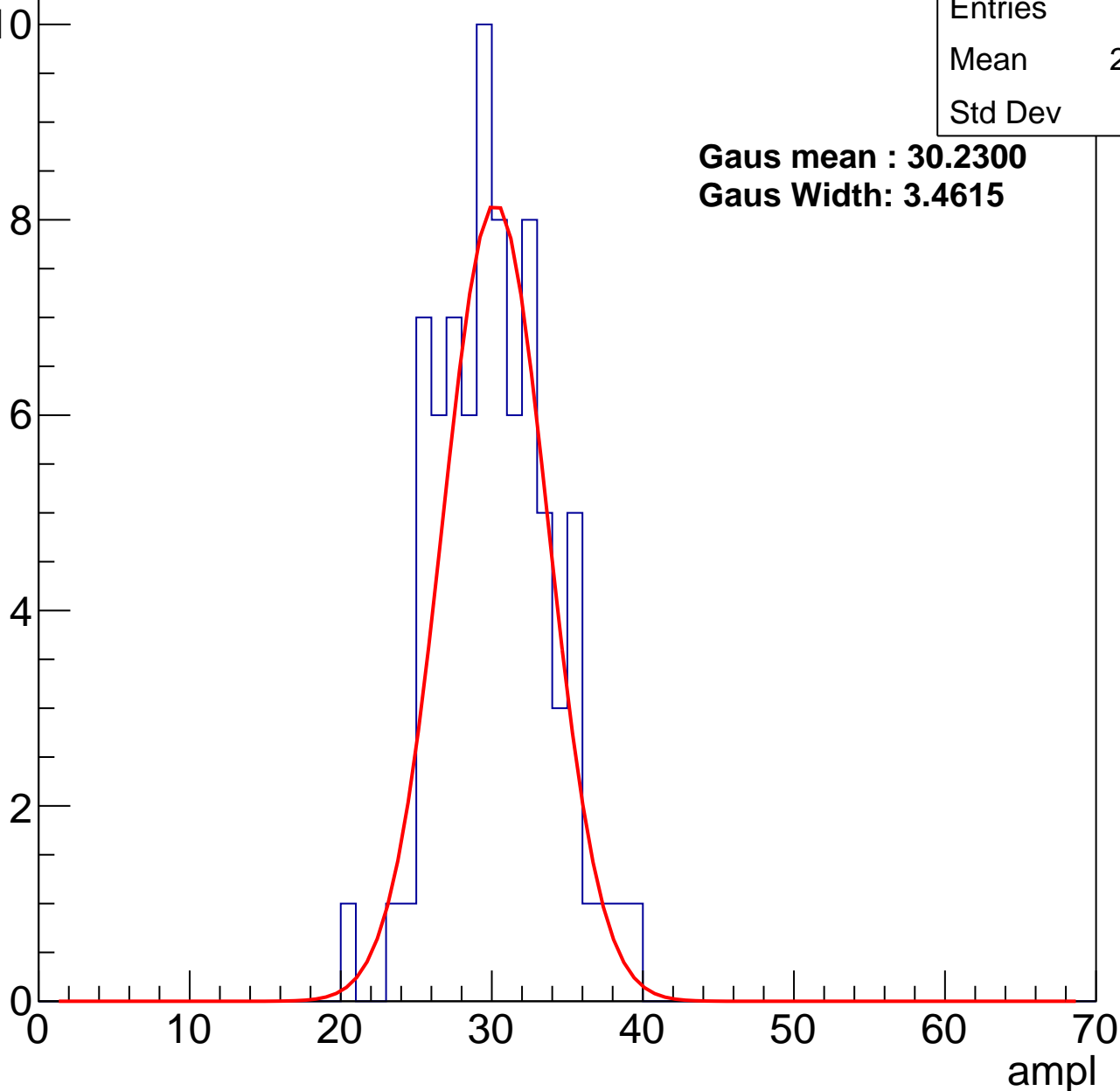
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.73
Std Dev	3.64

Gaus mean : 30.2300

Gaus Width: 3.4615



B1L100S, U10-ch74, adc1

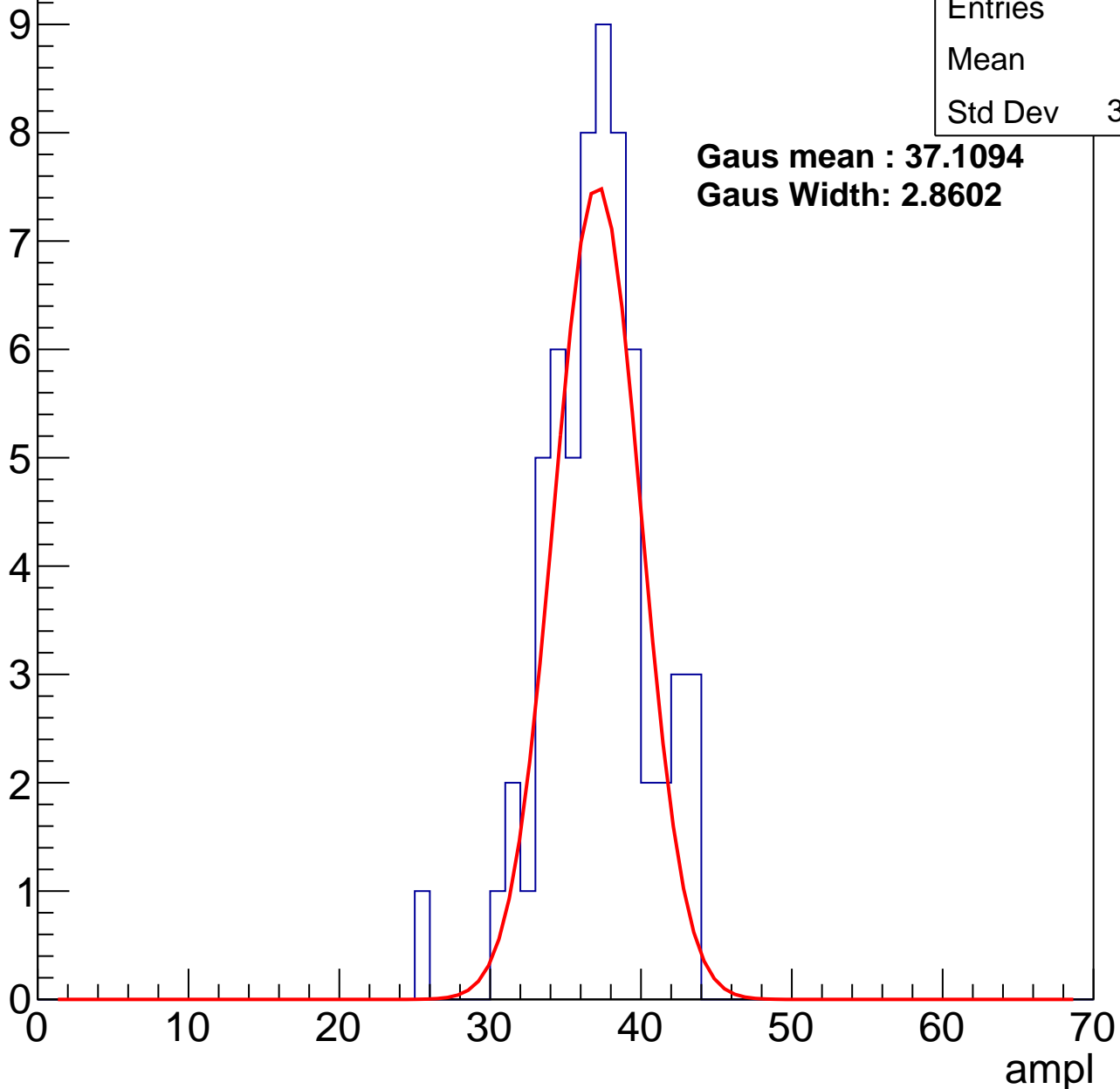
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.6
Std Dev	3.377

Gaus mean : 37.1094

Gaus Width: 2.8602



B1L100S, U10-ch74, adc2

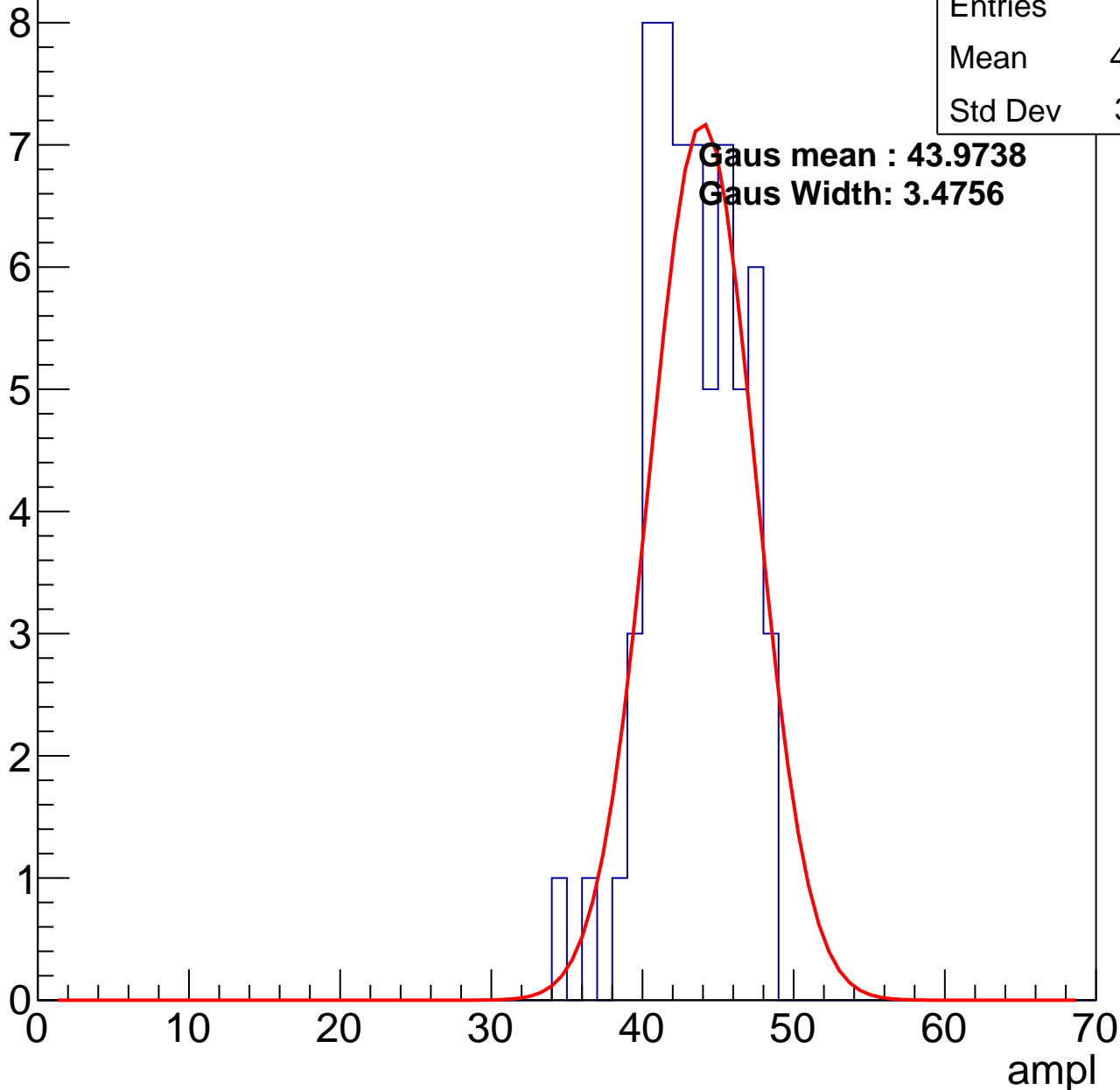
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.89
Std Dev	3.011

Gaus mean : 43.9738

Gaus Width: 3.4756

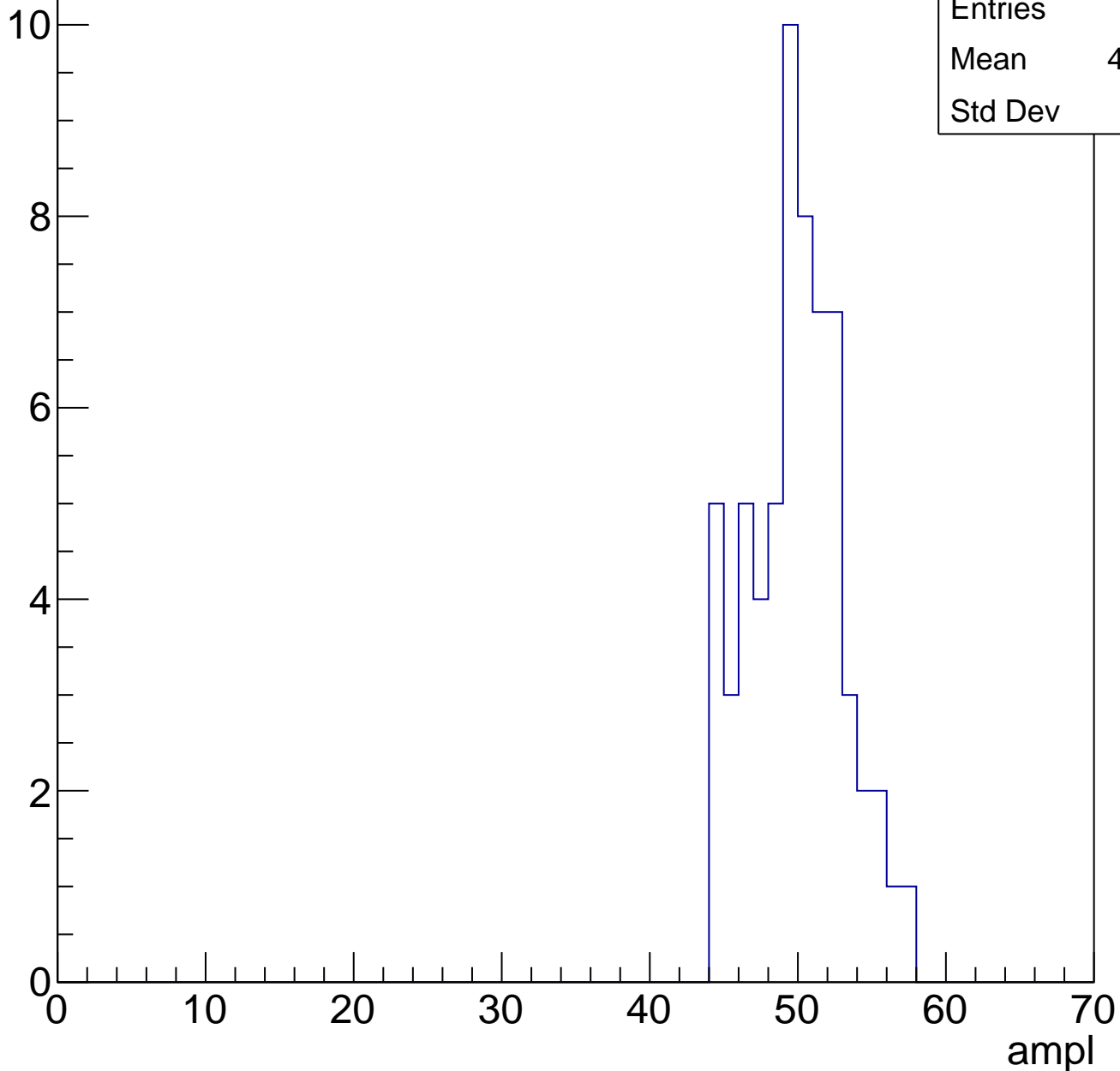


B1L100S, U10-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	49.43
Std Dev	3.11



B1L100S, U10-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

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3

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60

70

ampl

Entries 53

Mean 55.19

Std Dev 3.222

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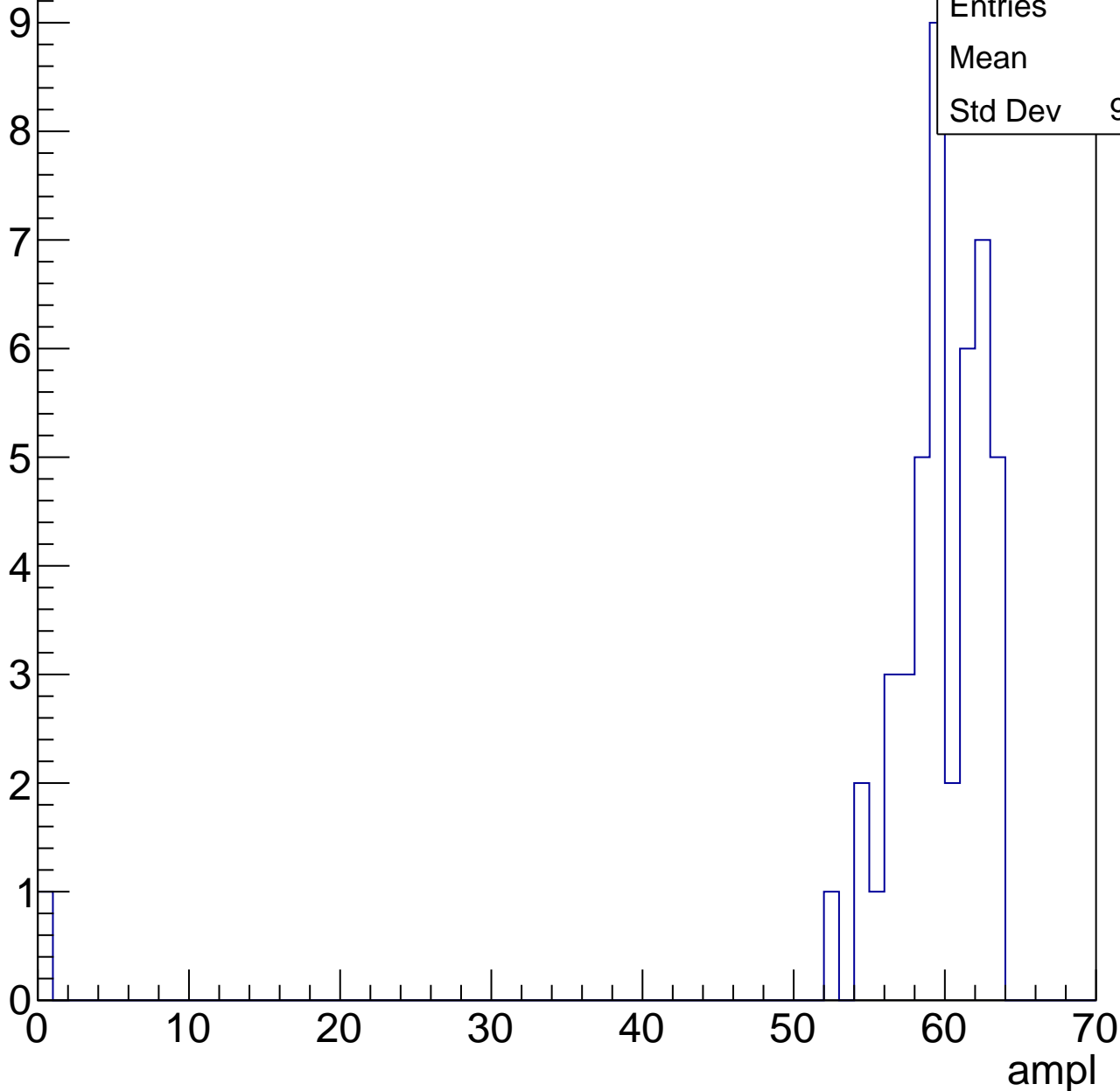
326

B1L100S, U10-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

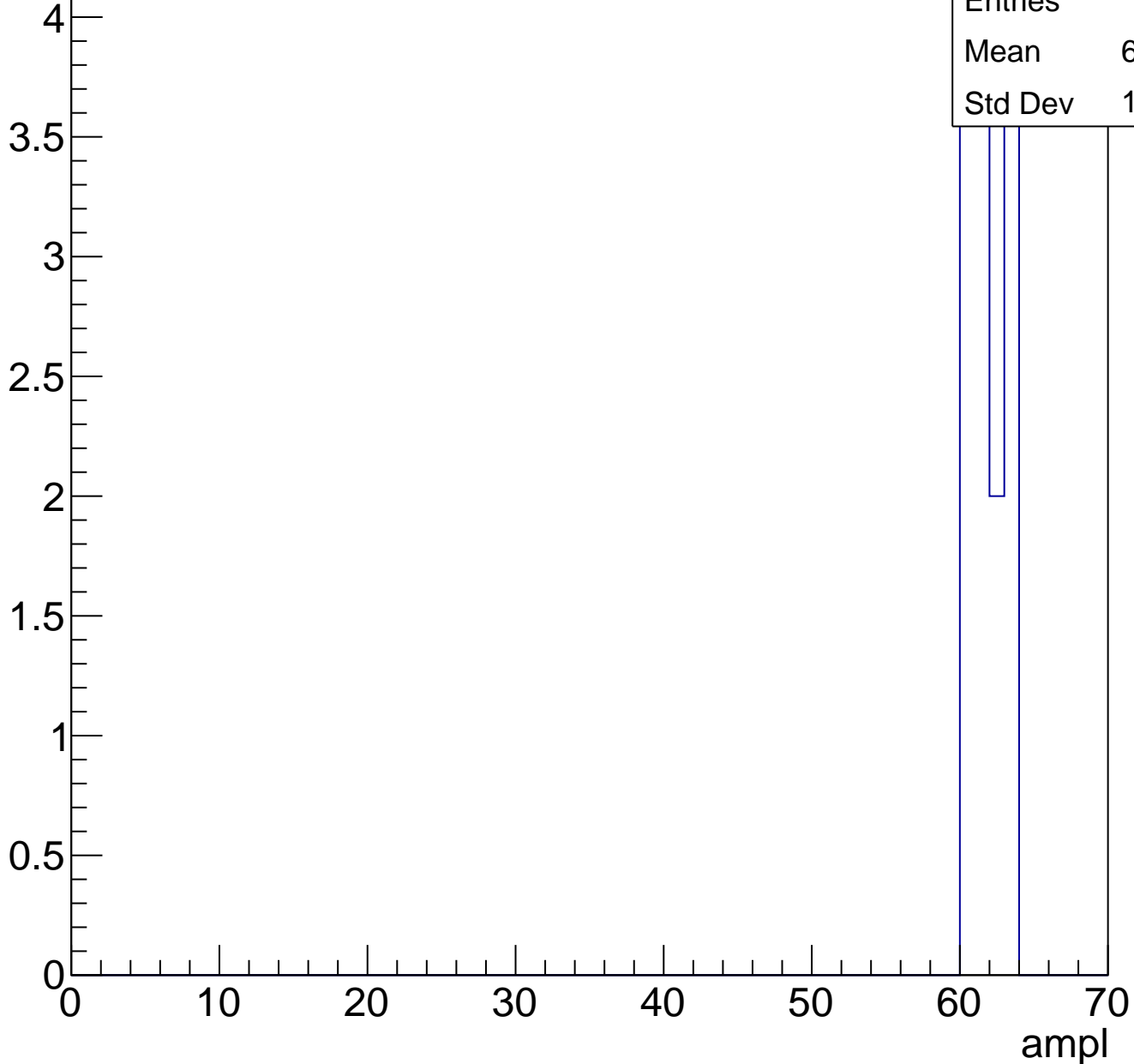
Entries	45
Mean	58
Std Dev	9.146



B1L100S, U10-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch75, adc0

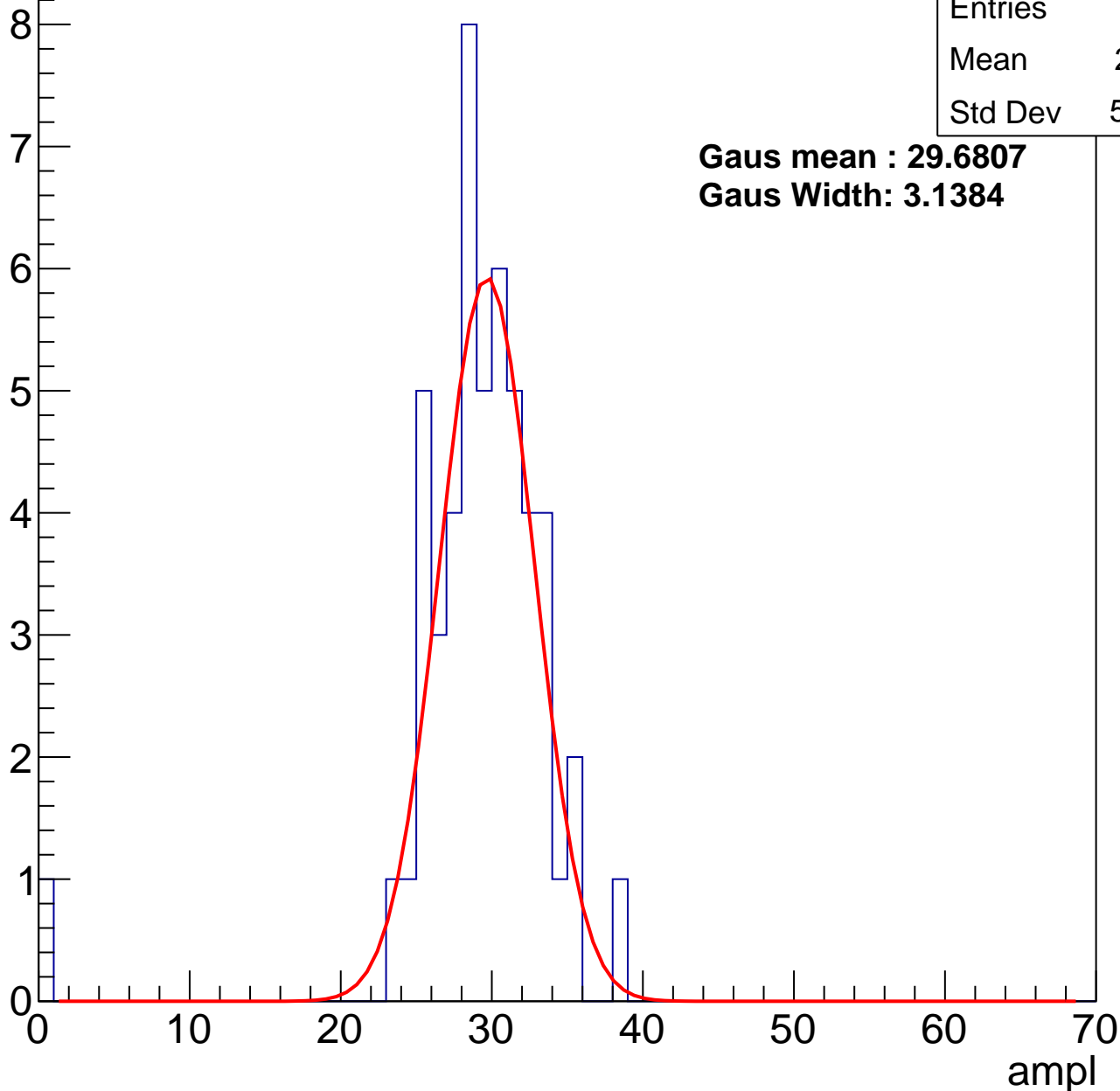
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.71
Std Dev	5.112

Gaus mean : 29.6807

Gaus Width: 3.1384



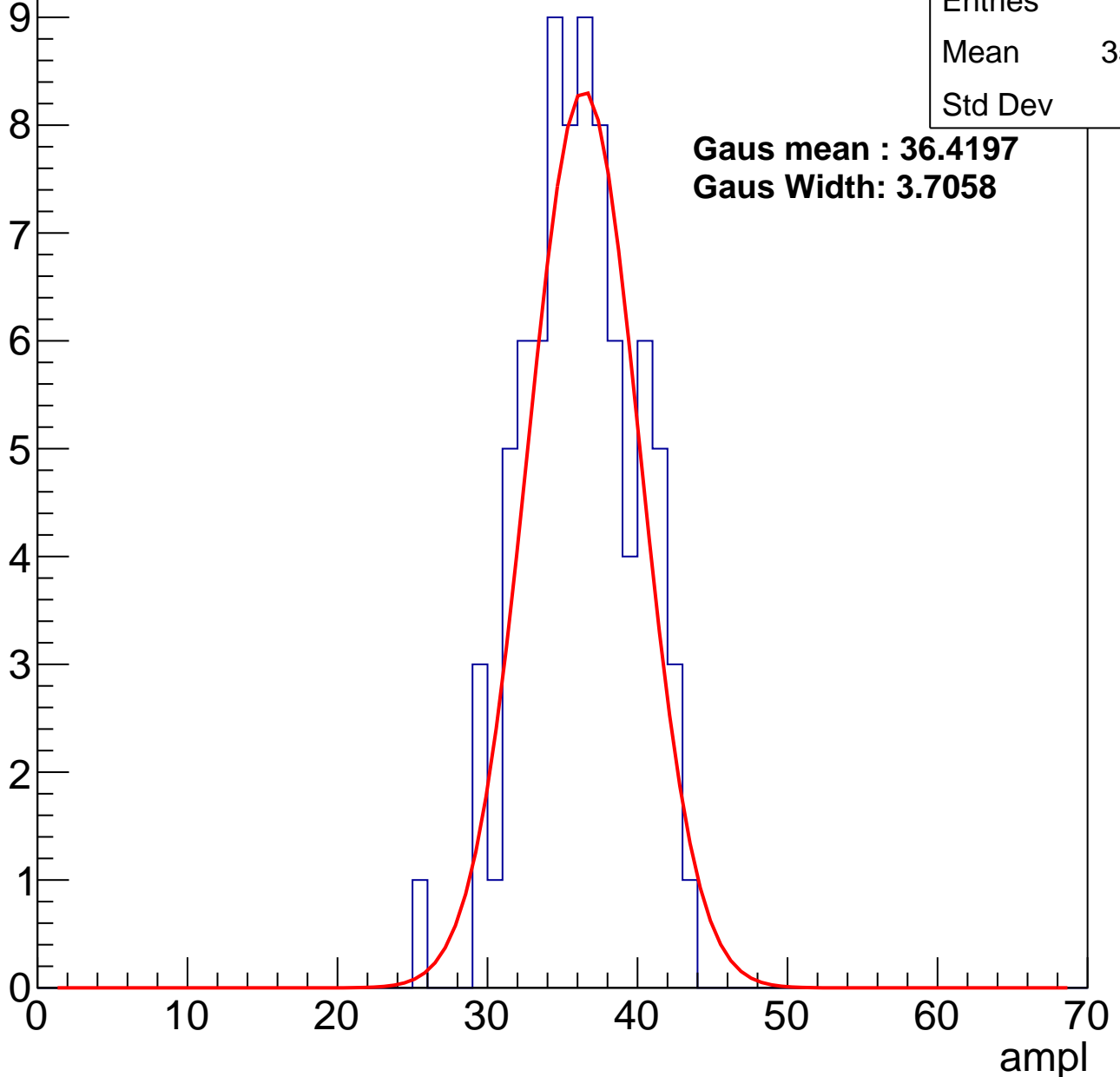
B1L100S, U10-ch75, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	35.69
Std Dev	3.62

Gaus mean : 36.4197
Gaus Width: 3.7058



B1L100S, U10-ch75, adc2

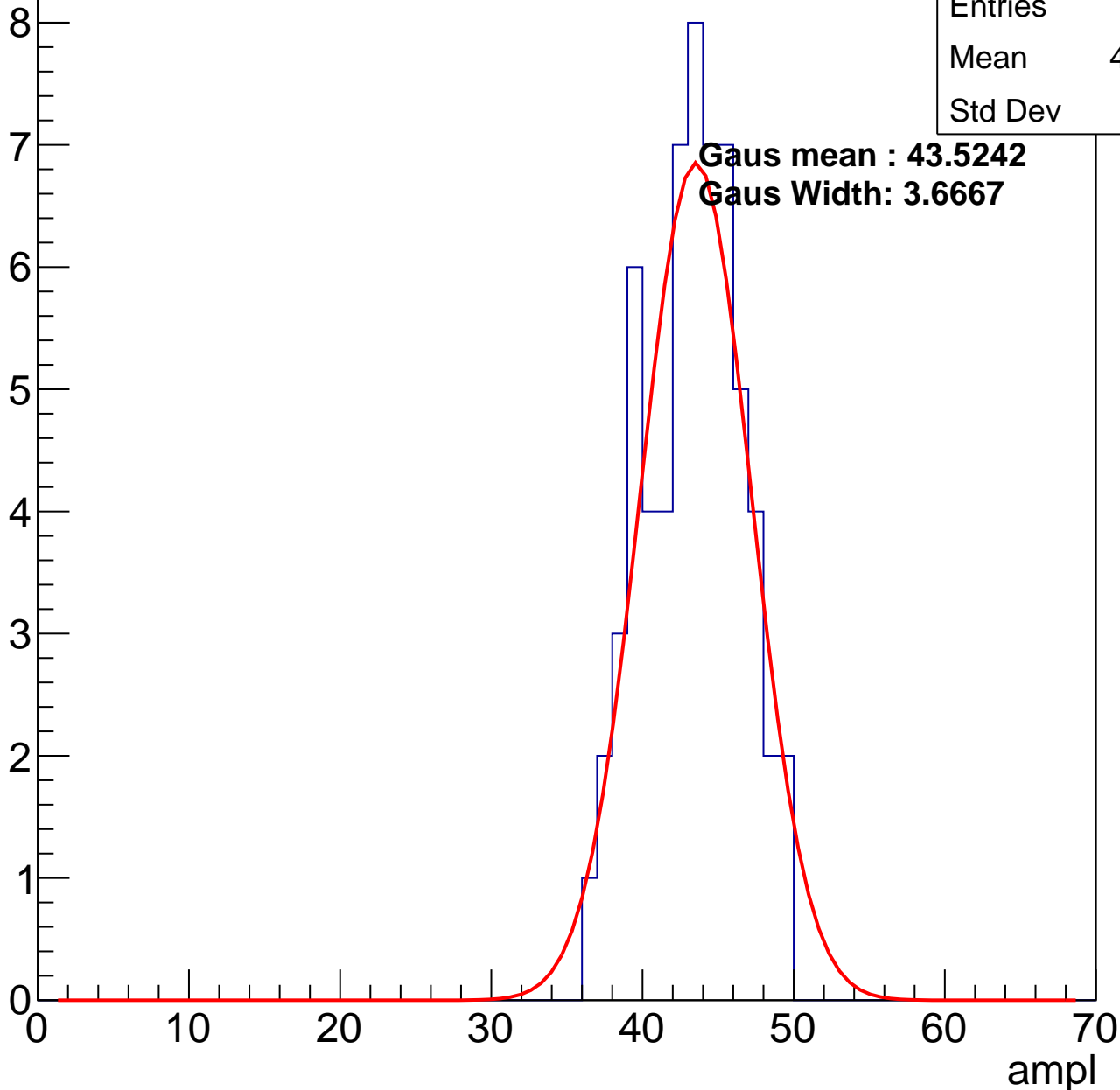
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.82
Std Dev	3.15

Gaus mean : 43.5242

Gaus Width: 3.6667

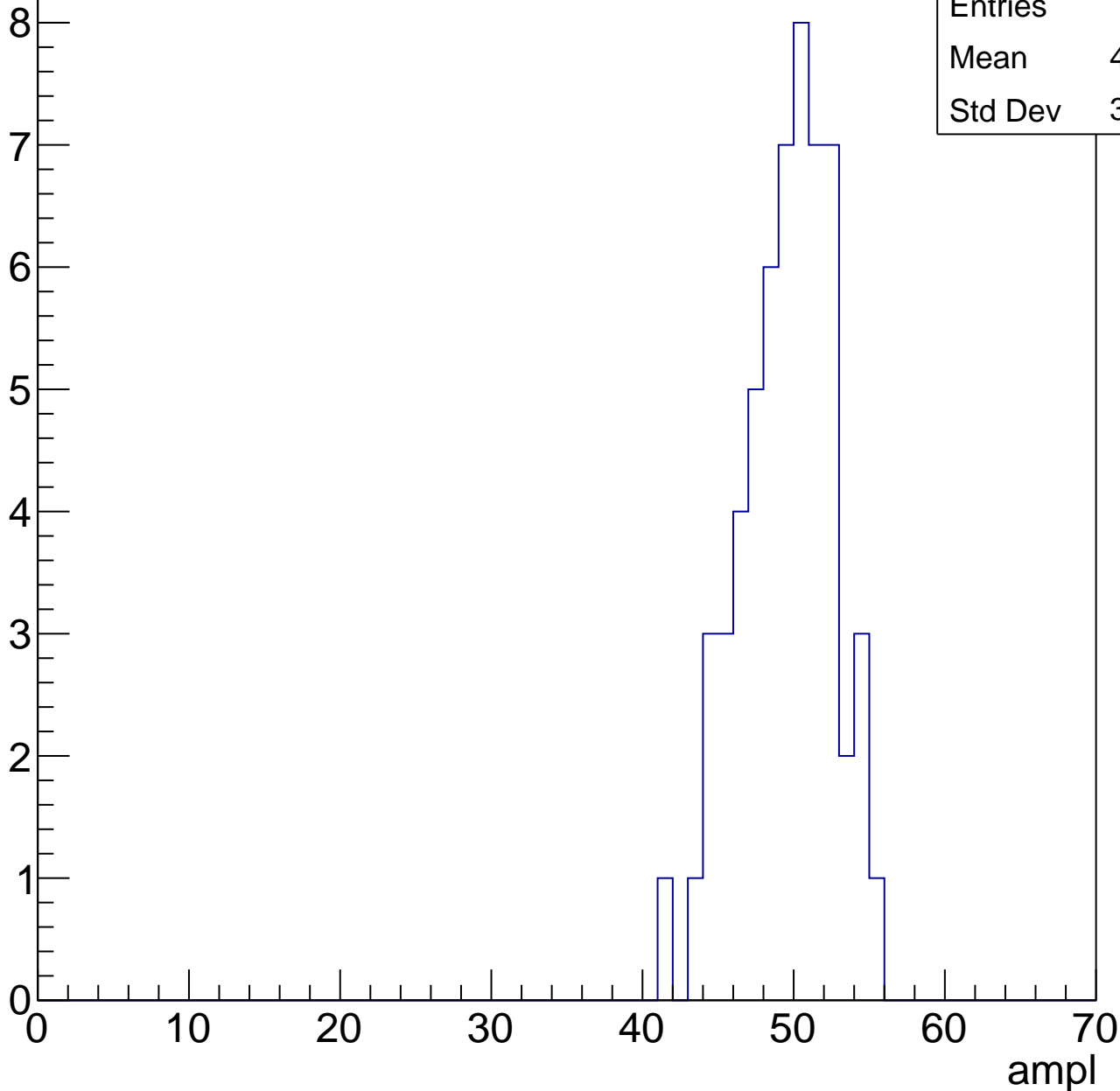


B1L100S, U10-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.05
Std Dev	3.014

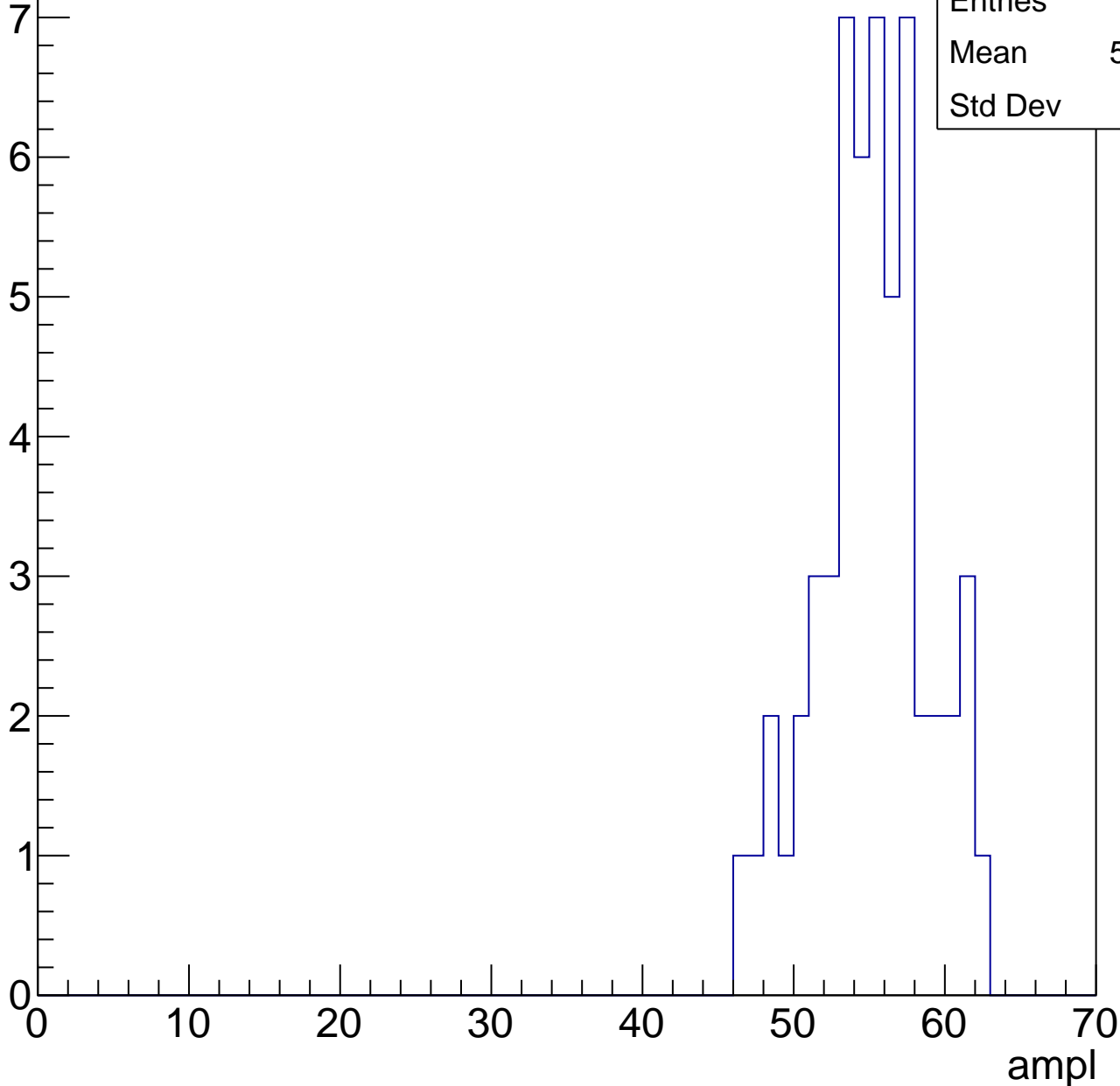


B1L100S, U10-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

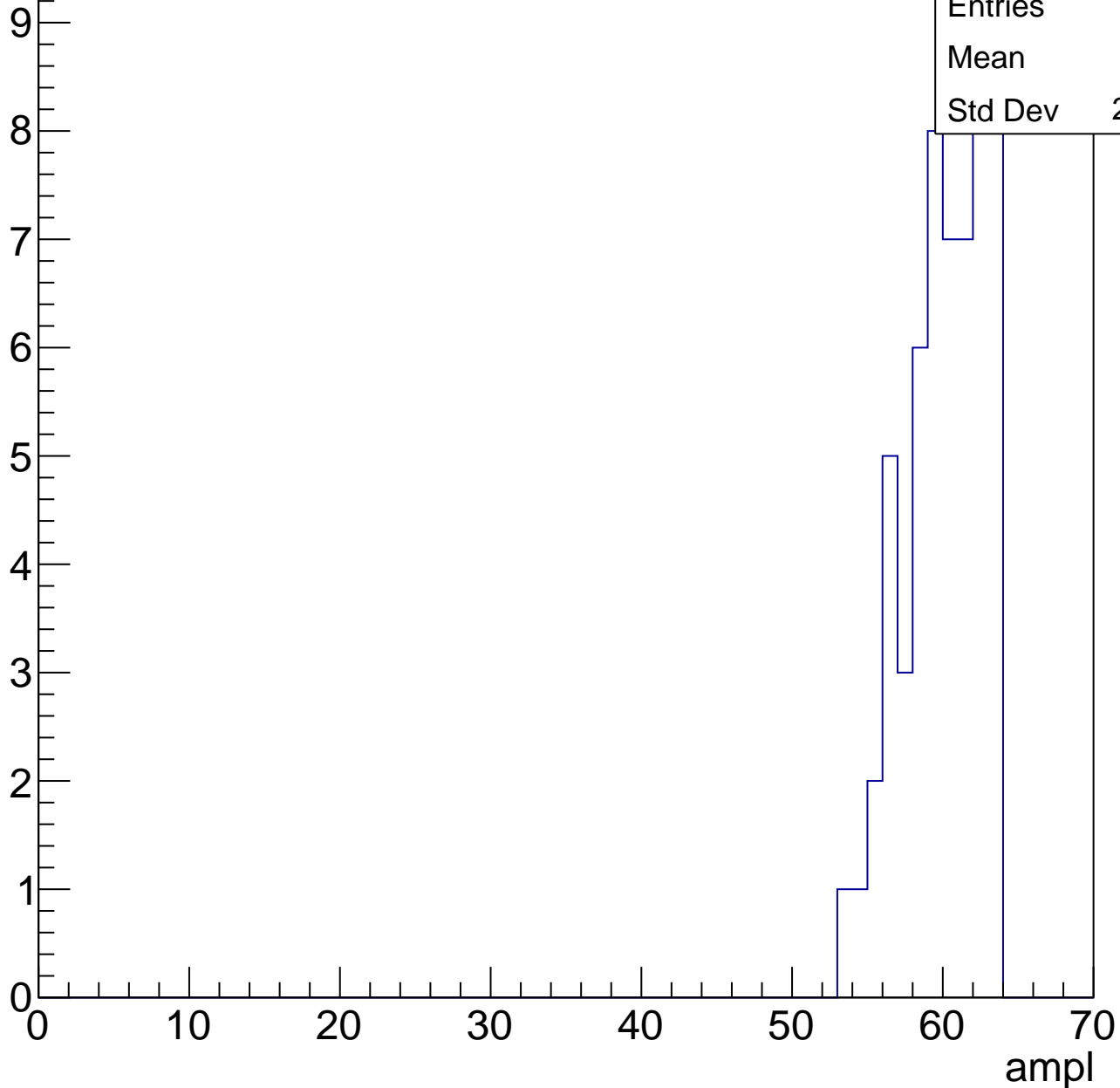
Entries	55
Mean	54.64
Std Dev	3.62



B1L100S, U10-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

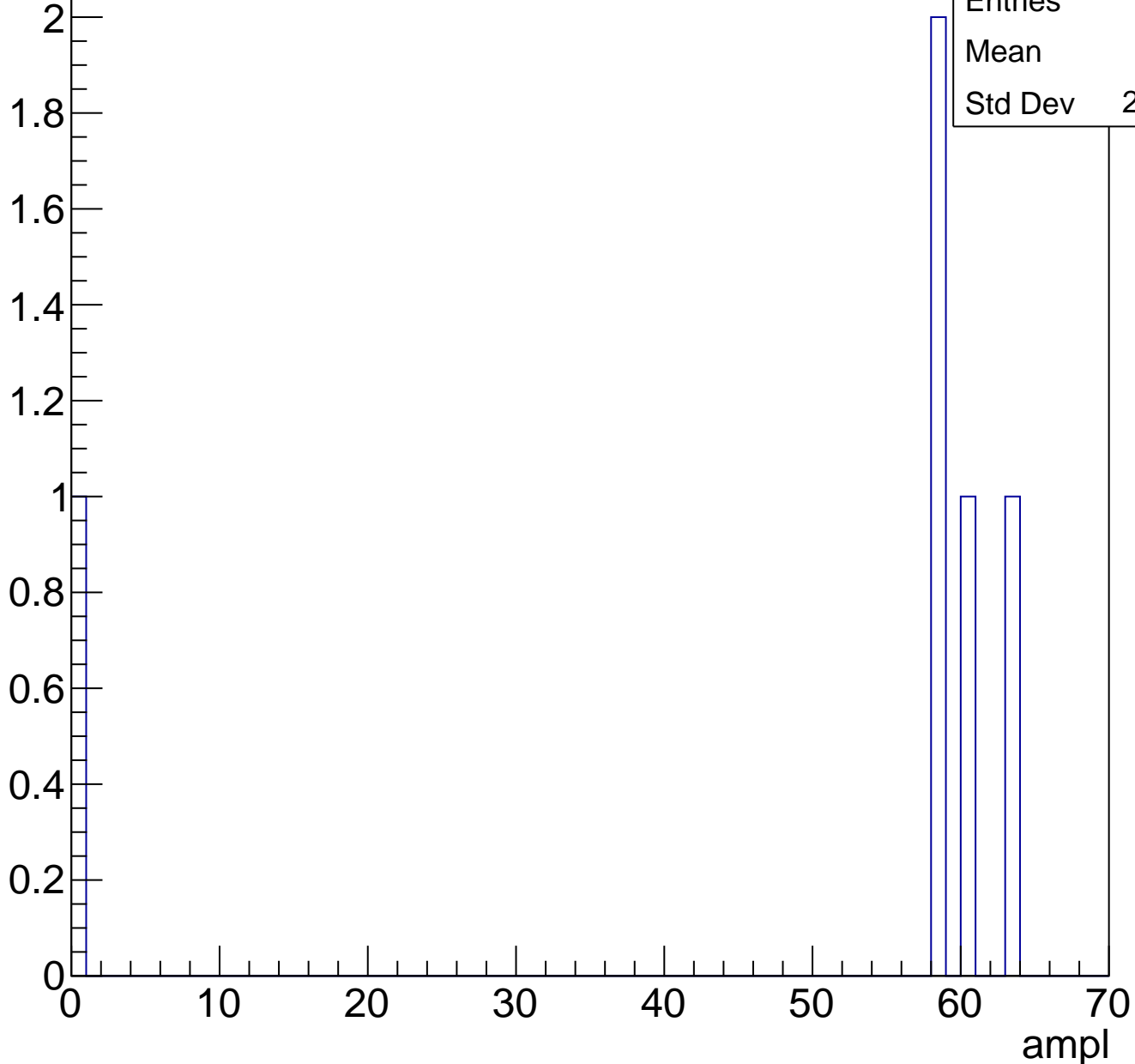


Entries	57
Mean	59.6
Std Dev	2.581

B1L100S, U10-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

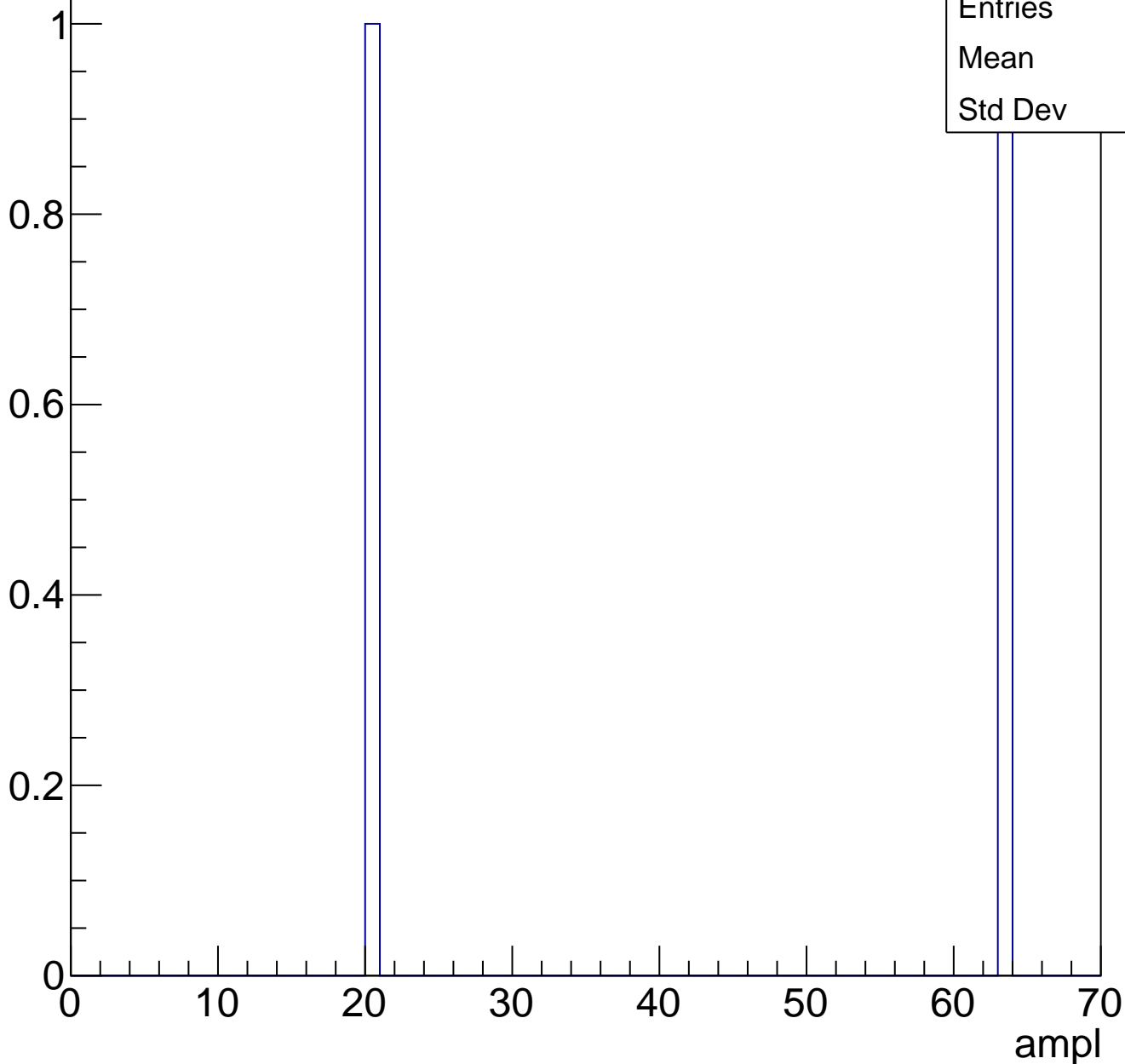


Entries	5
Mean	47.8
Std Dev	23.97

B1L100S, U10-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch76, adc0

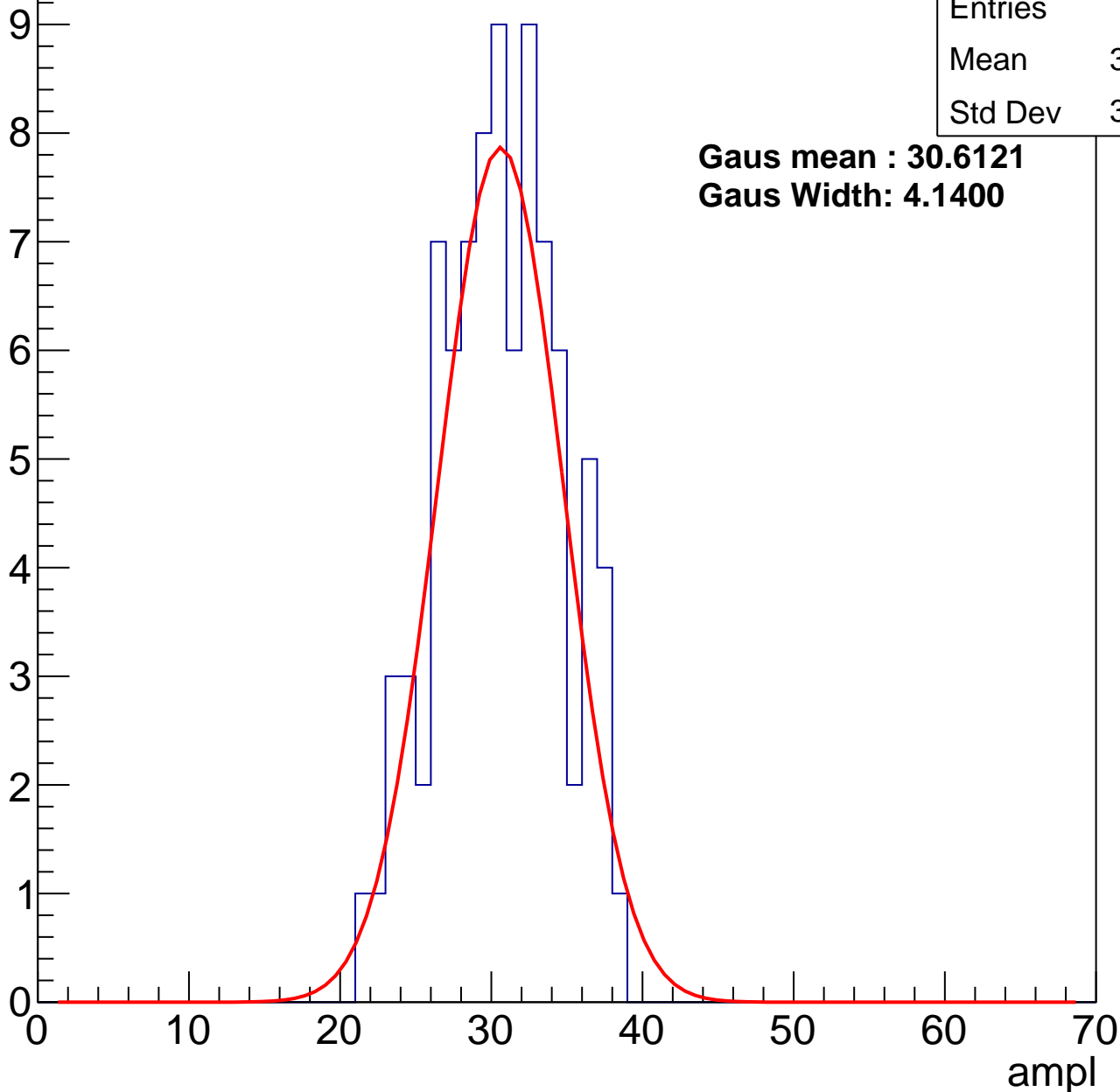
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	30.13
Std Dev	3.927

Gaus mean : 30.6121

Gaus Width: 4.1400



B1L100S, U10-ch76, adc1

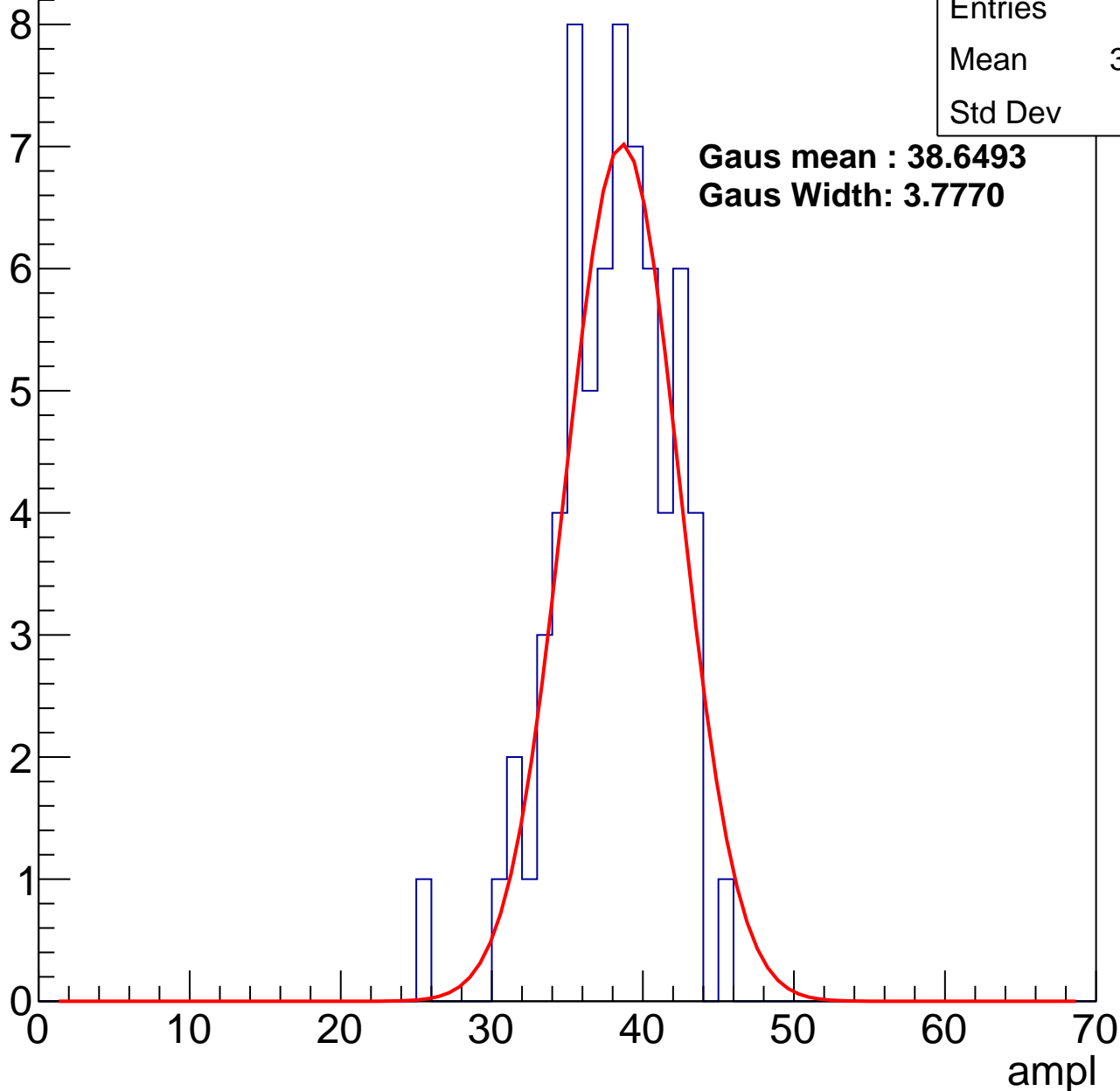
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.55
Std Dev	3.67

Gaus mean : 38.6493

Gaus Width: 3.7770

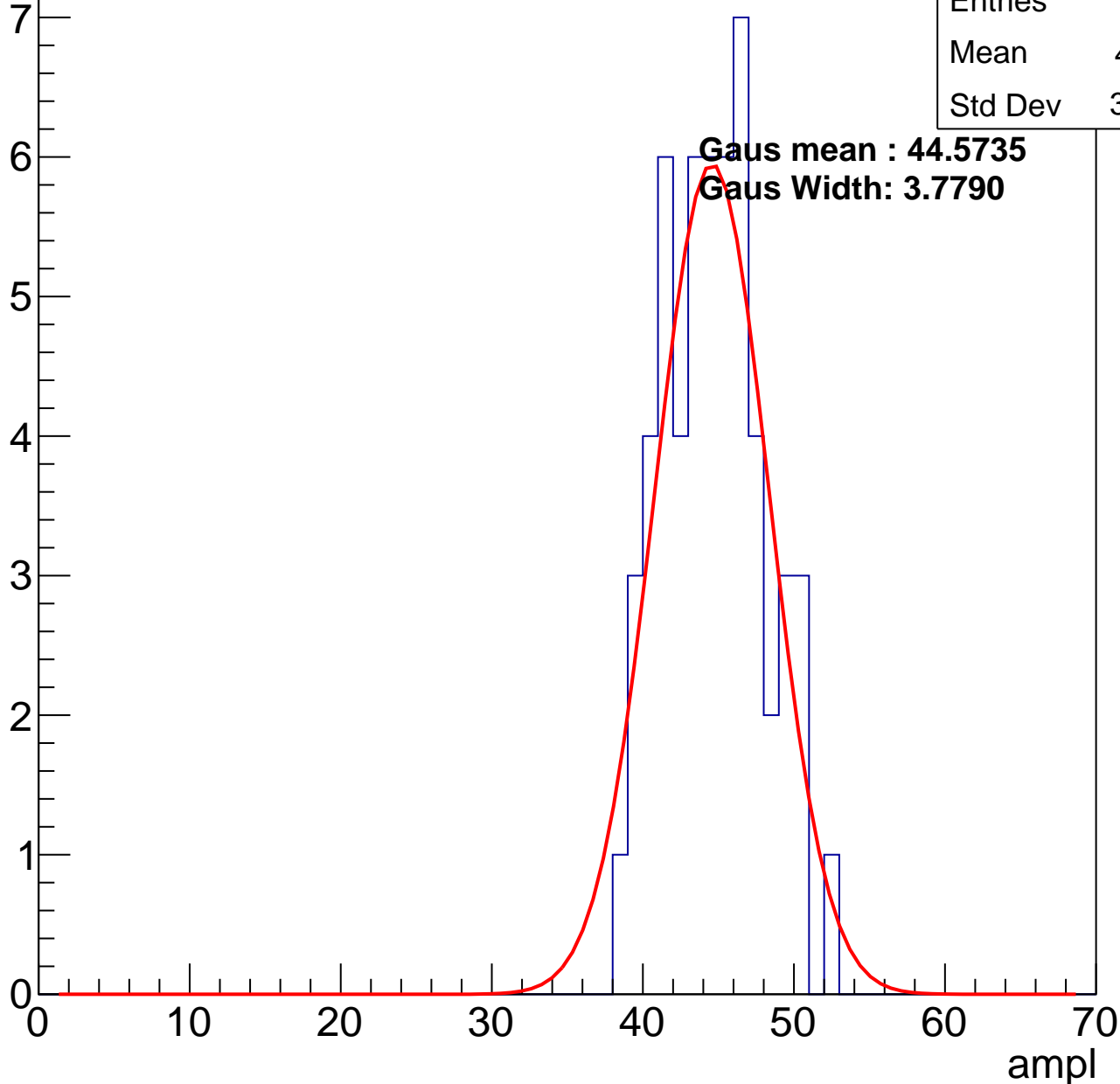


B1L100S, U10-ch76, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.21
Std Dev	3.266



B1L100S, U10-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	49.98
Std Dev	3.61

Entry

10

8

6

4

2

0

0

10

20

30

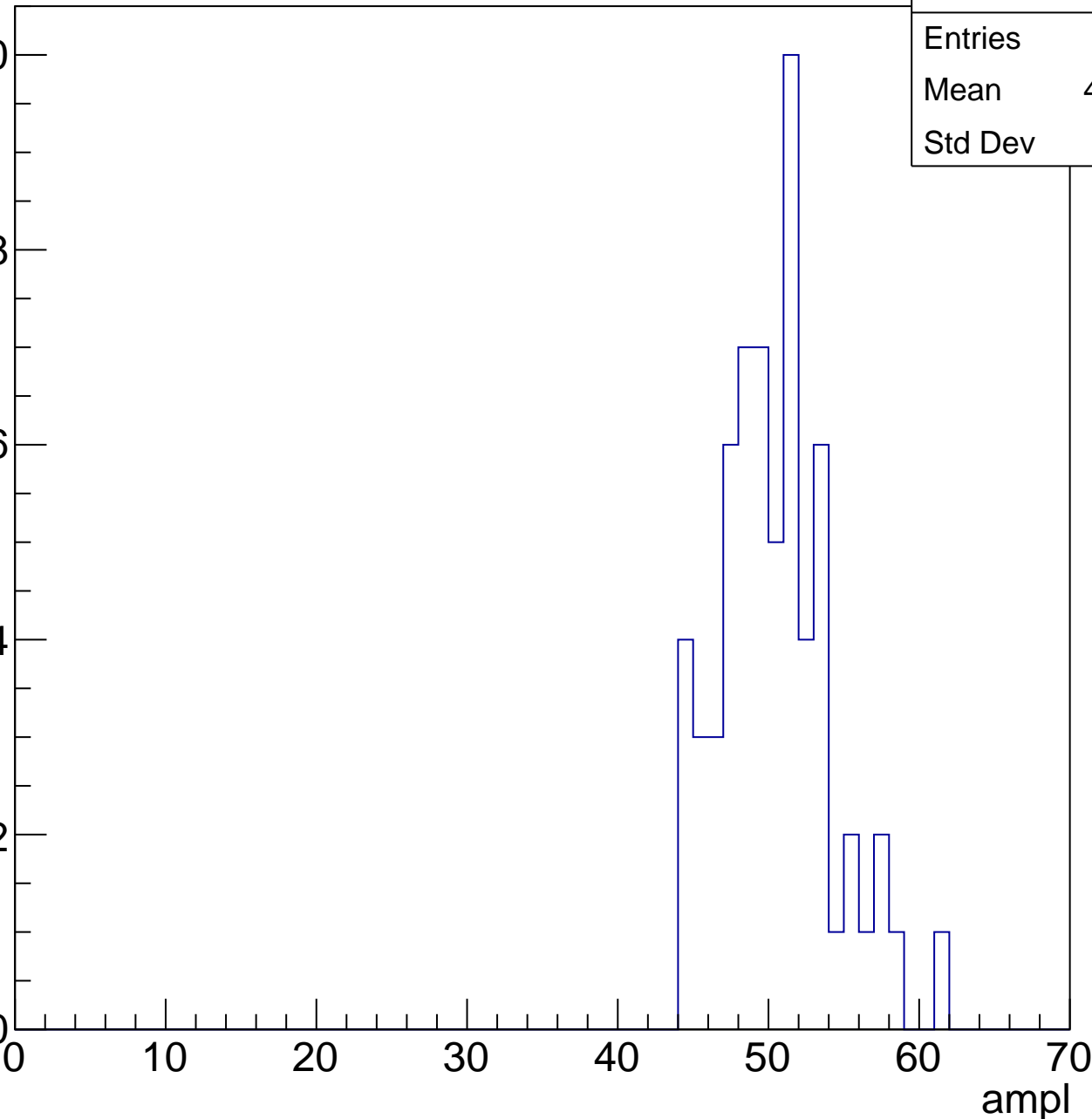
40

50

60

ampl

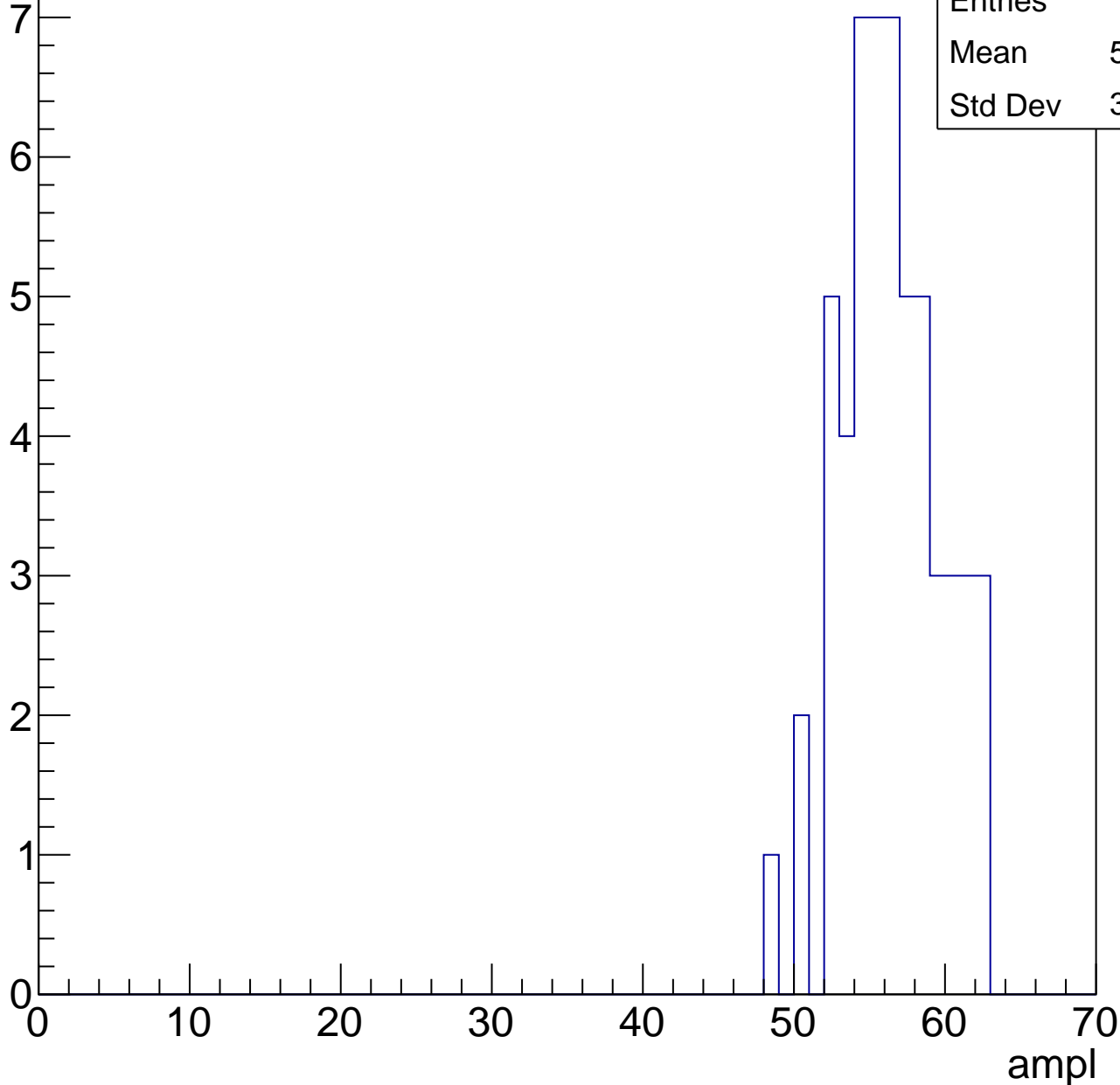
70



B1L100S, U10-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



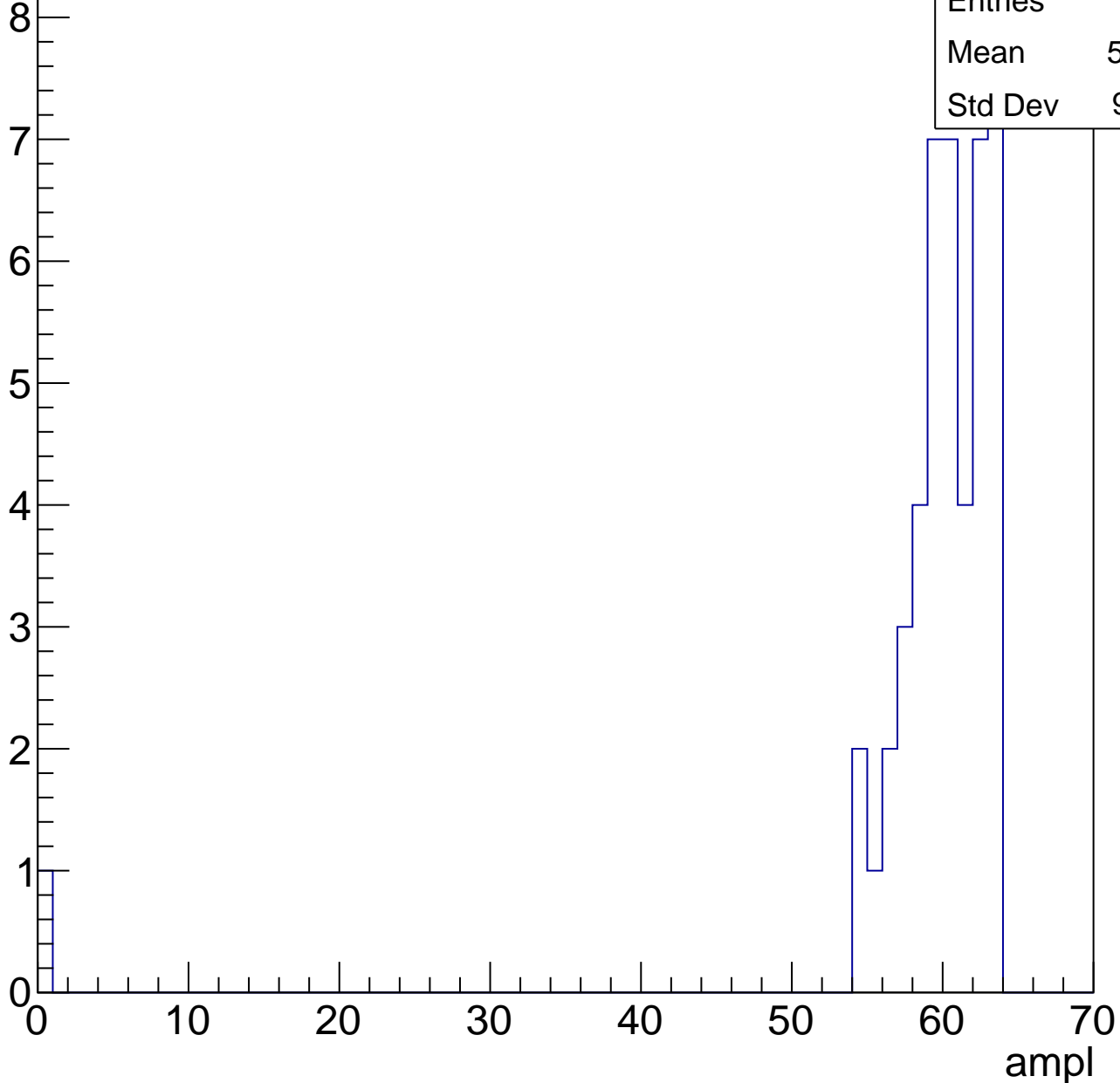
Entries	55
Mean	55.93
Std Dev	3.218

B1L100S, U10-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	58.54
Std Dev	9.071



B1L100S, U10-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch77, adc0

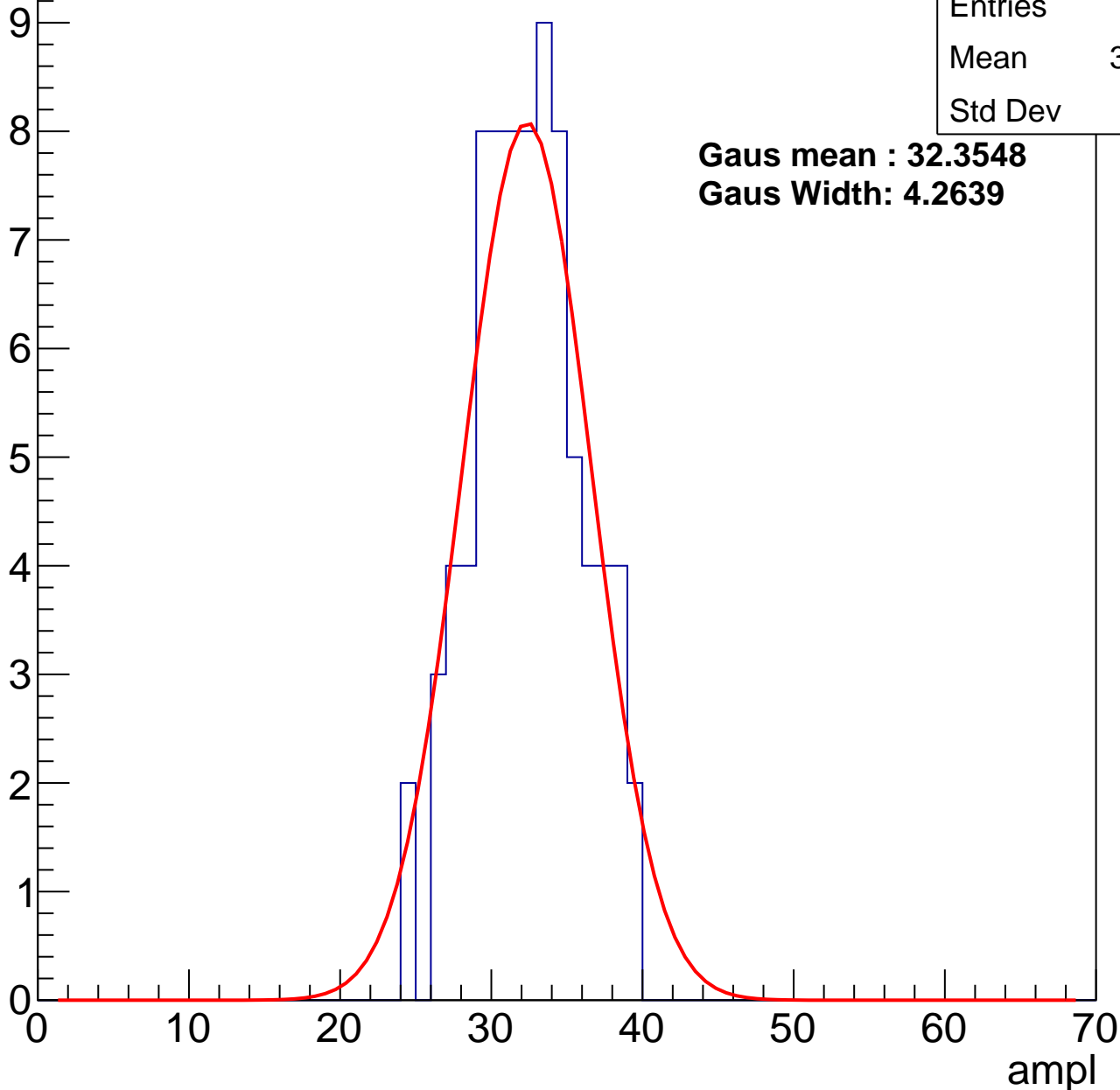
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	31.95
Std Dev	3.52

Gaus mean : 32.3548

Gaus Width: 4.2639



B1L100S, U10-ch77, adc1

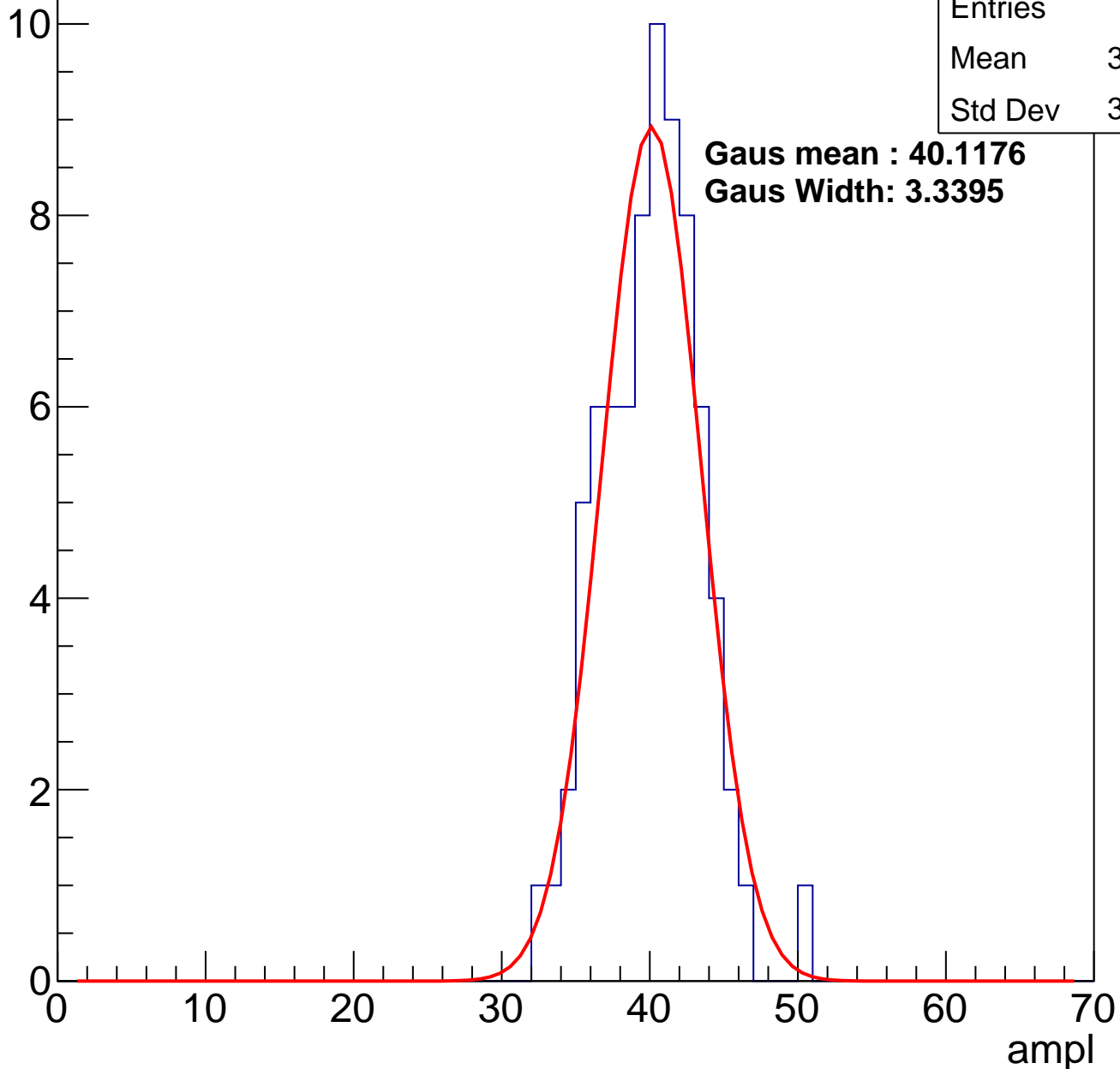
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	39.62
Std Dev	3.297

Gaus mean : 40.1176

Gaus Width: 3.3395

Entry



B1L100S, U10-ch77, adc2

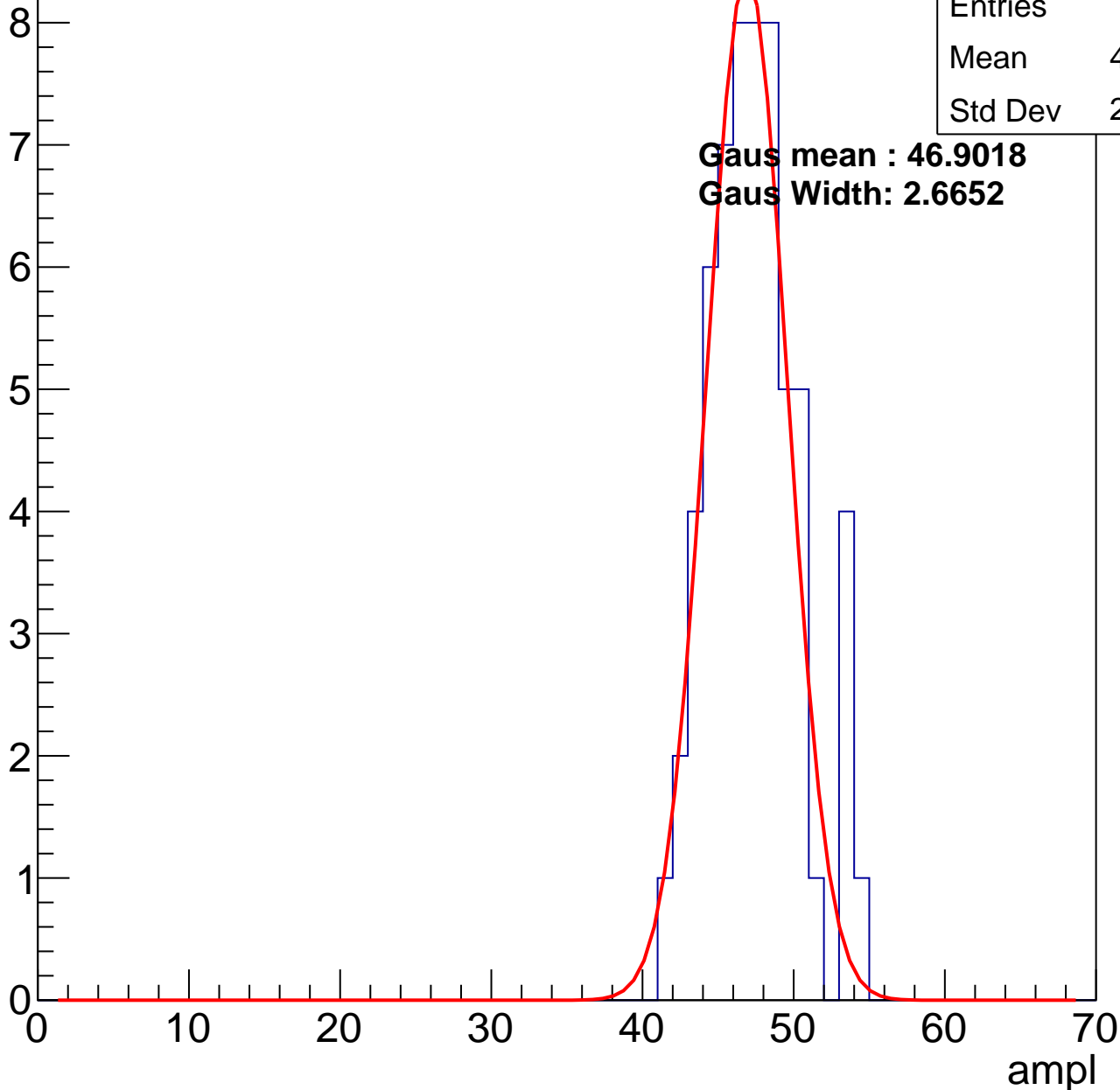
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	46.93
Std Dev	2.949

Gaus mean : 46.9018

Gaus Width: 2.6652

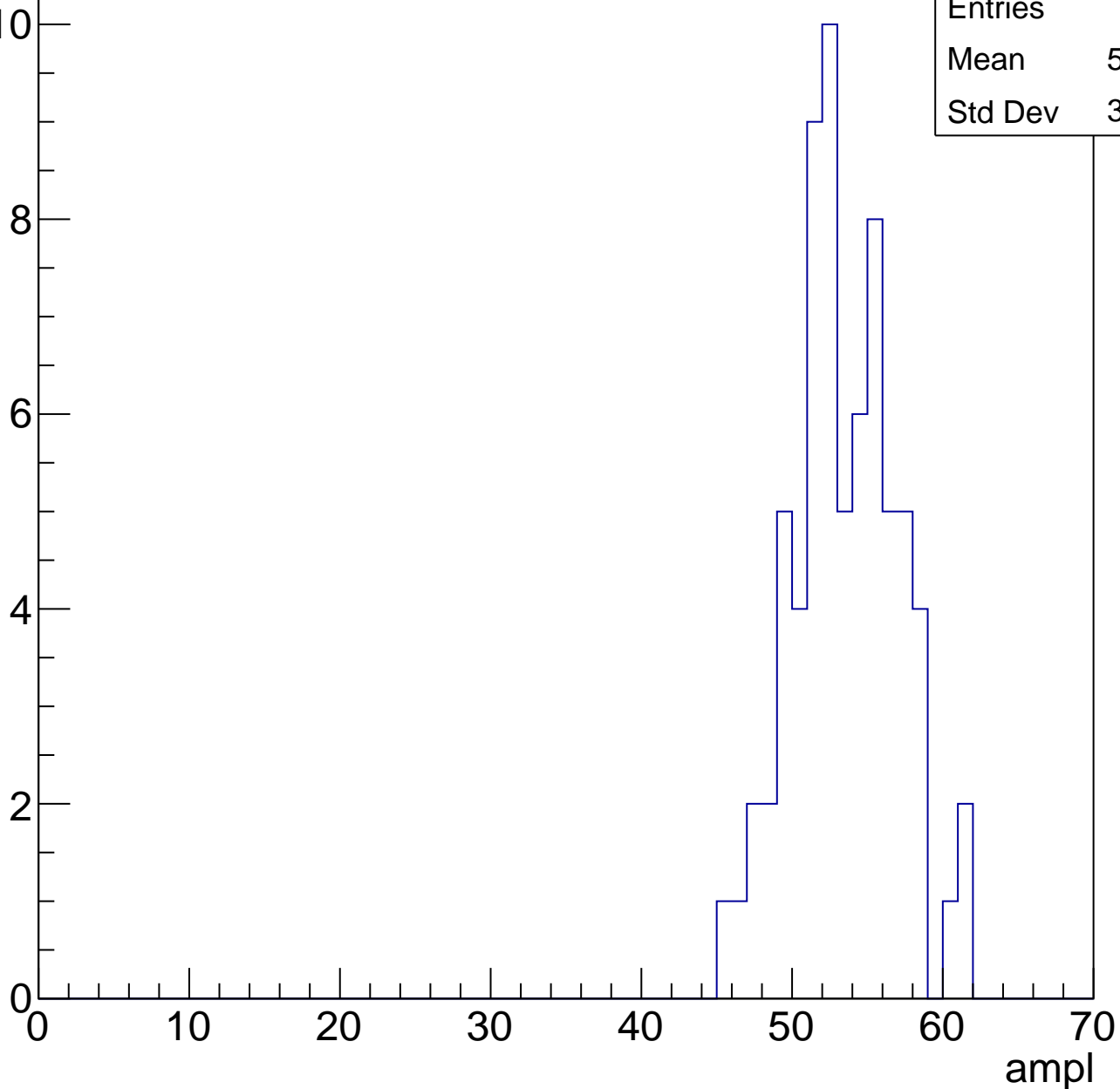


B1L100S, U10-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	53.04
Std Dev	3.458

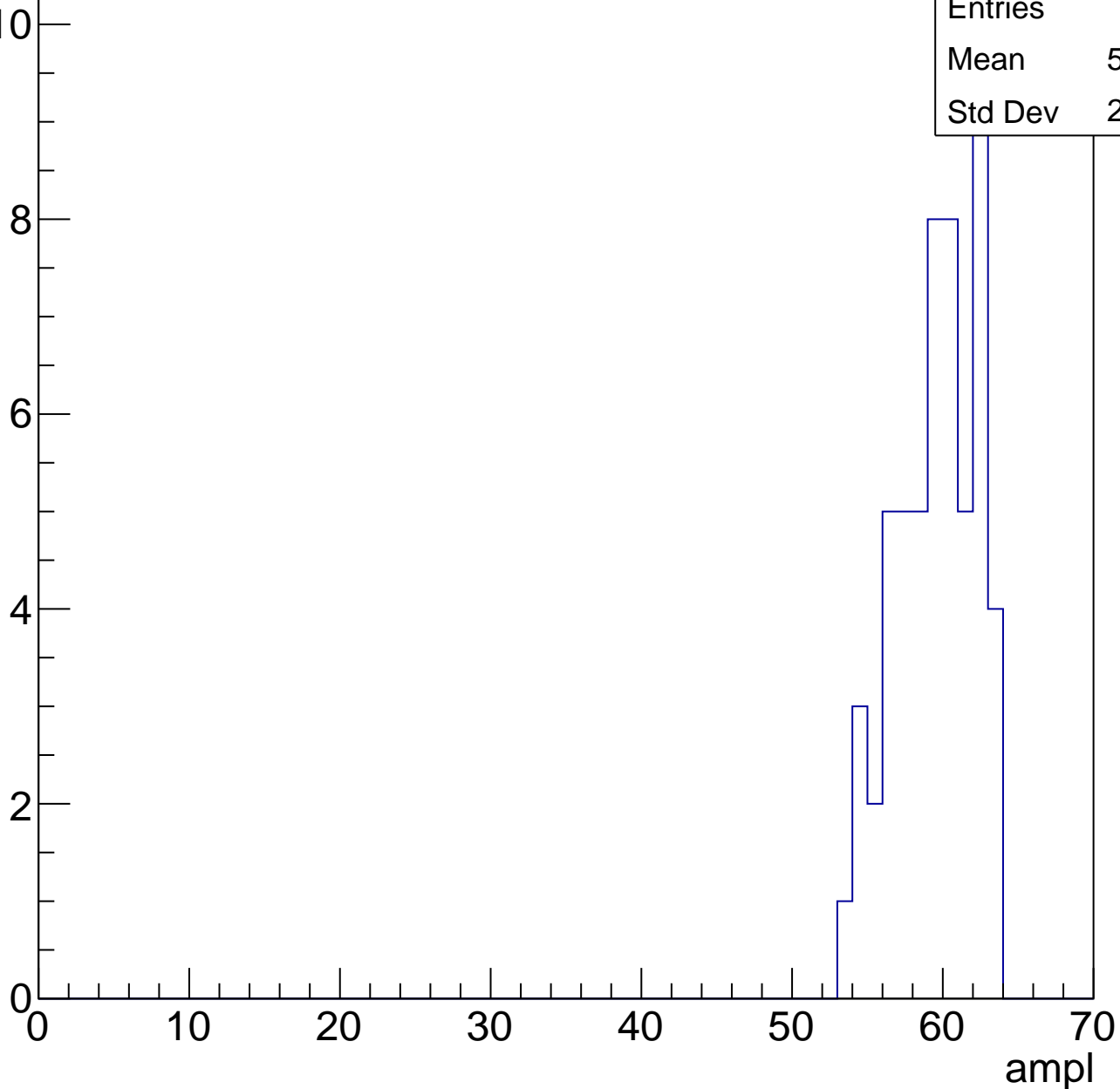


B1L100S, U10-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	59.09
Std Dev	2.654

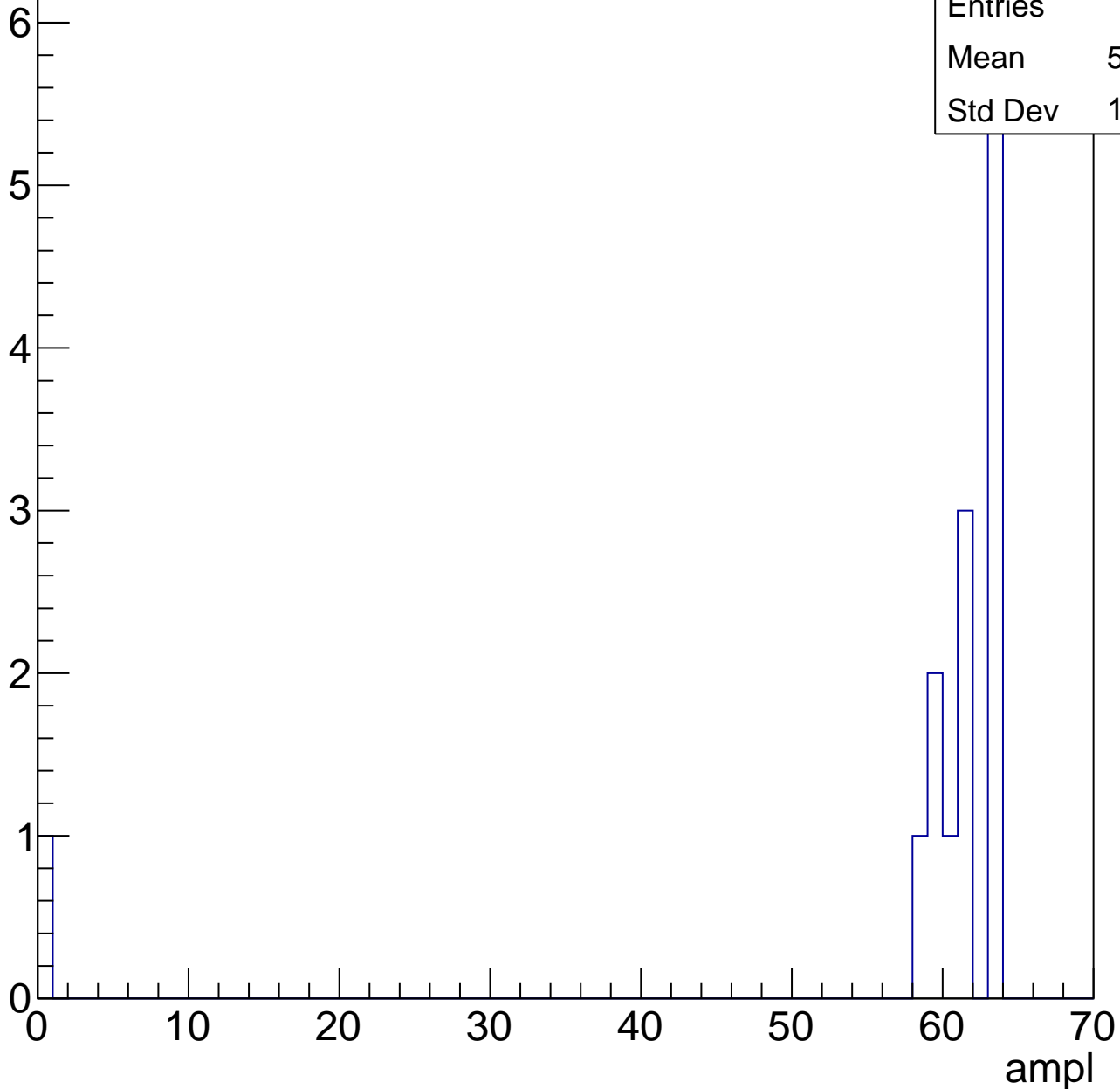


B1L100S, U10-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	56.93
Std Dev	15.88



B1L100S, U10-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch78, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	30.02
Std Dev	3.3

Gaus mean : 30.3062

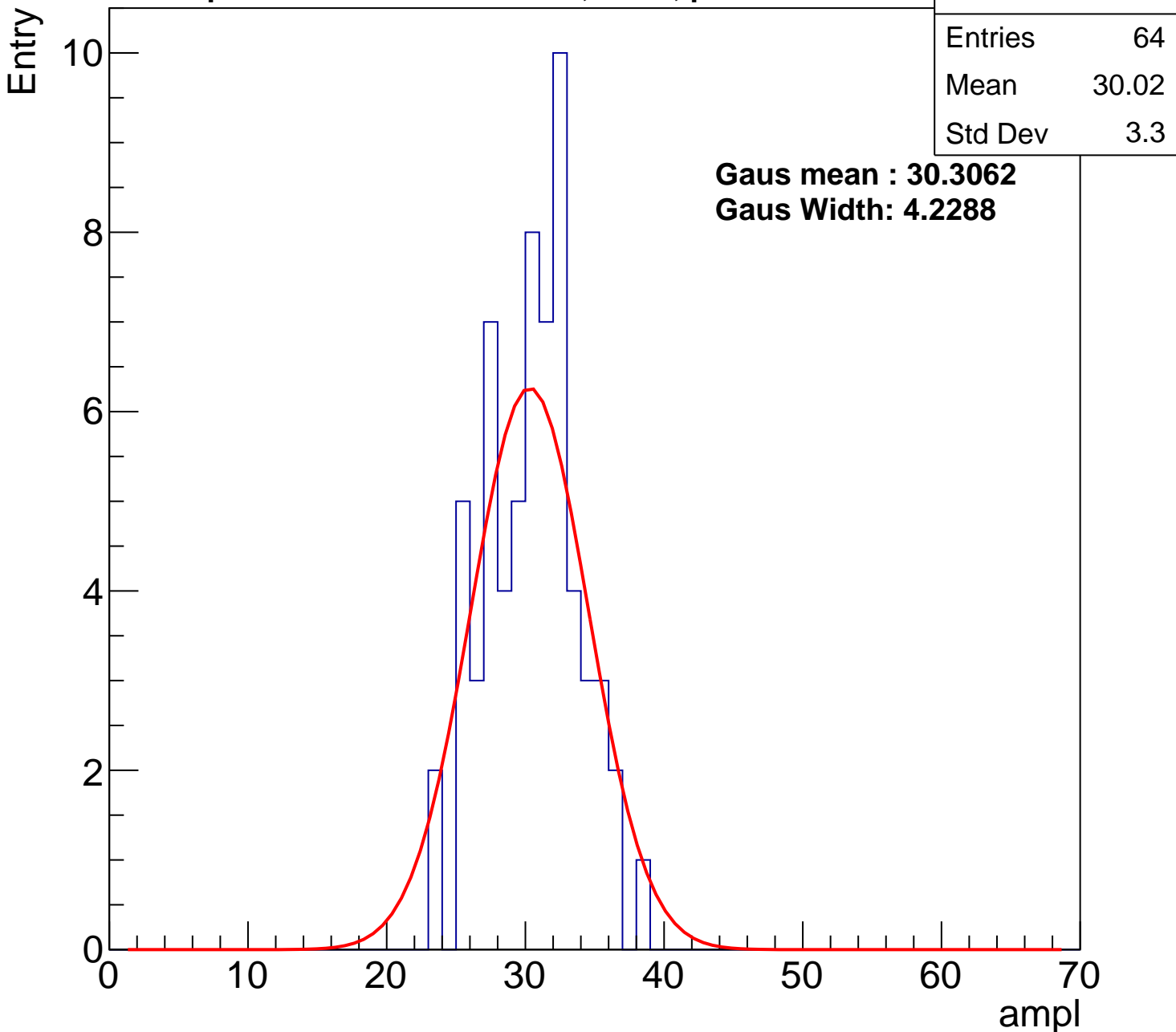
Gaus Width: 4.2288

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch78, adc1

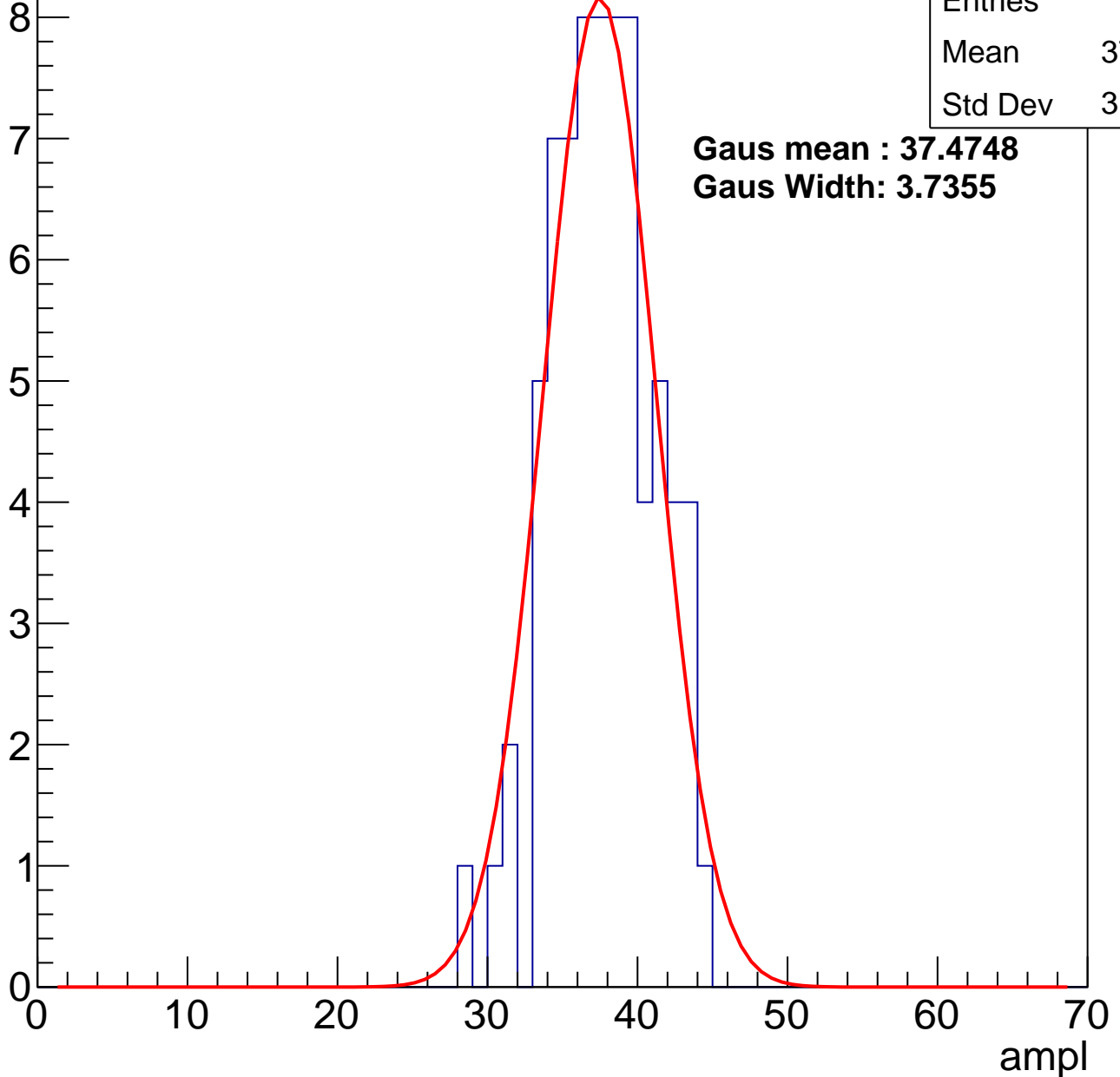
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.22
Std Dev	3.365

Gaus mean : 37.4748

Gaus Width: 3.7355



B1L100S, U10-ch78, adc2

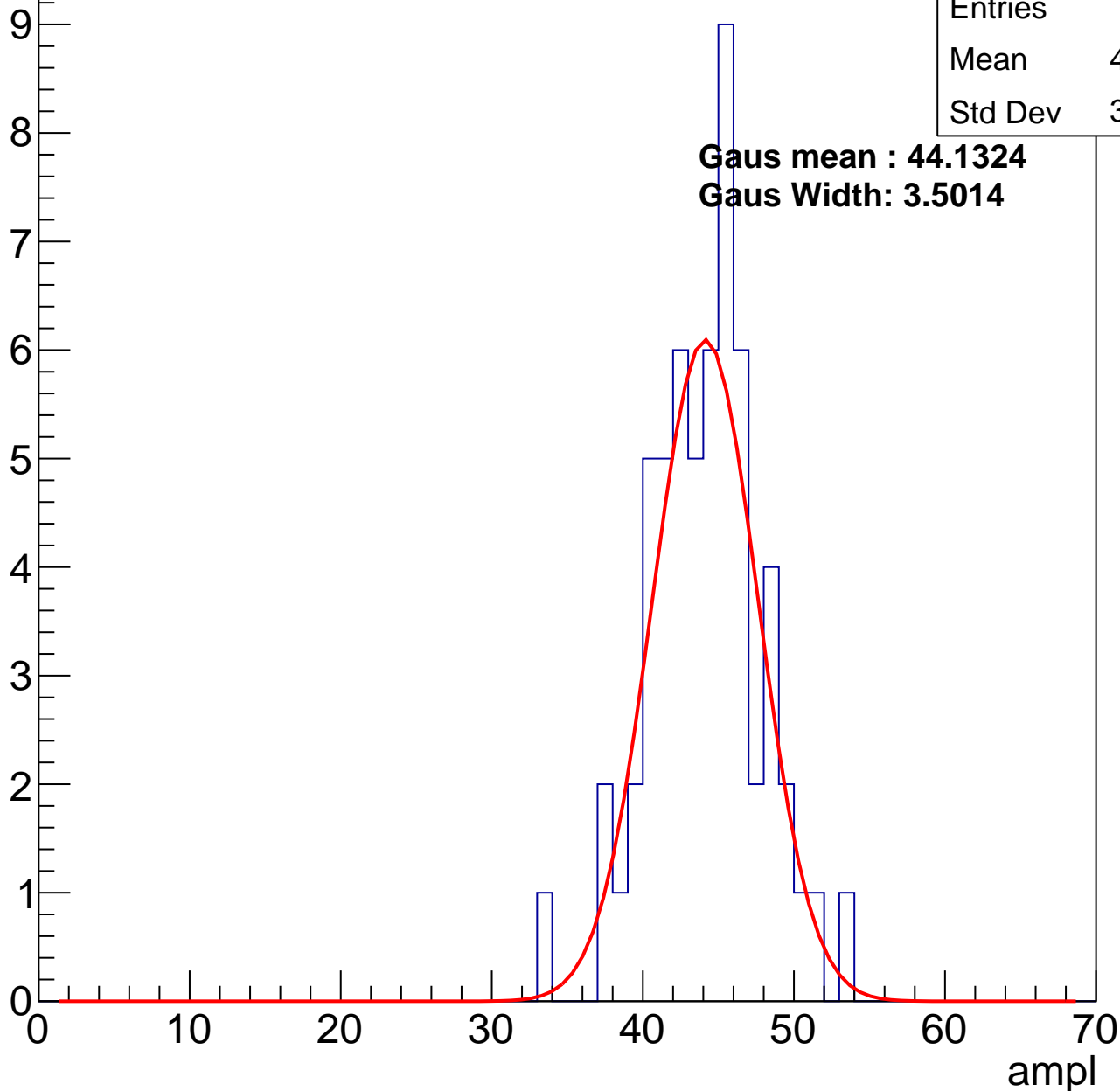
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.69
Std Dev	3.646

Gaus mean : 44.1324

Gaus Width: 3.5014

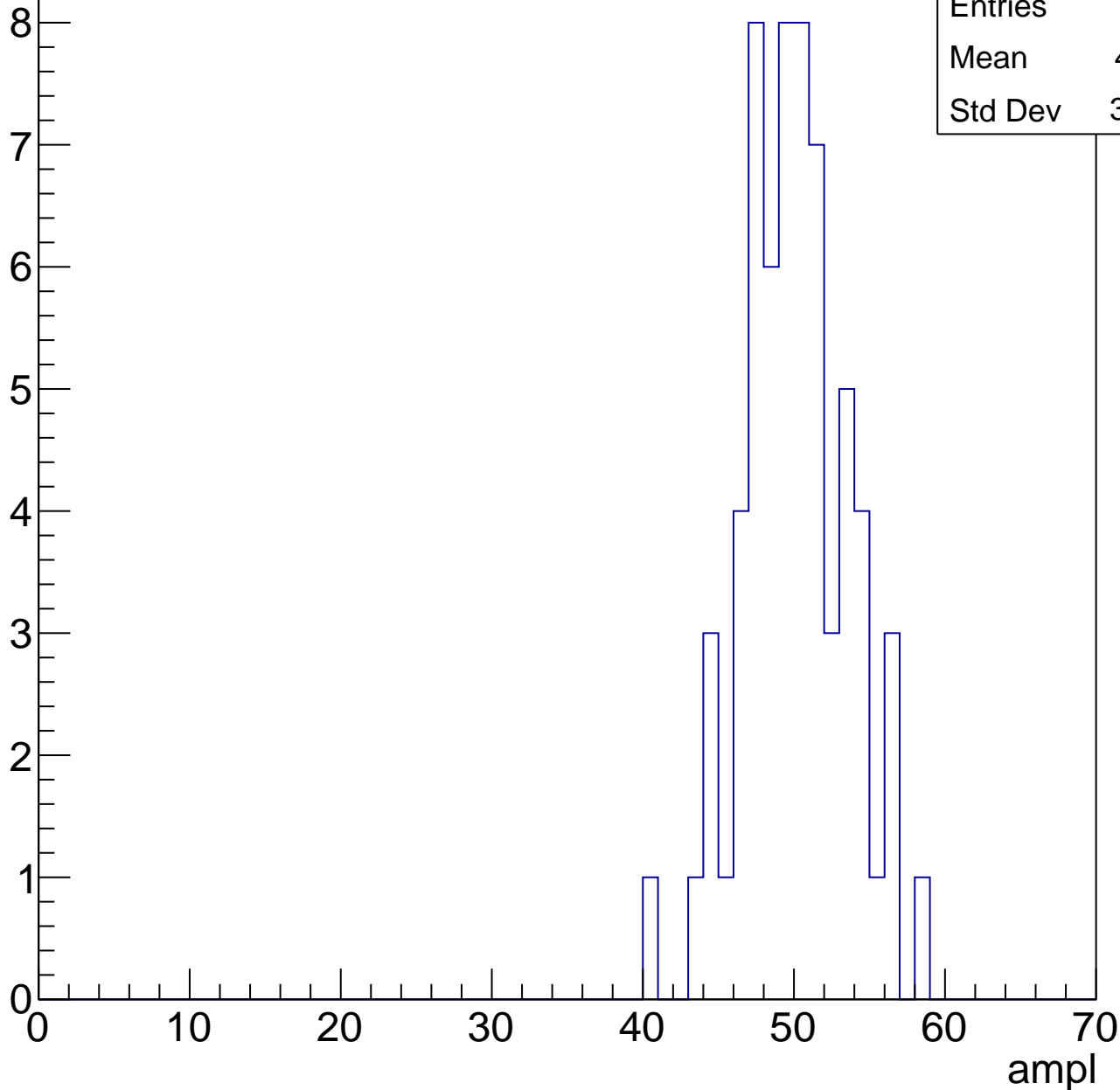


B1L100S, U10-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.61
Std Dev	3.458

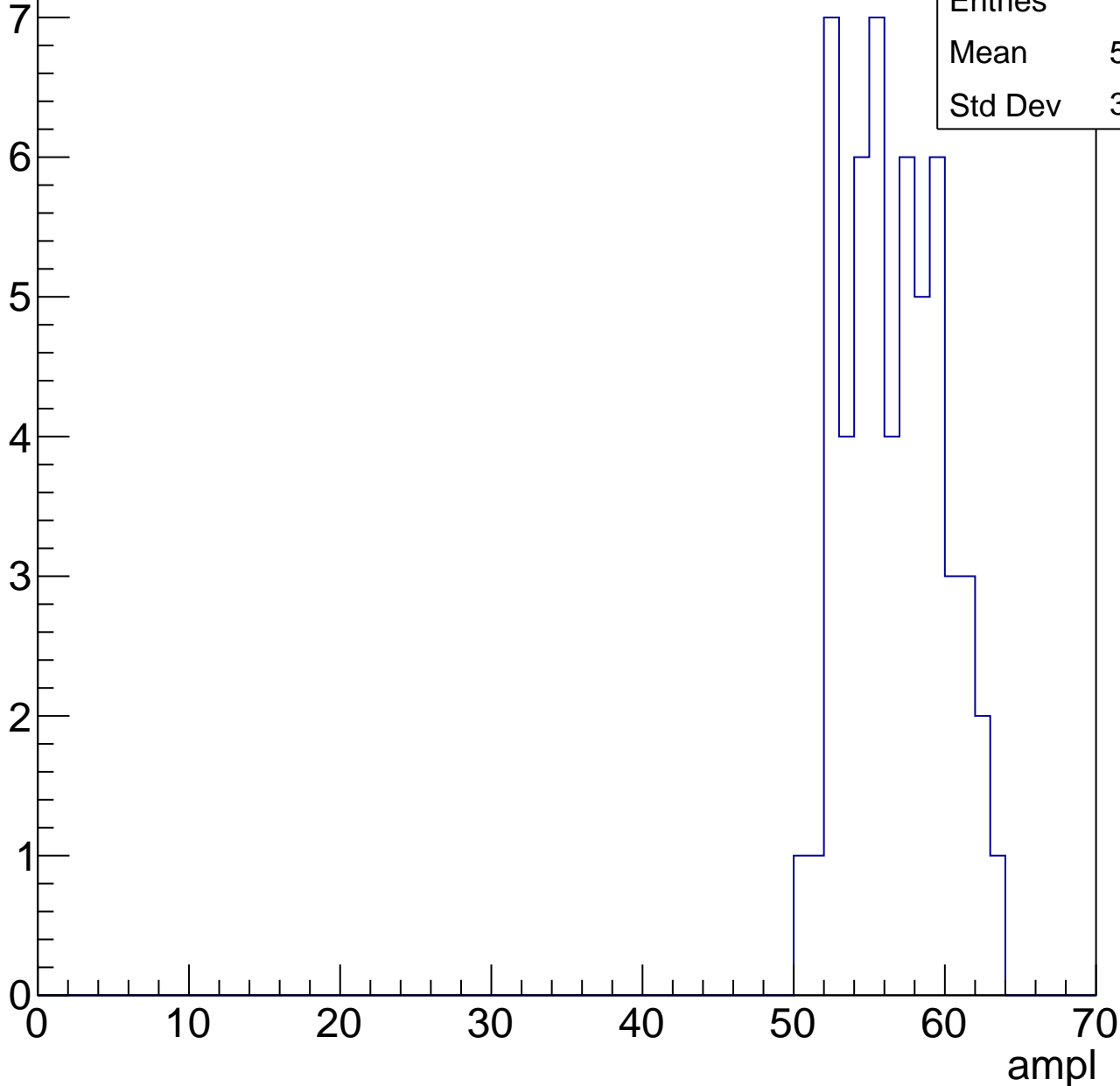


B1L100S, U10-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.18
Std Dev	3.169

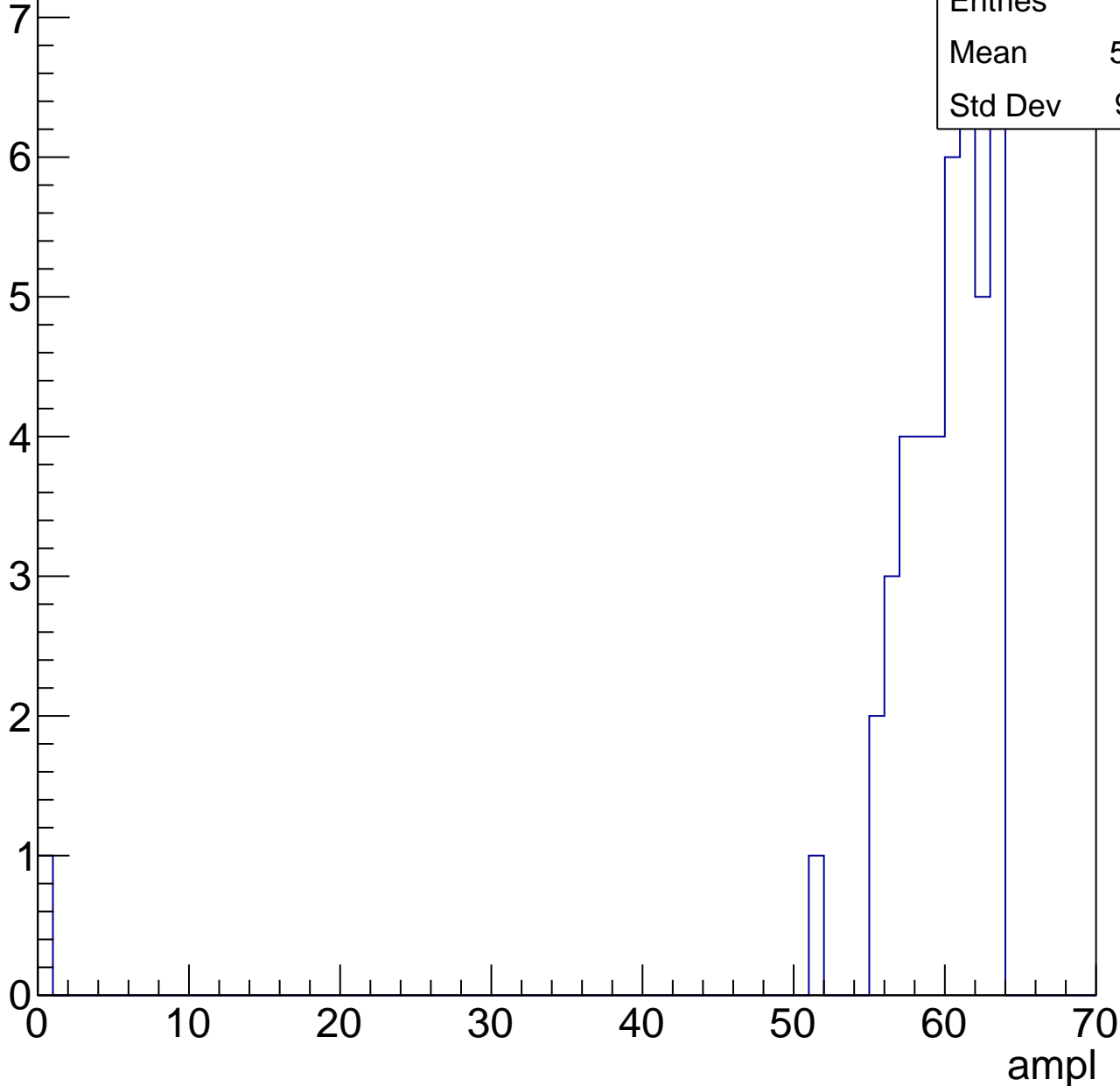


B1L100S, U10-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.25
Std Dev	9.281



B1L100S, U10-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

6

Mean

62

Std Dev

1

Entries	6
Mean	62
Std Dev	1

B1L100S, U10-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch79, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	28.89
Std Dev	5.646

Gaus mean : 29.9890

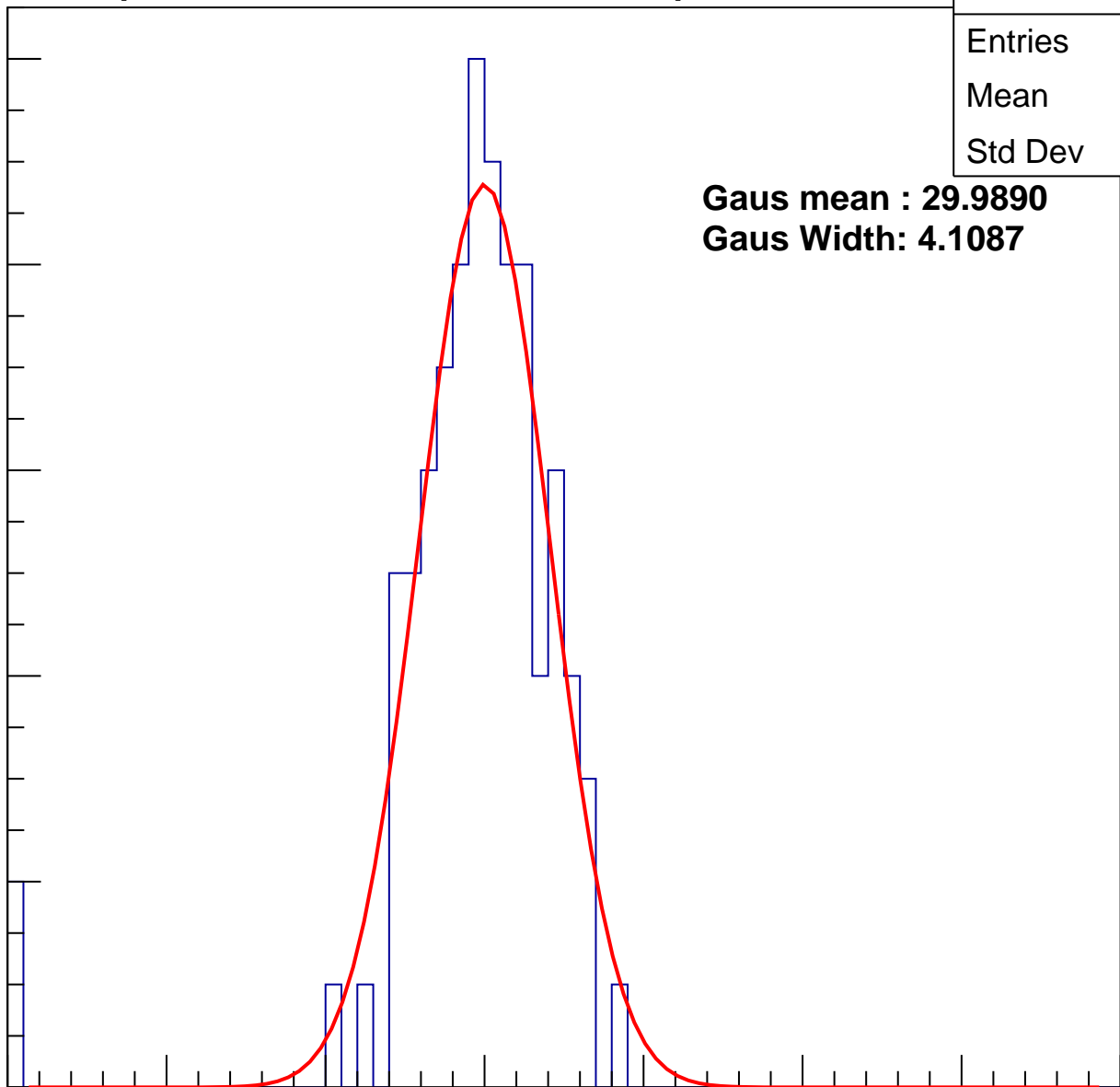
Gaus Width: 4.1087

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch79, adc1

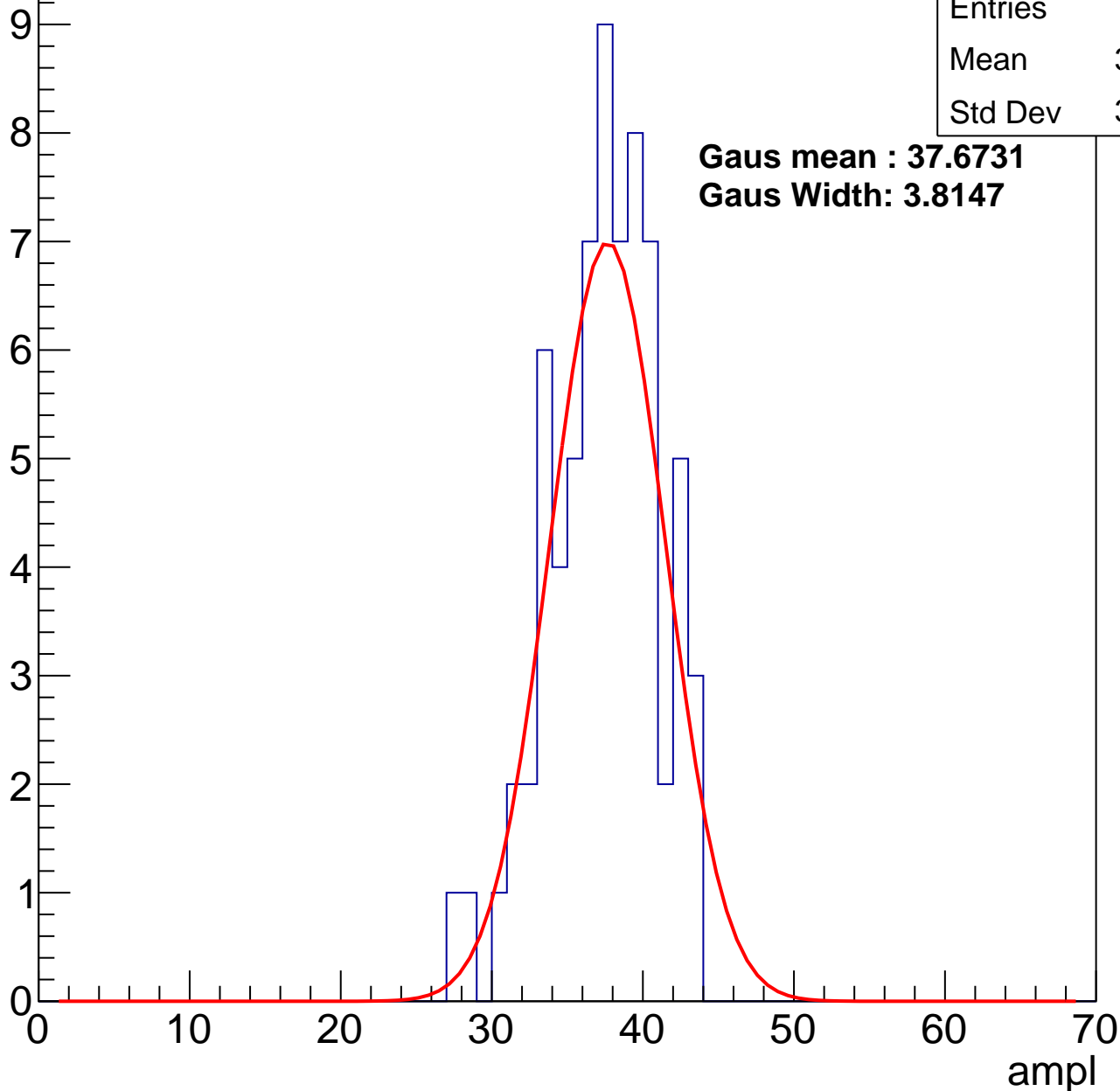
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.91
Std Dev	3.541

Gaus mean : 37.6731

Gaus Width: 3.8147



B1L100S, U10-ch79, adc2

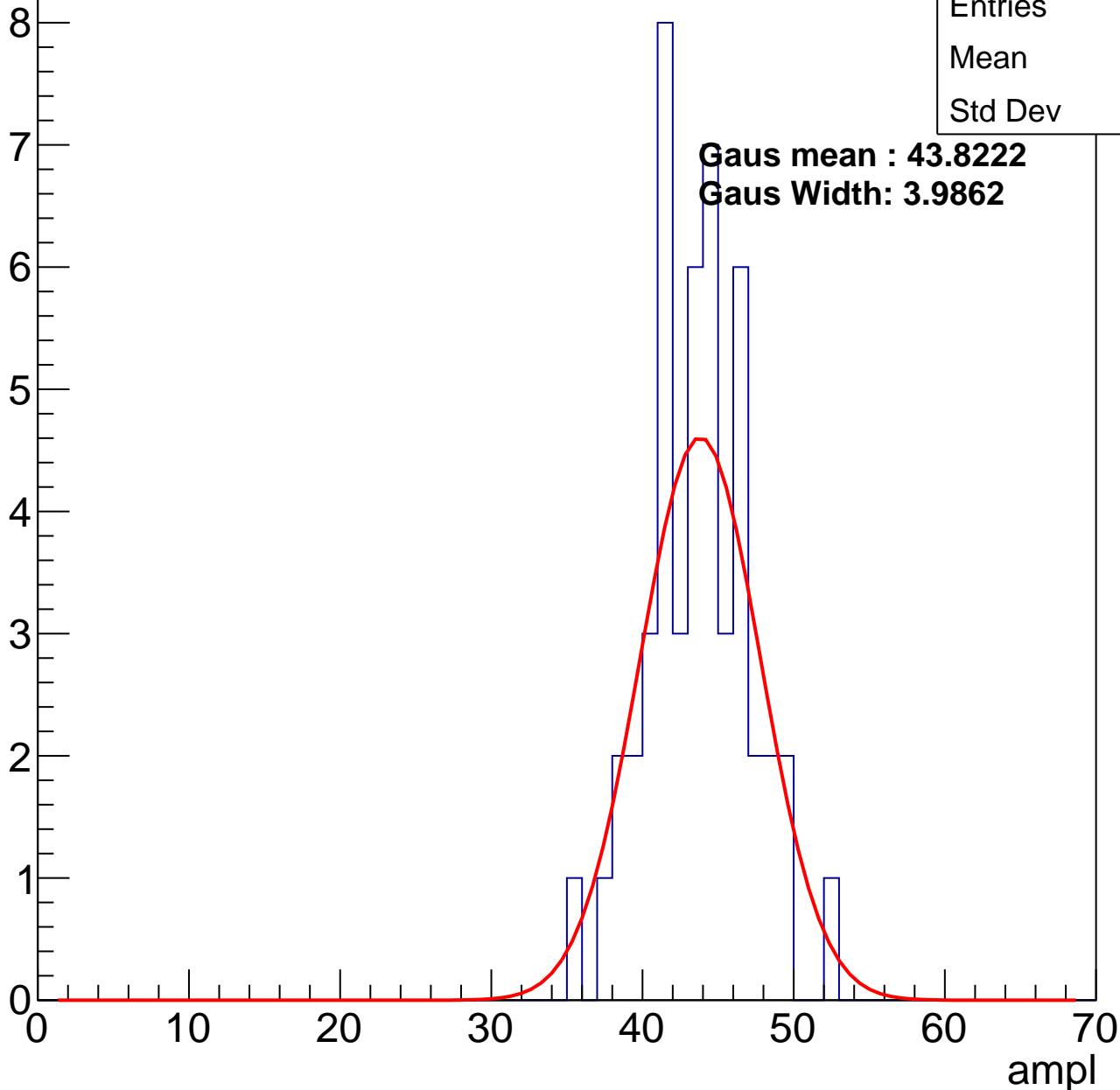
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	43.2
Std Dev	3.35

Gaus mean : 43.8222

Gaus Width: 3.9862

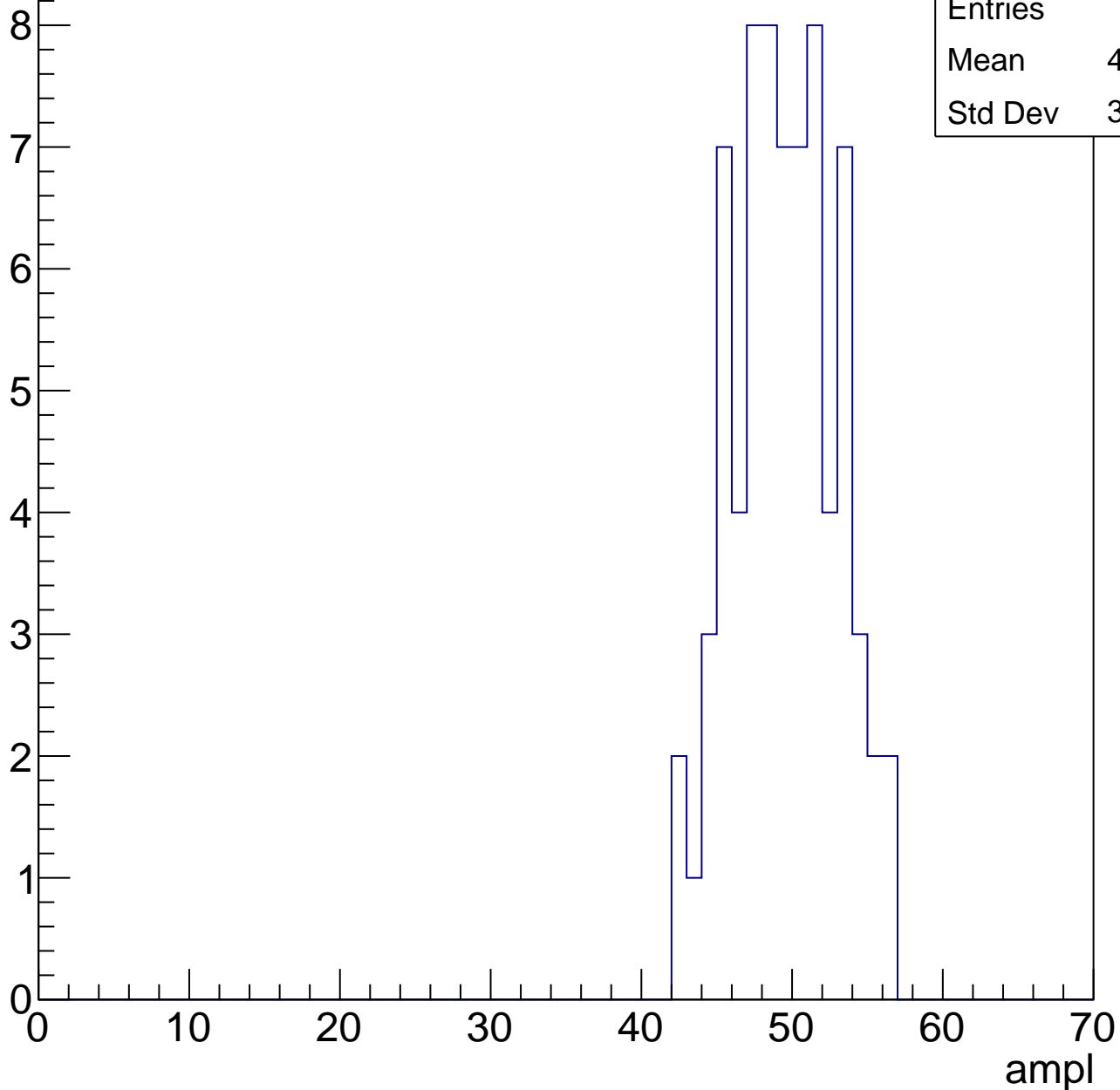


B1L100S, U10-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.07
Std Dev	3.369

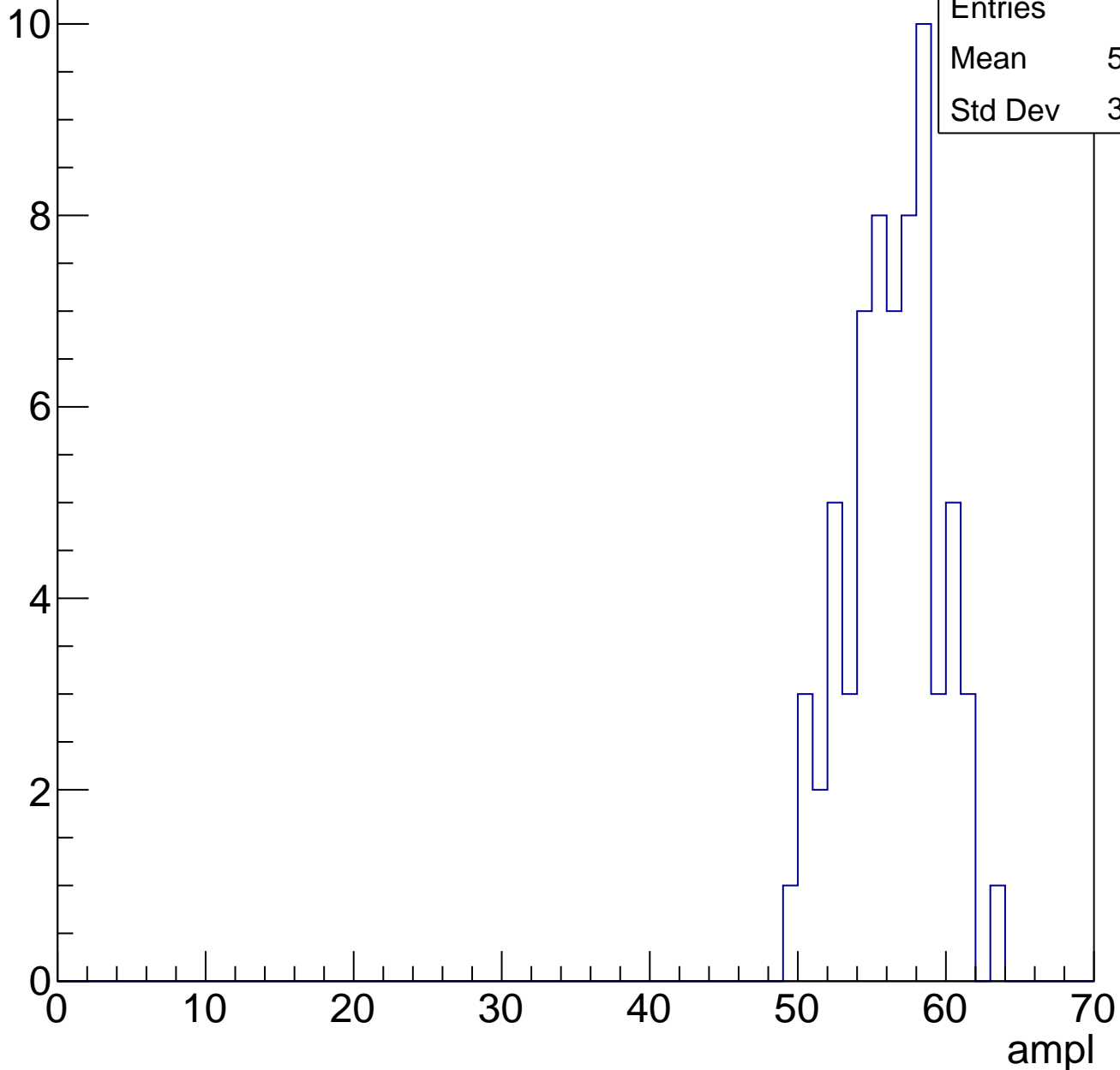


B1L100S, U10-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	55.89
Std Dev	3.085

Entry

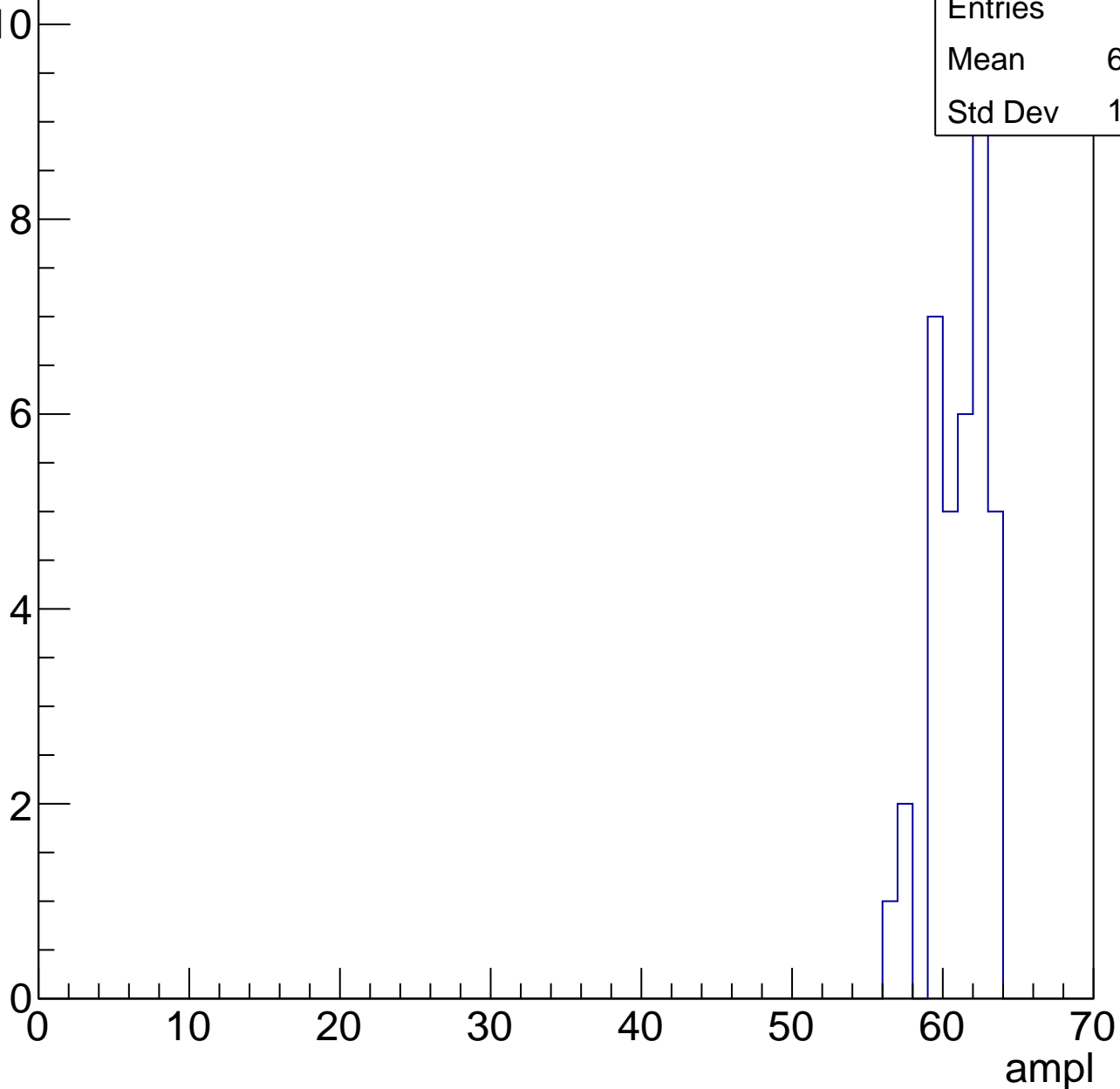


B1L100S, U10-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

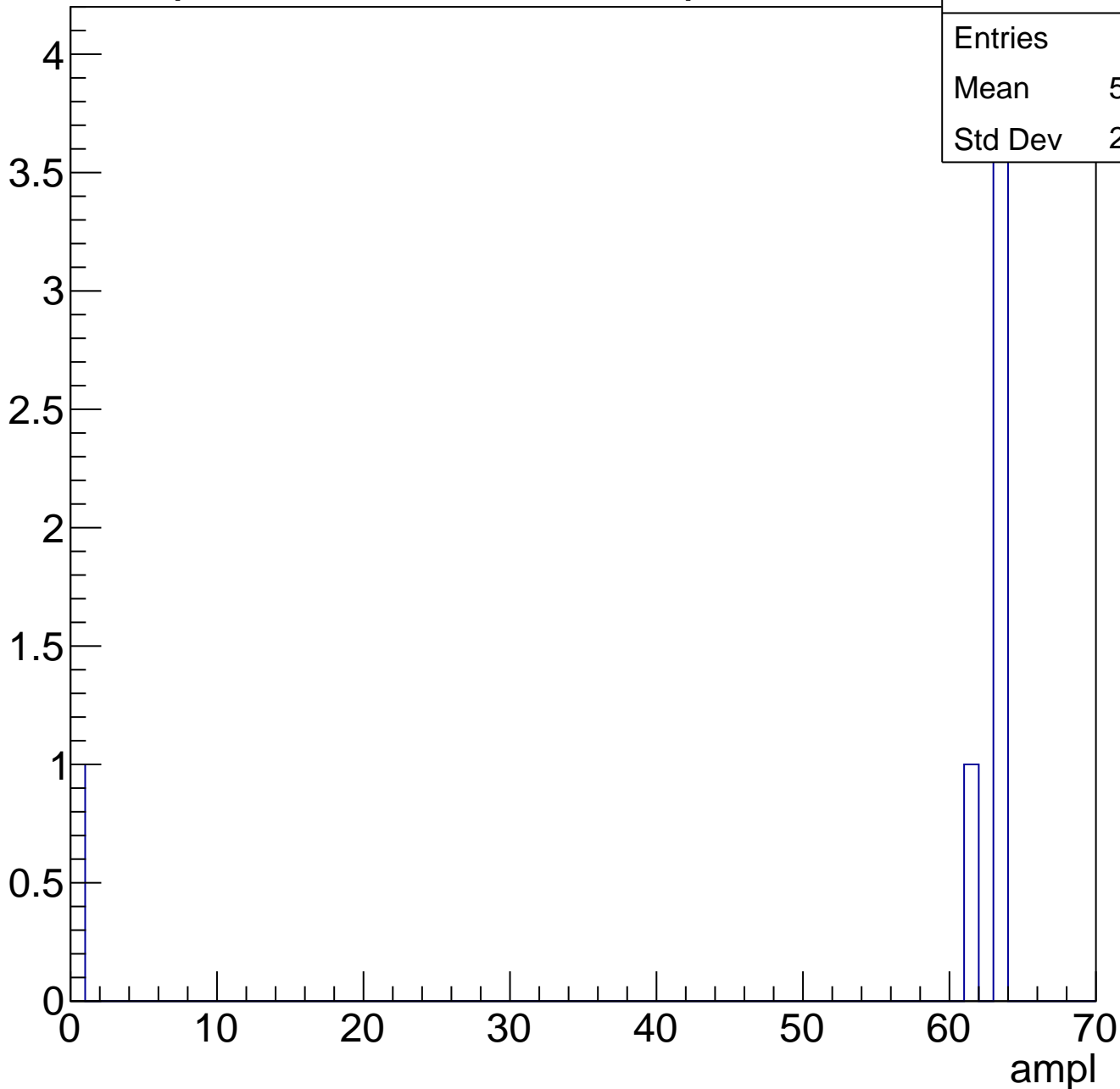
Entries	36
Mean	60.67
Std Dev	1.795



B1L100S, U10-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch80, adc0

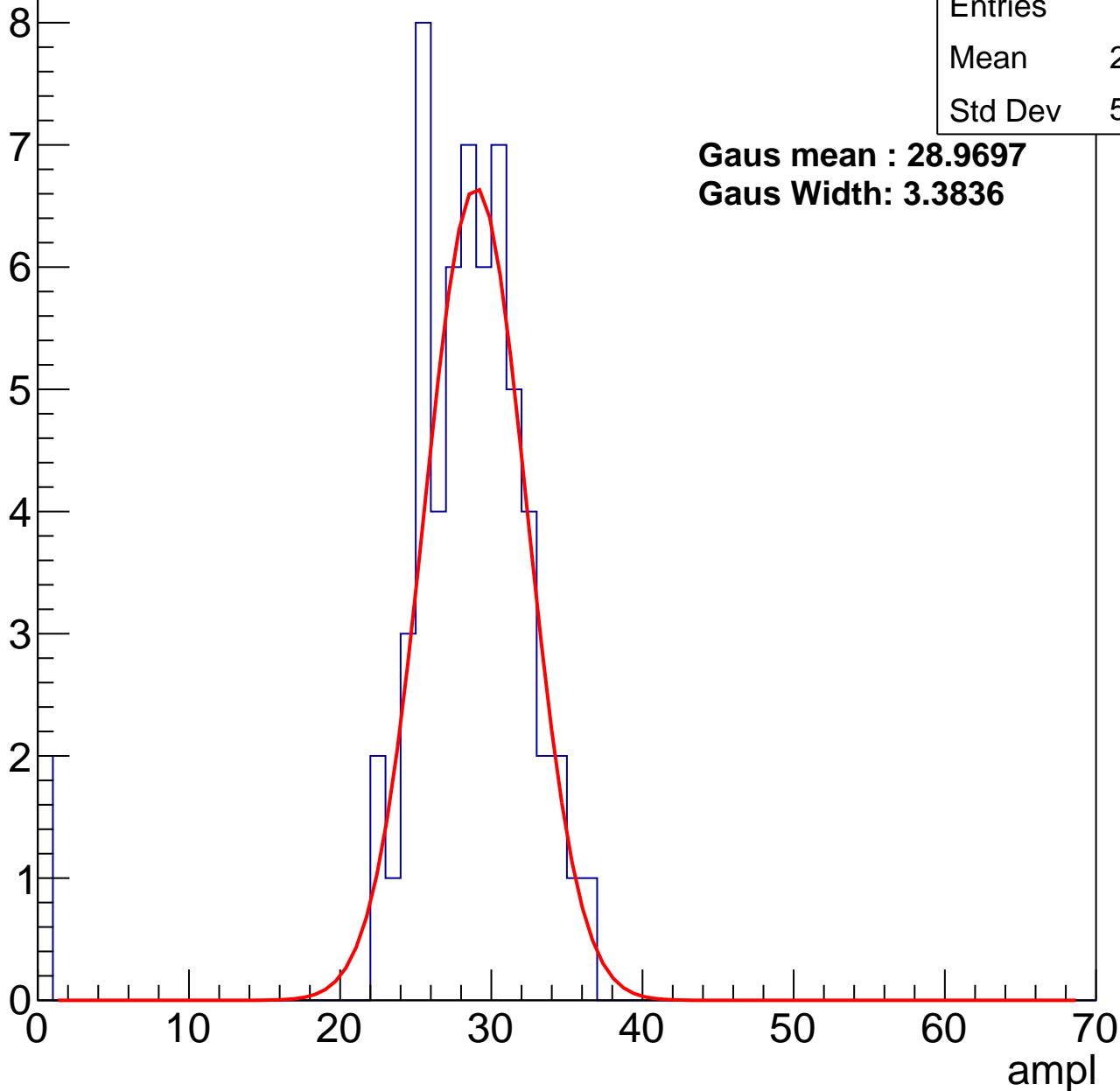
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	27.43
Std Dev	5.957

Gaus mean : 28.9697

Gaus Width: 3.3836



B1L100S, U10-ch80, adc1

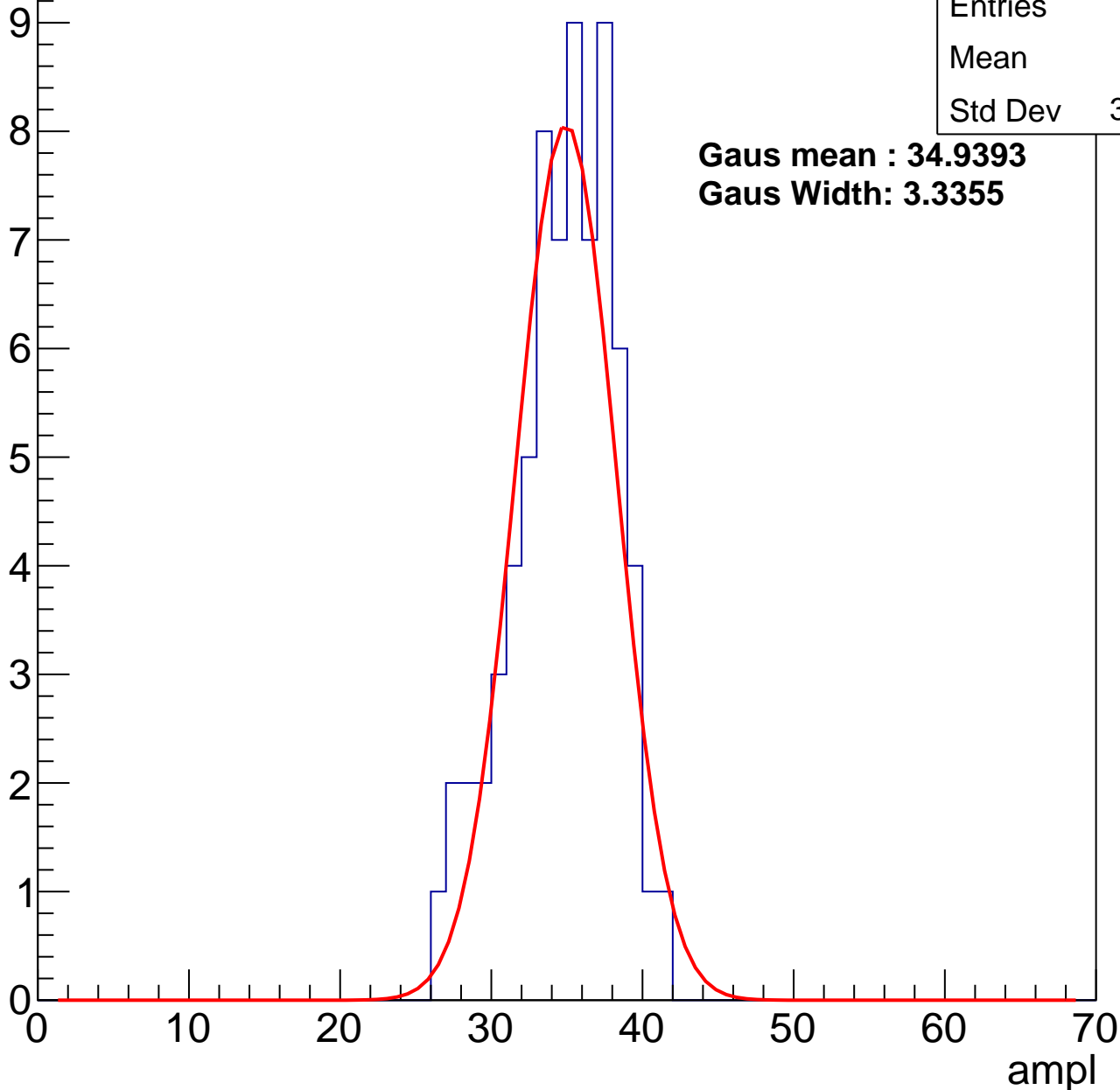
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	34.3
Std Dev	3.333

Gaus mean : 34.9393

Gaus Width: 3.3355



B1L100S, U10-ch80, adc2

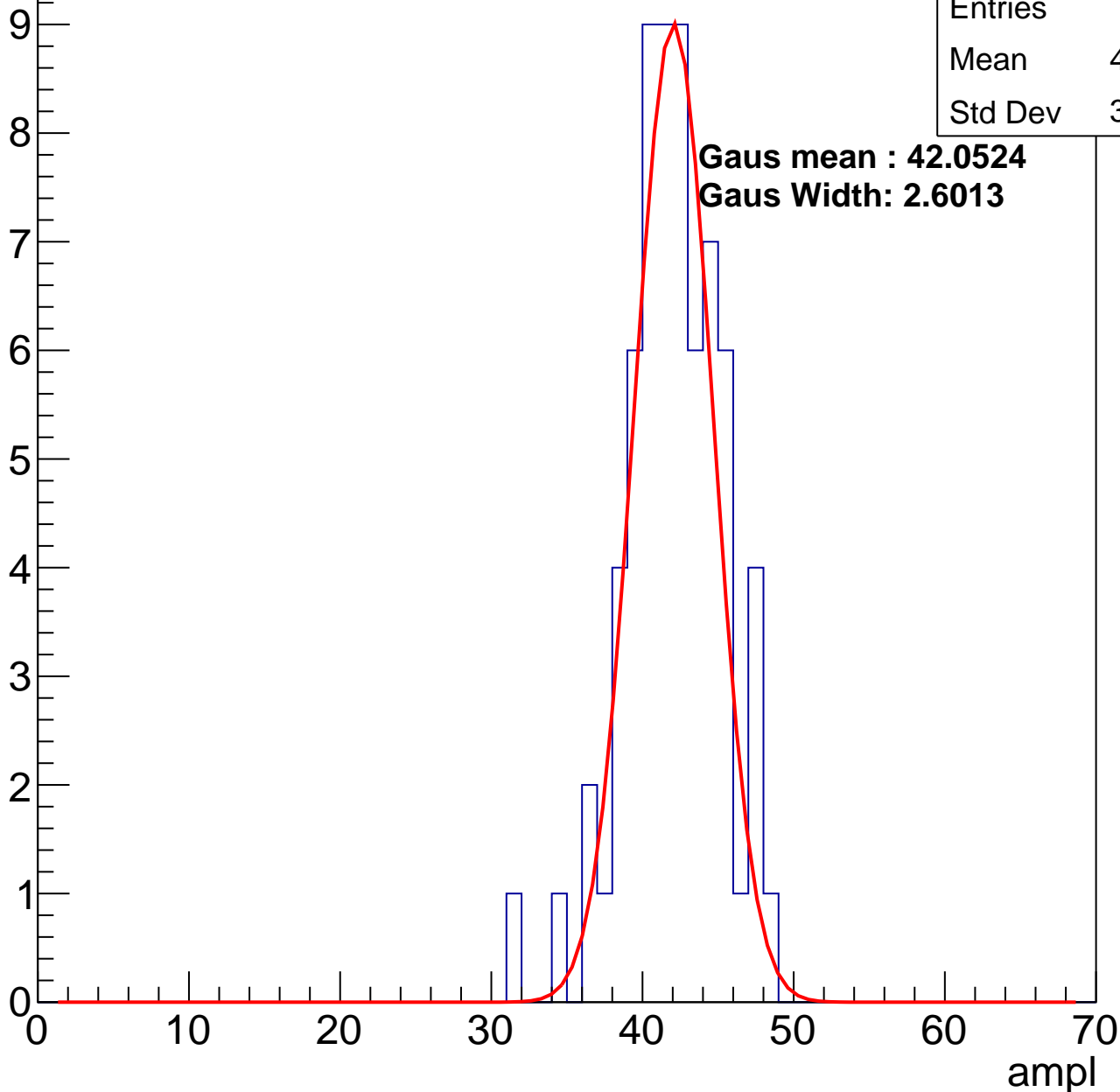
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	41.57
Std Dev	3.168

Gaus mean : 42.0524

Gaus Width: 2.6013

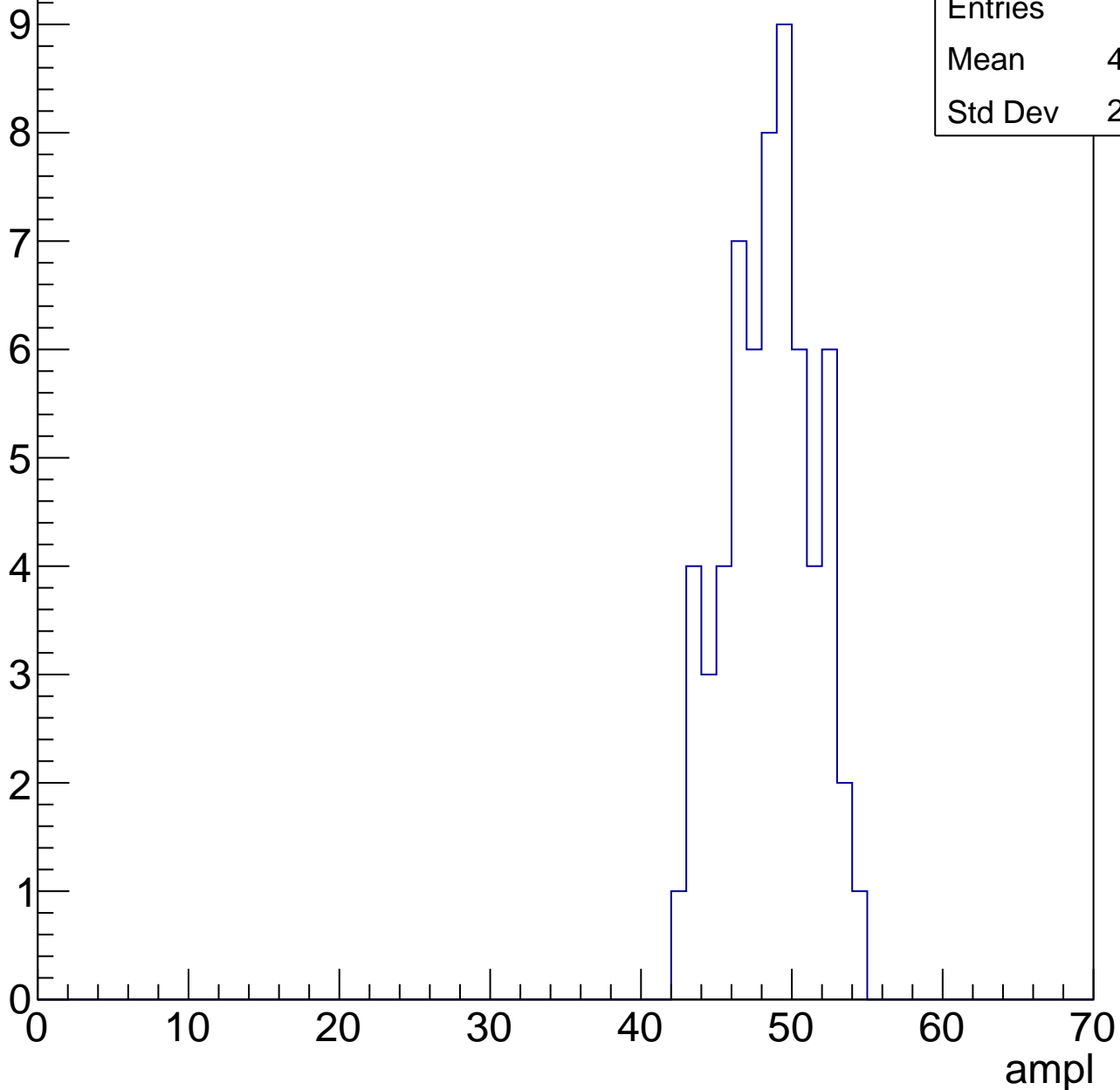


B1L100S, U10-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

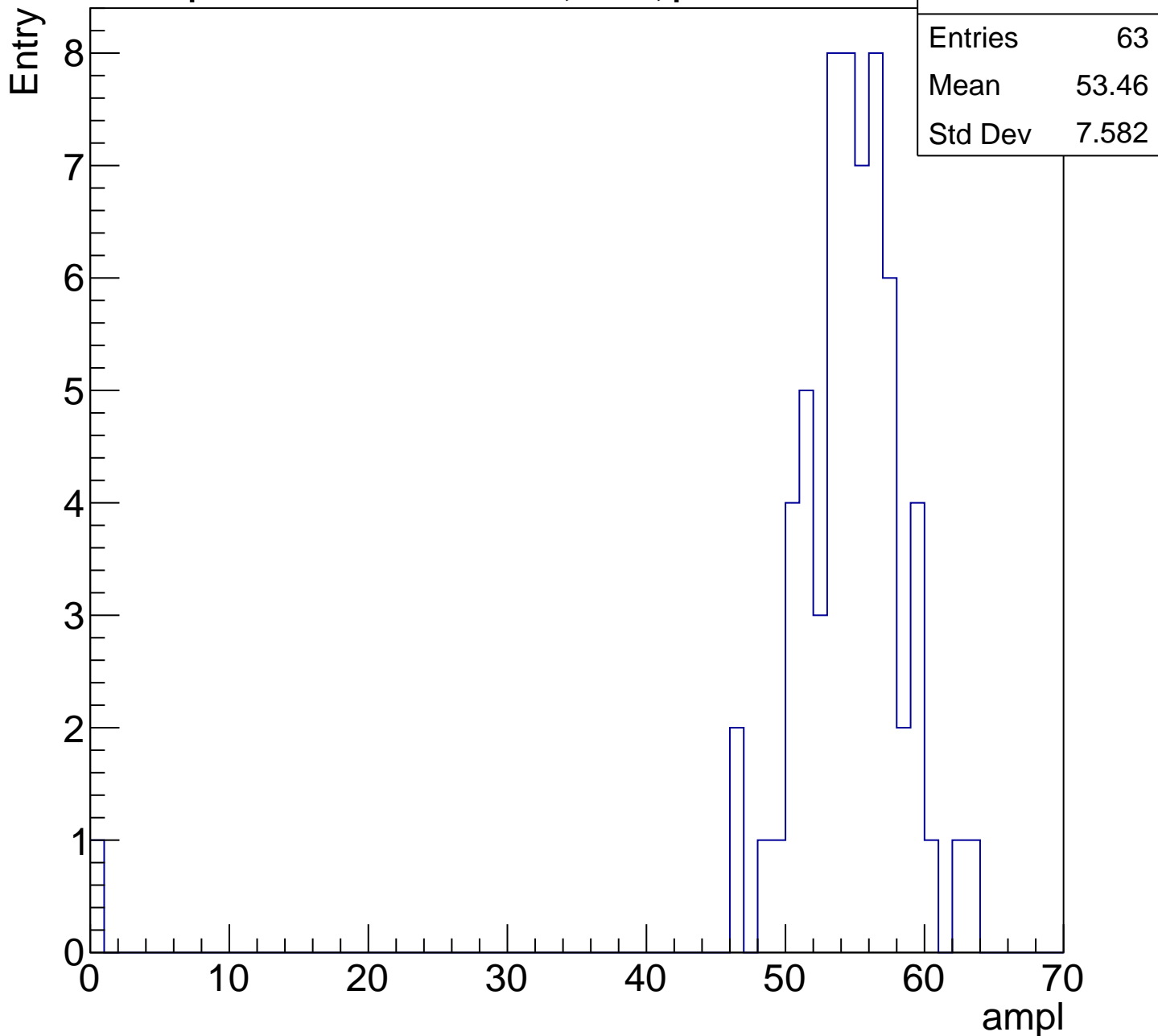
Entry

Entries	61
Mean	48.05
Std Dev	2.877



B1L100S, U10-ch80, adc4

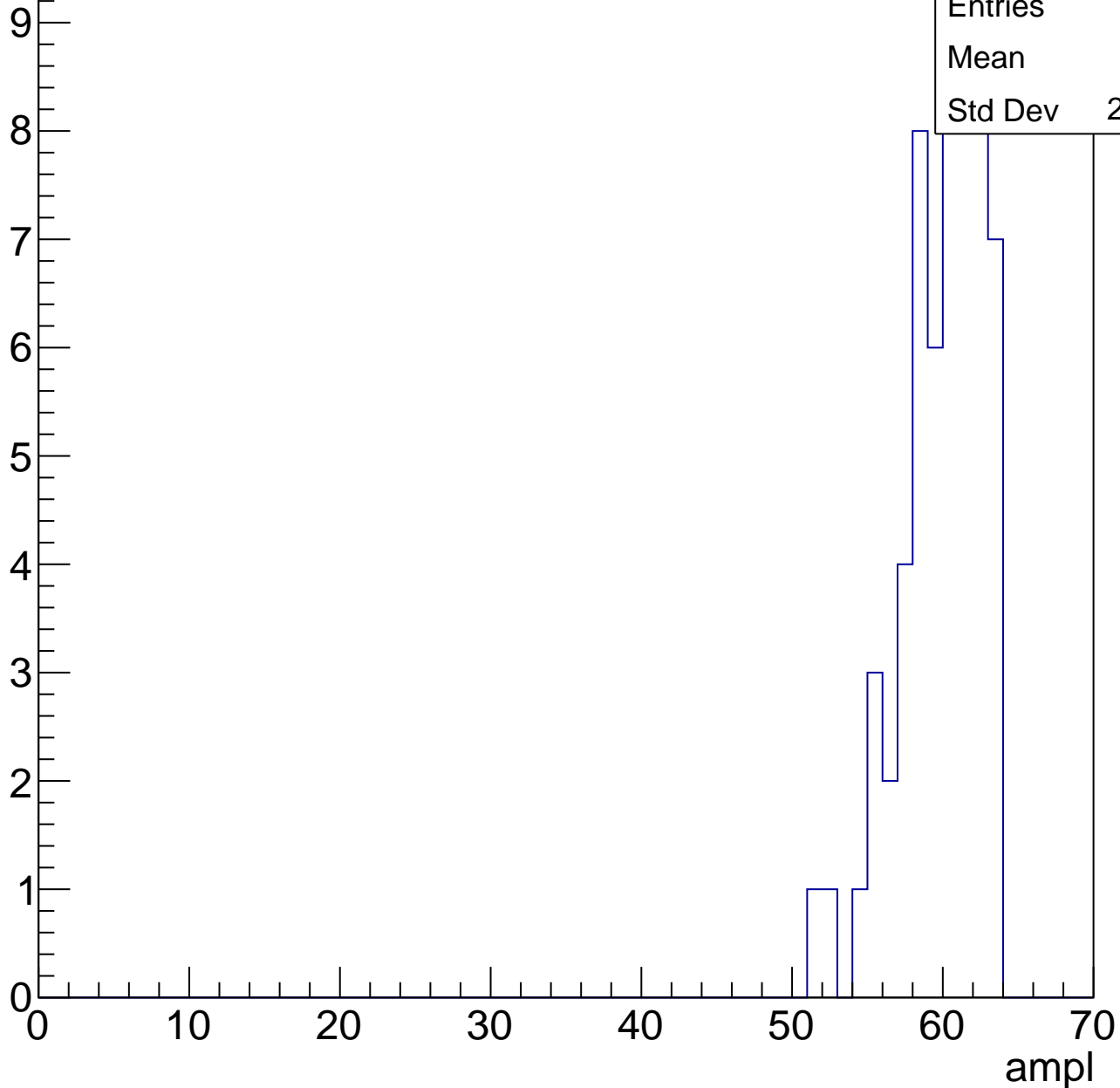
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

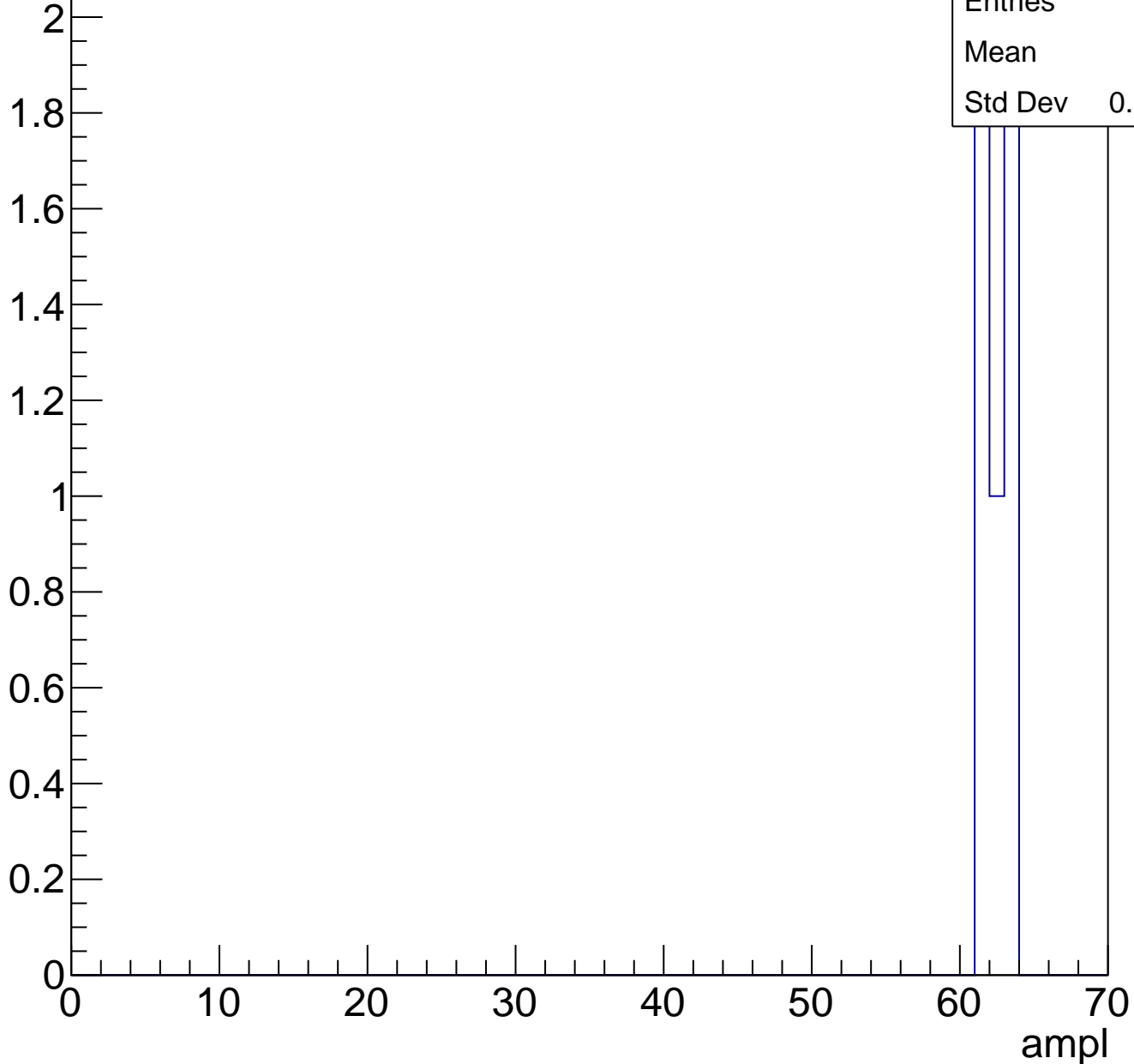


Entries	58
Mean	59.4
Std Dev	2.766

B1L100S, U10-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch81, adc0

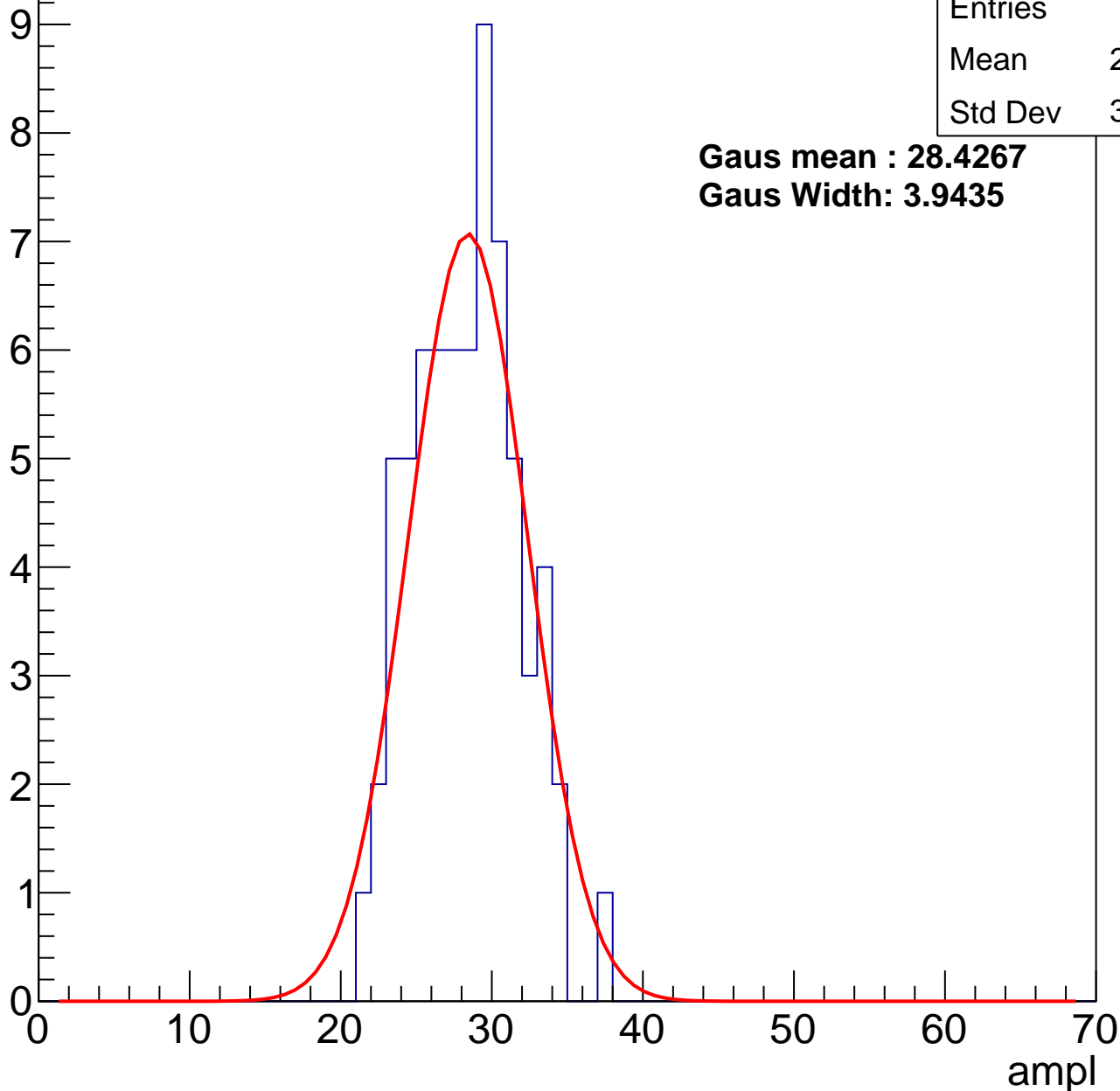
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	27.87
Std Dev	3.412

Gaus mean : 28.4267

Gaus Width: 3.9435



B1L100S, U10-ch81, adc1

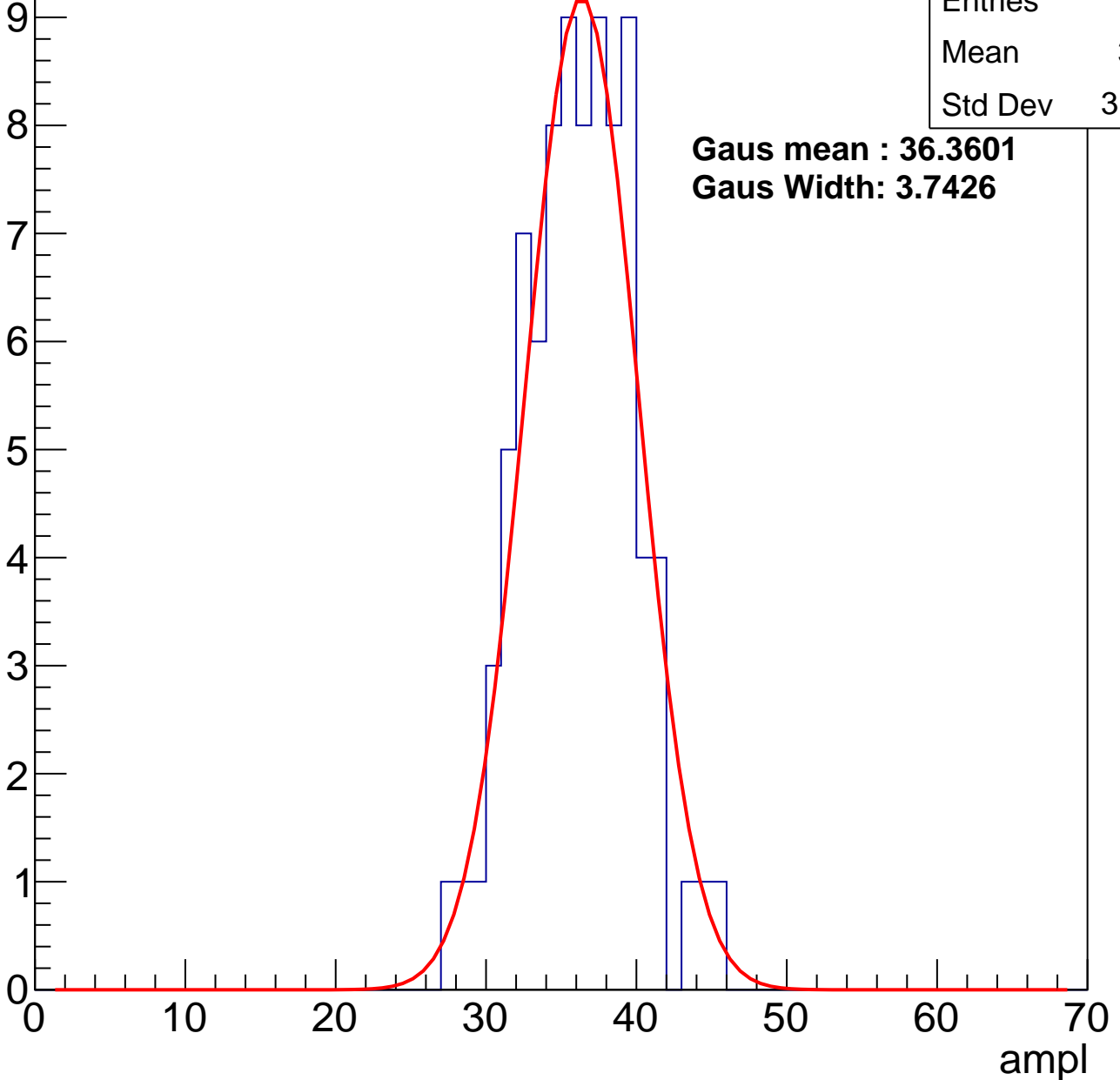
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	35.7
Std Dev	3.586

Gaus mean : 36.3601

Gaus Width: 3.7426



B1L100S, U10-ch81, adc2

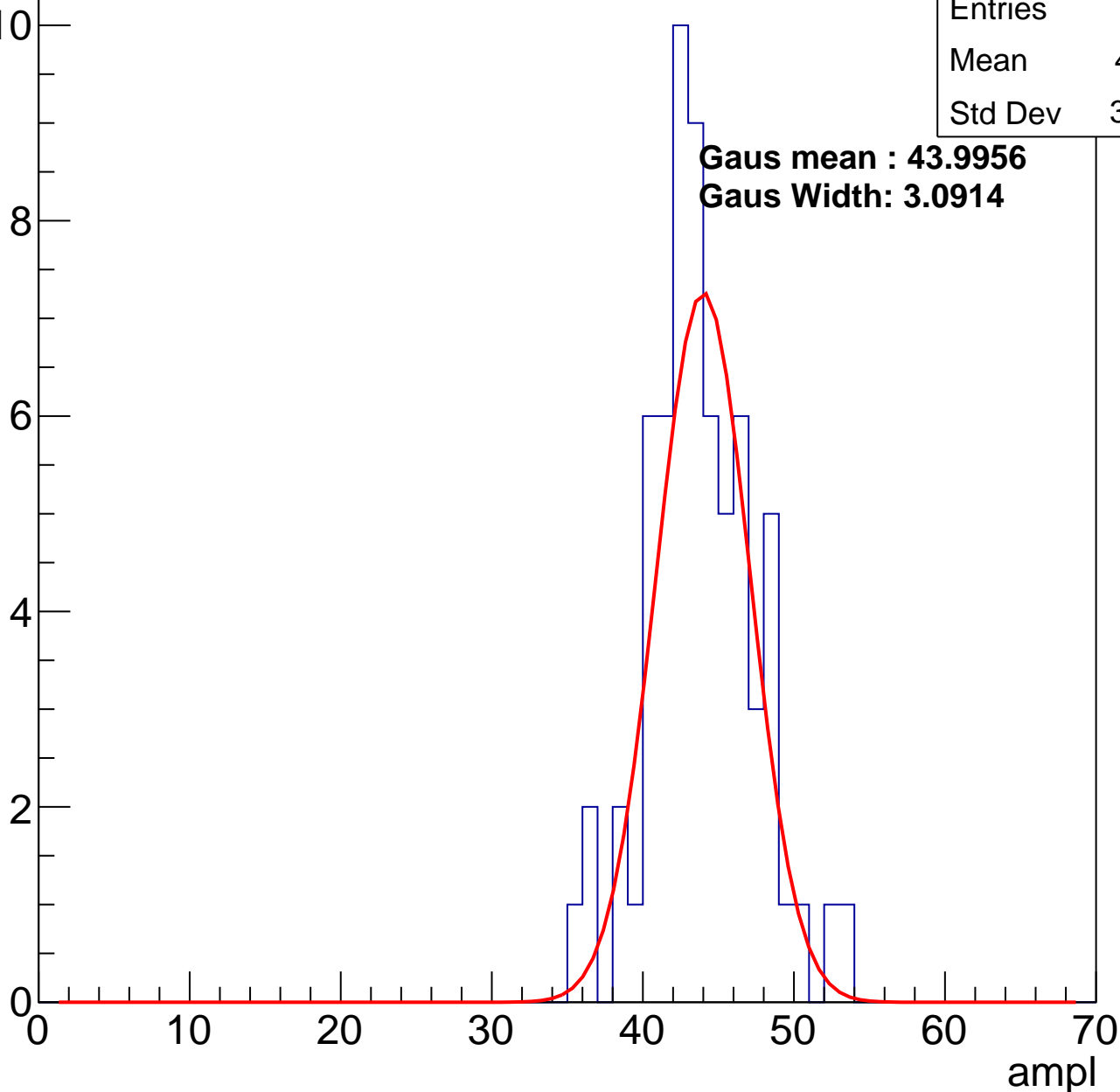
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.41
Std Dev	3.533

Gaus mean : 43.9956

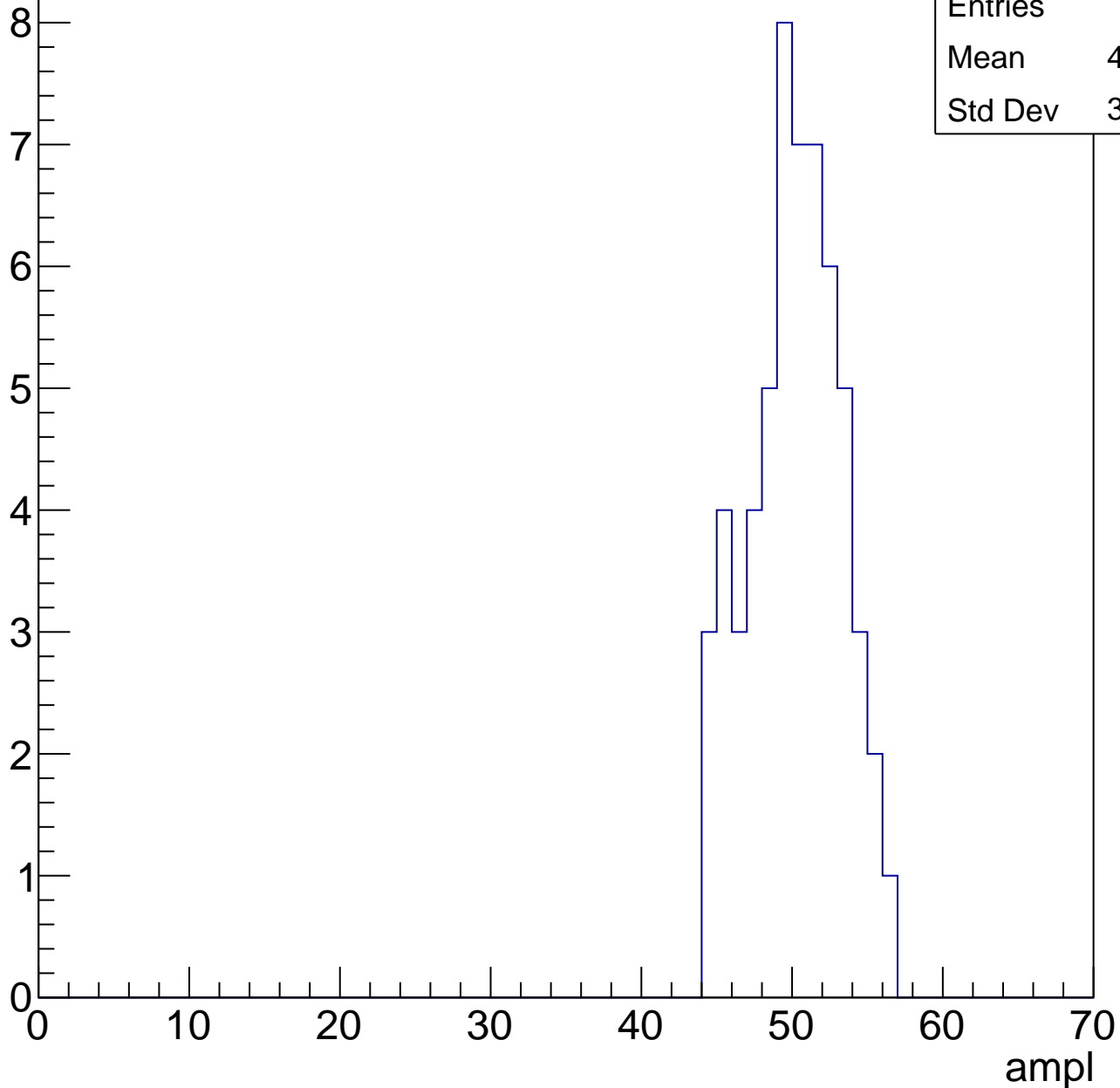
Gaus Width: 3.0914



B1L100S, U10-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	58
Mean	49.69
Std Dev	3.007

B1L100S, U10-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	55.79
Std Dev	3.603

Entry

10

8

6

4

2

0

0

10

20

30

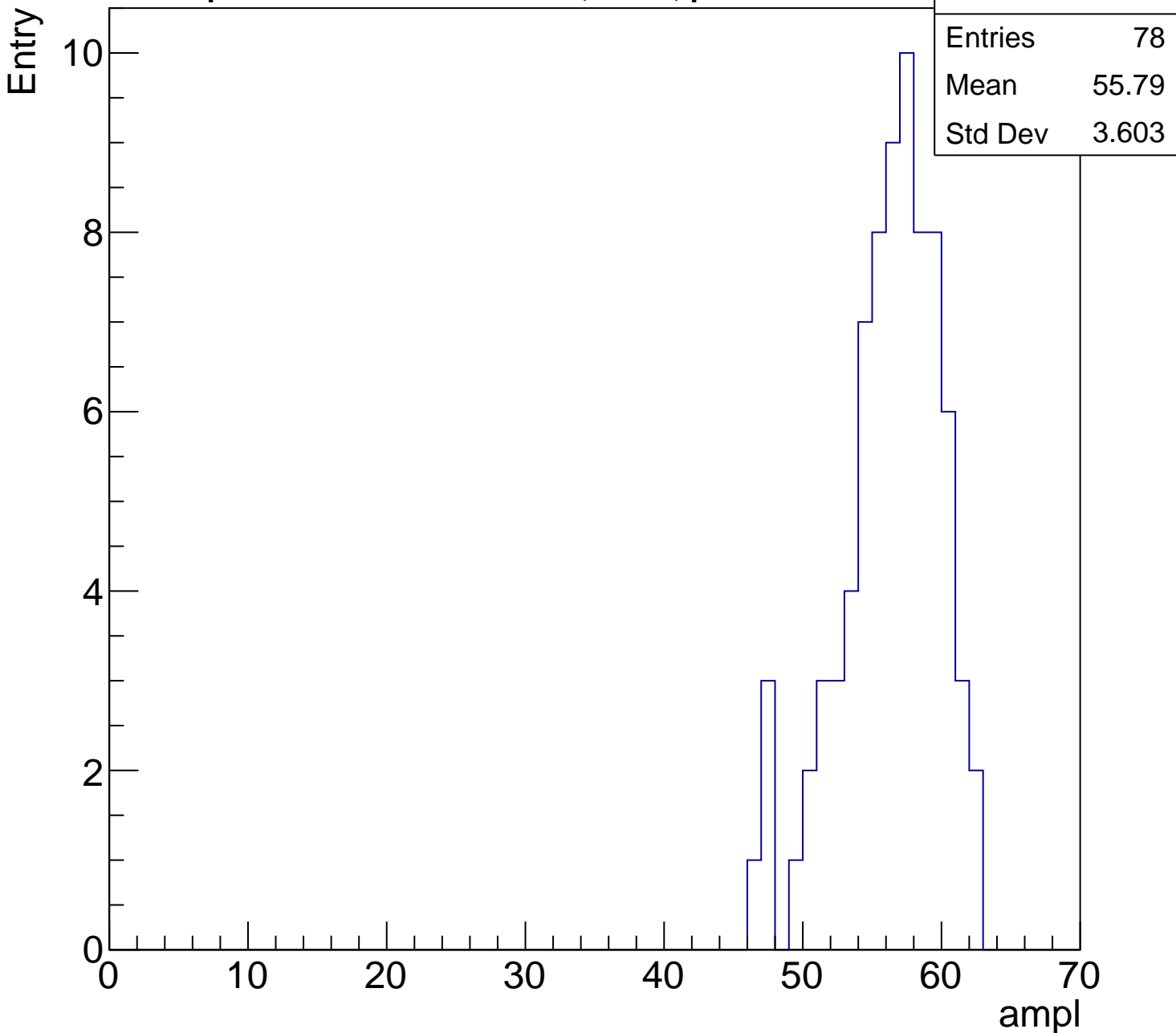
40

50

60

70

ampl

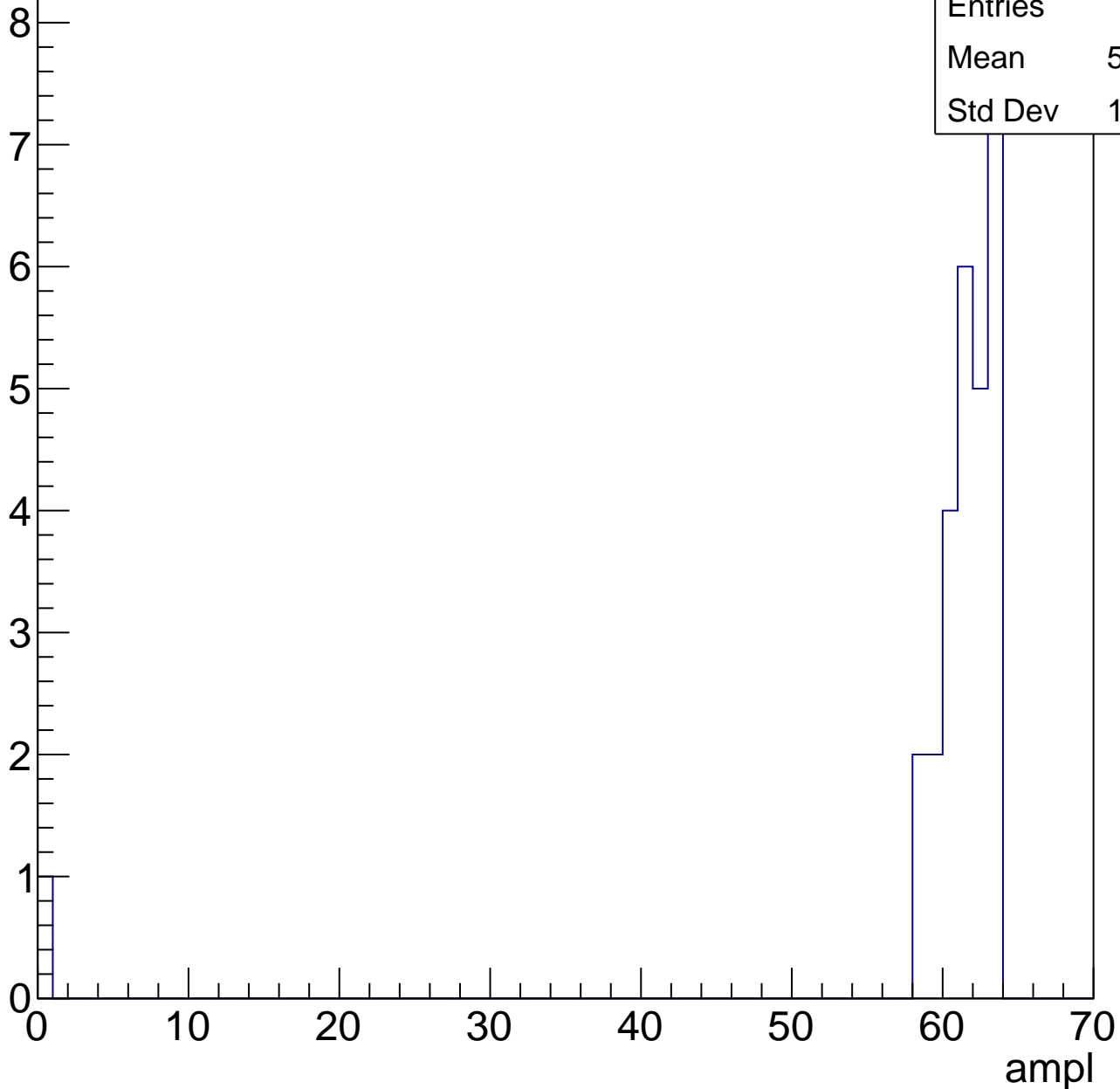


B1L100S, U10-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

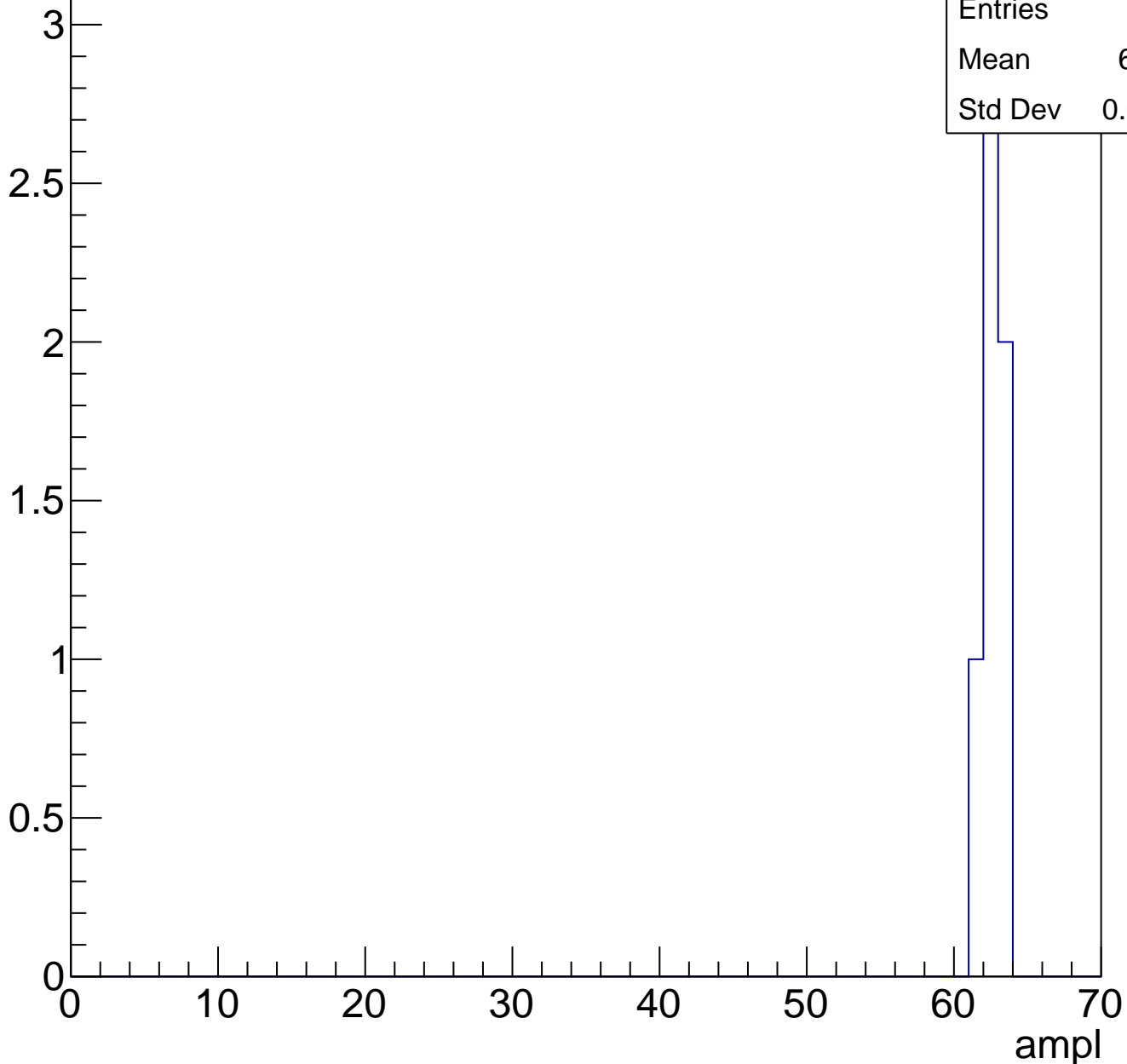
Entries	28
Mean	59.07
Std Dev	11.47



B1L100S, U10-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch82, adc0

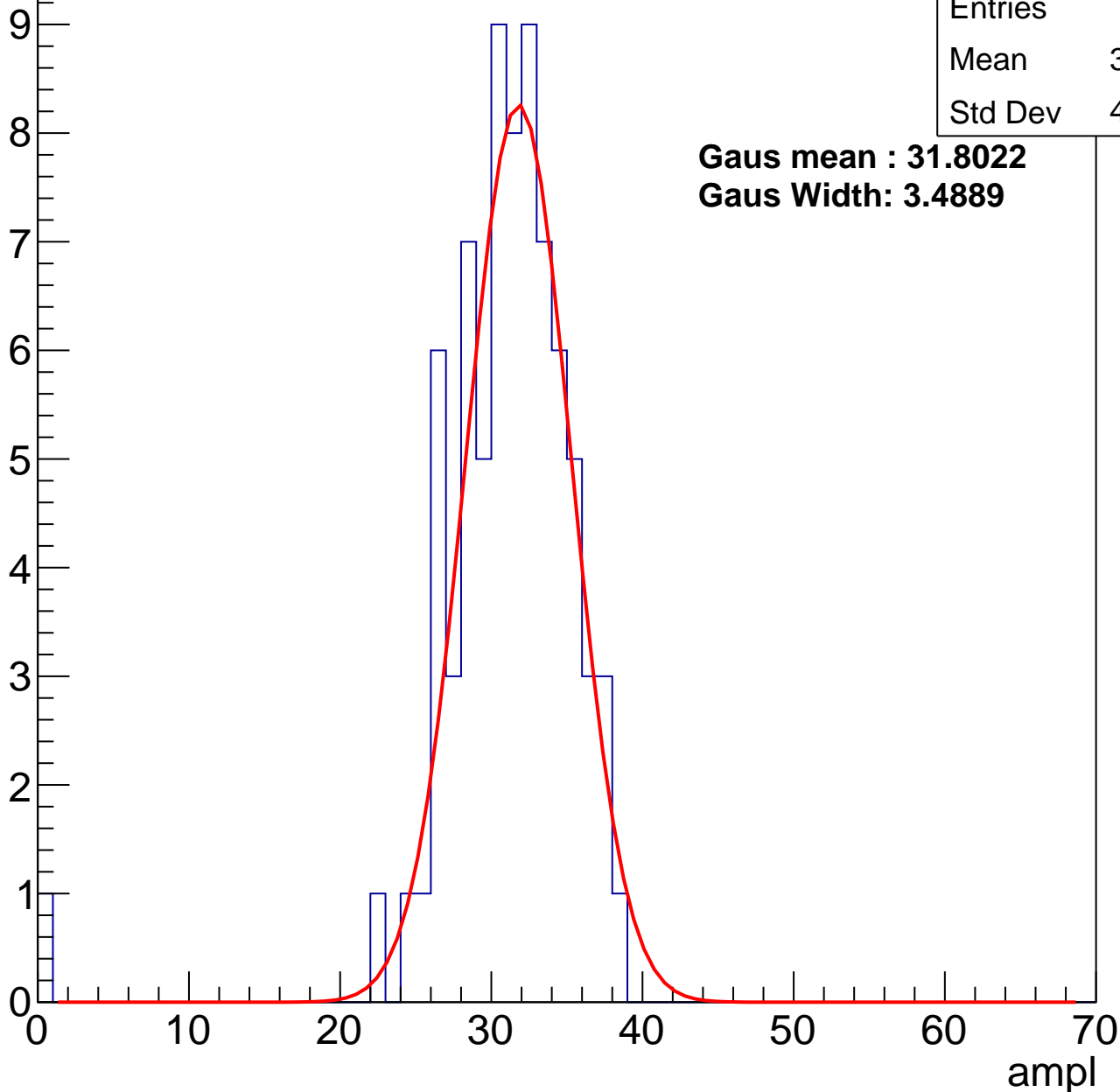
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.55
Std Dev	4.873

Gaus mean : 31.8022

Gaus Width: 3.4889



B1L100S, U10-ch82, adc1

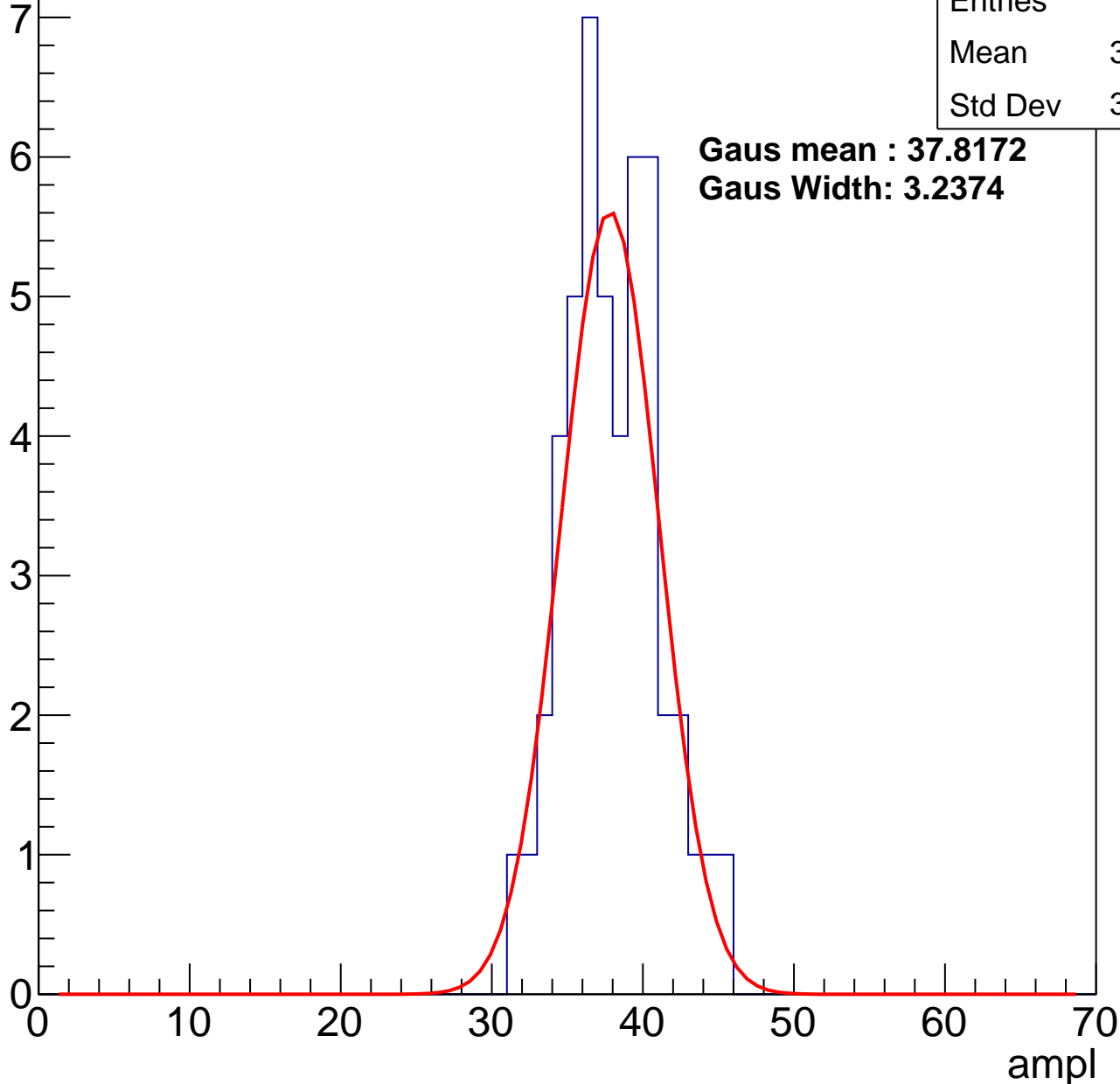
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	37.52
Std Dev	3.082

Gaus mean : 37.8172

Gaus Width: 3.2374



B1L100S, U10-ch82, adc2

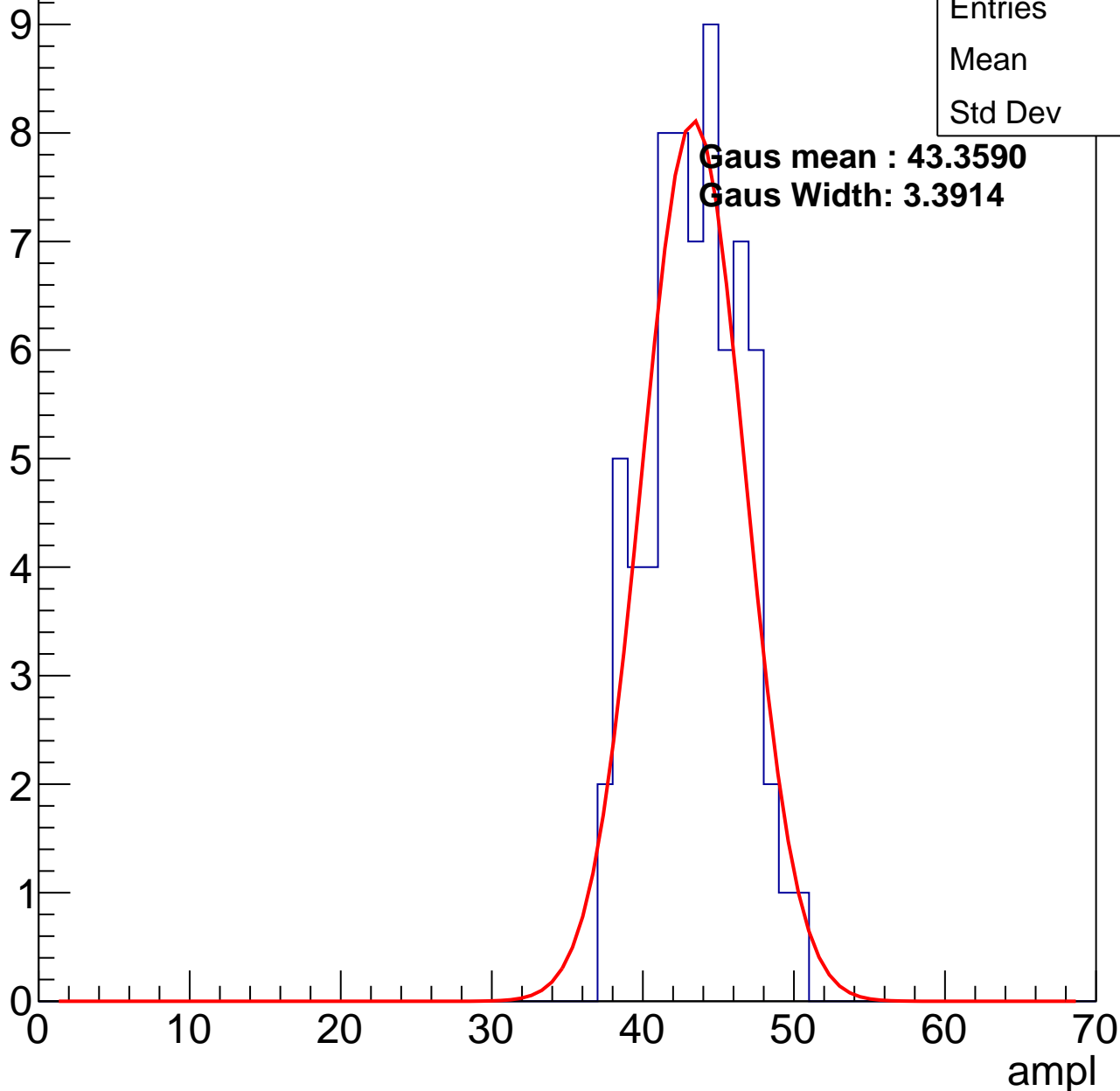
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43
Std Dev	3.08

Gaus mean : 43.3590

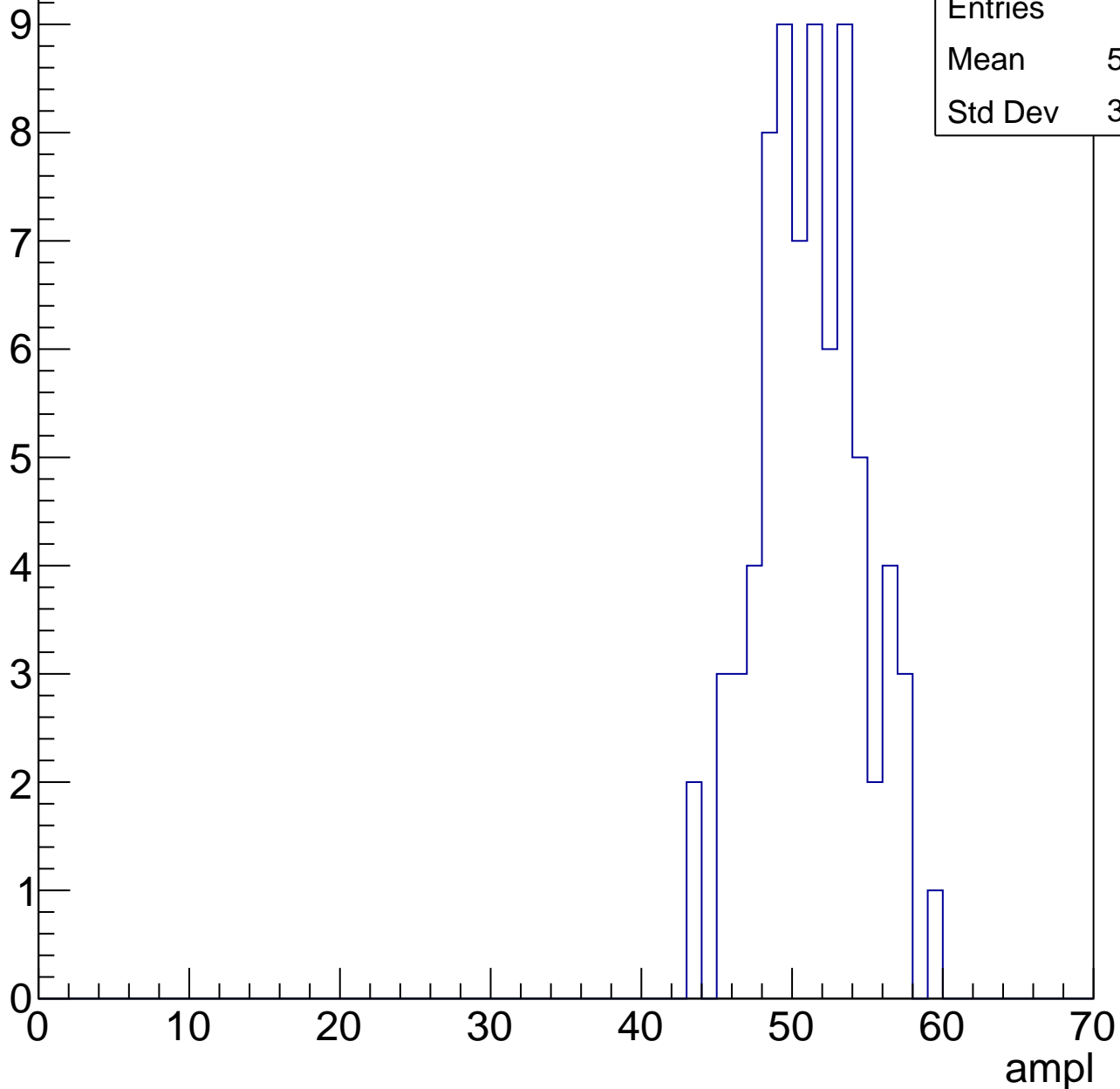
Gaus Width: 3.3914



B1L100S, U10-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



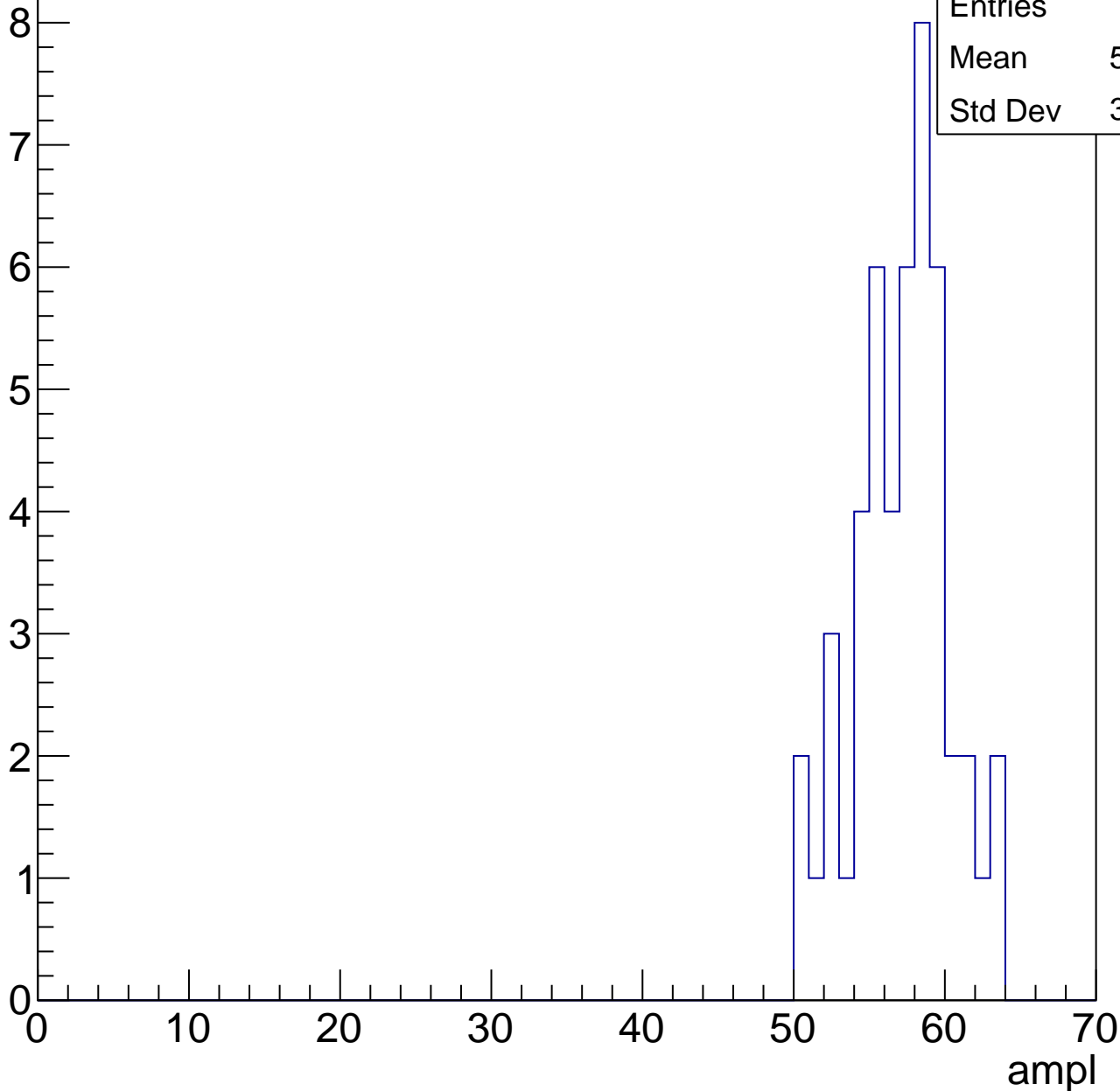
Entries	75
Mean	50.72
Std Dev	3.416

B1L100S, U10-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	56.67
Std Dev	3.105

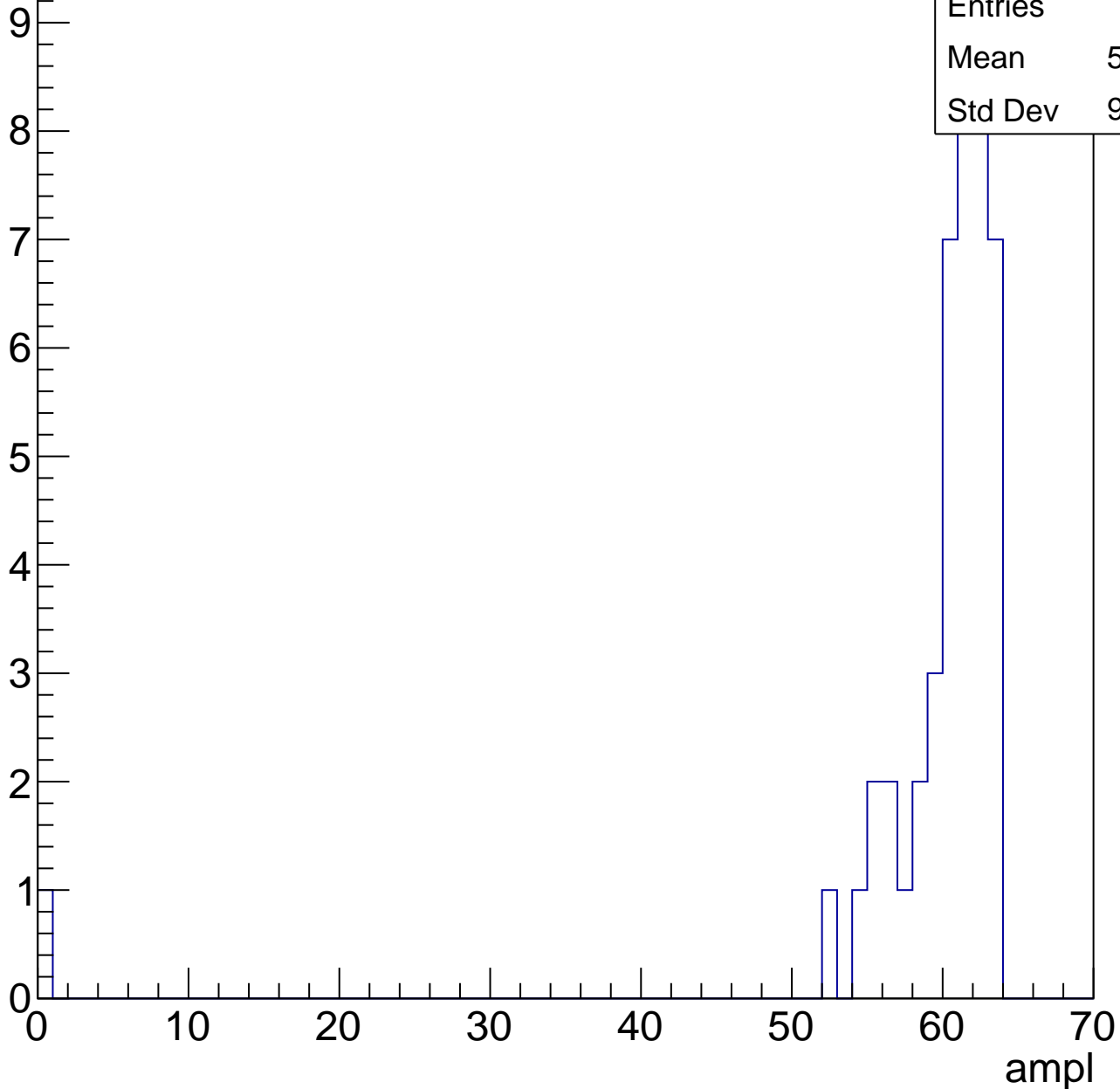


B1L100S, U10-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.75
Std Dev	9.342



B1L100S, U10-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch83, adc0

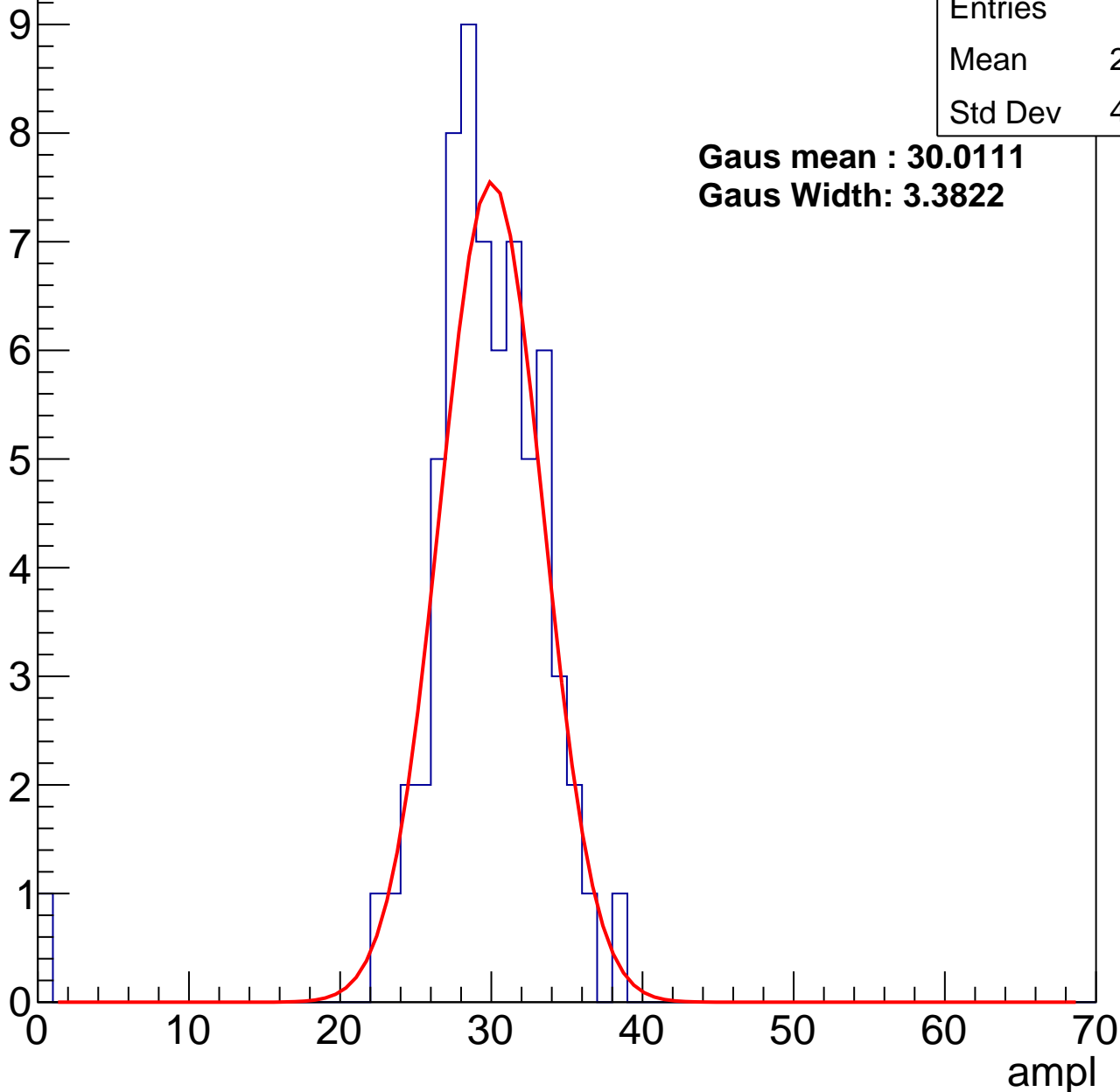
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.03
Std Dev	4.804

Gaus mean : 30.0111

Gaus Width: 3.3822



B1L100S, U10-ch83, adc1

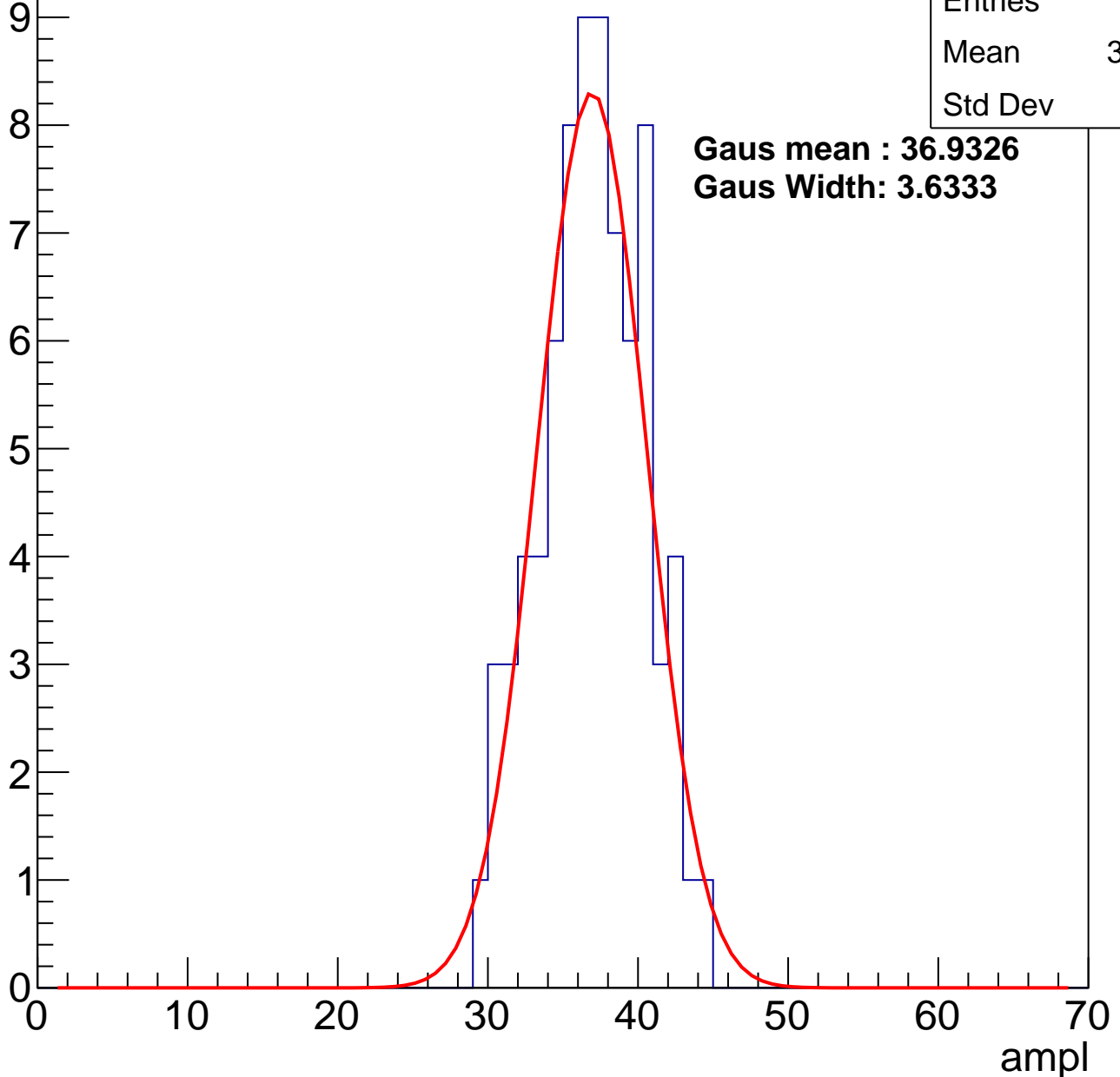
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	36.51
Std Dev	3.41

Gaus mean : 36.9326

Gaus Width: 3.6333



B1L100S, U10-ch83, adc2

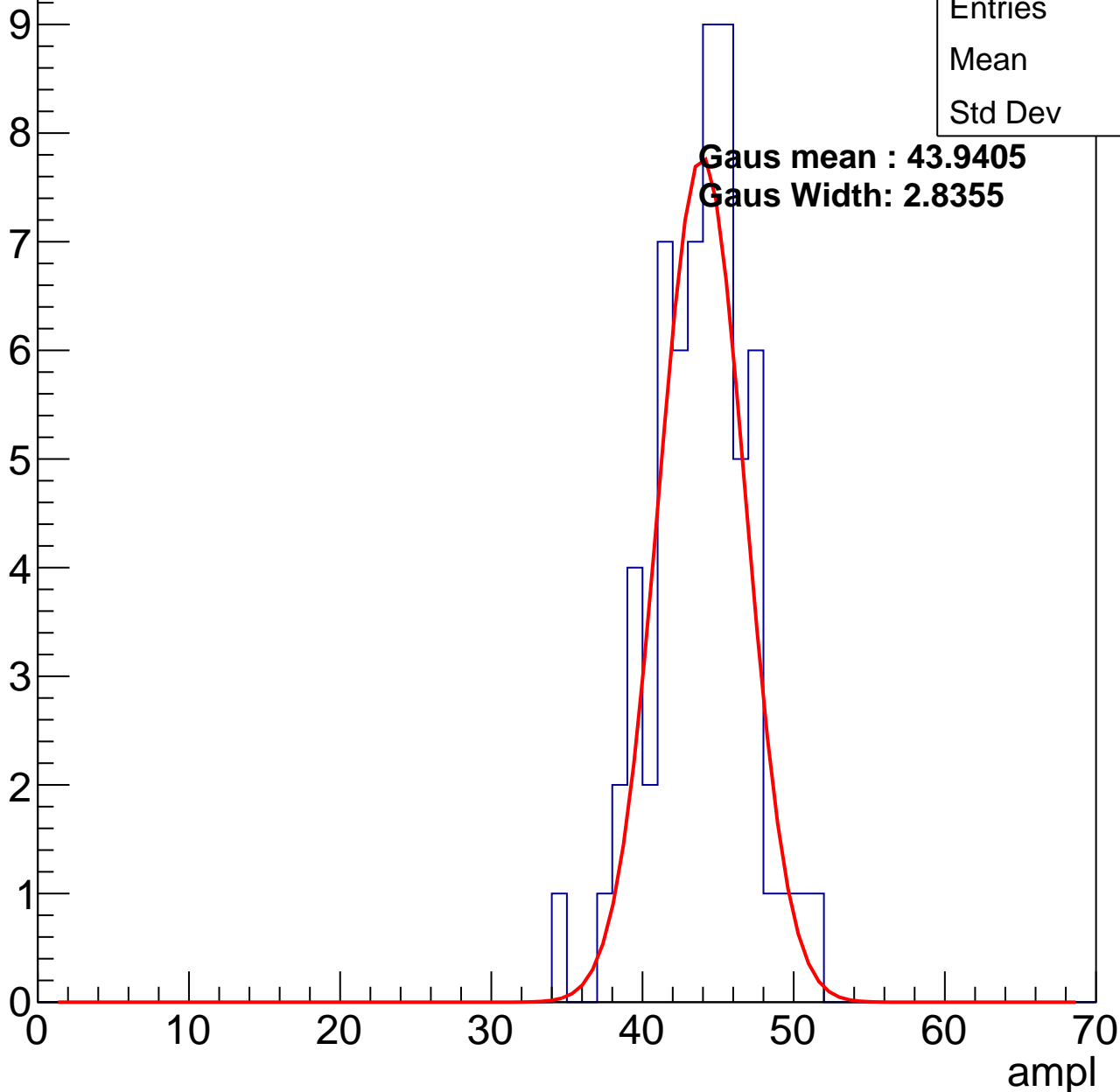
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.4
Std Dev	3.17

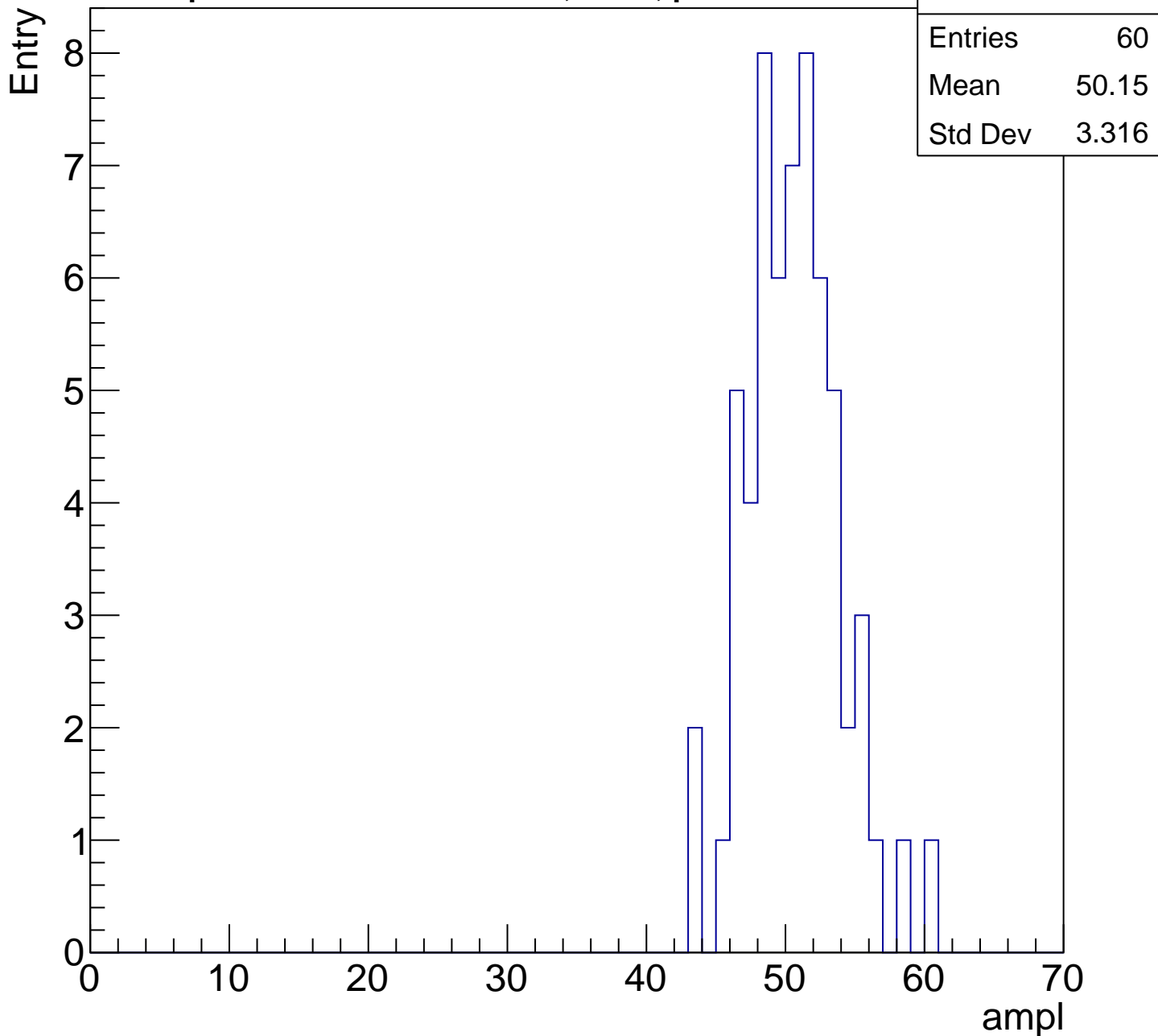
Gaus mean : 43.9405

Gaus Width: 2.8355



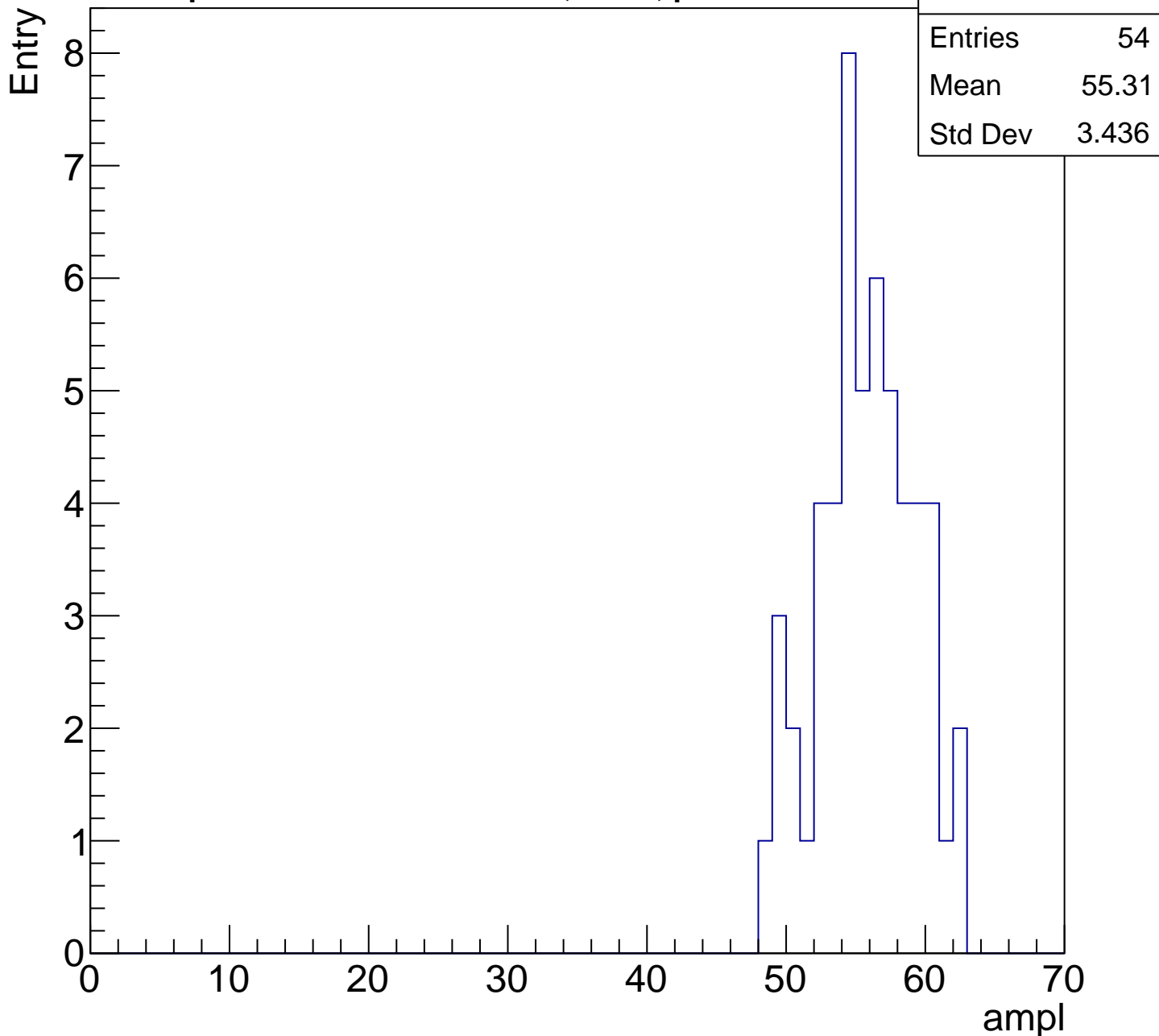
B1L100S, U10-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

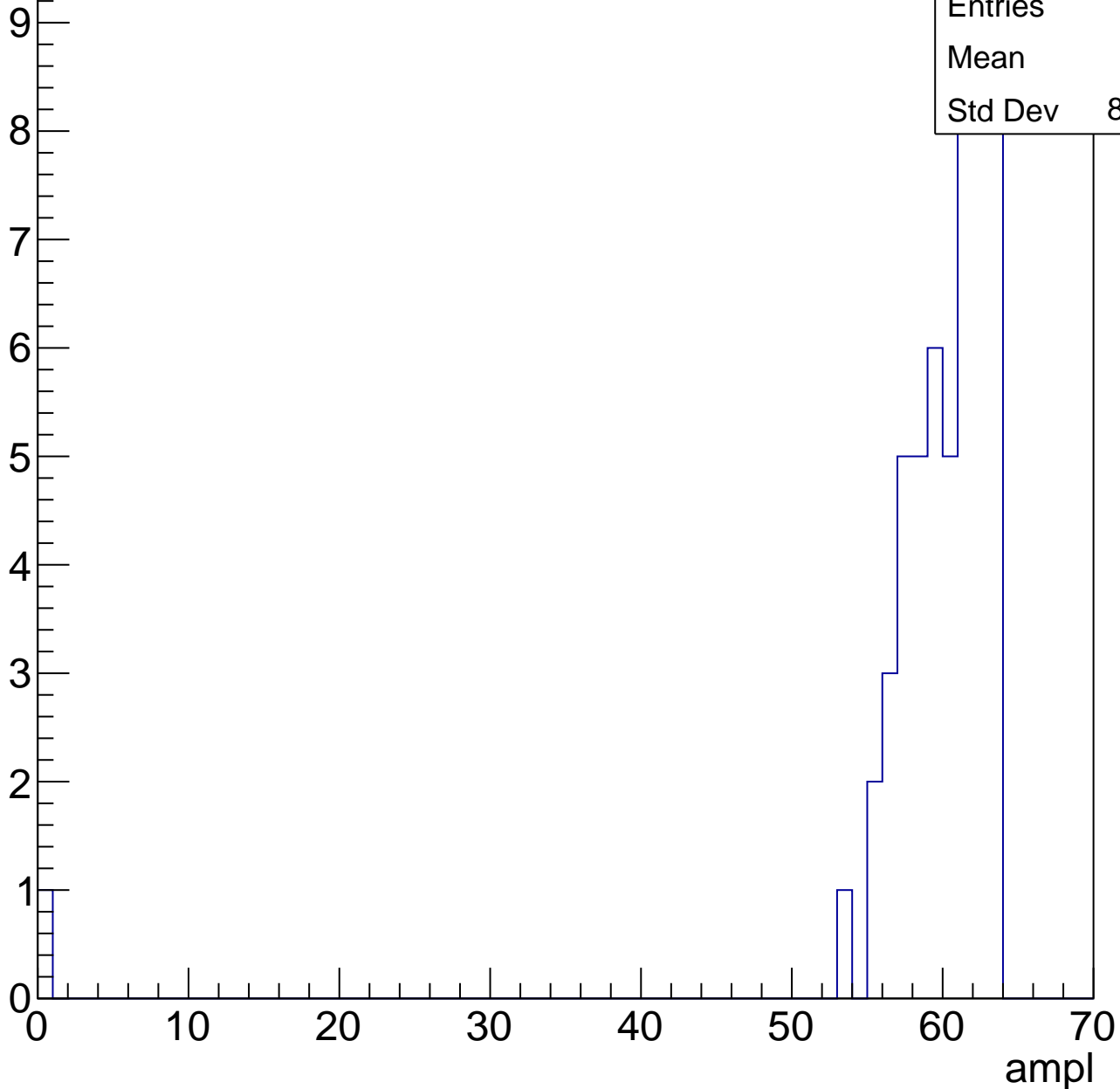


B1L100S, U10-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.7
Std Dev	8.522



B1L100S, U10-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch84, adc0

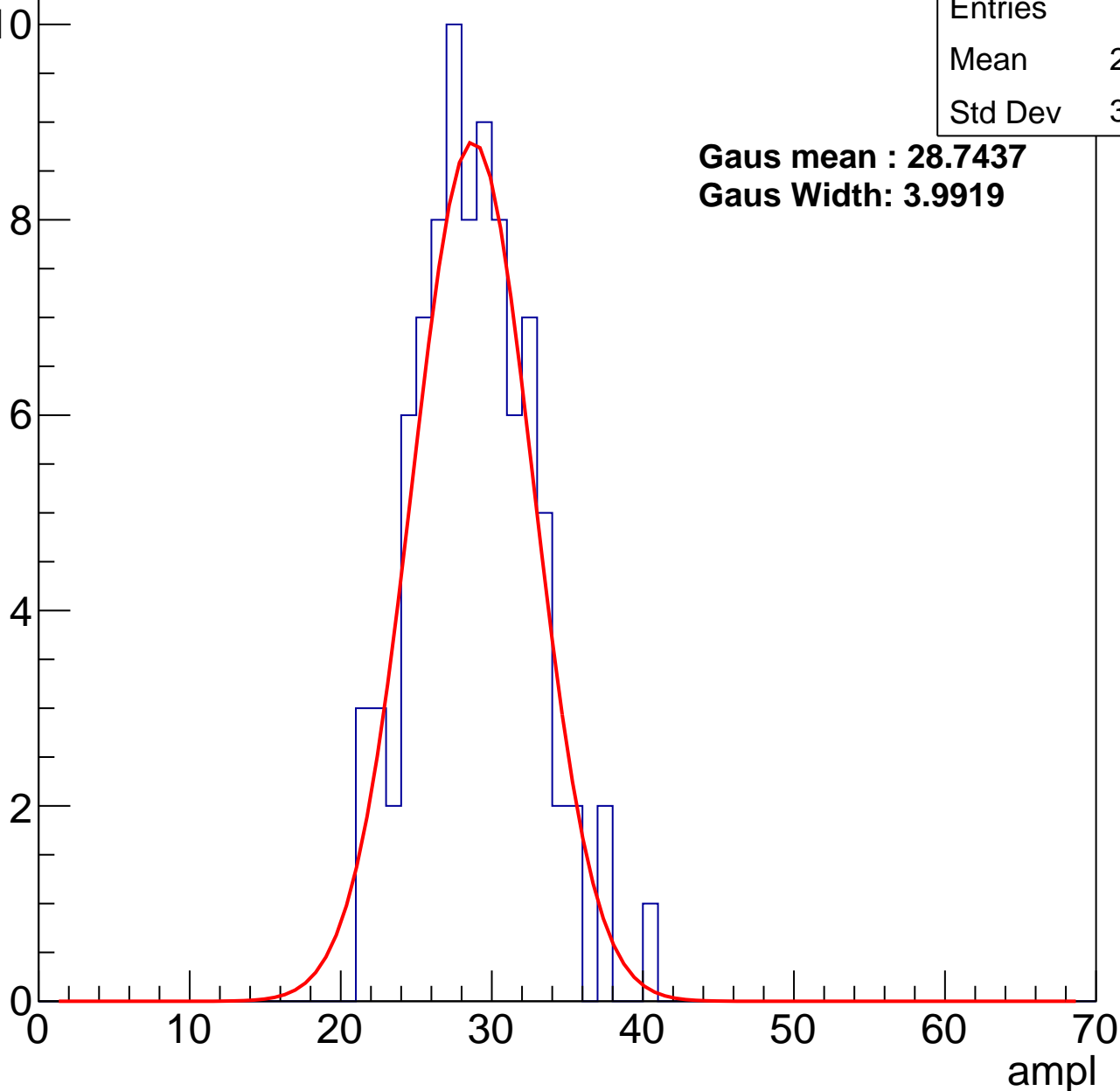
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	28.36
Std Dev	3.826

Gaus mean : 28.7437

Gaus Width: 3.9919



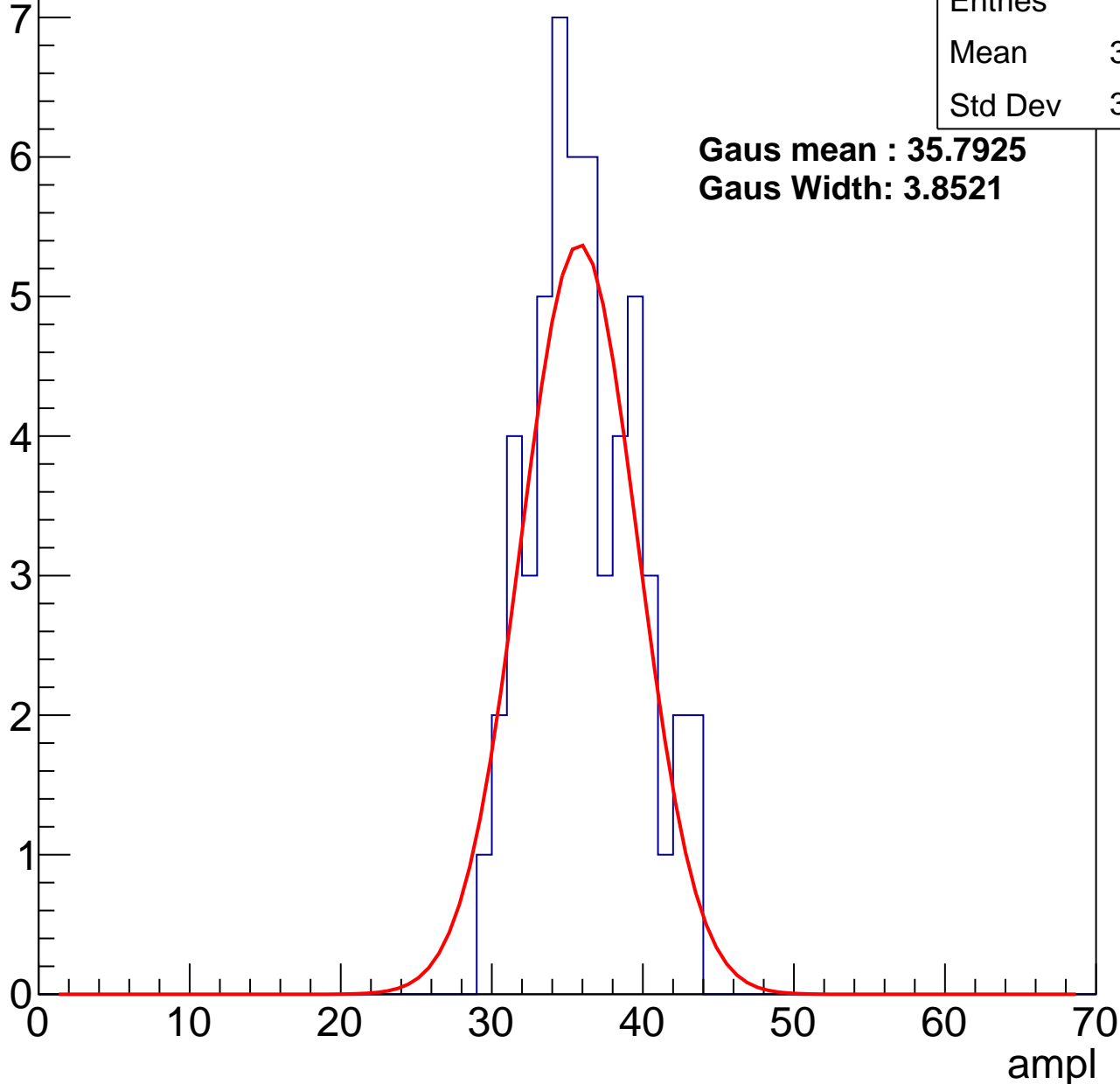
B1L100S, U10-ch84, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	35.69
Std Dev	3.469

Gaus mean : 35.7925
Gaus Width: 3.8521



B1L100S, U10-ch84, adc2

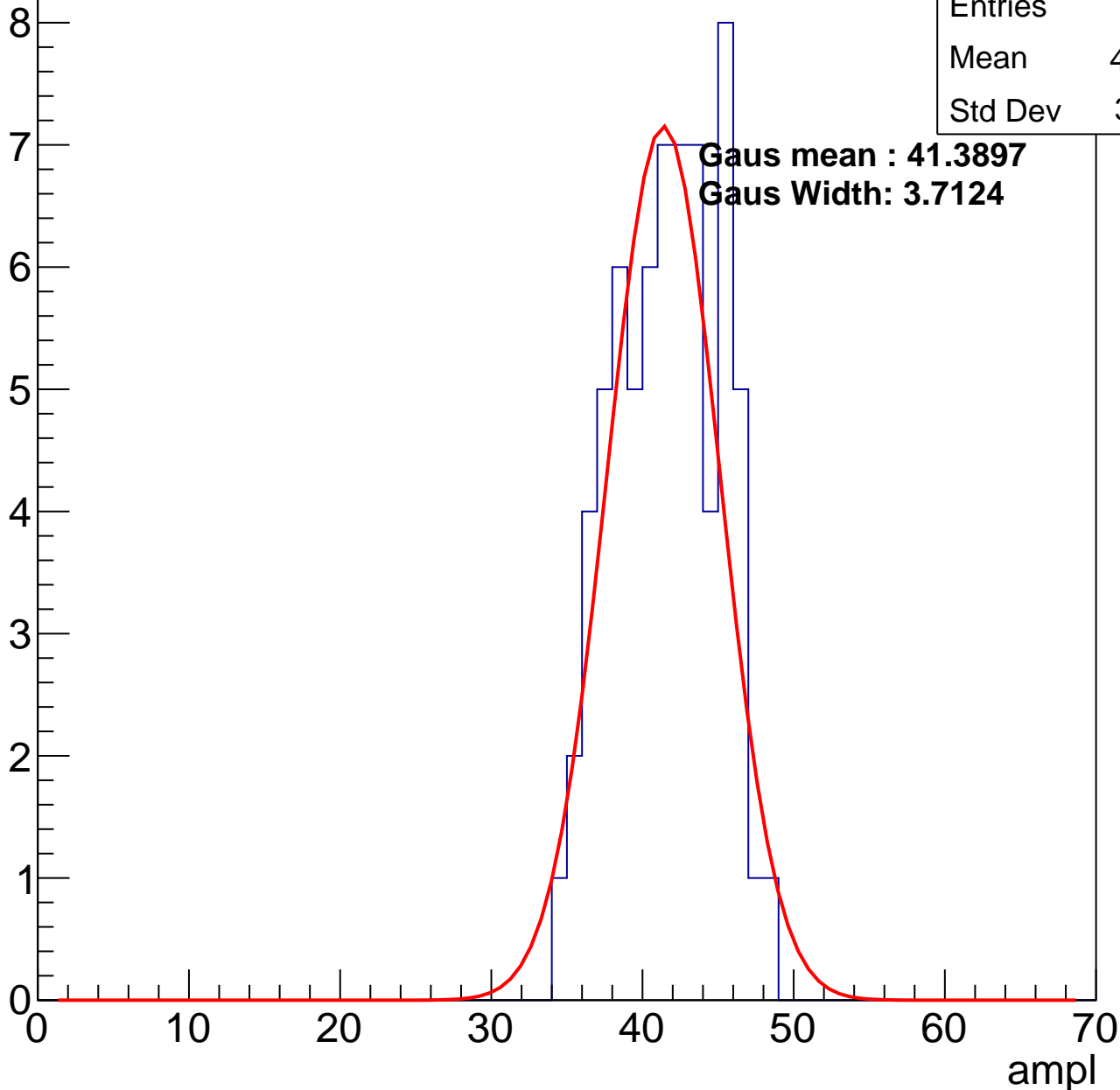
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.14
Std Dev	3.381

Gaus mean : 41.3897

Gaus Width: 3.7124

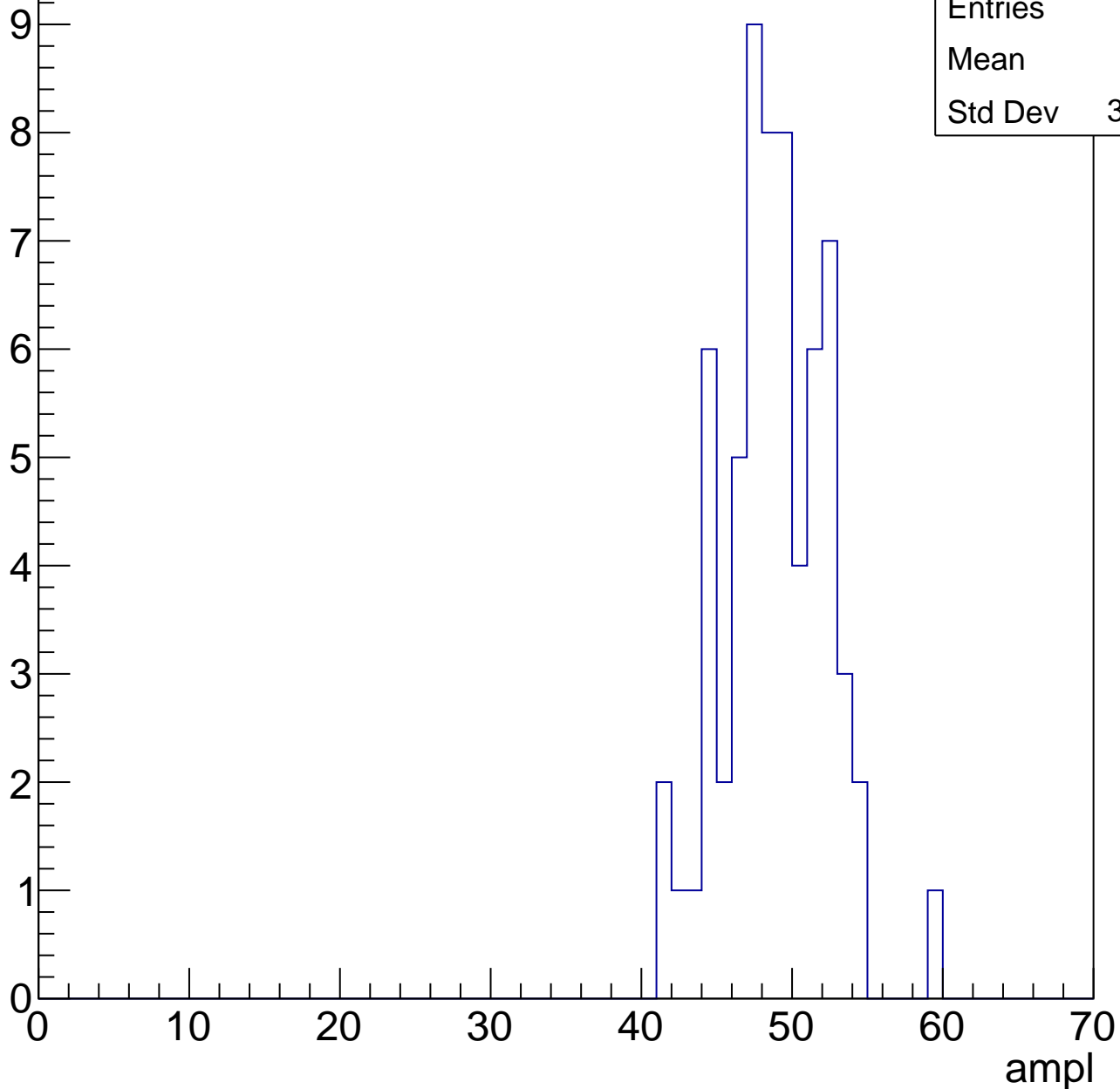


B1L100S, U10-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	48.4
Std Dev	3.396

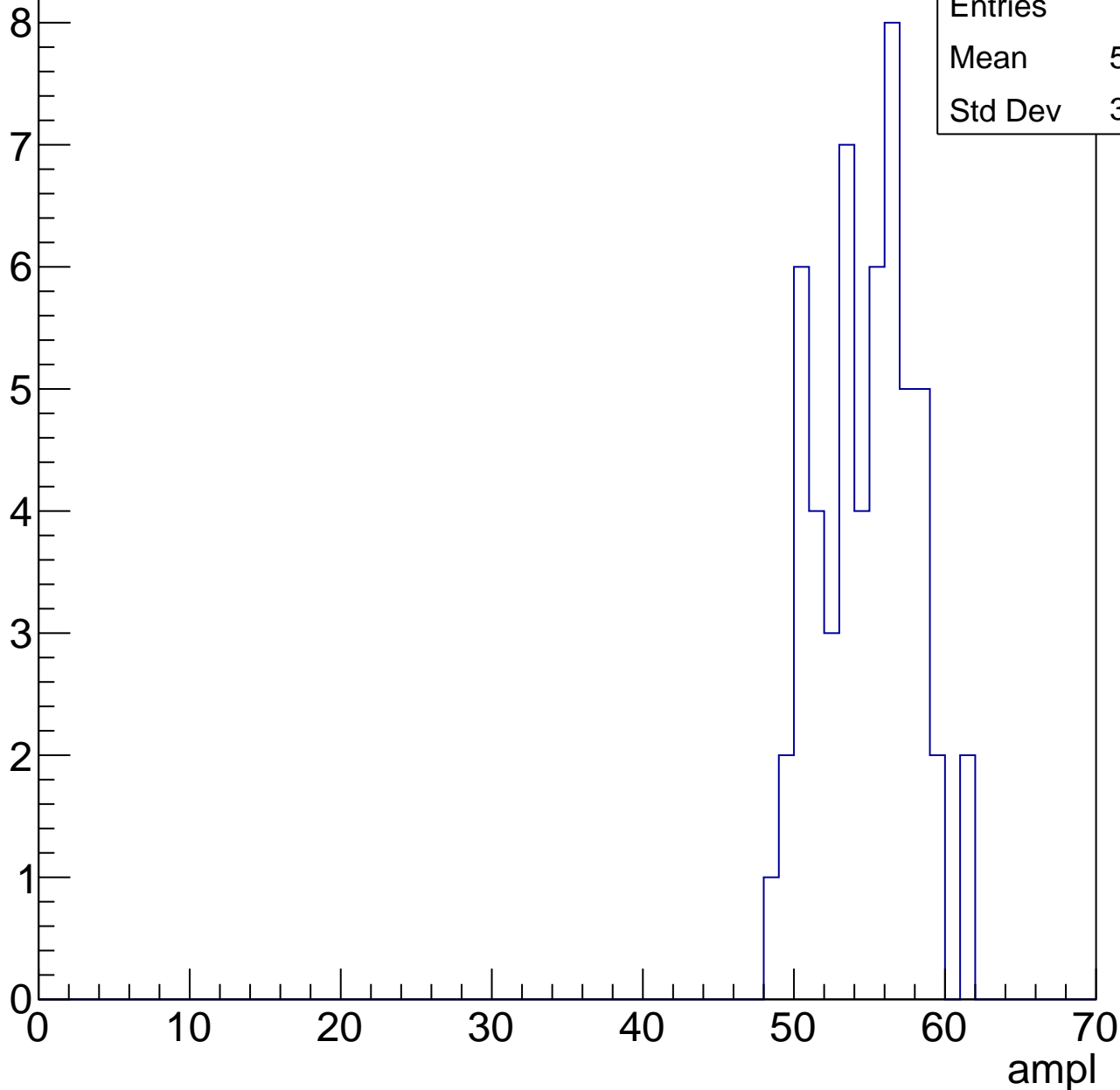


B1L100S, U10-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

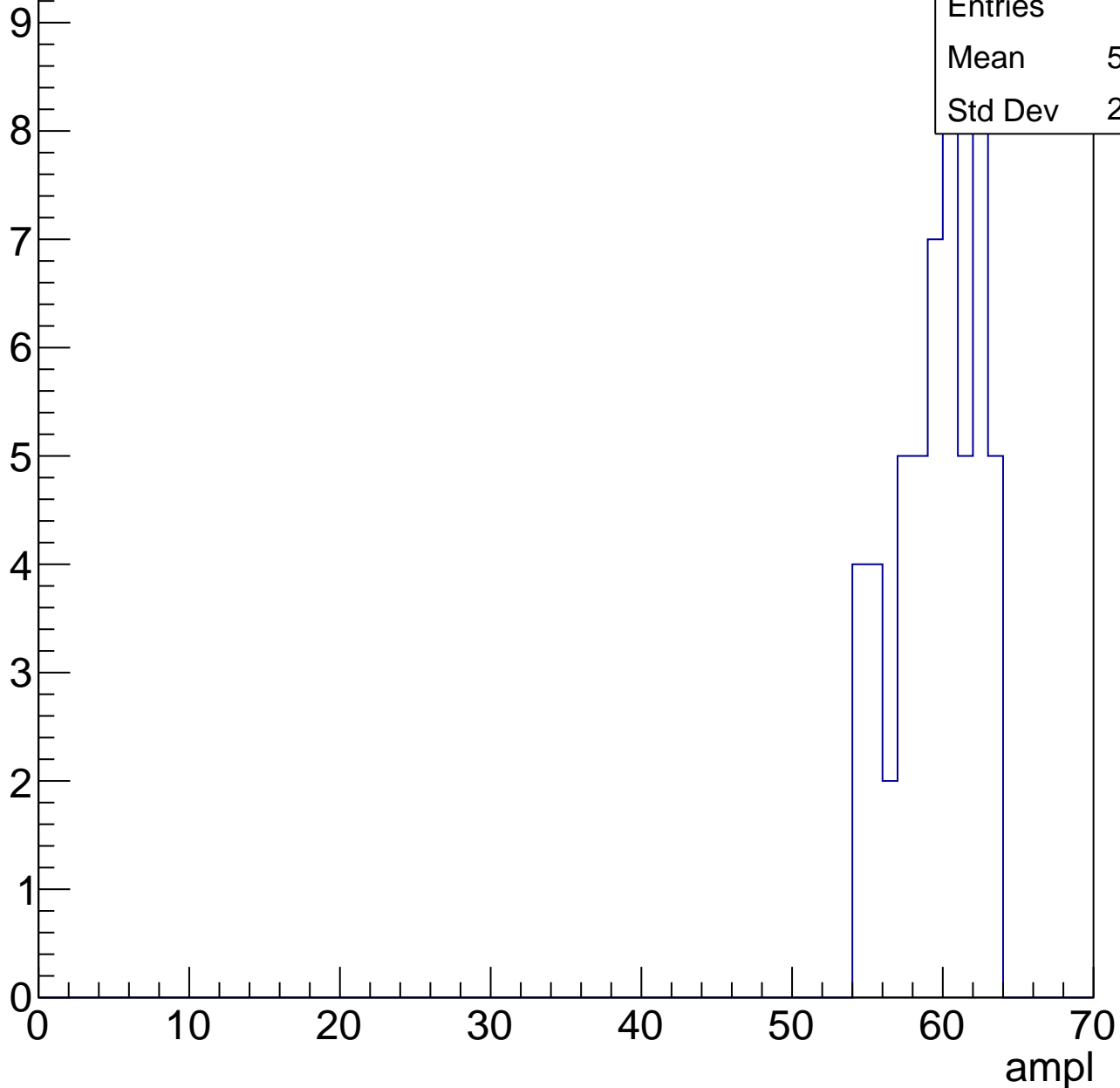
Entries	55
Mean	54.29
Std Dev	3.143



B1L100S, U10-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



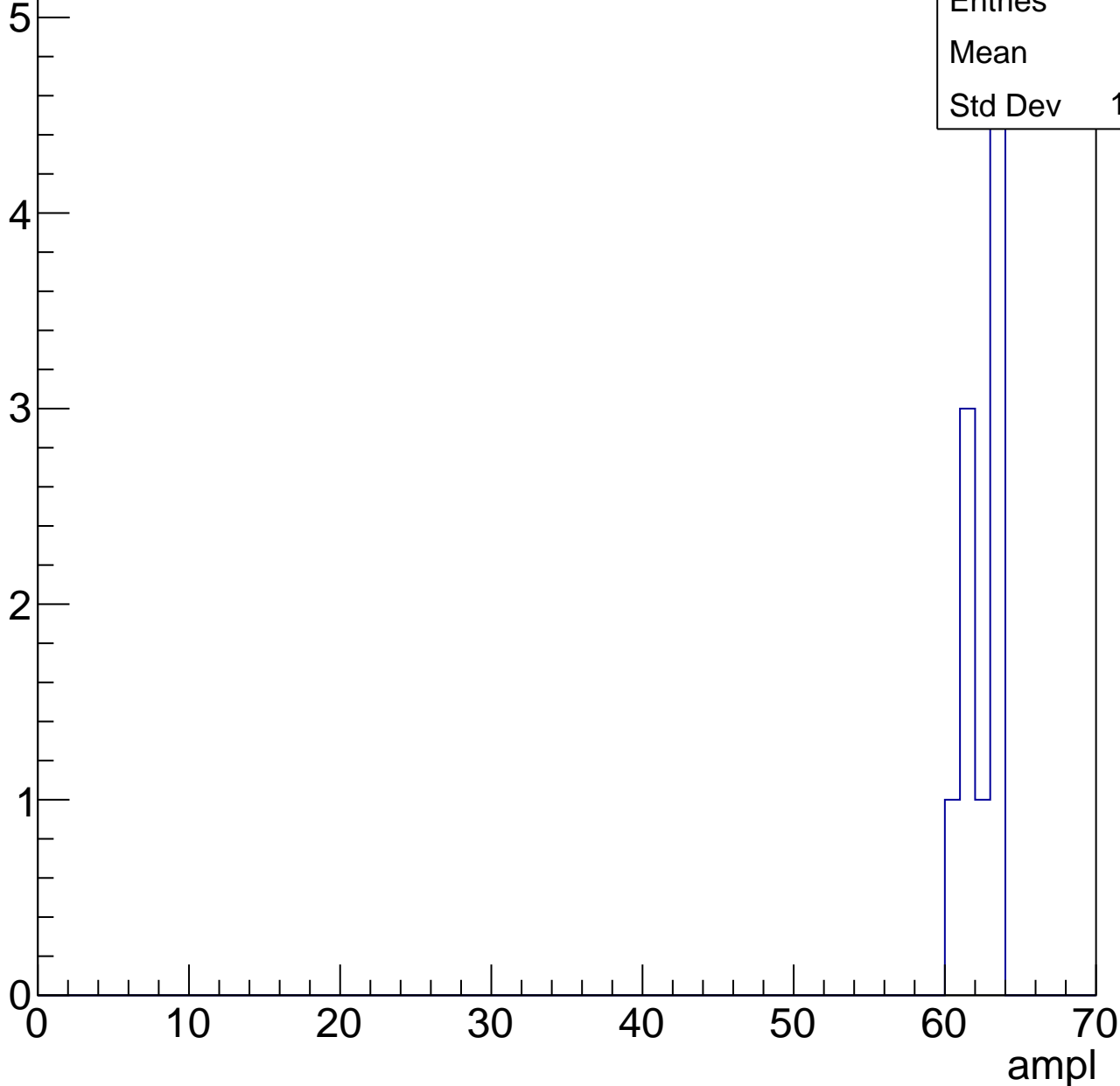
Entries	55
Mean	59.16
Std Dev	2.682

B1L100S, U10-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

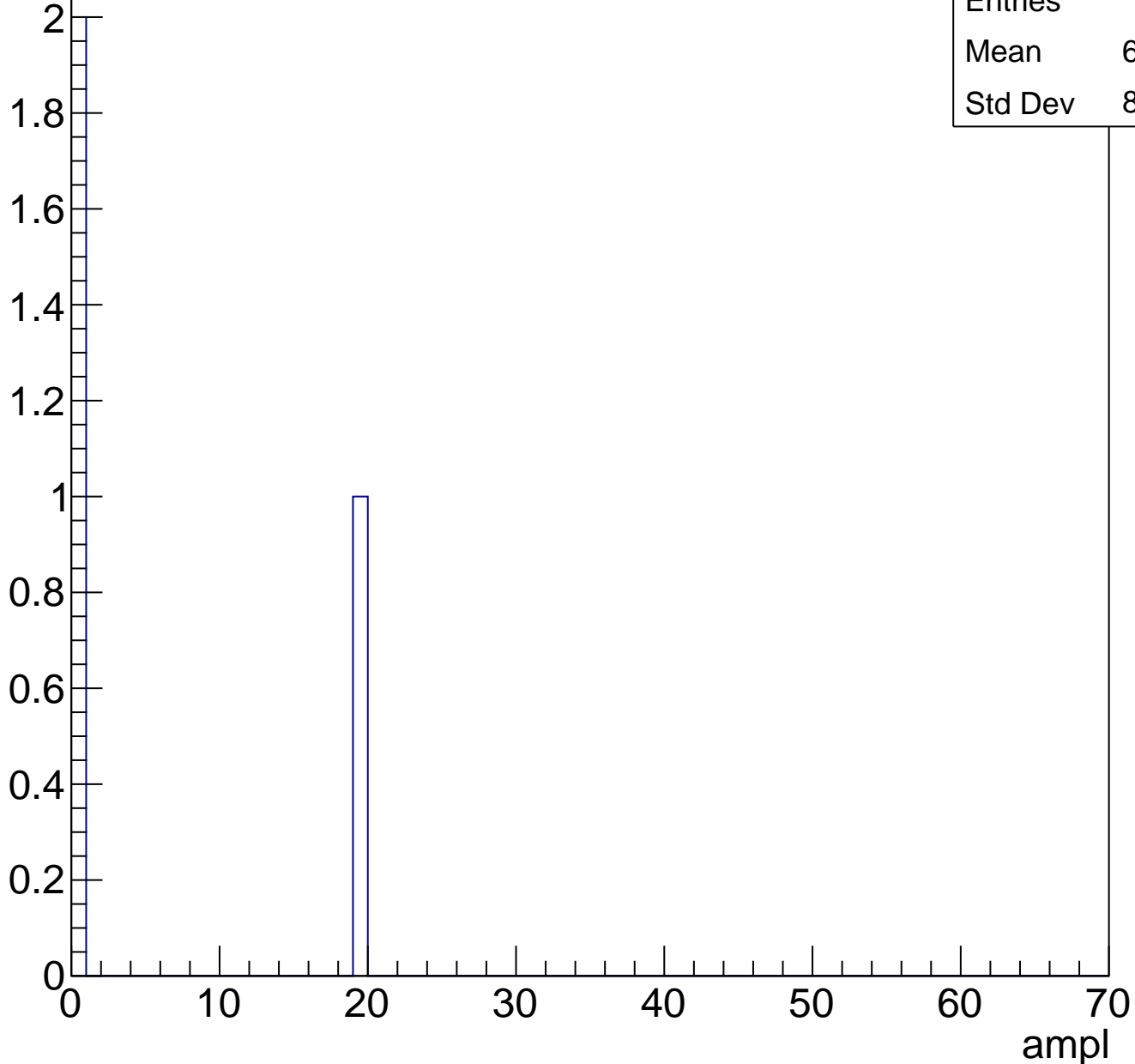
Entries	10
Mean	62
Std Dev	1.095



B1L100S, U10-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L100S, U10-ch85, adc0

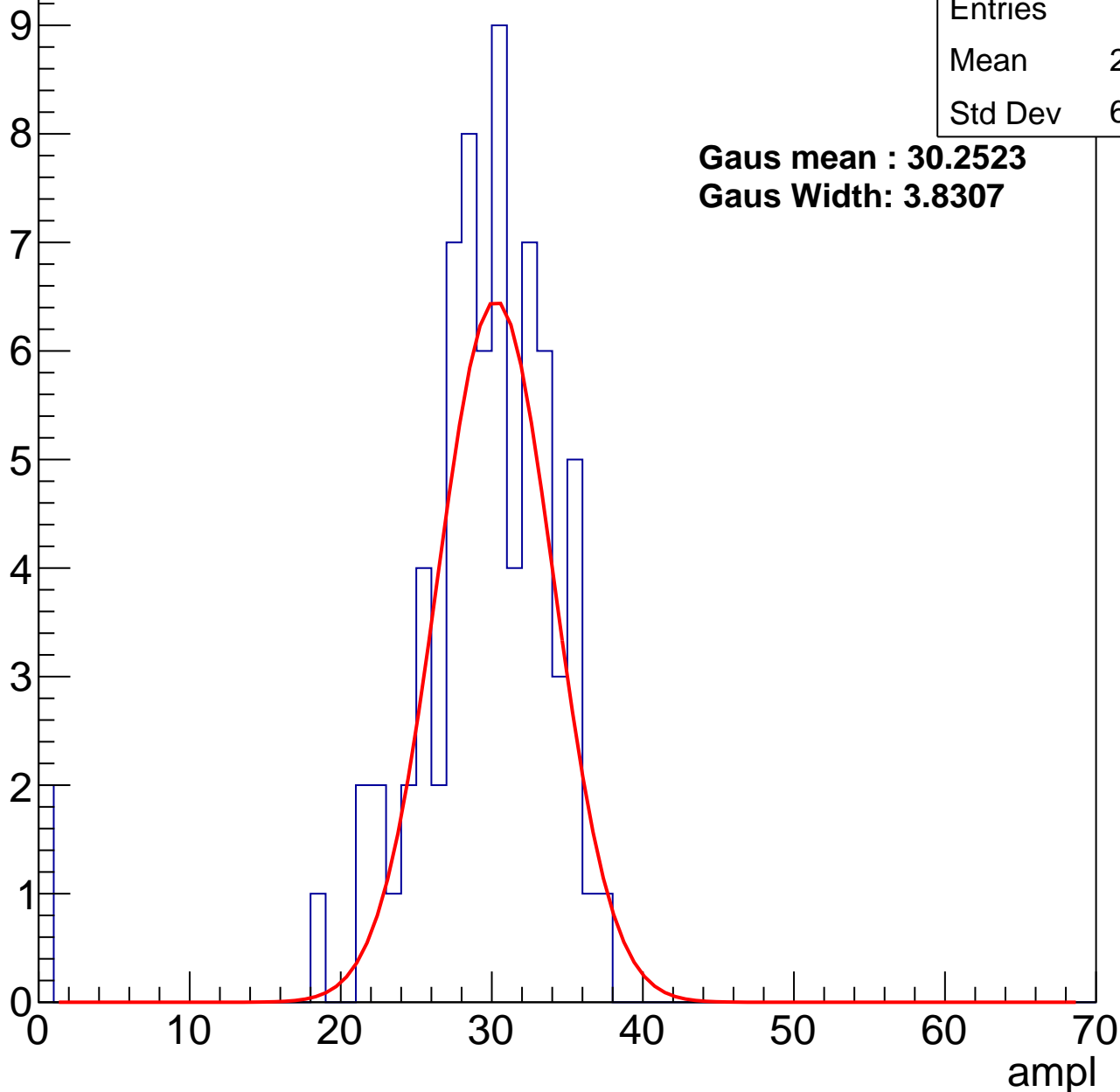
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.49
Std Dev	6.156

Gaus mean : 30.2523

Gaus Width: 3.8307



B1L100S, U10-ch85, adc1

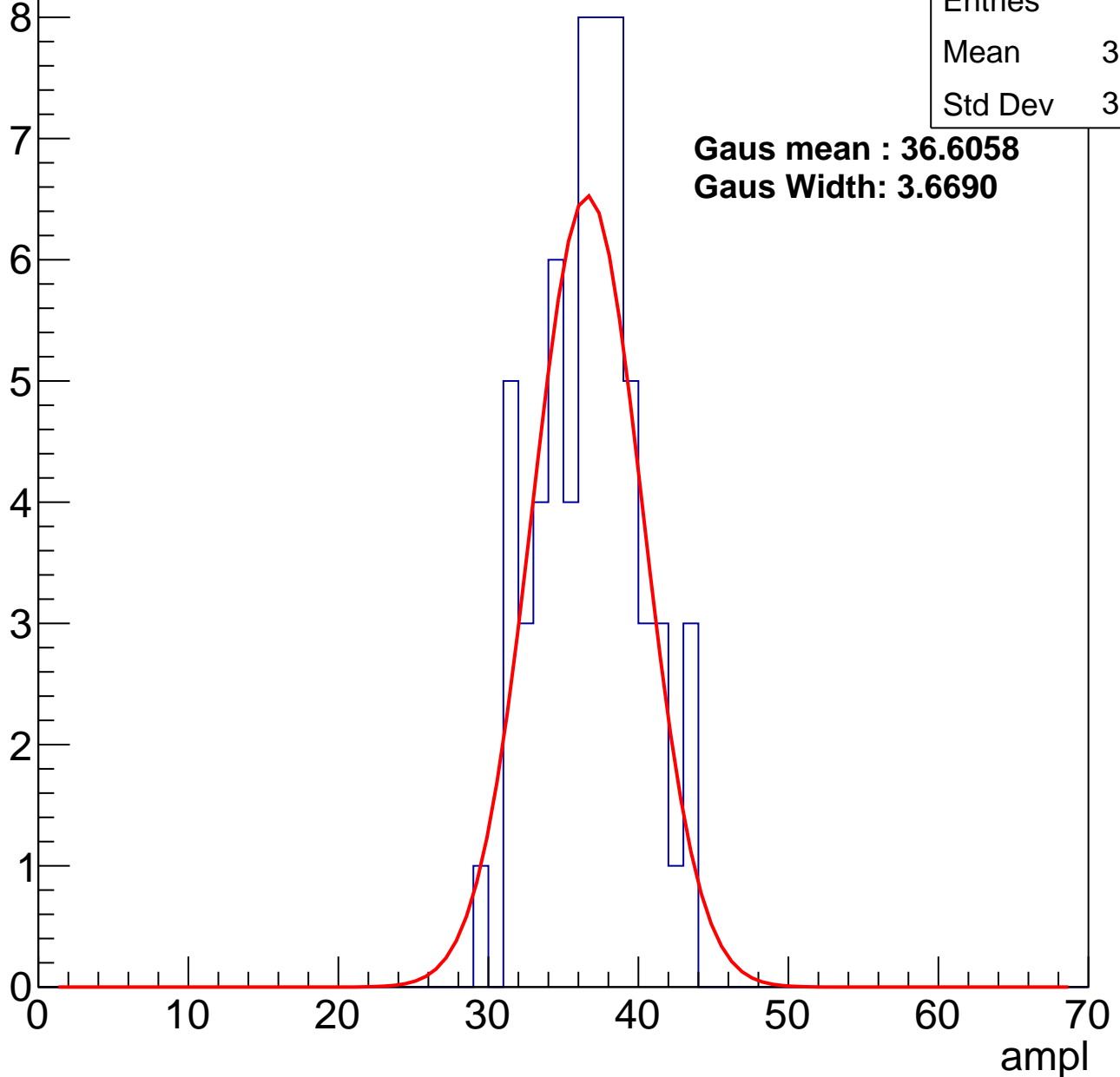
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.34
Std Dev	3.287

Gaus mean : 36.6058

Gaus Width: 3.6690



B1L100S, U10-ch85, adc2

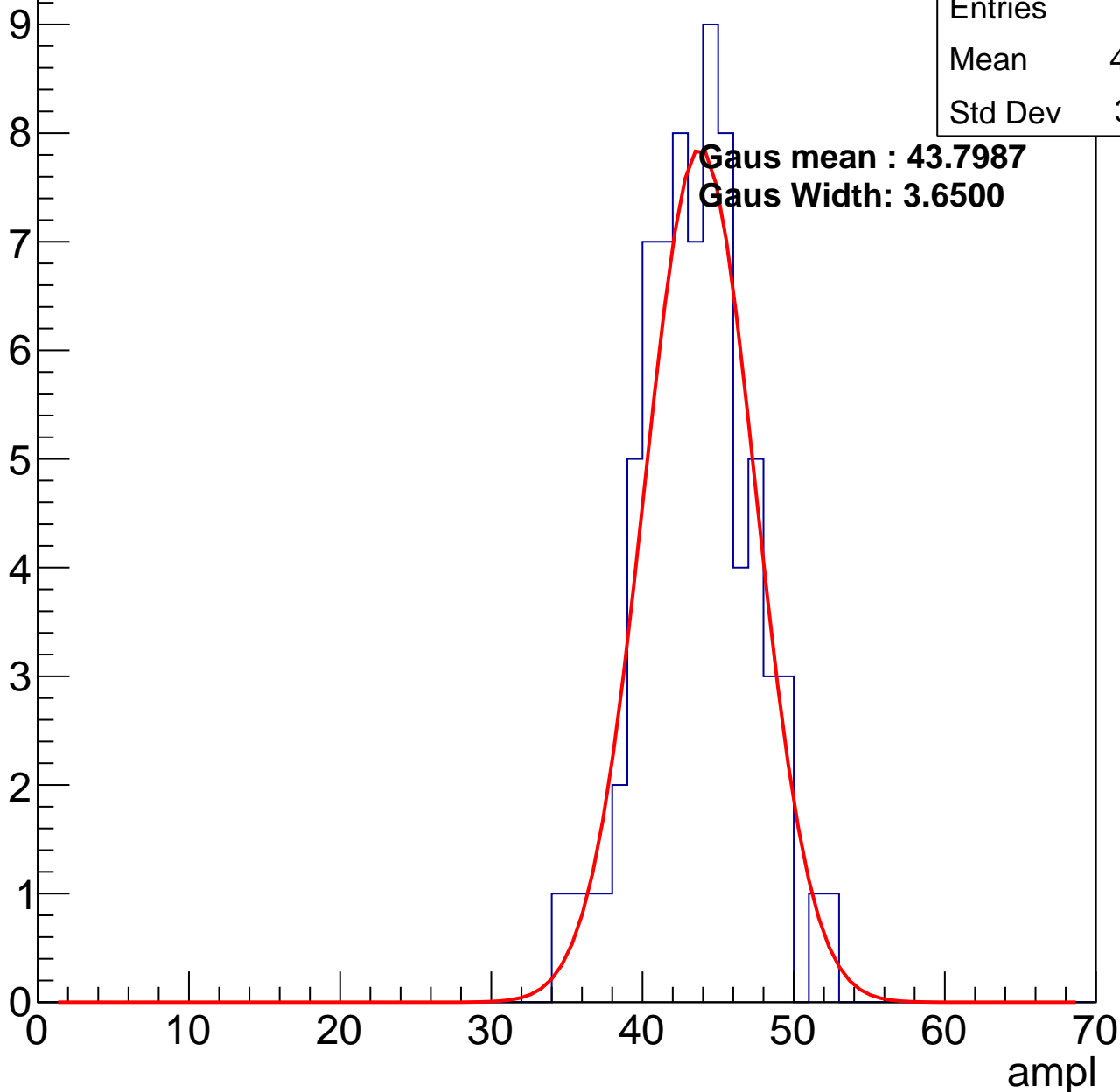
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.05
Std Dev	3.571

Gaus mean : 43.7987

Gaus Width: 3.6500

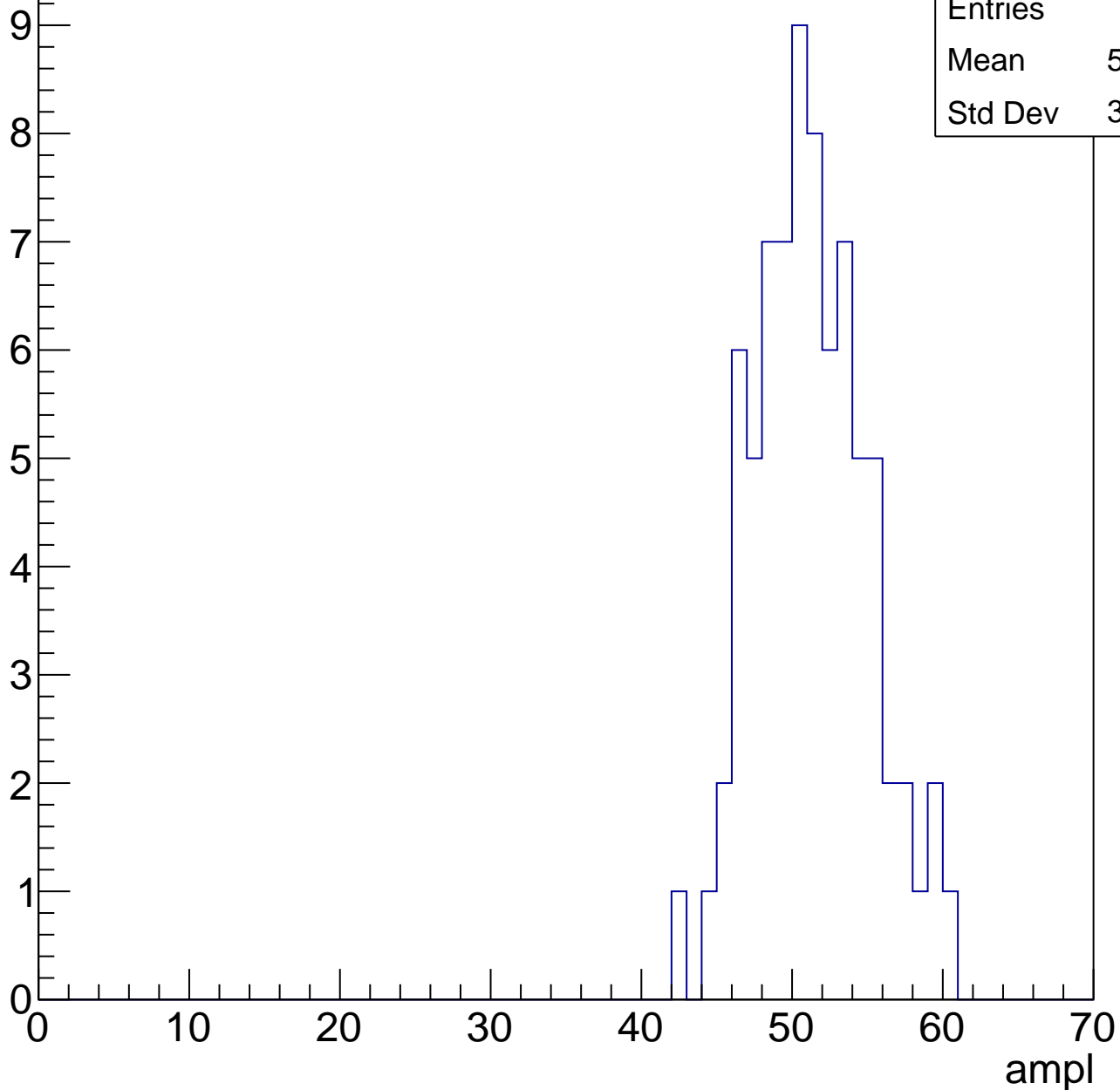


B1L100S, U10-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

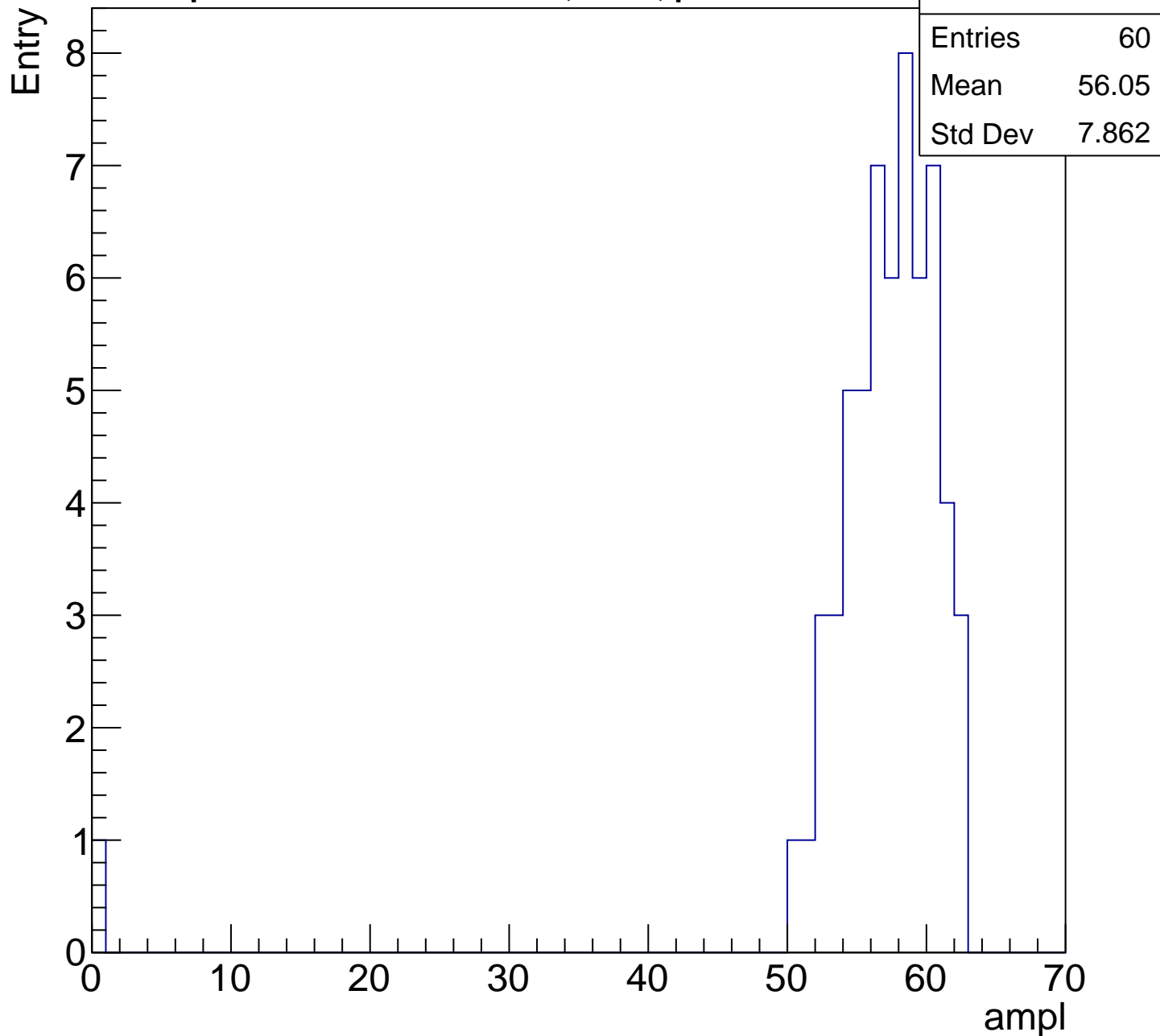
Entry

Entries	77
Mean	50.83
Std Dev	3.726



B1L100S, U10-ch85, adc4

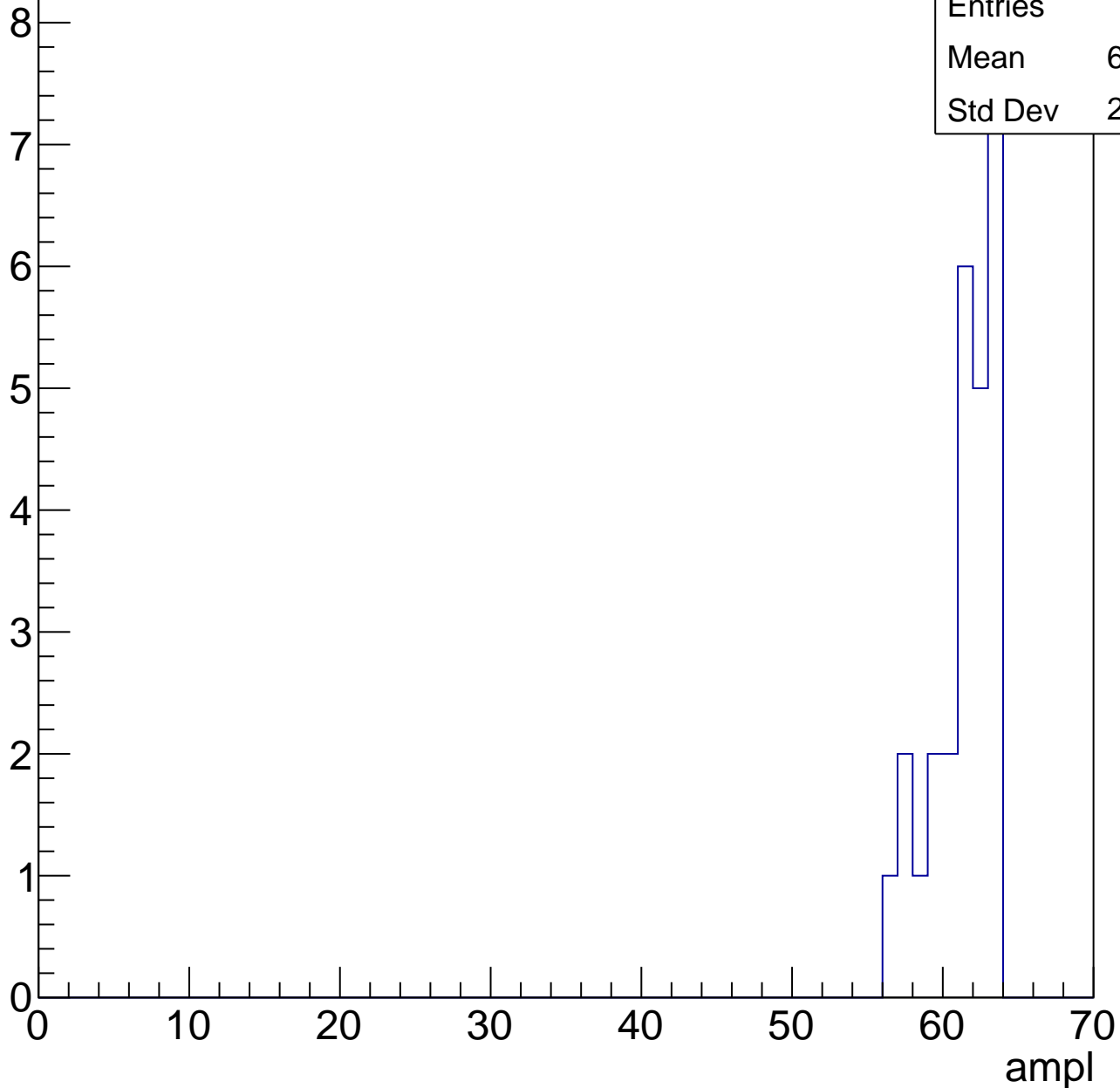
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U10-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch86, adc0

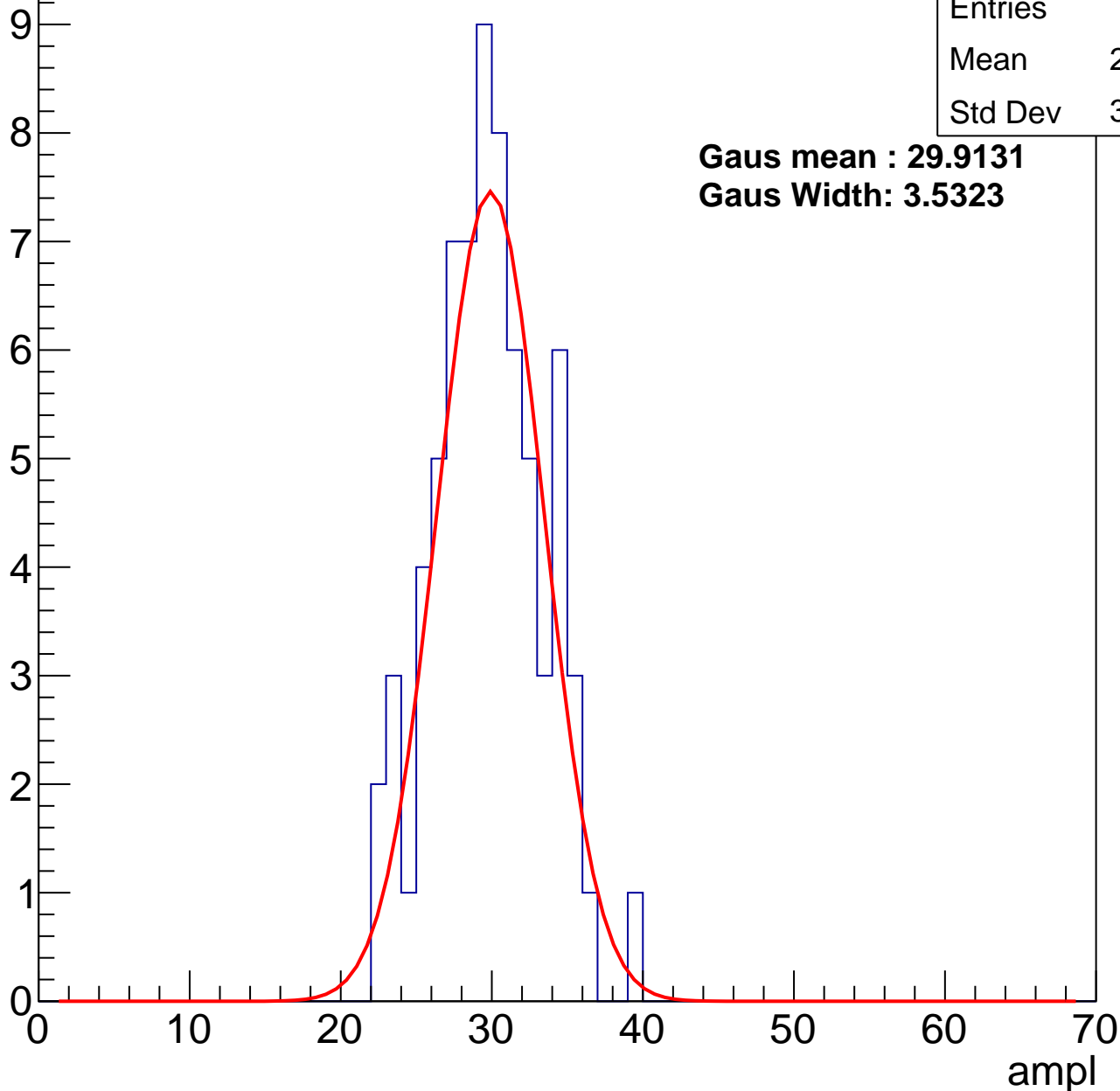
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.32
Std Dev	3.559

Gaus mean : 29.9131

Gaus Width: 3.5323



B1L100S, U10-ch86, adc1

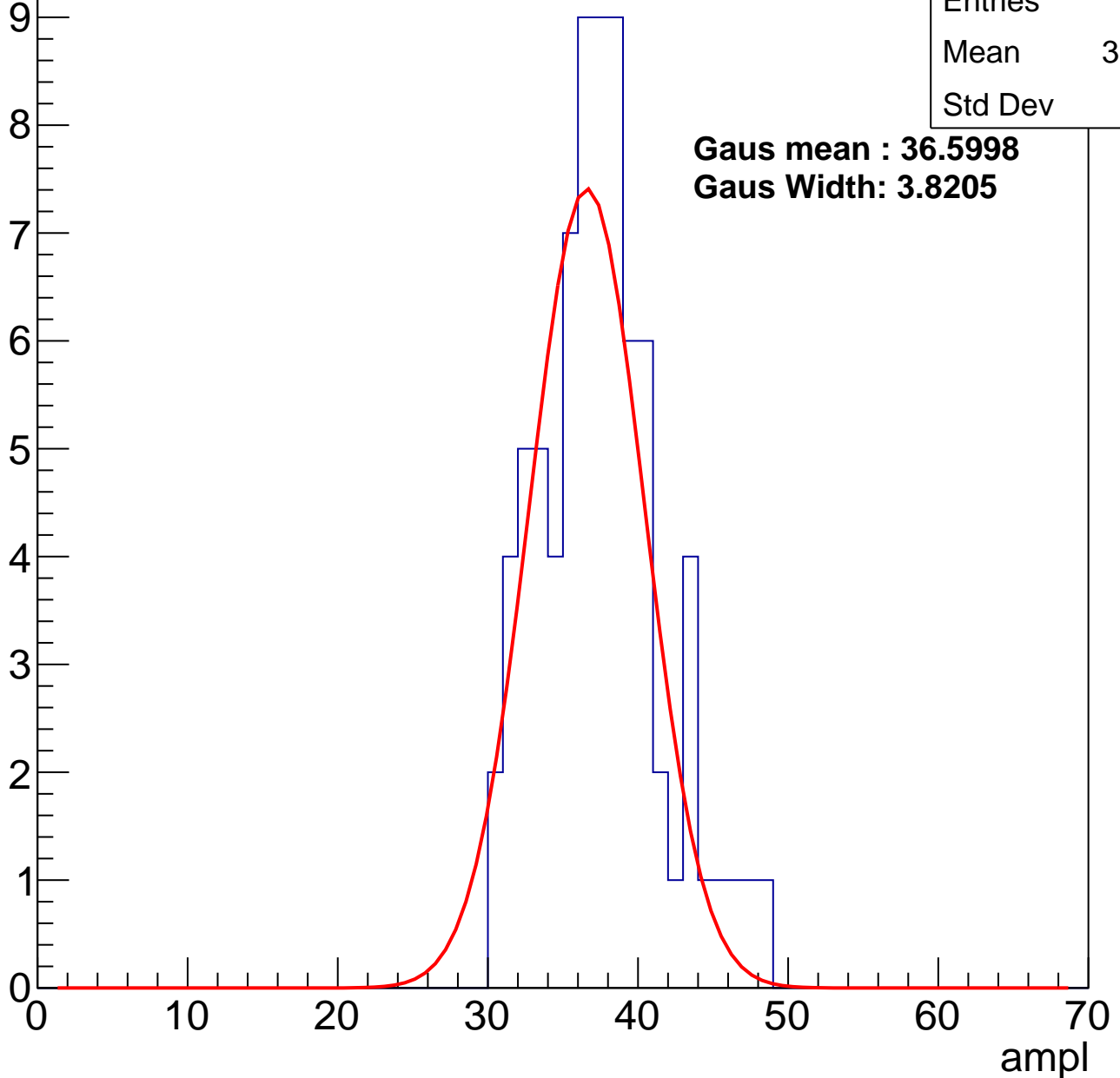
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	37.04
Std Dev	3.96

Gaus mean : 36.5998

Gaus Width: 3.8205



B1L100S, U10-ch86, adc2

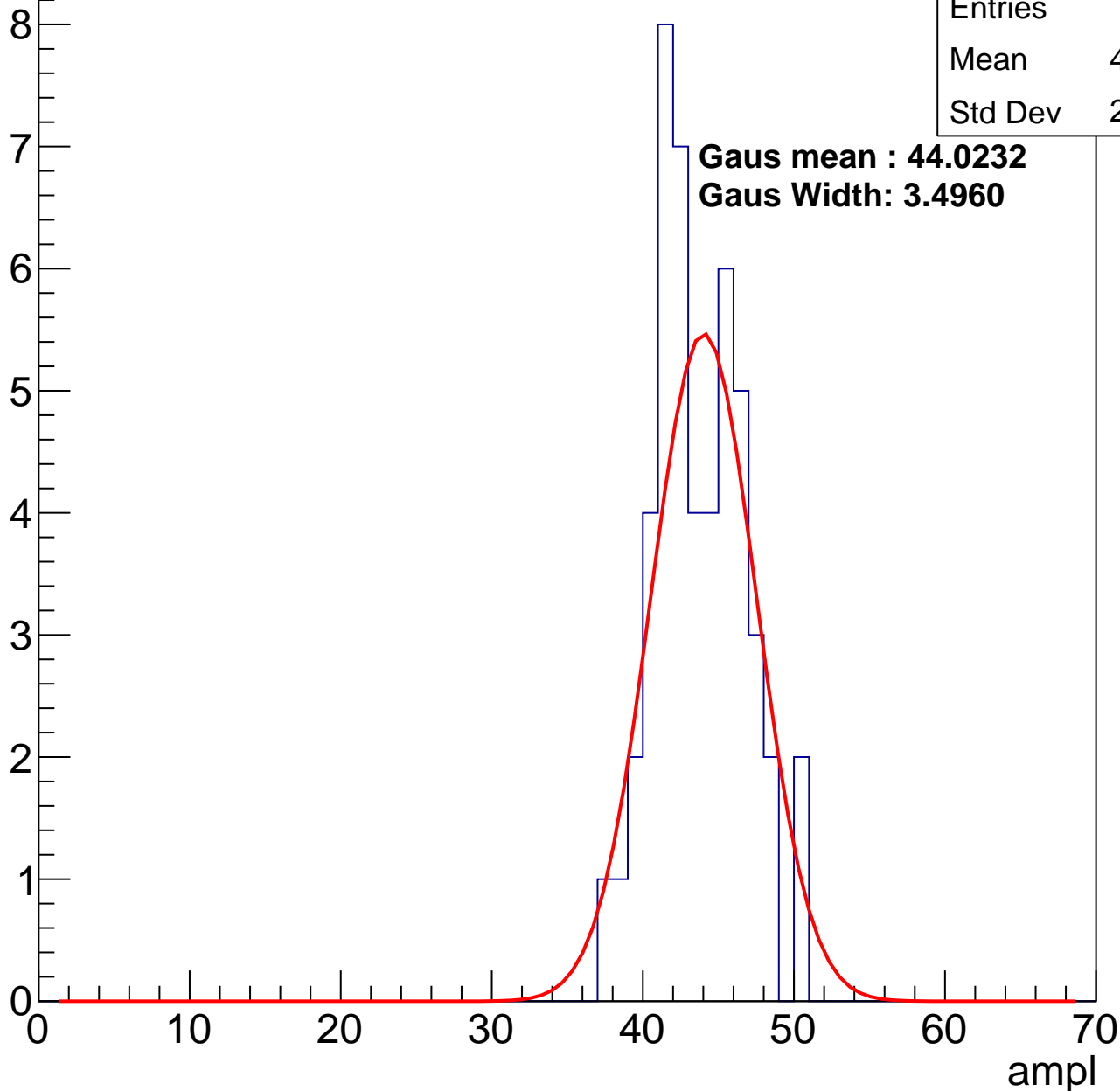
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	43.27
Std Dev	2.975

Gaus mean : 44.0232

Gaus Width: 3.4960

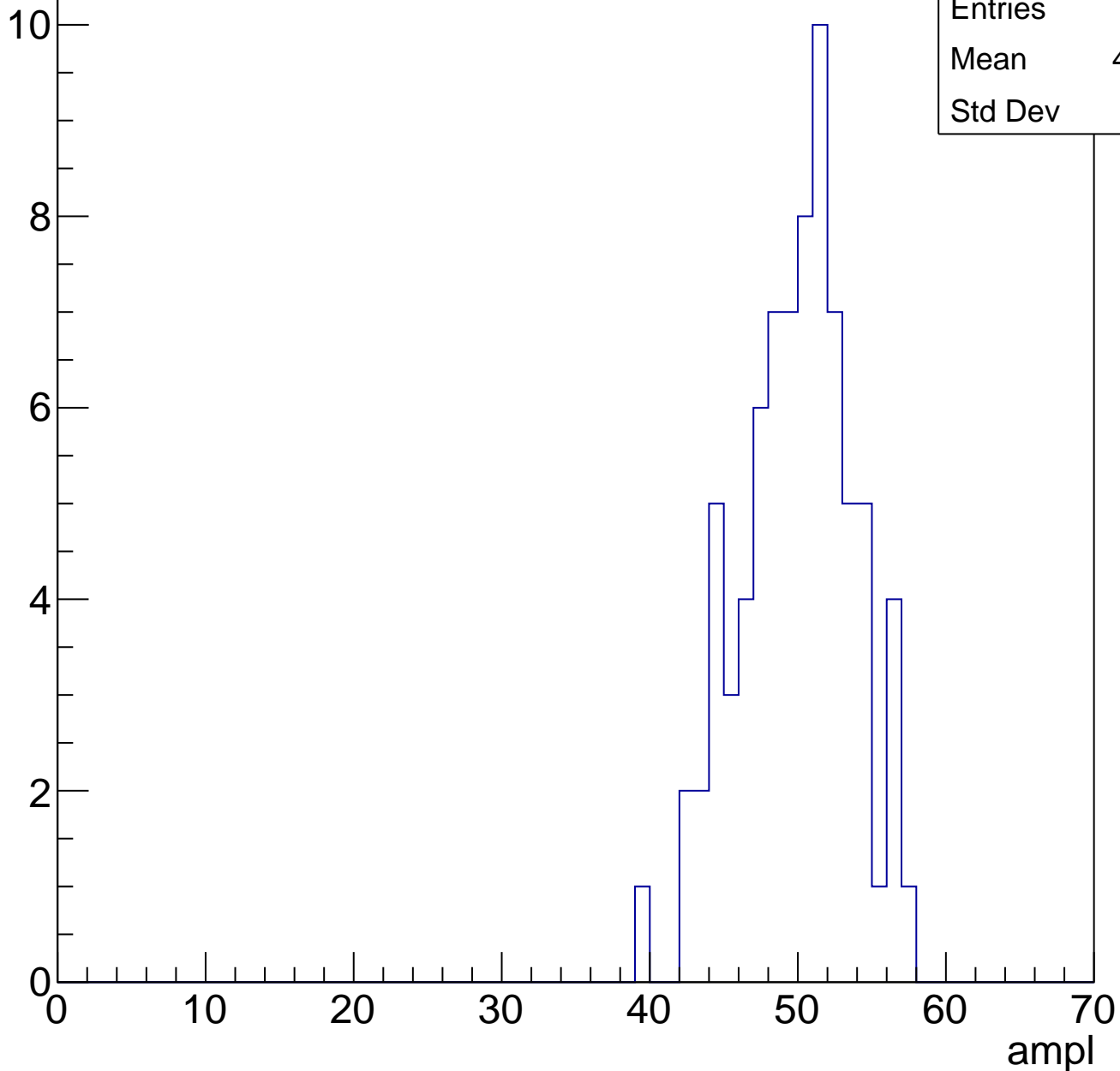


B1L100S, U10-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

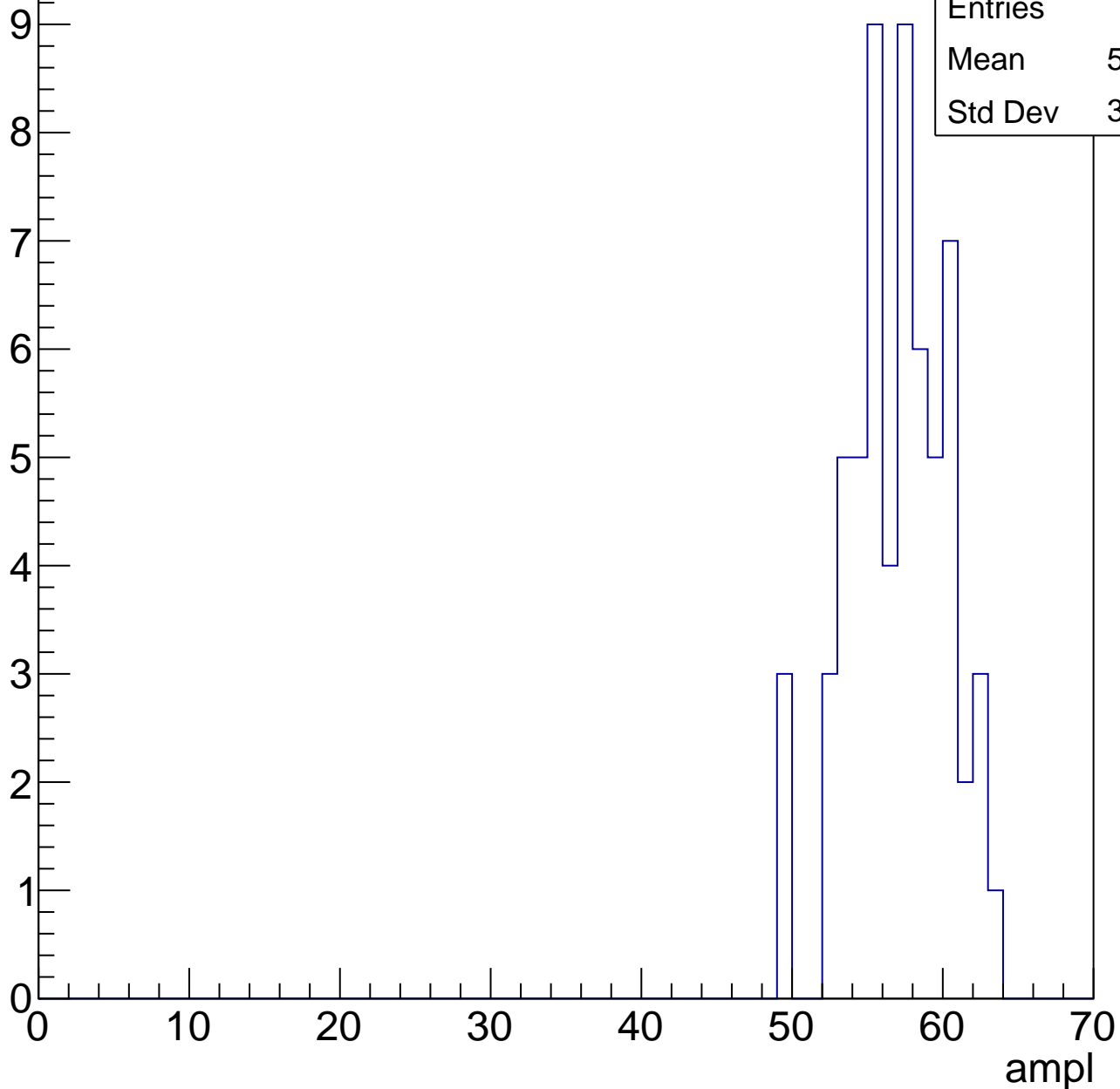
Entries	78
Mean	49.41
Std Dev	3.77



B1L100S, U10-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

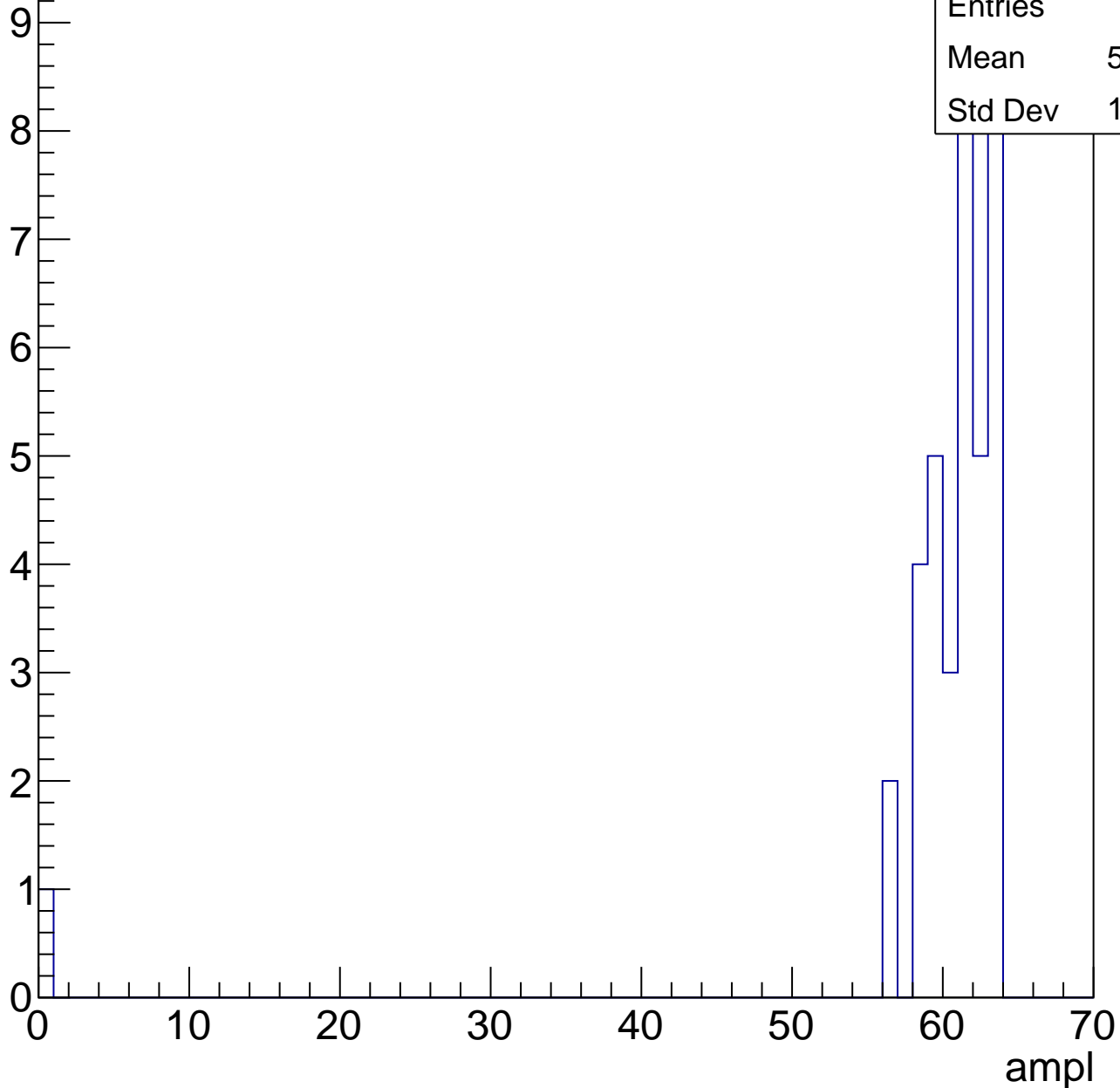
Entry



B1L100S, U10-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch87, adc0

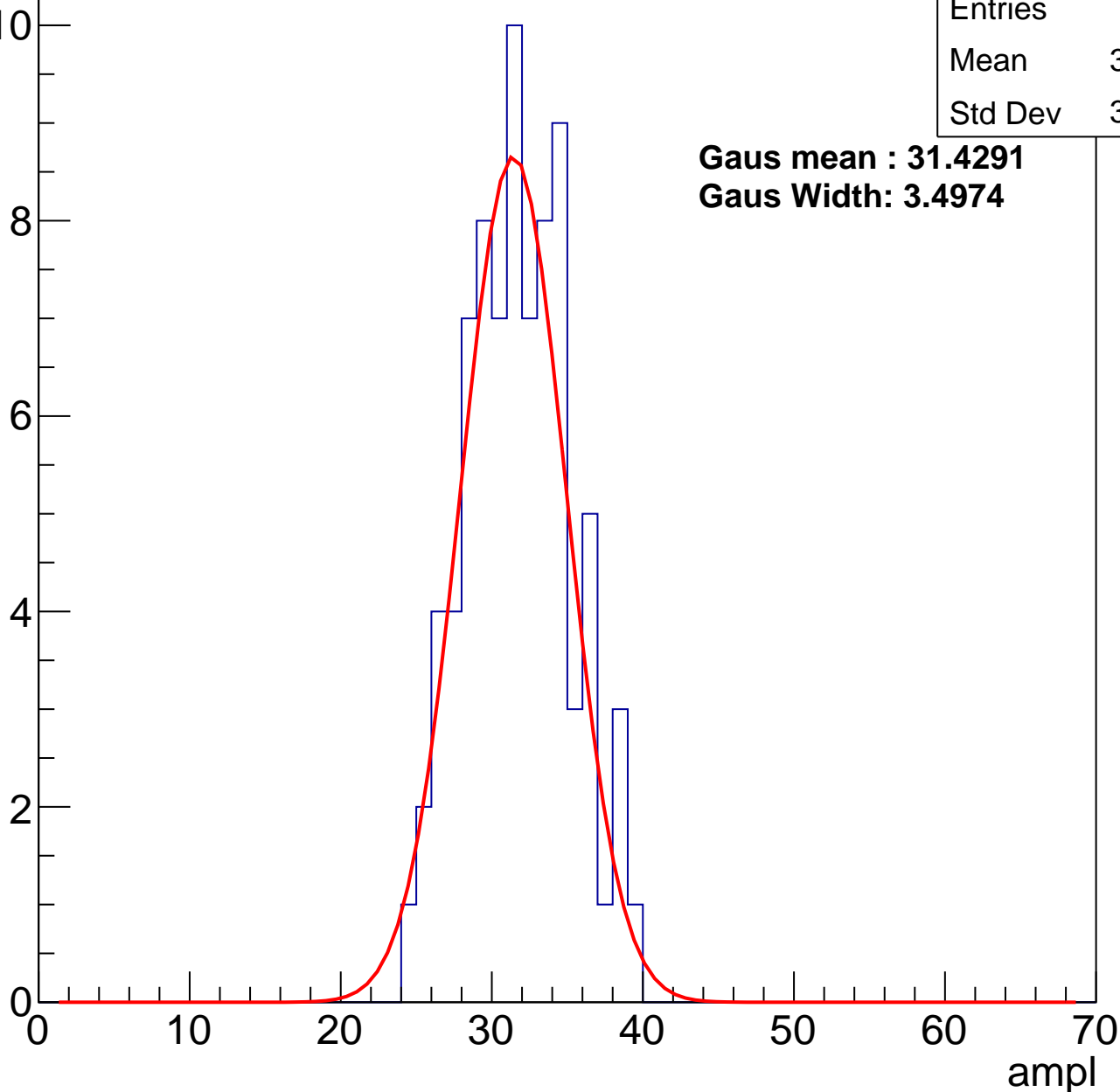
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	31.29
Std Dev	3.392

Gaus mean : 31.4291

Gaus Width: 3.4974



B1L100S, U10-ch87, adc1

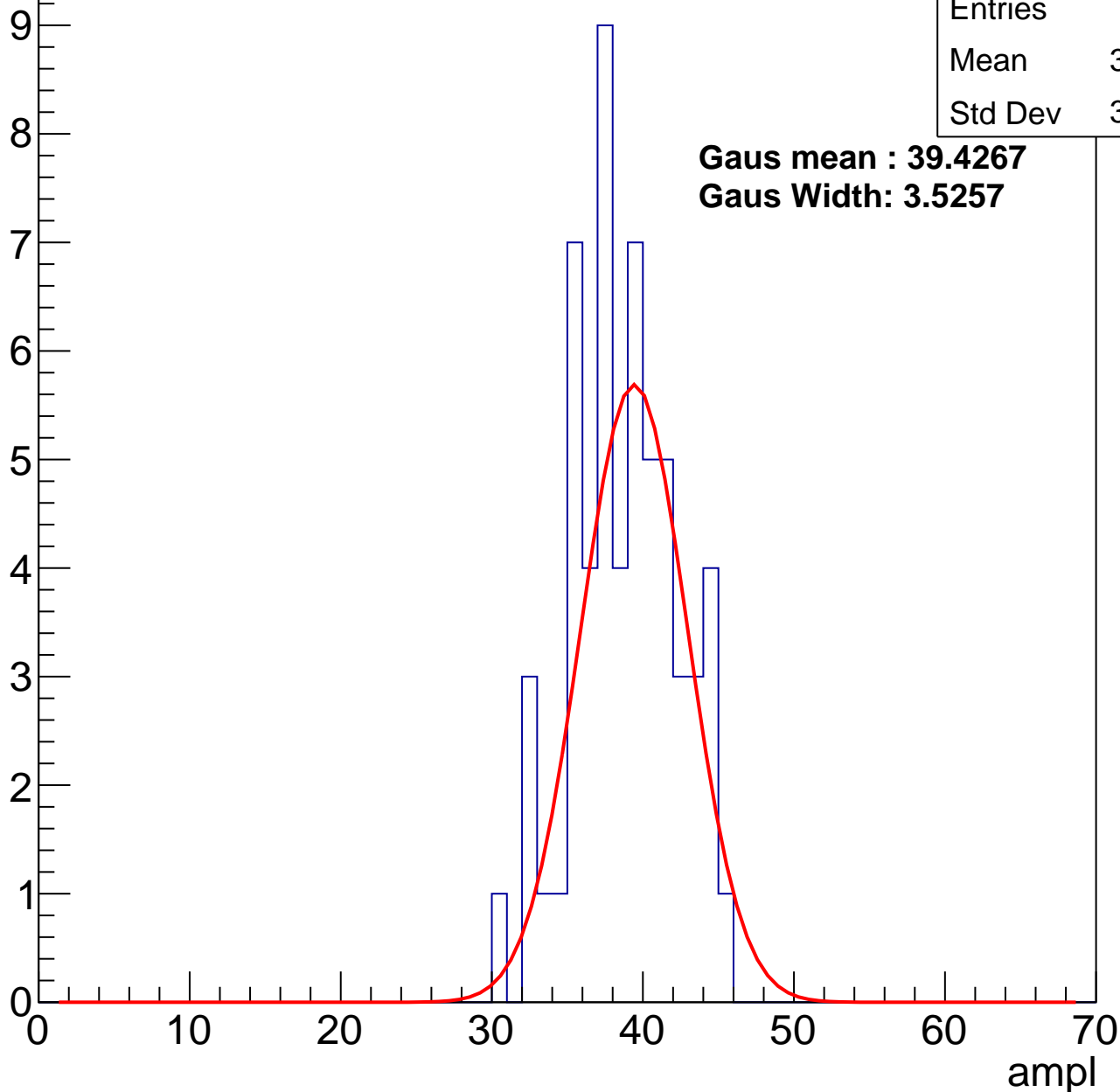
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.29
Std Dev	3.434

Gaus mean : 39.4267

Gaus Width: 3.5257



B1L100S, U10-ch87, adc2

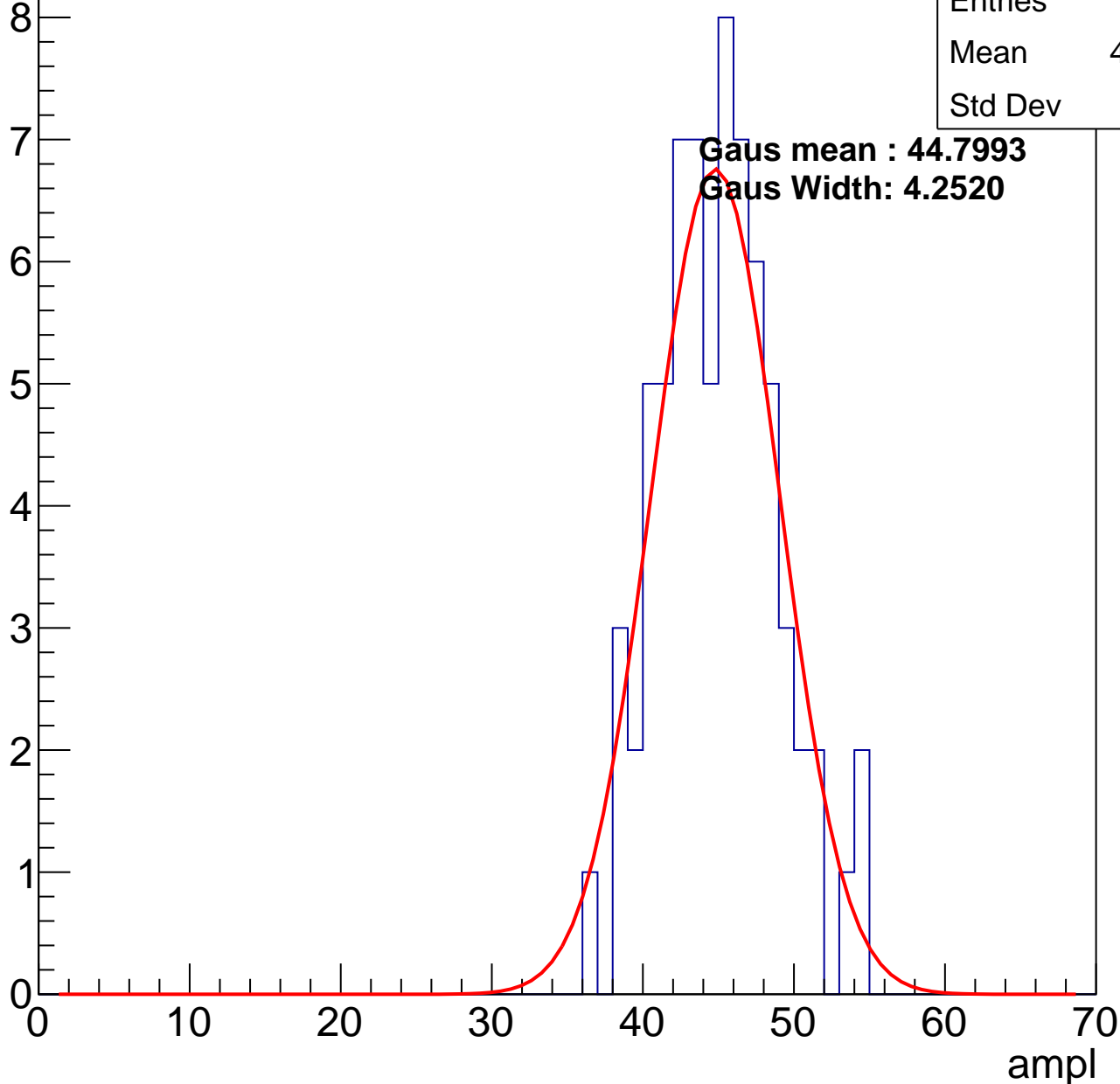
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.54
Std Dev	3.86

Gaus mean : 44.7993

Gaus Width: 4.2520

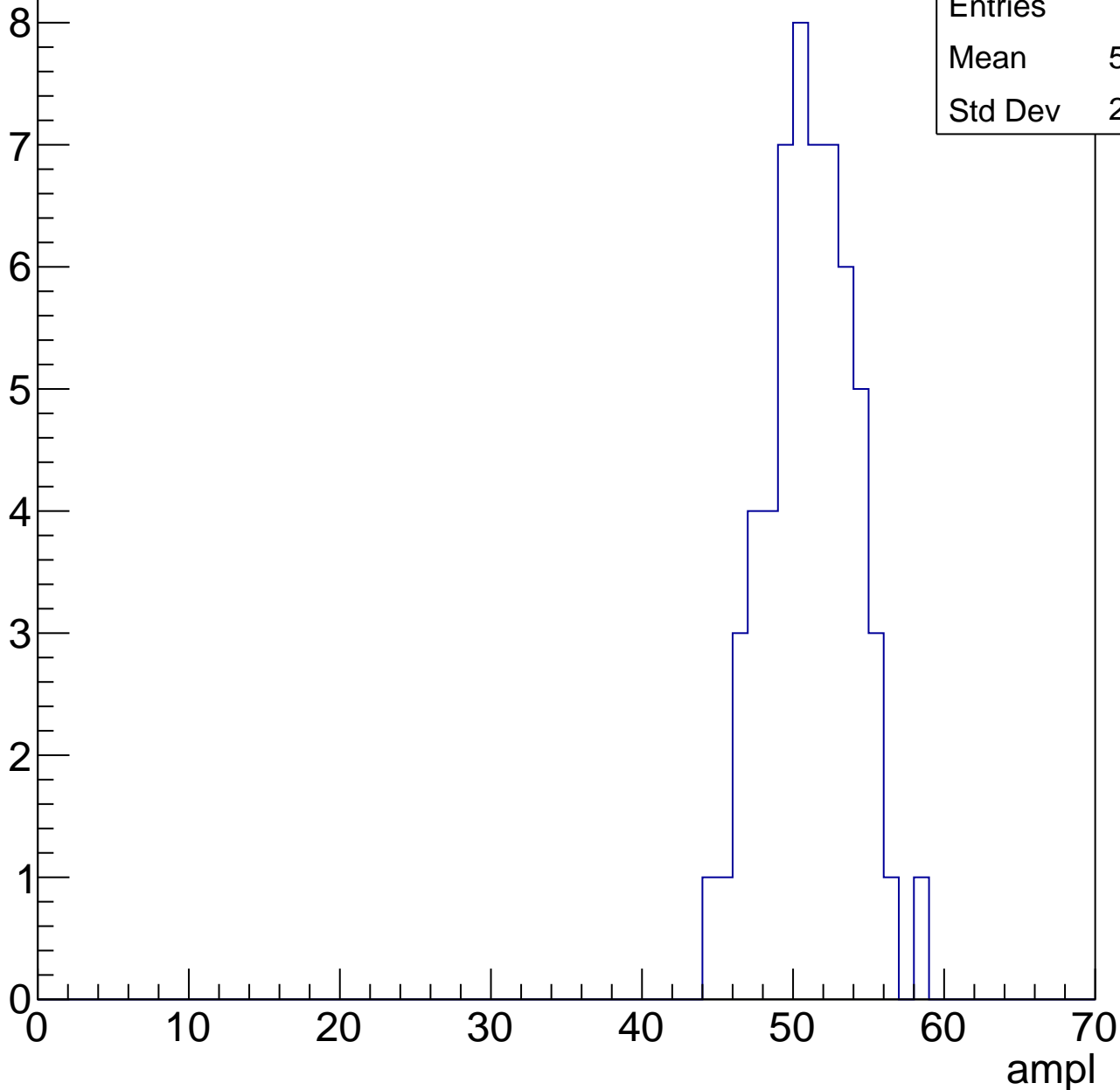


B1L100S, U10-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	50.66
Std Dev	2.898

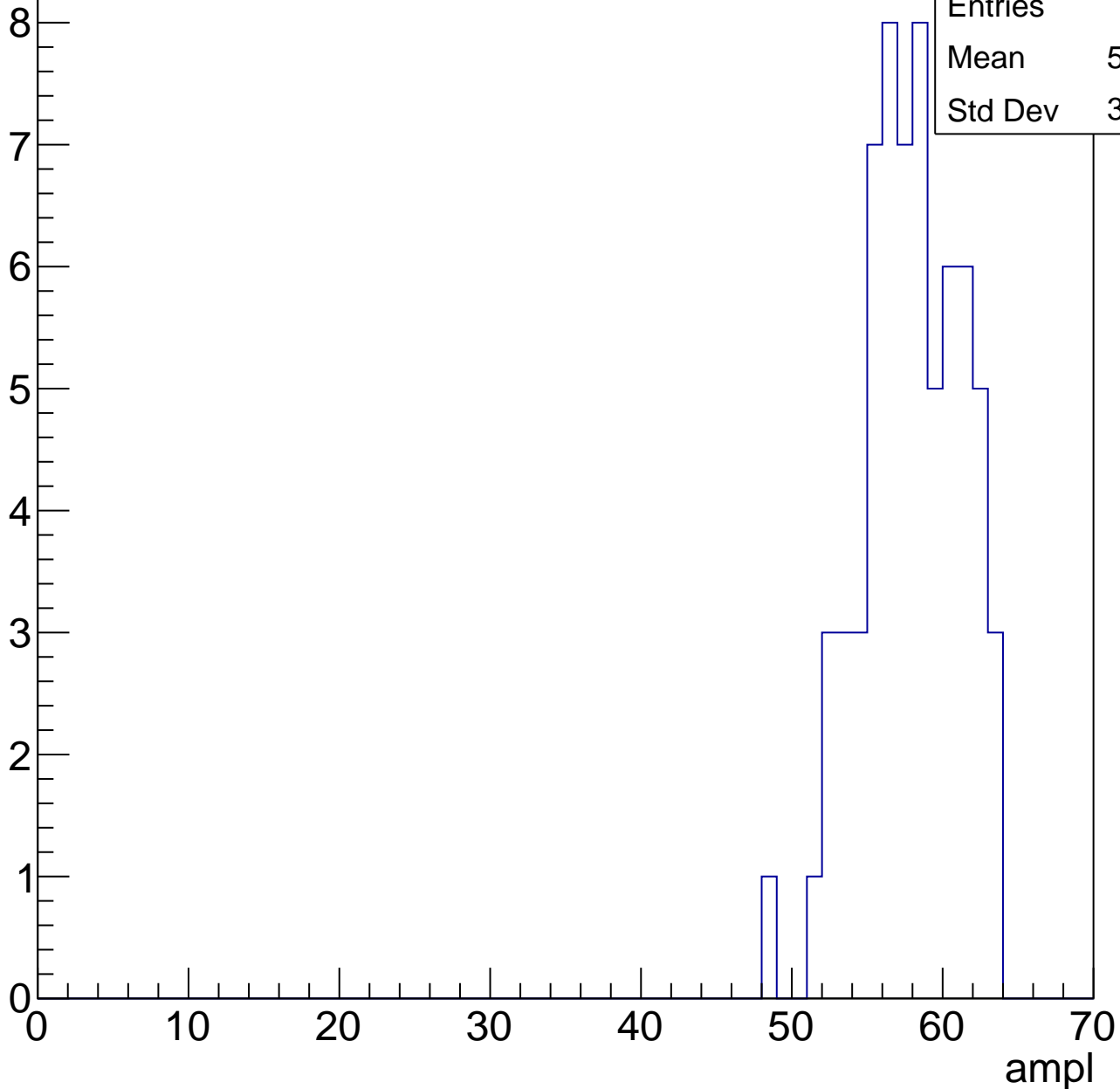


B1L100S, U10-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.45
Std Dev	3.258

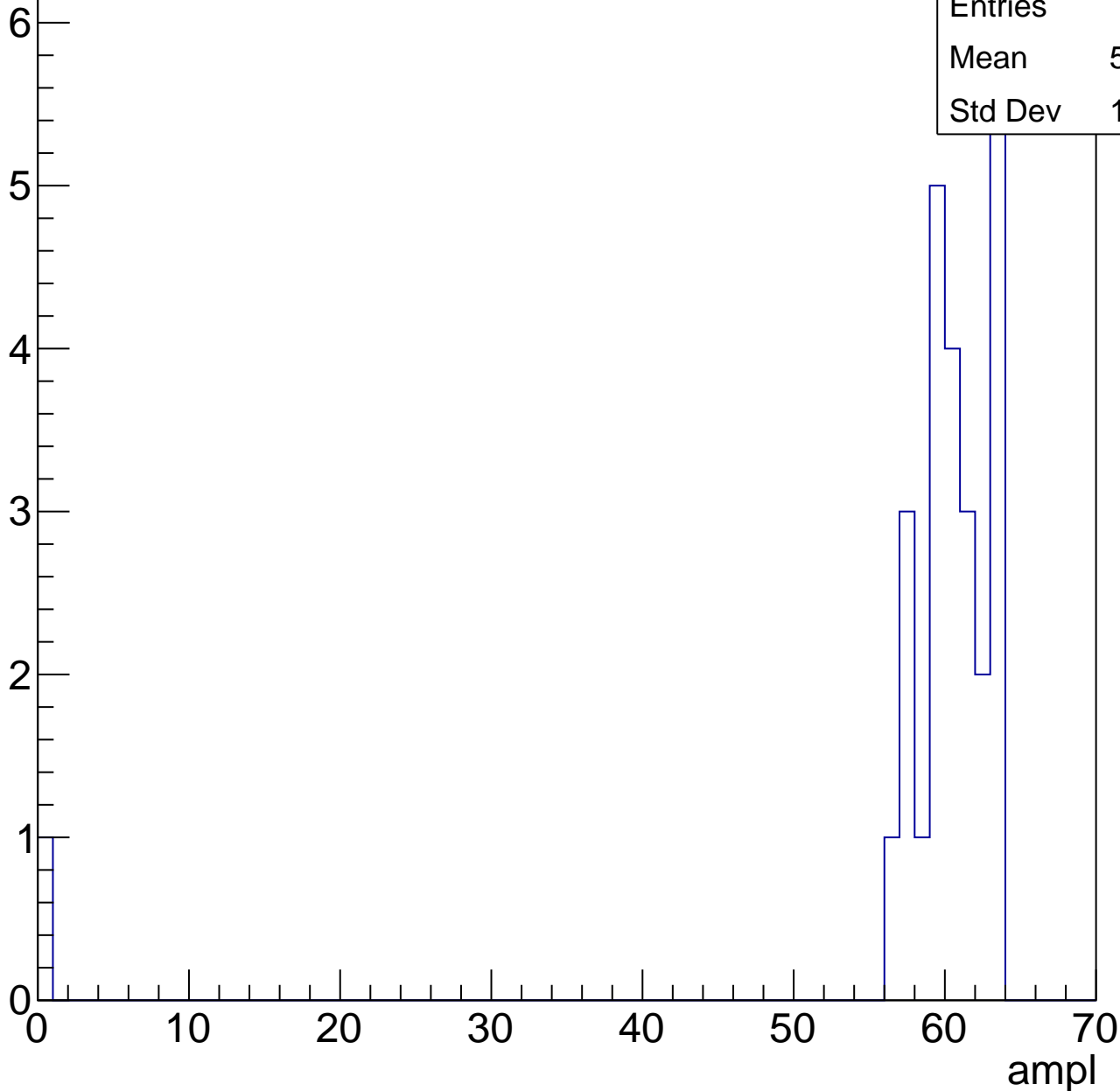


B1L100S, U10-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

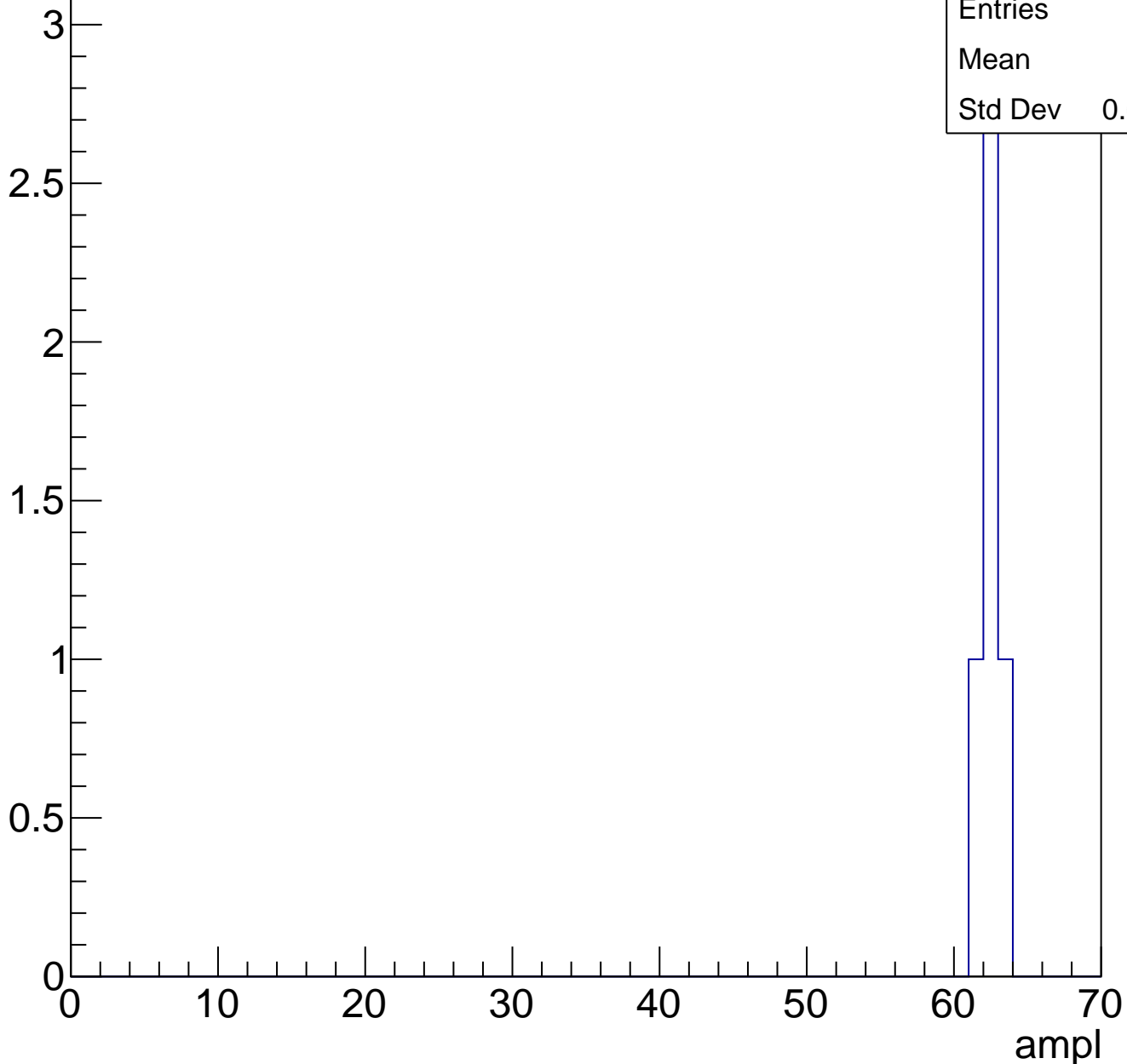
Entries	26
Mean	57.88
Std Dev	11.77



B1L100S, U10-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch88, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	29.57
Std Dev	3.478

Gaus mean : 30.0210

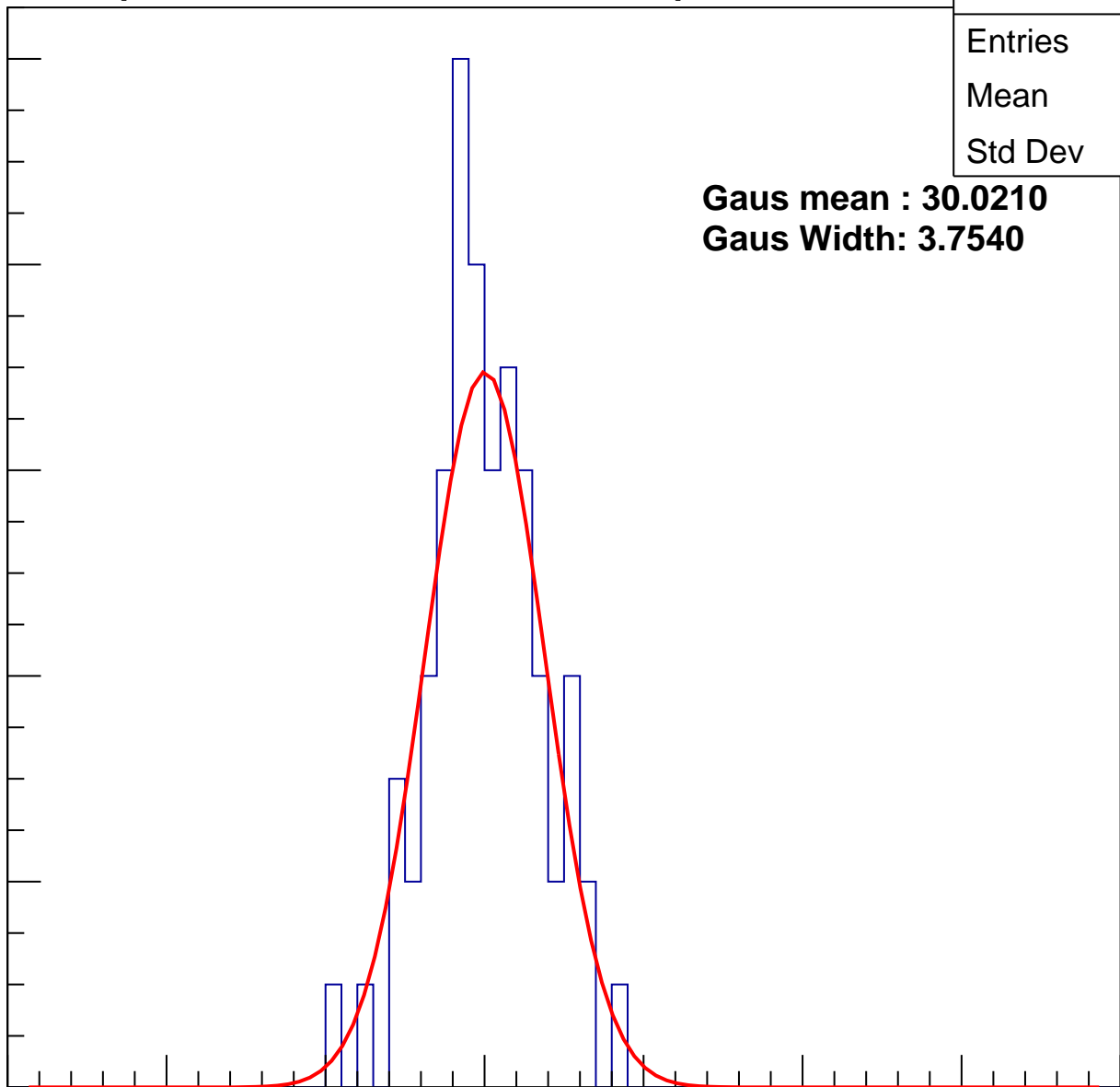
Gaus Width: 3.7540

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch88, adc1

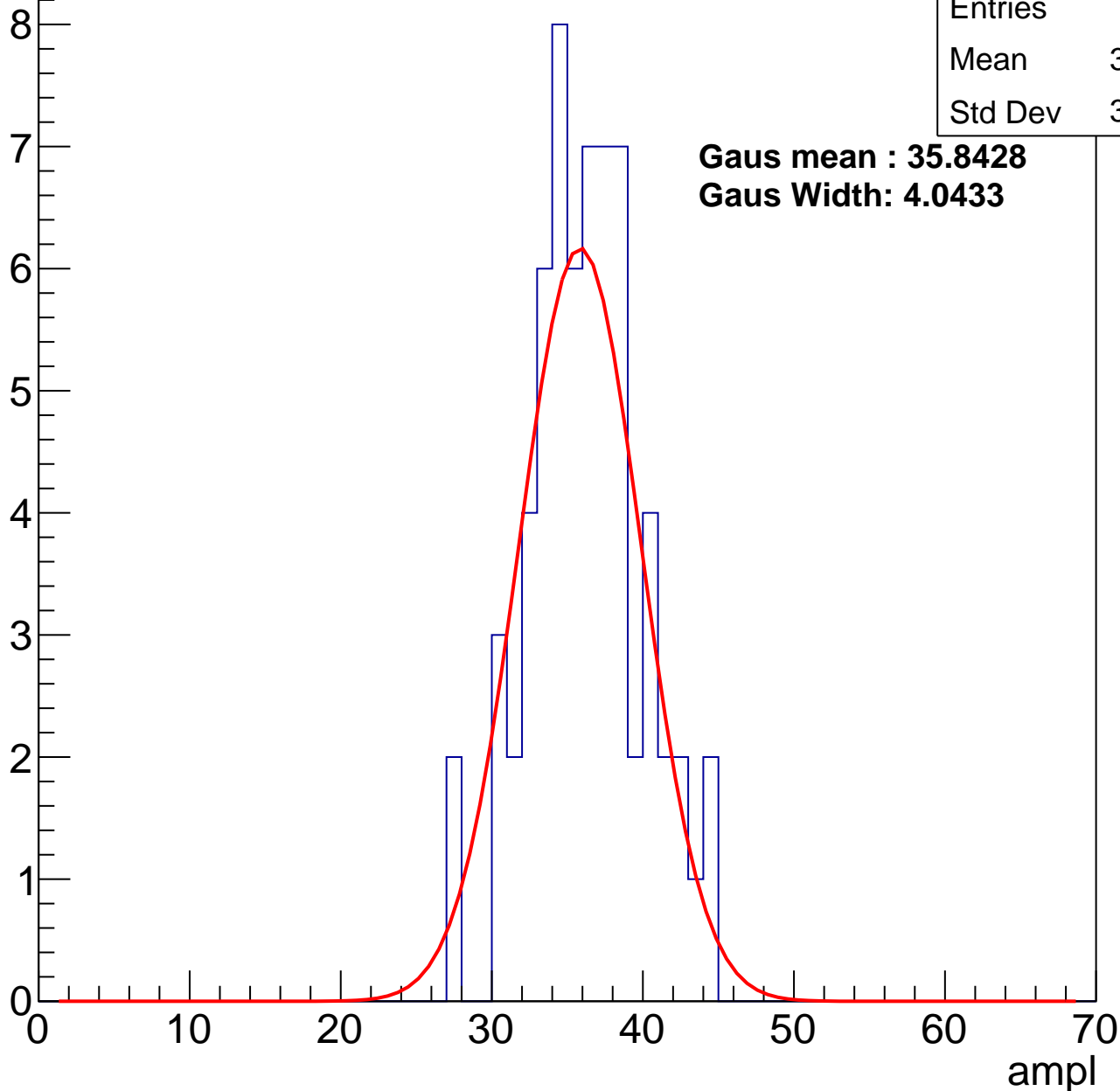
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.78
Std Dev	3.698

Gaus mean : 35.8428

Gaus Width: 4.0433



B1L100S, U10-ch88, adc2

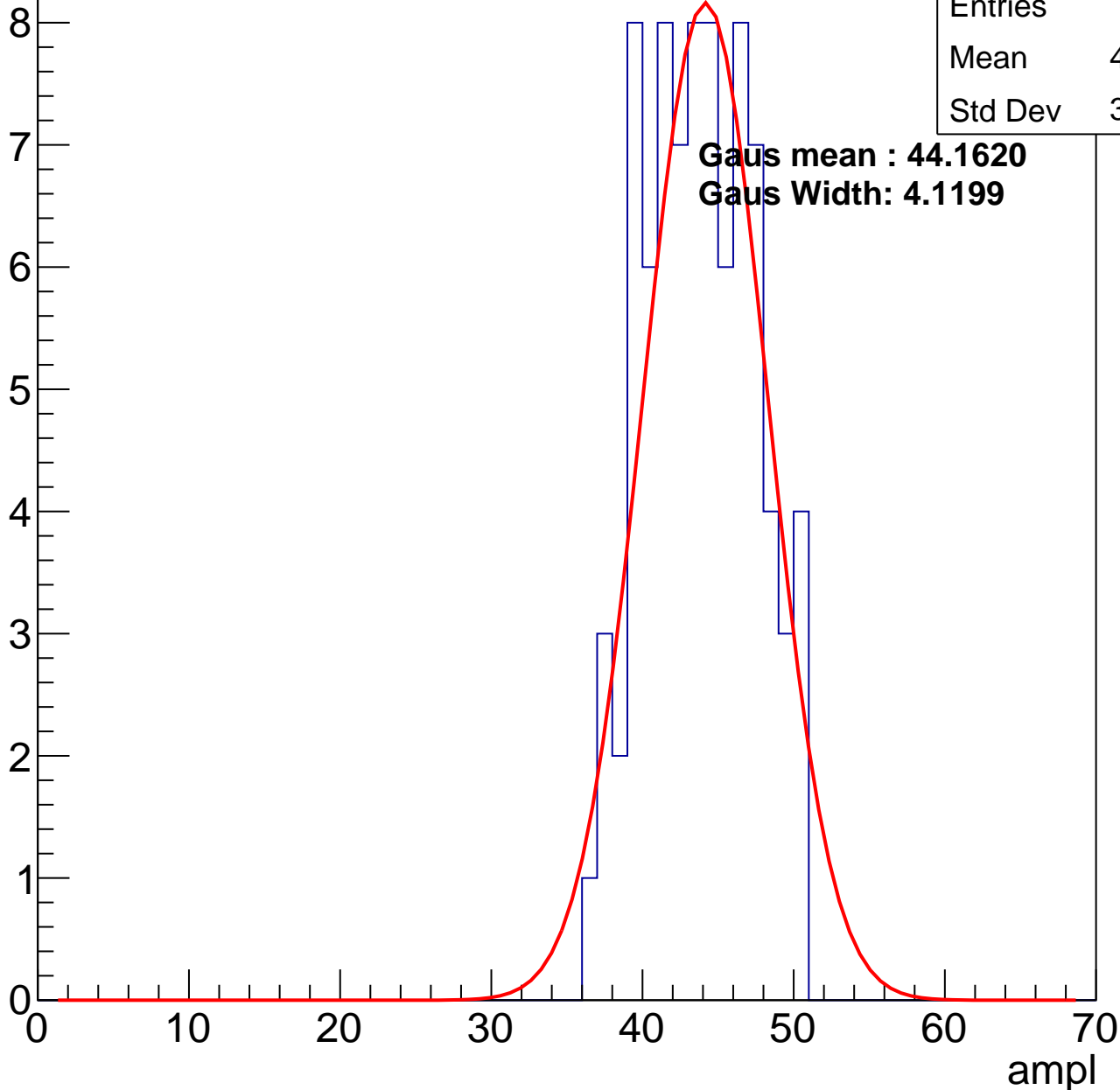
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	43.36
Std Dev	3.535

Gaus mean : 44.1620

Gaus Width: 4.1199

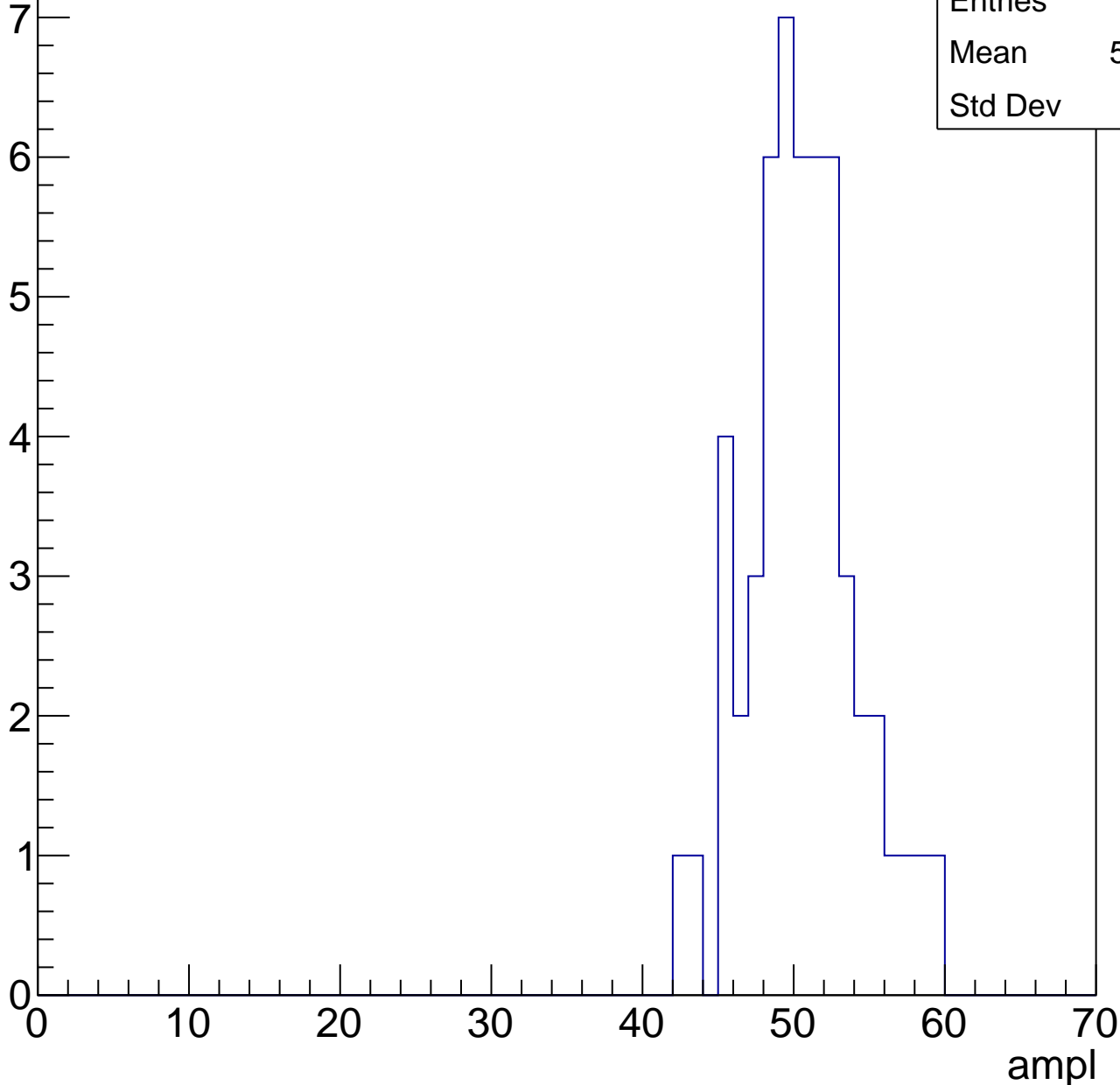


B1L100S, U10-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

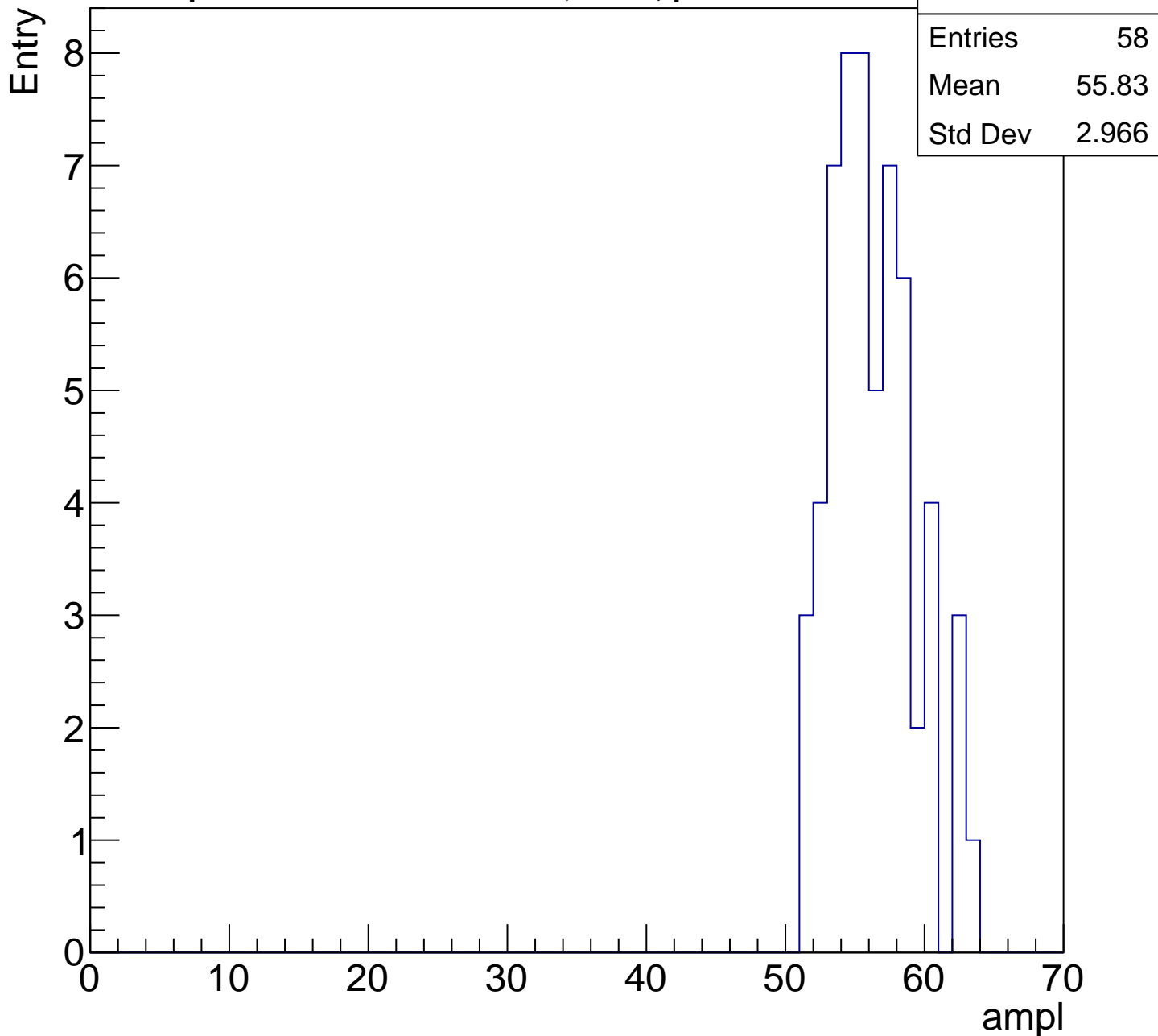
Entry

Entries	53
Mean	50.08
Std Dev	3.56



B1L100S, U10-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

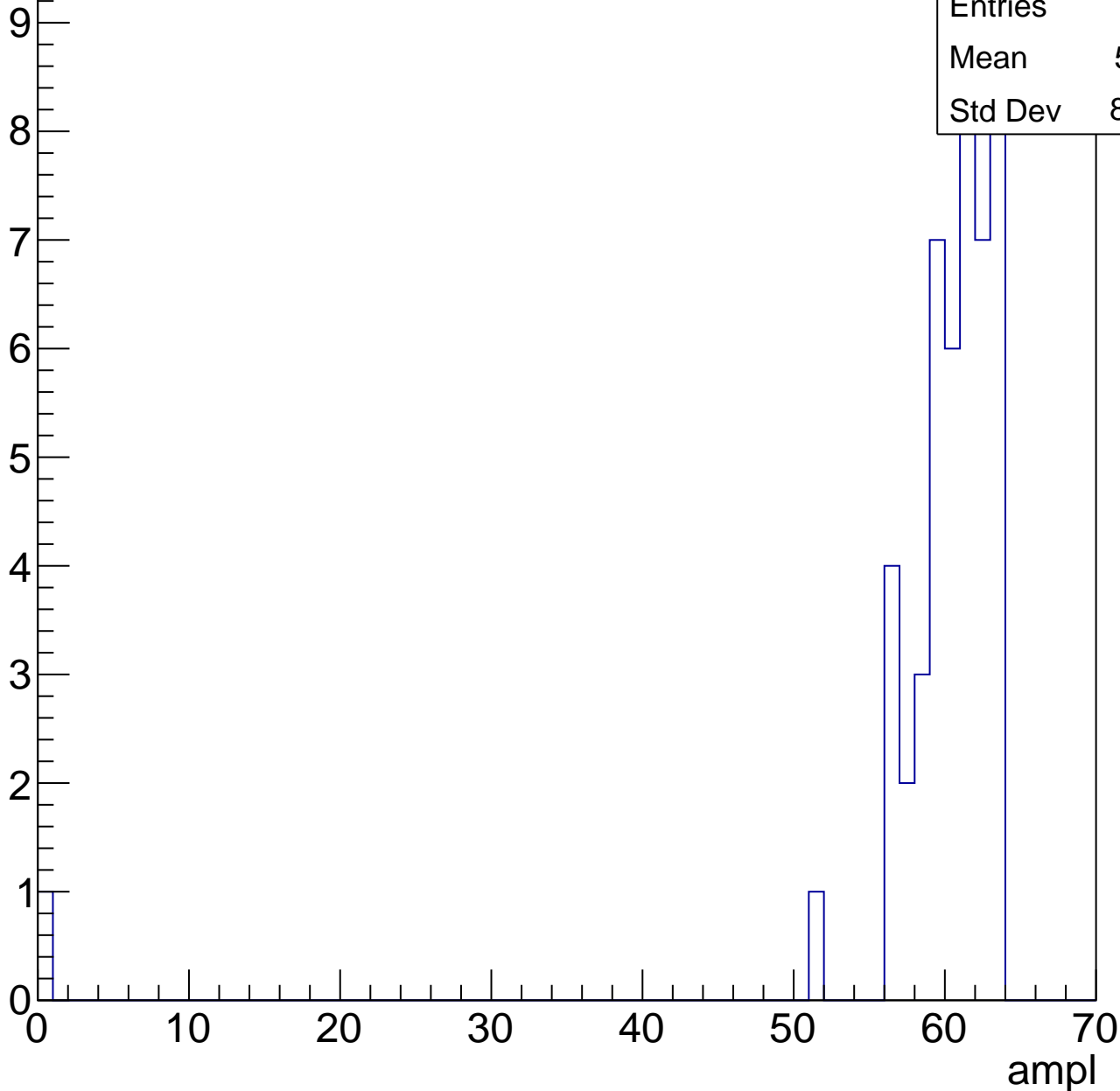


B1L100S, U10-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	58.81
Std Dev	8.925



B1L100S, U10-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch89, adc0

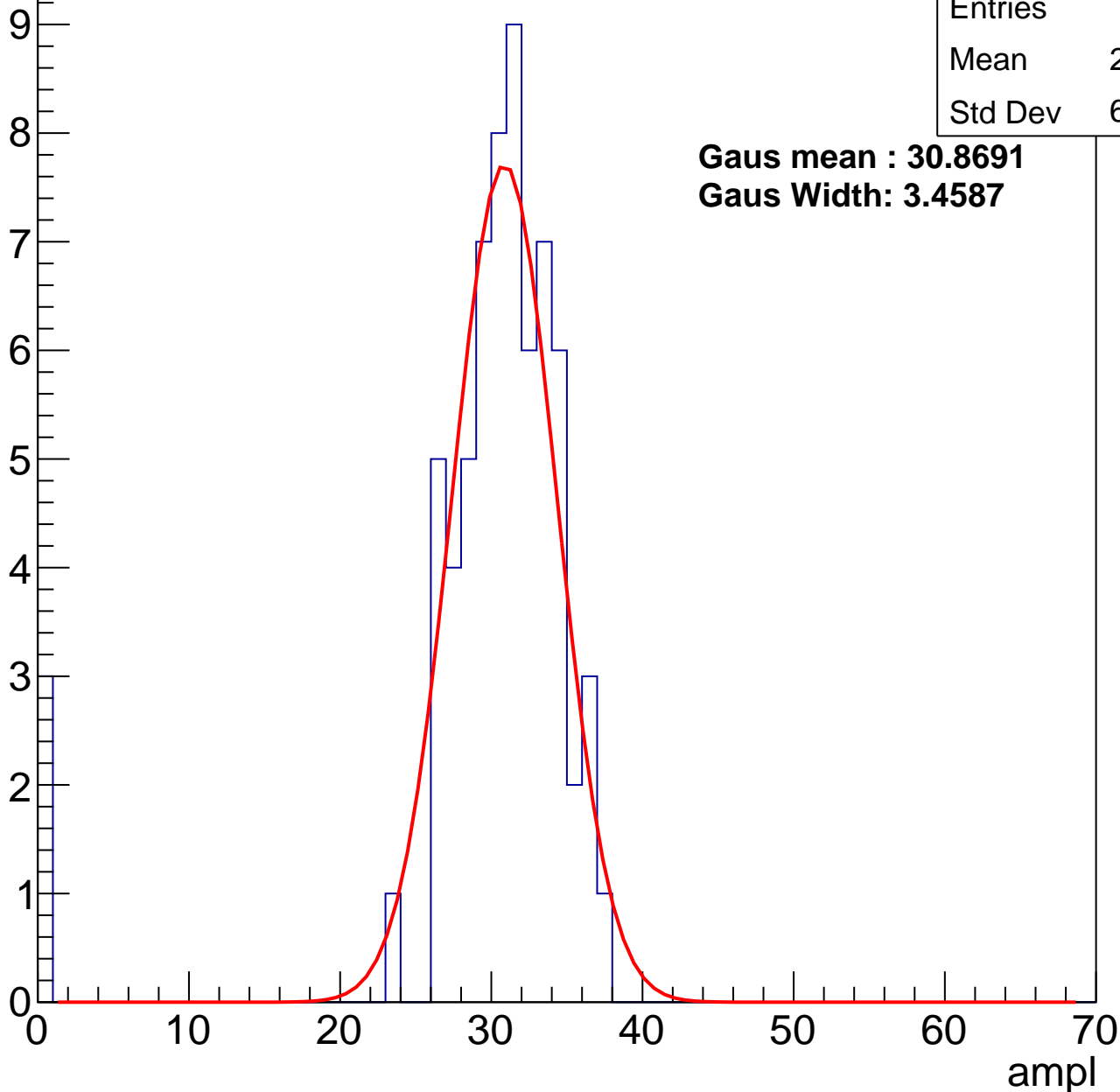
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.33
Std Dev	6.976

Gaus mean : 30.8691

Gaus Width: 3.4587



B1L100S, U10-ch89, adc1

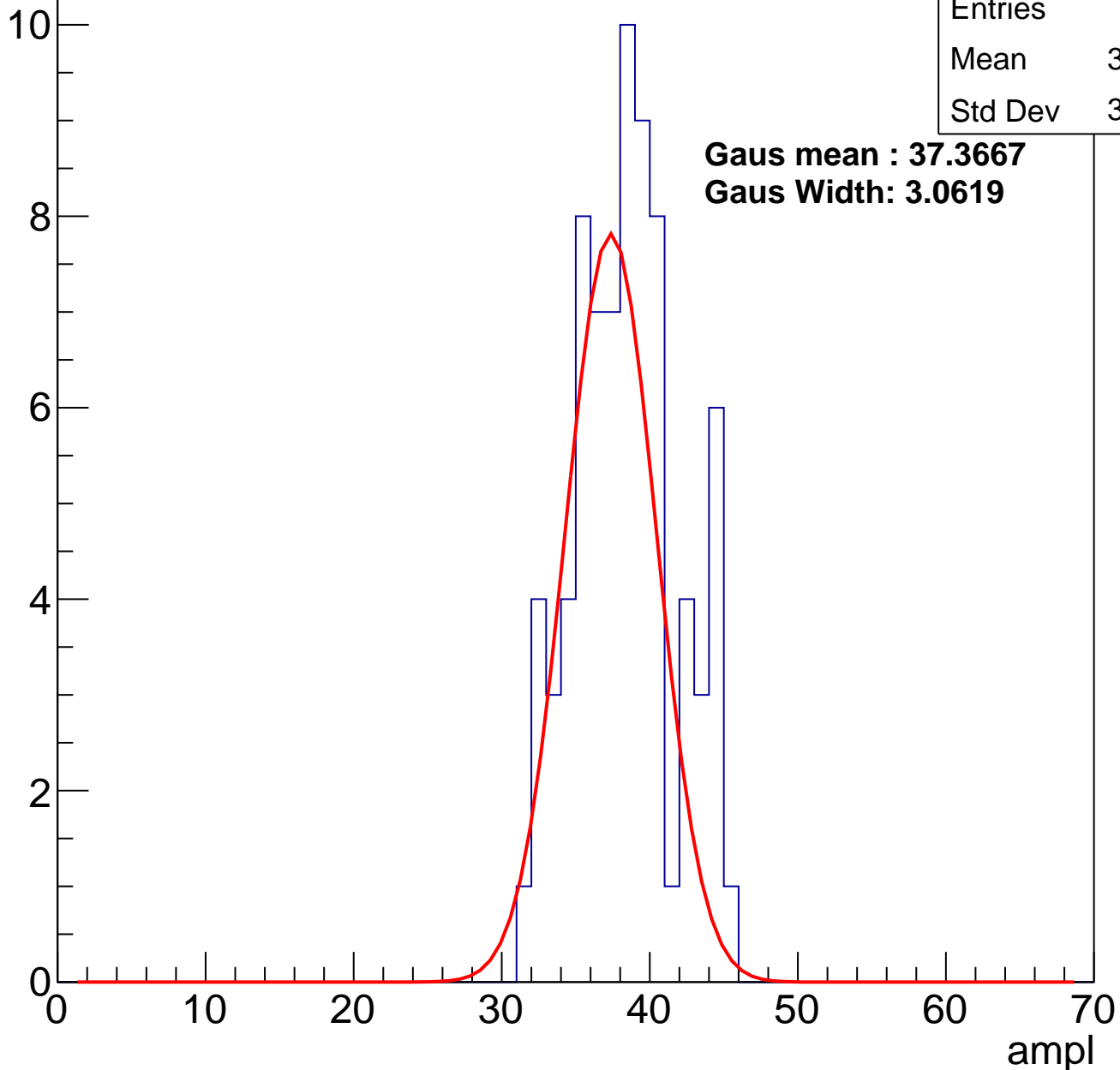
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	37.93
Std Dev	3.427

Gaus mean : 37.3667

Gaus Width: 3.0619

Entry



B1L100S, U10-ch89, adc2

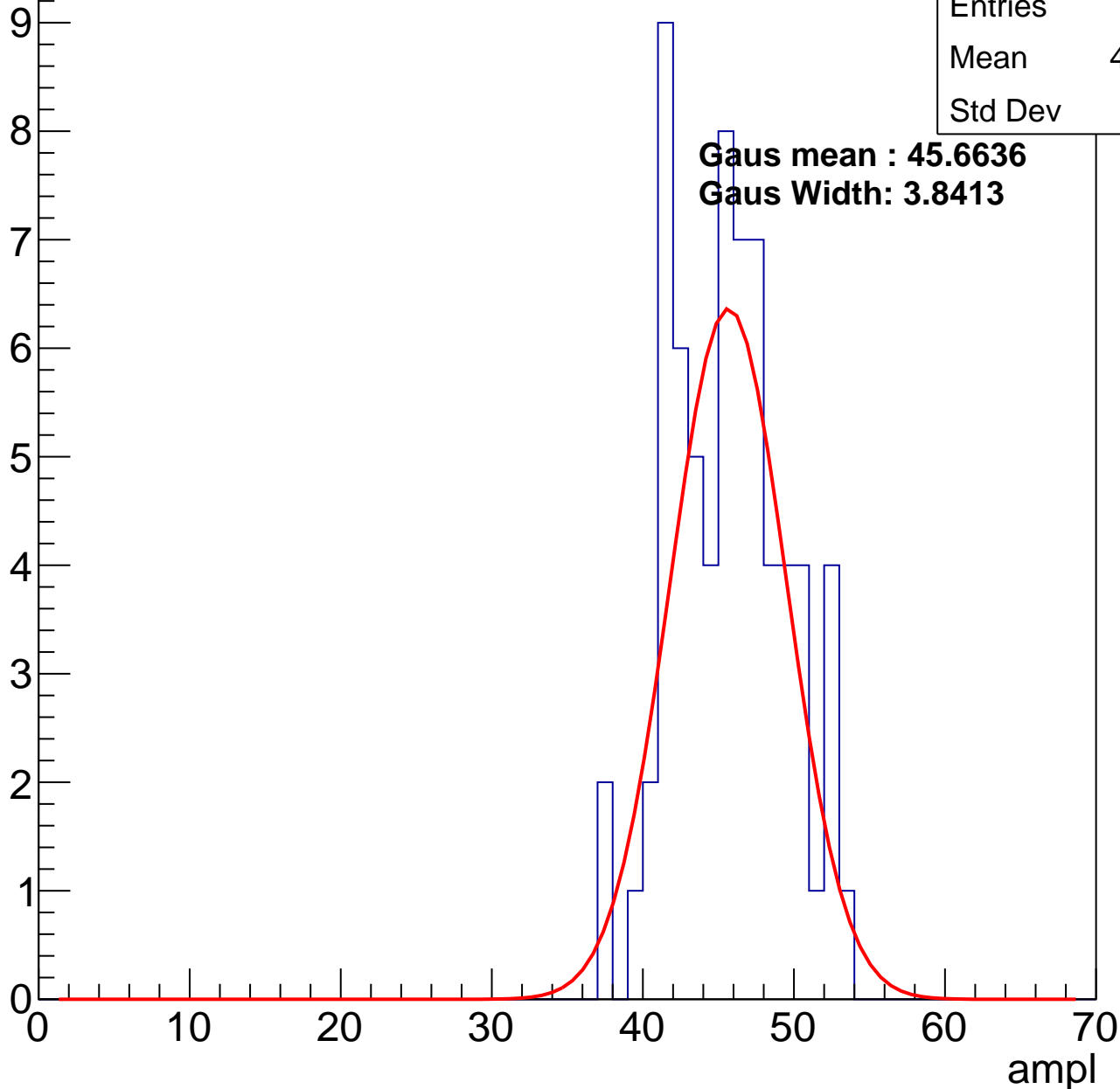
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	45.16
Std Dev	3.74

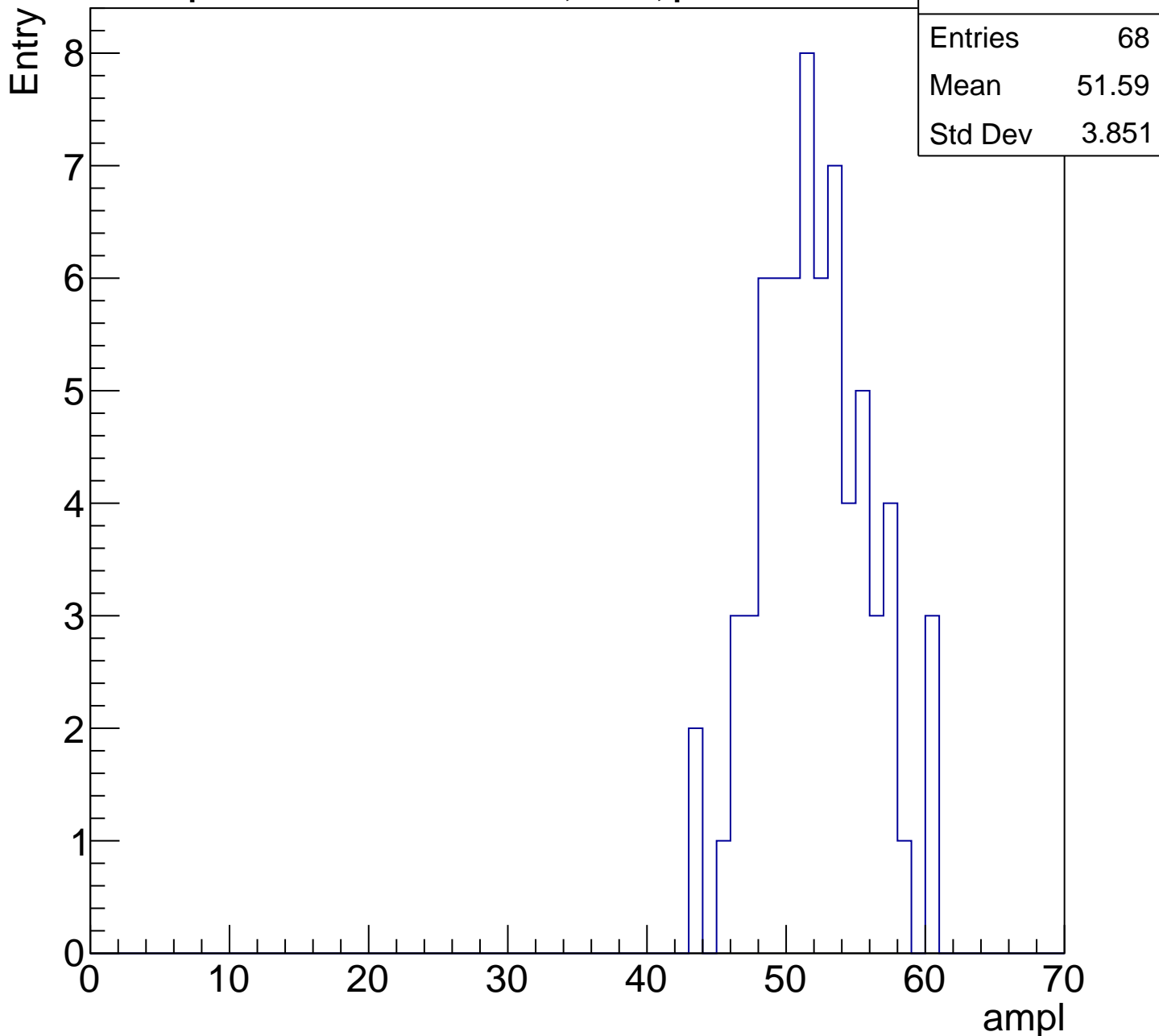
Gaus mean : 45.6636

Gaus Width: 3.8413



B1L100S, U10-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

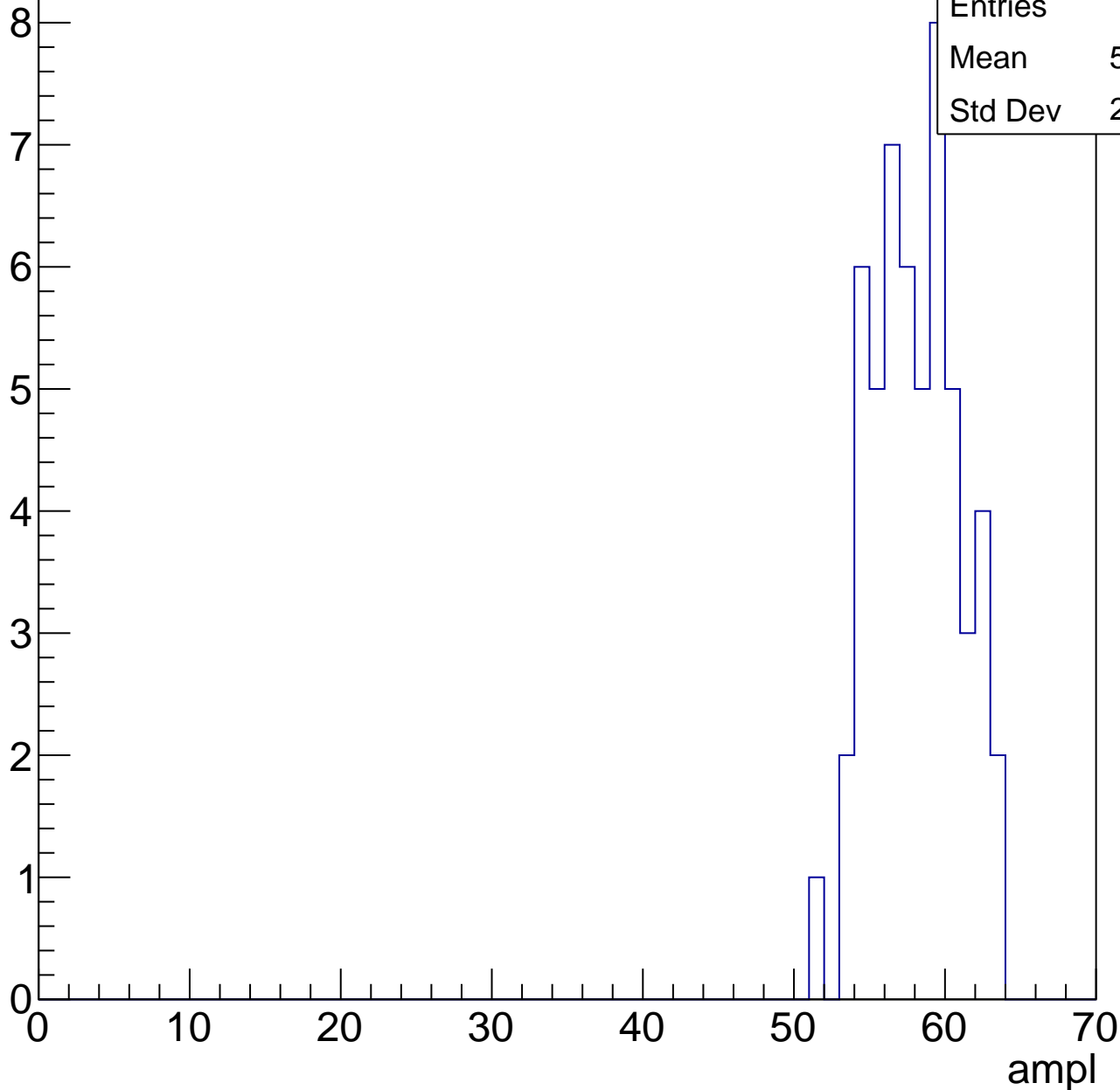


B1L100S, U10-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.57
Std Dev	2.832

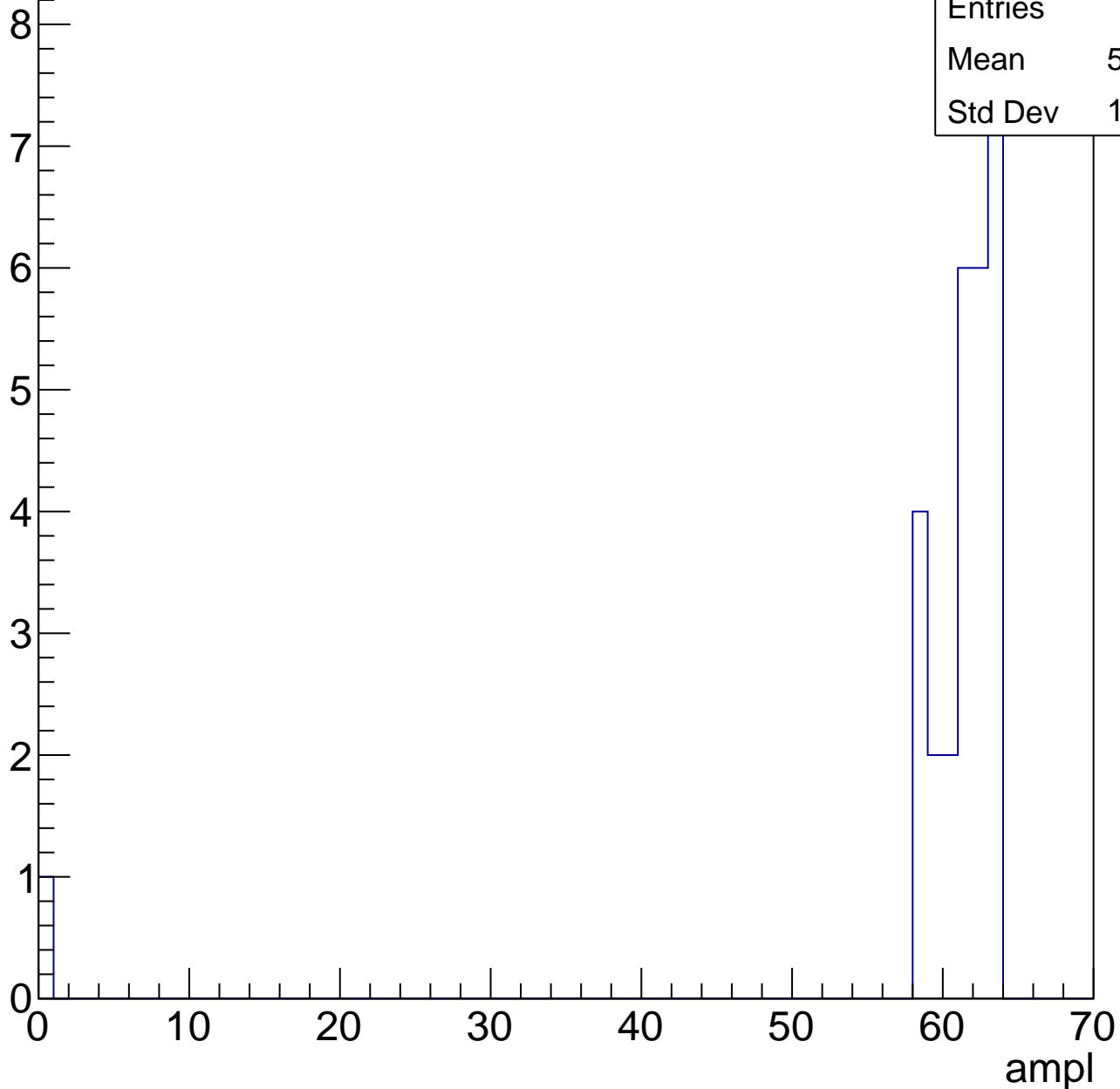


B1L100S, U10-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	59.03
Std Dev	11.28



B1L100S, U10-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch90, adc0

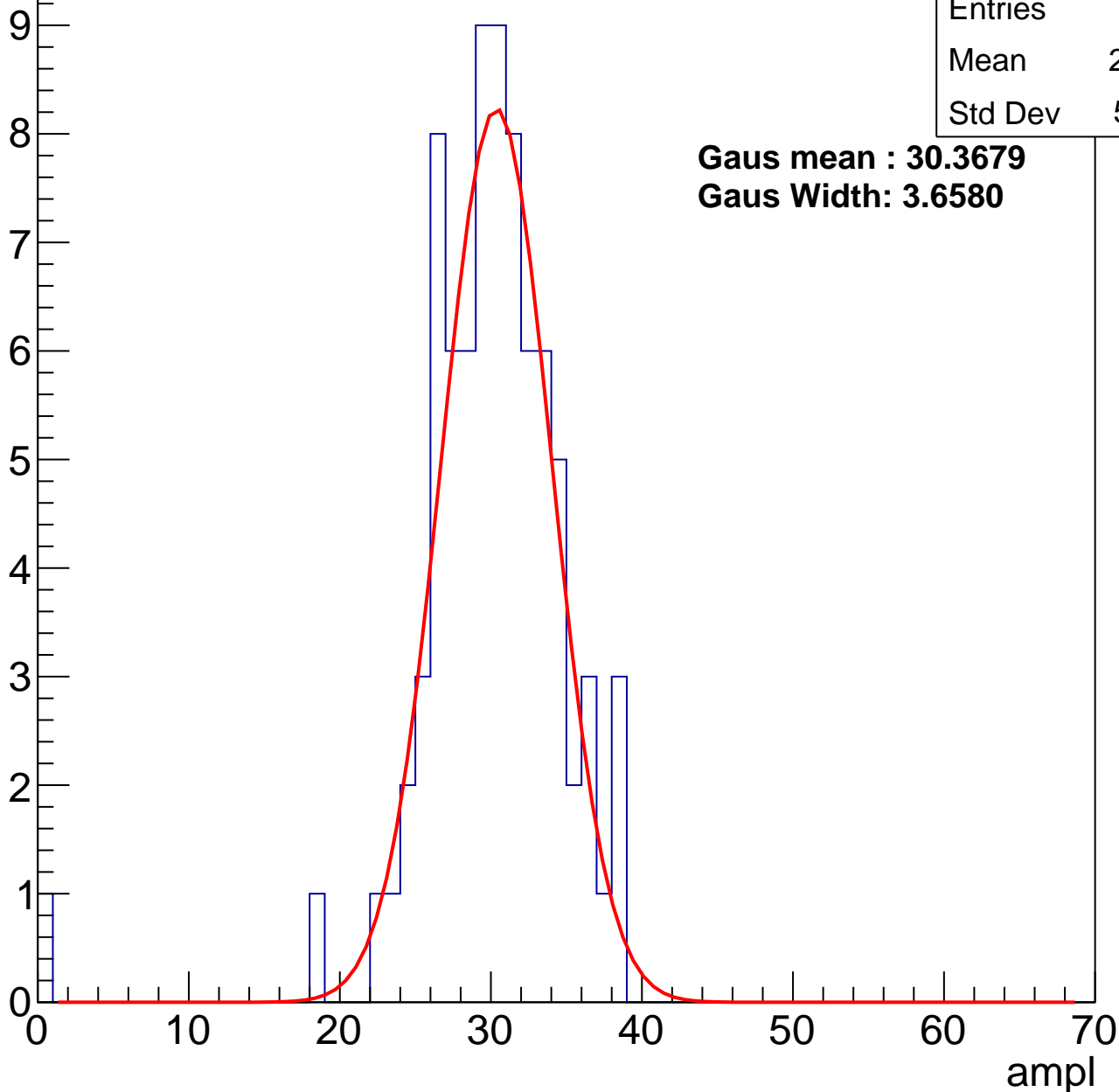
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	29.53
Std Dev	5.051

Gaus mean : 30.3679

Gaus Width: 3.6580



B1L100S, U10-ch90, adc1

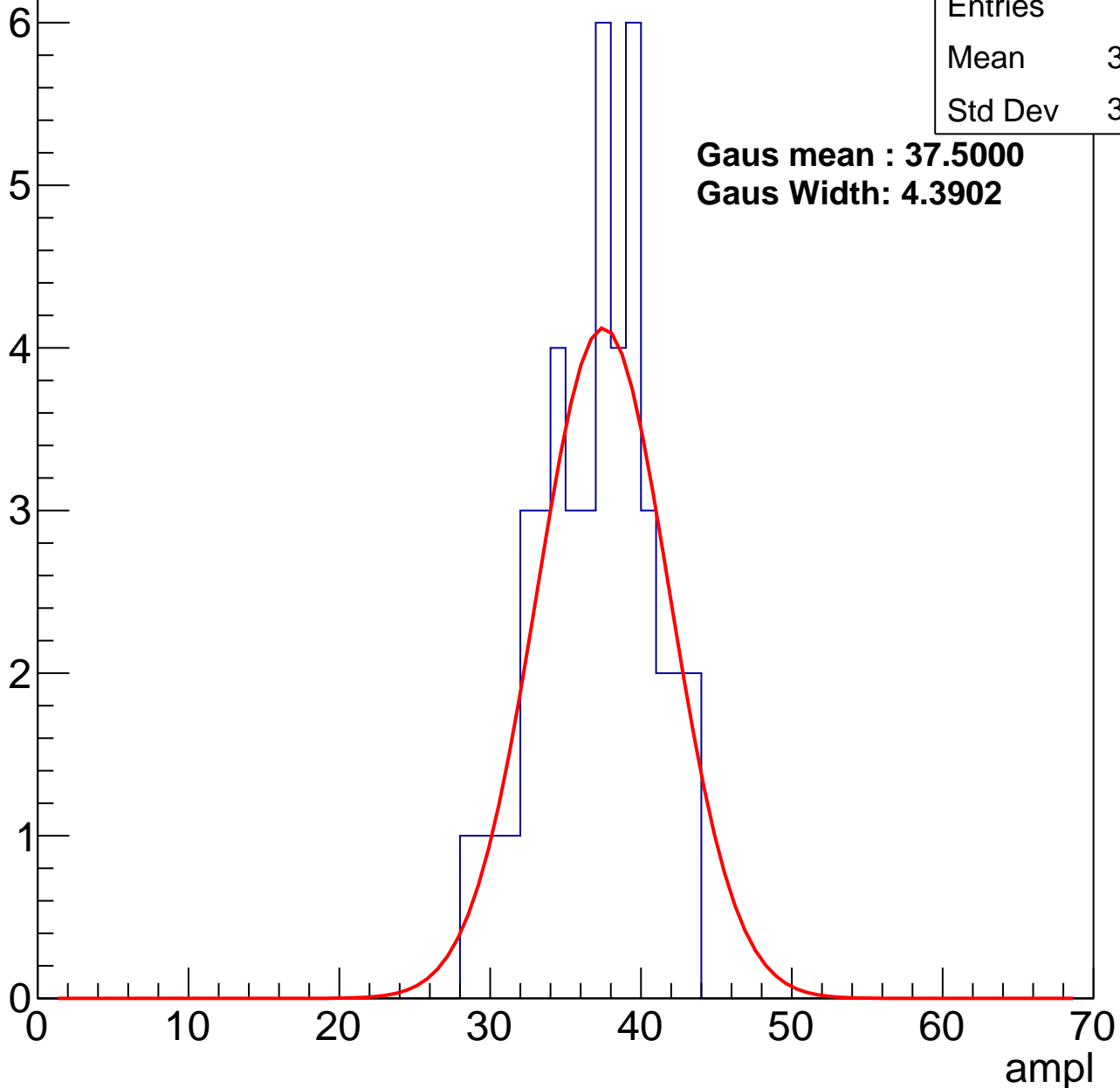
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	36.49
Std Dev	3.655

Gaus mean : 37.5000

Gaus Width: 4.3902



B1L100S, U10-ch90, adc2

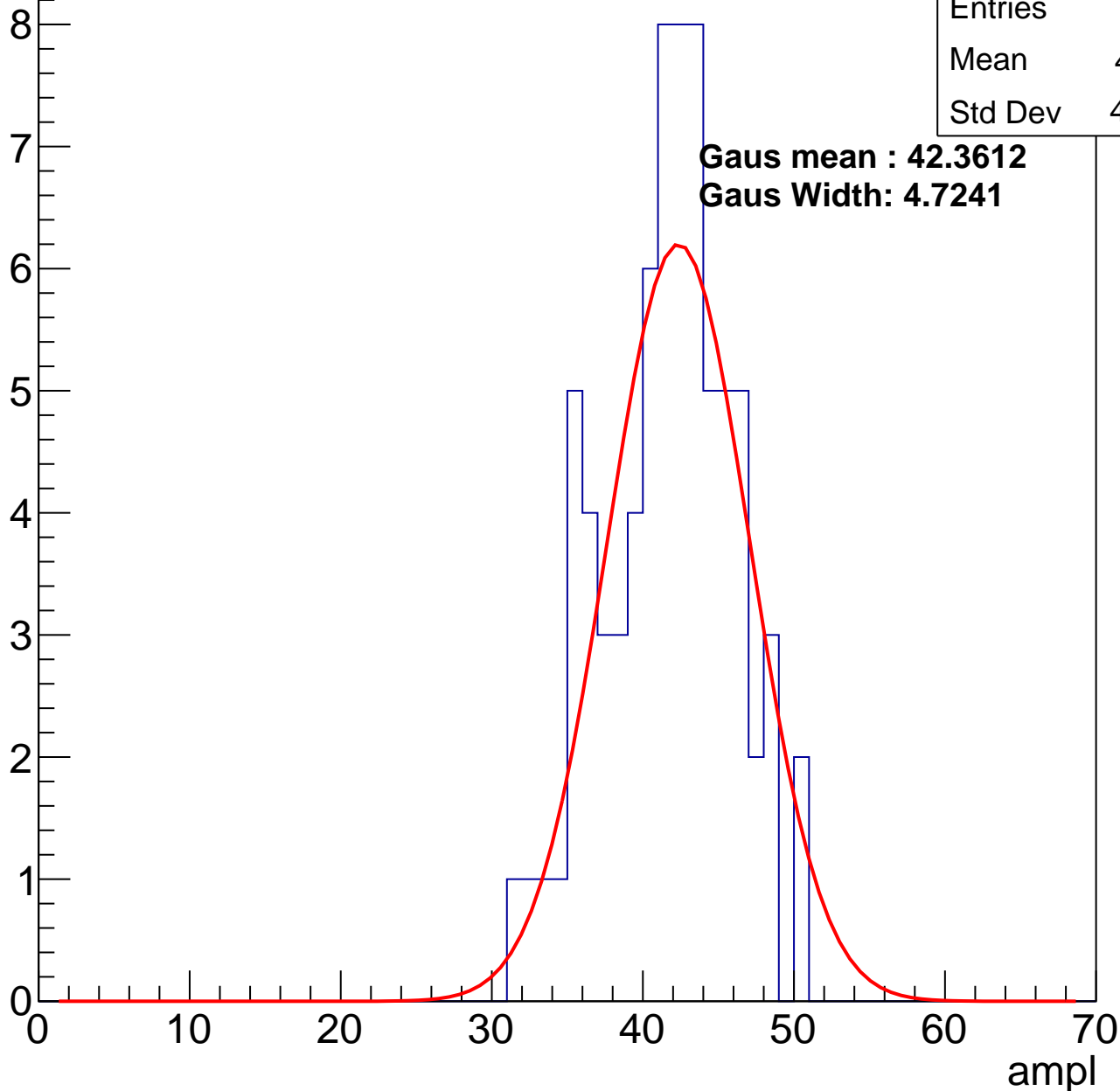
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	41.21
Std Dev	4.225

Gaus mean : 42.3612

Gaus Width: 4.7241

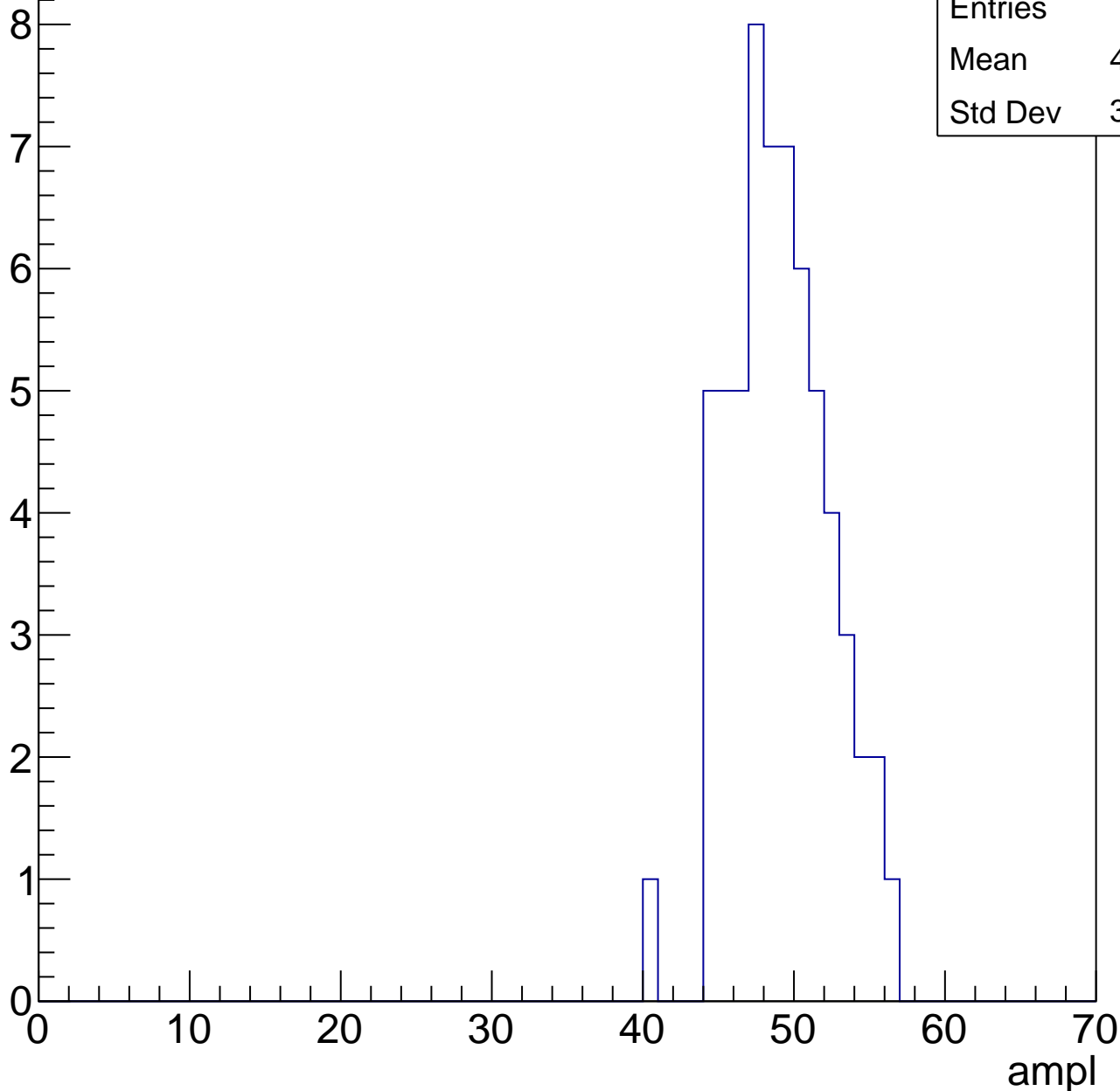


B1L100S, U10-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

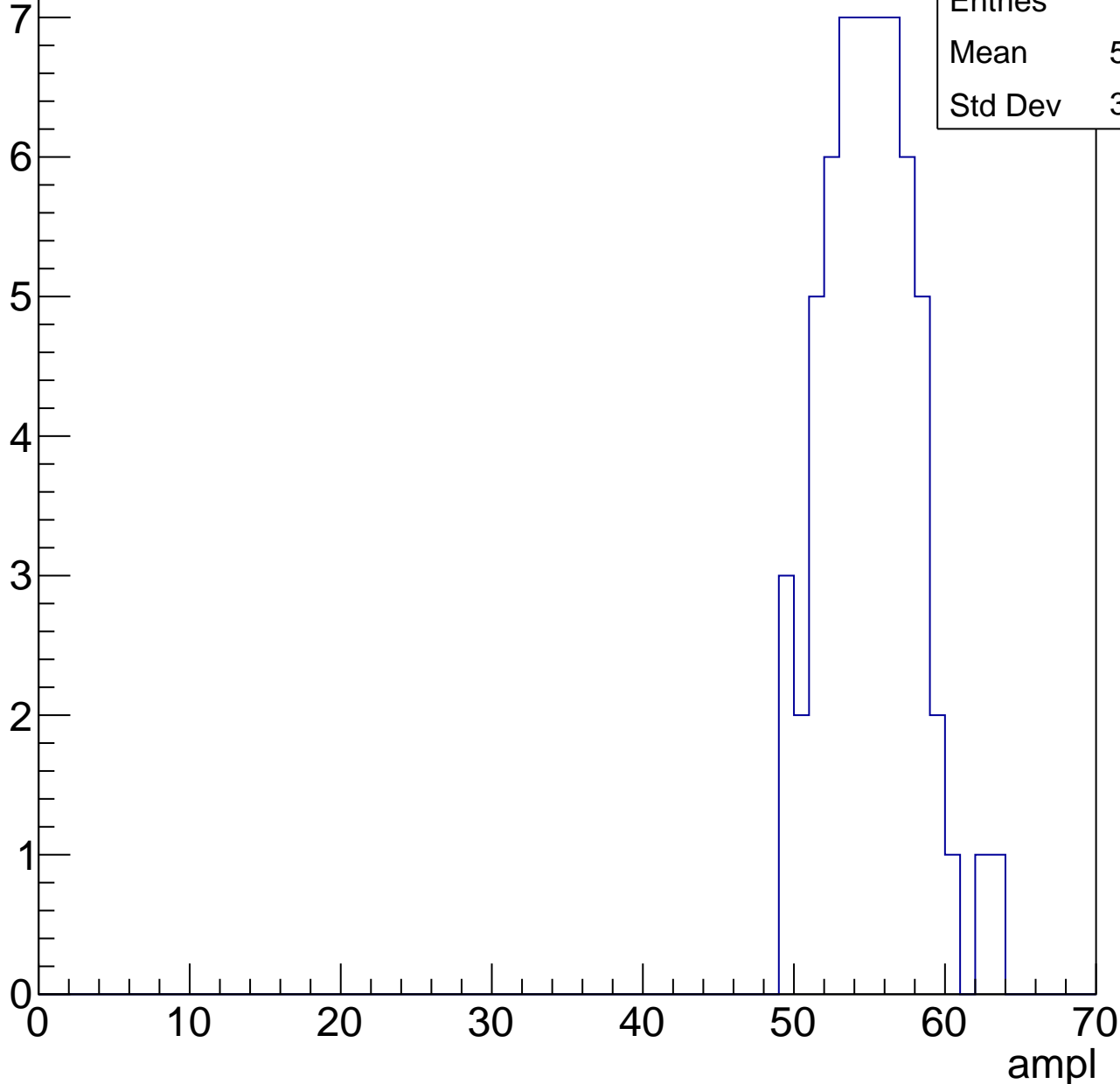
Entries	61
Mean	48.62
Std Dev	3.245



B1L100S, U10-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

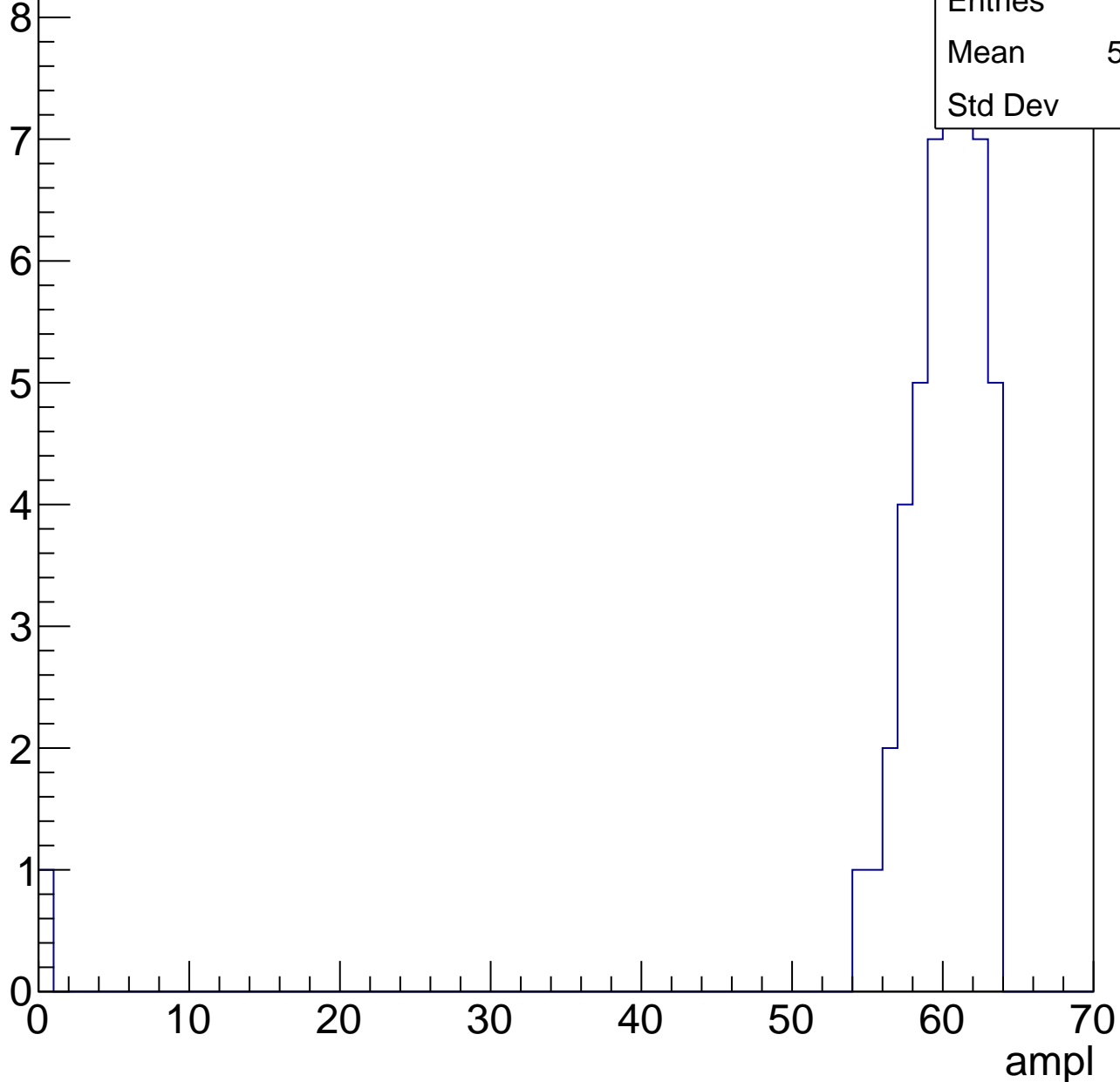


Entries	60
Mean	54.58
Std Dev	3.062

B1L100S, U10-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

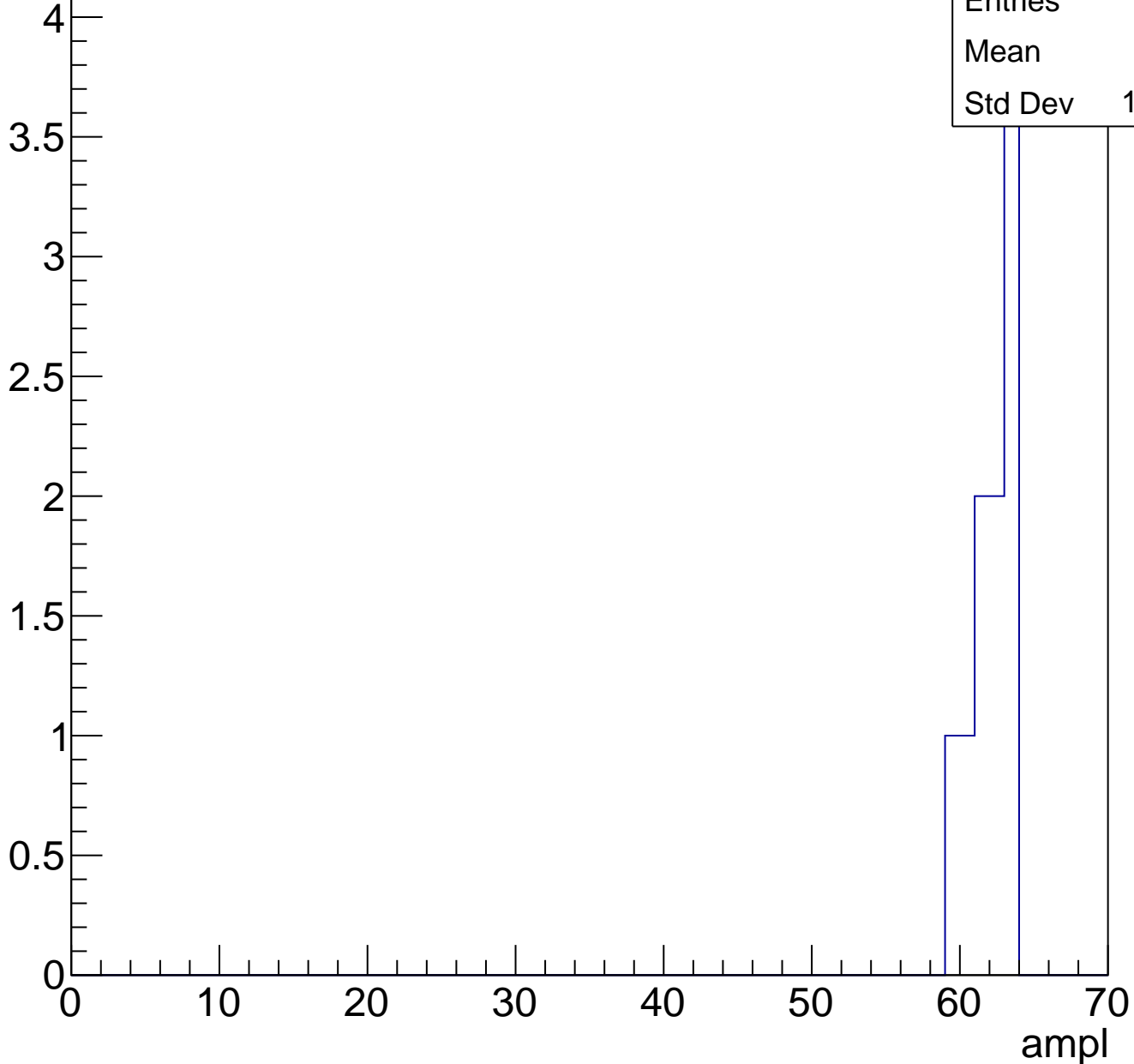
Entry



B1L100S, U10-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch91, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	29.12
Std Dev	4.641

Gaus mean : 30.2449

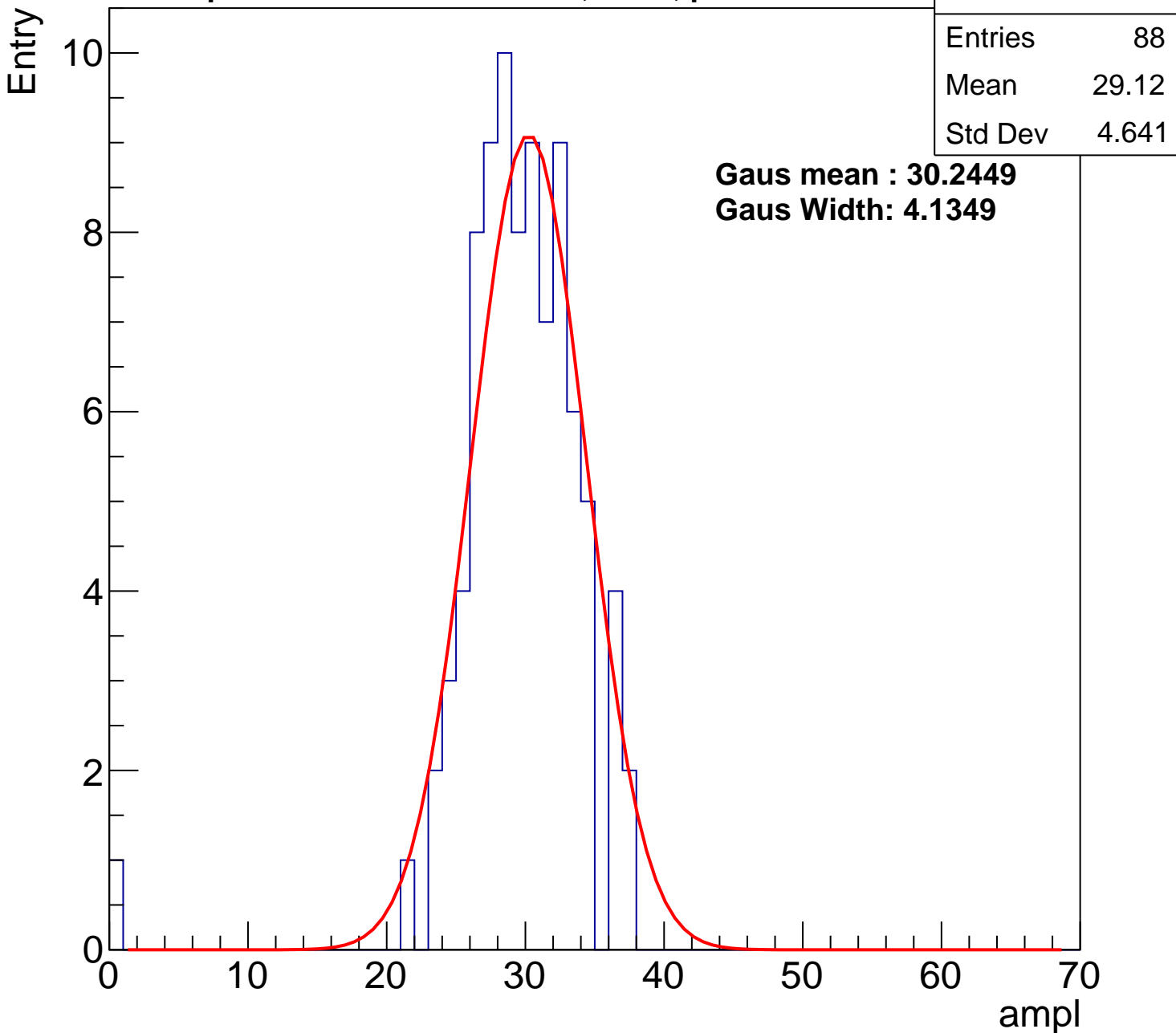
Gaus Width: 4.1349

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch91, adc1

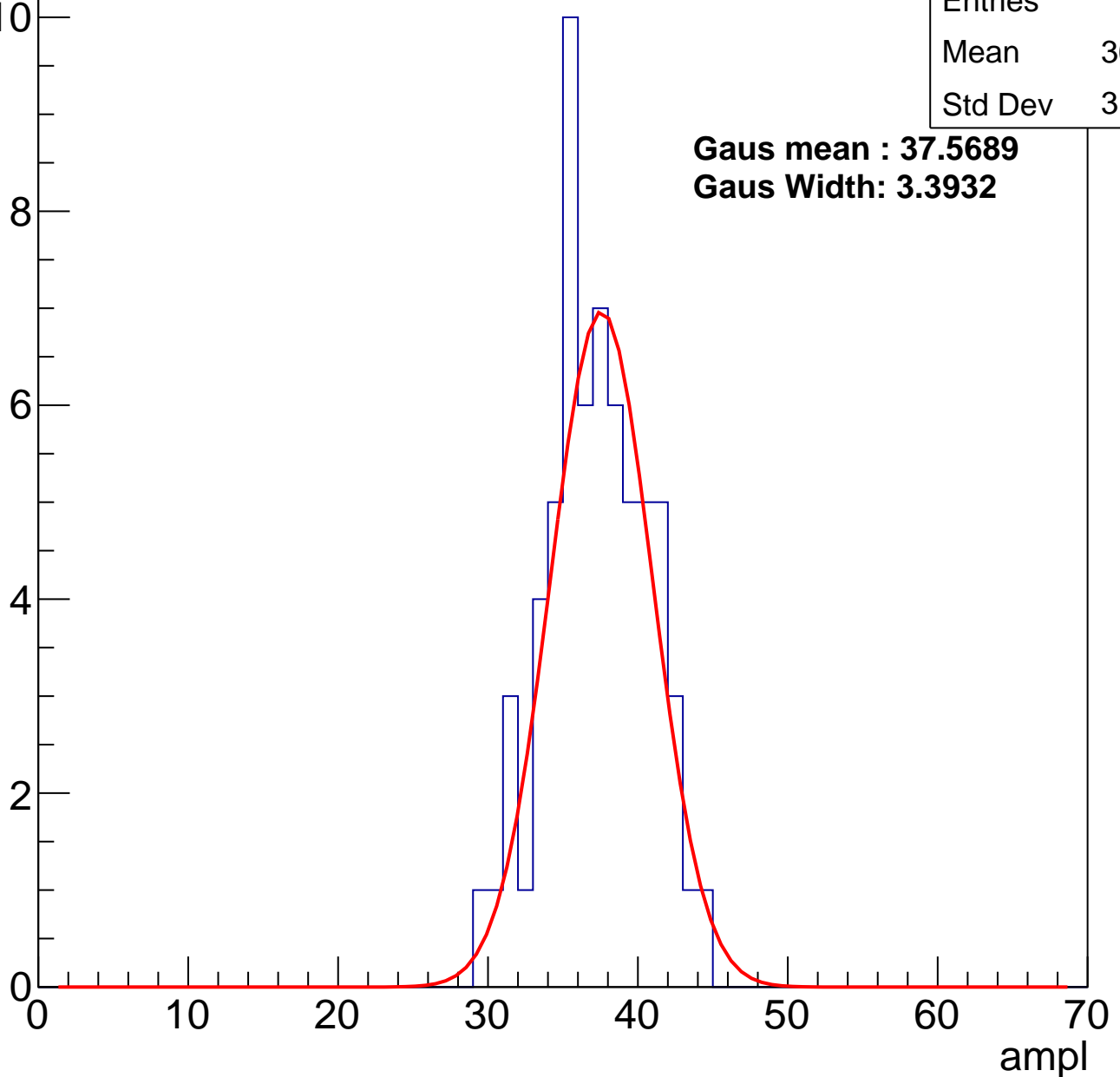
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.75
Std Dev	3.326

Gaus mean : 37.5689

Gaus Width: 3.3932



B1L100S, U10-ch91, adc2

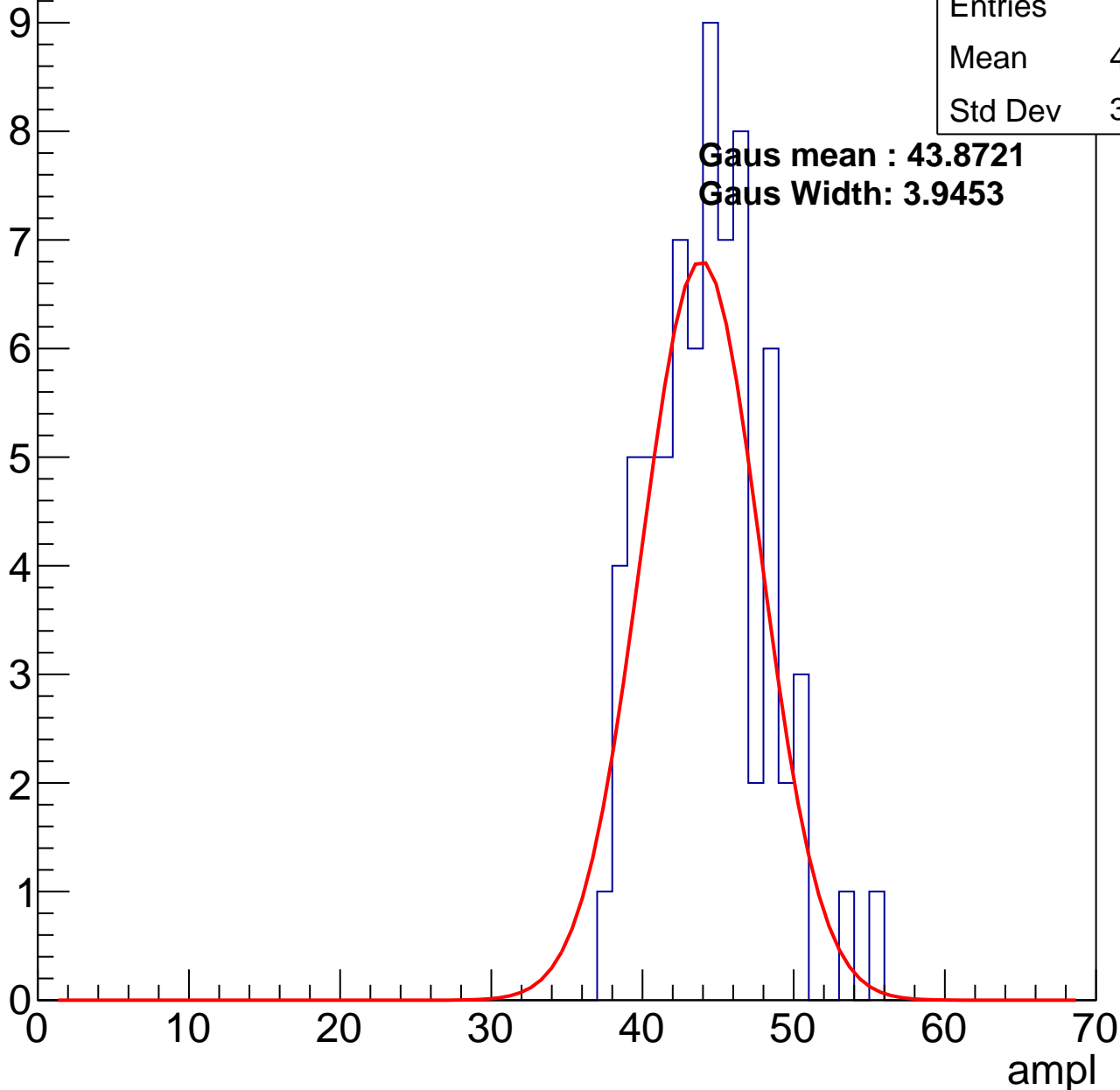
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.86
Std Dev	3.705

Gaus mean : 43.8721

Gaus Width: 3.9453

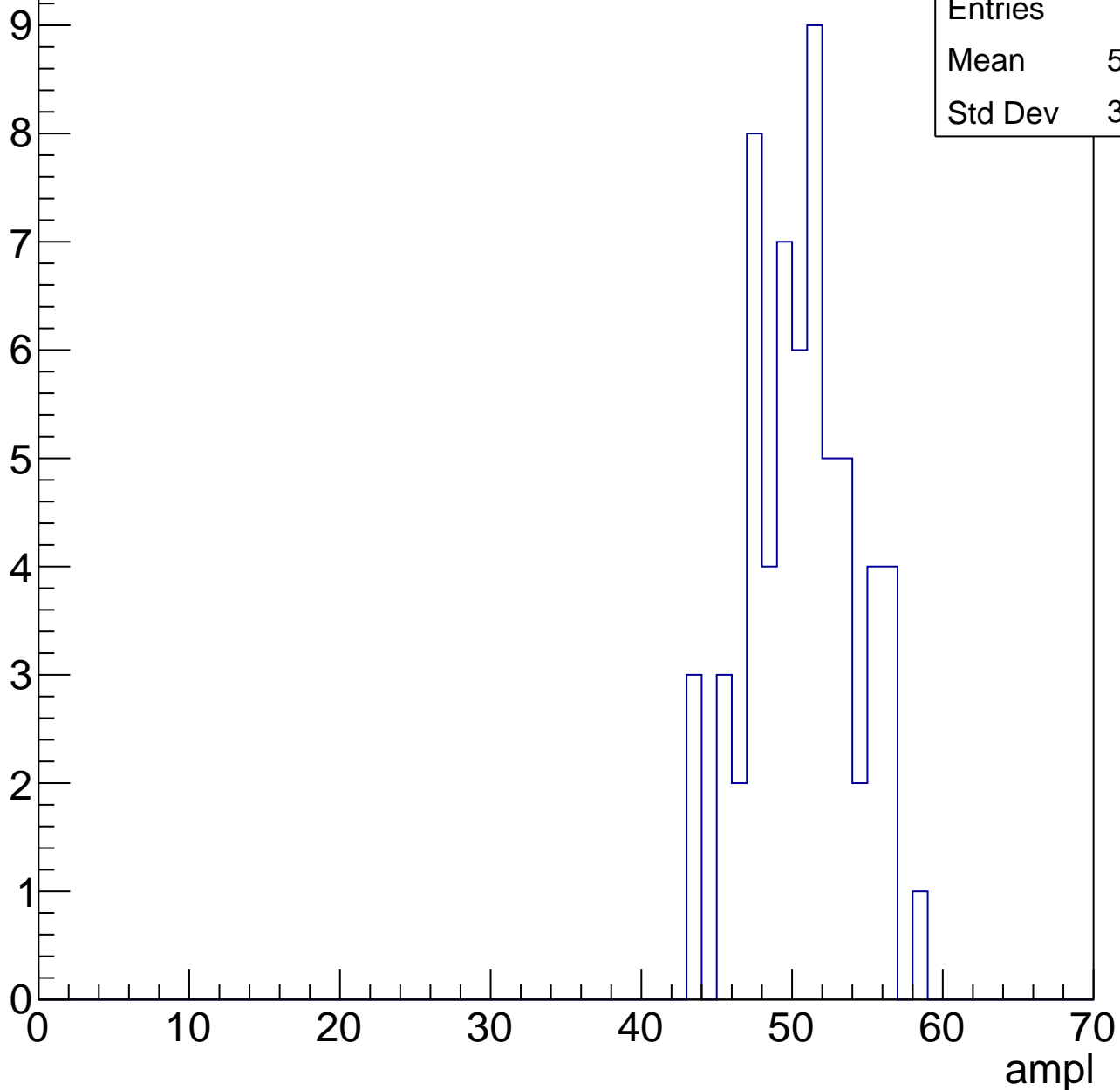


B1L100S, U10-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	50.17
Std Dev	3.476

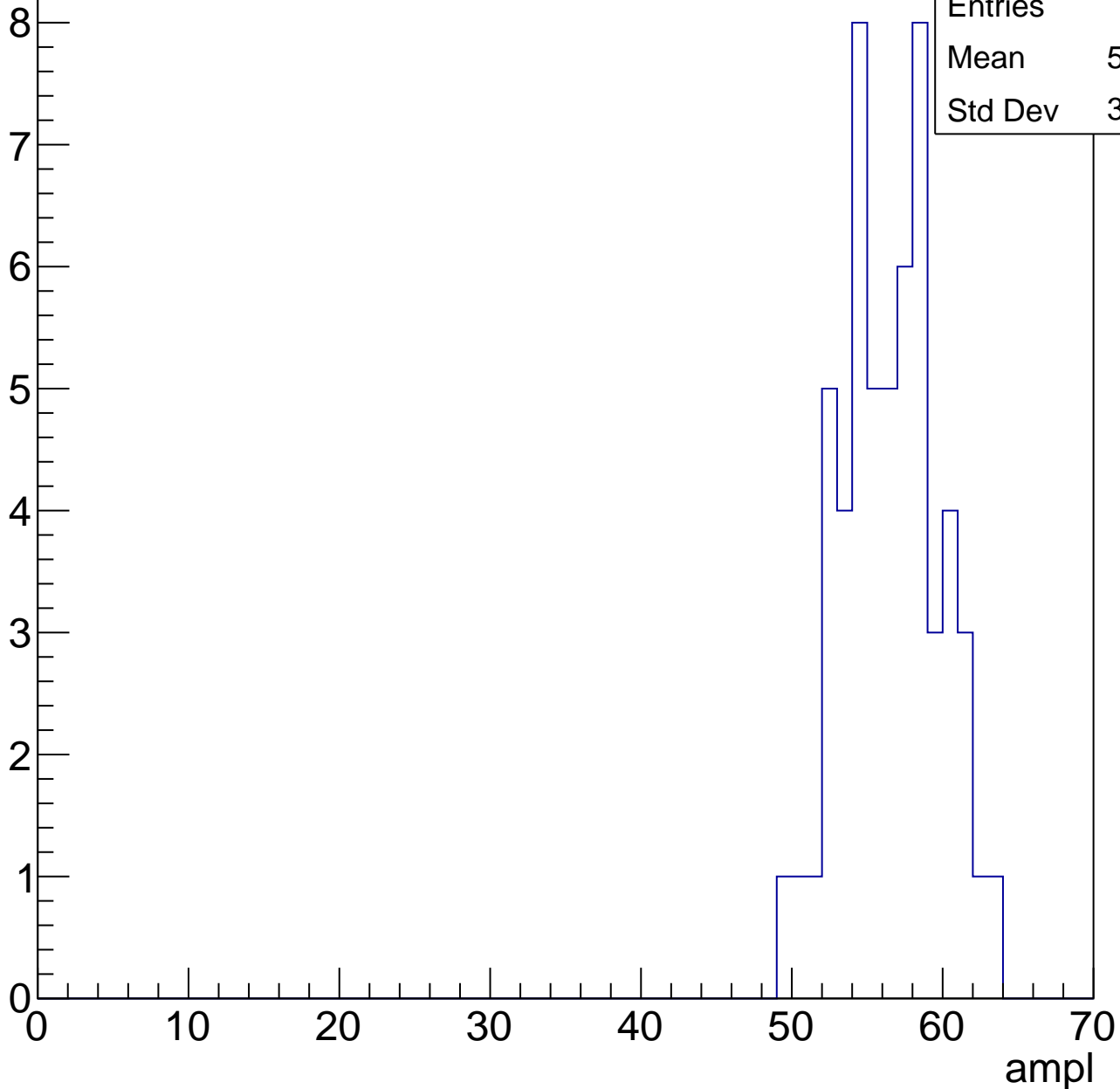


B1L100S, U10-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

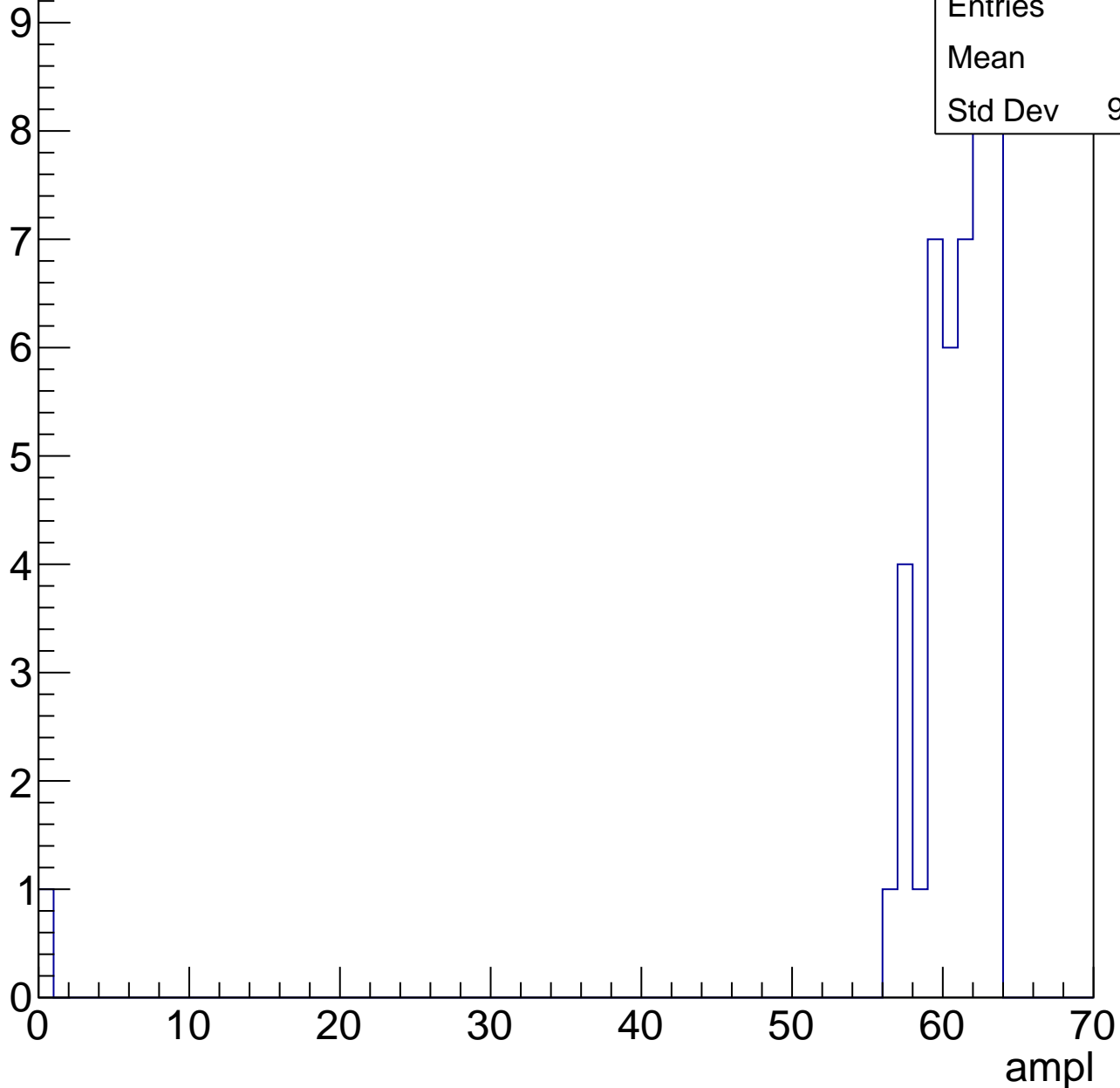
Entries	56
Mean	56.07
Std Dev	3.139



B1L100S, U10-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

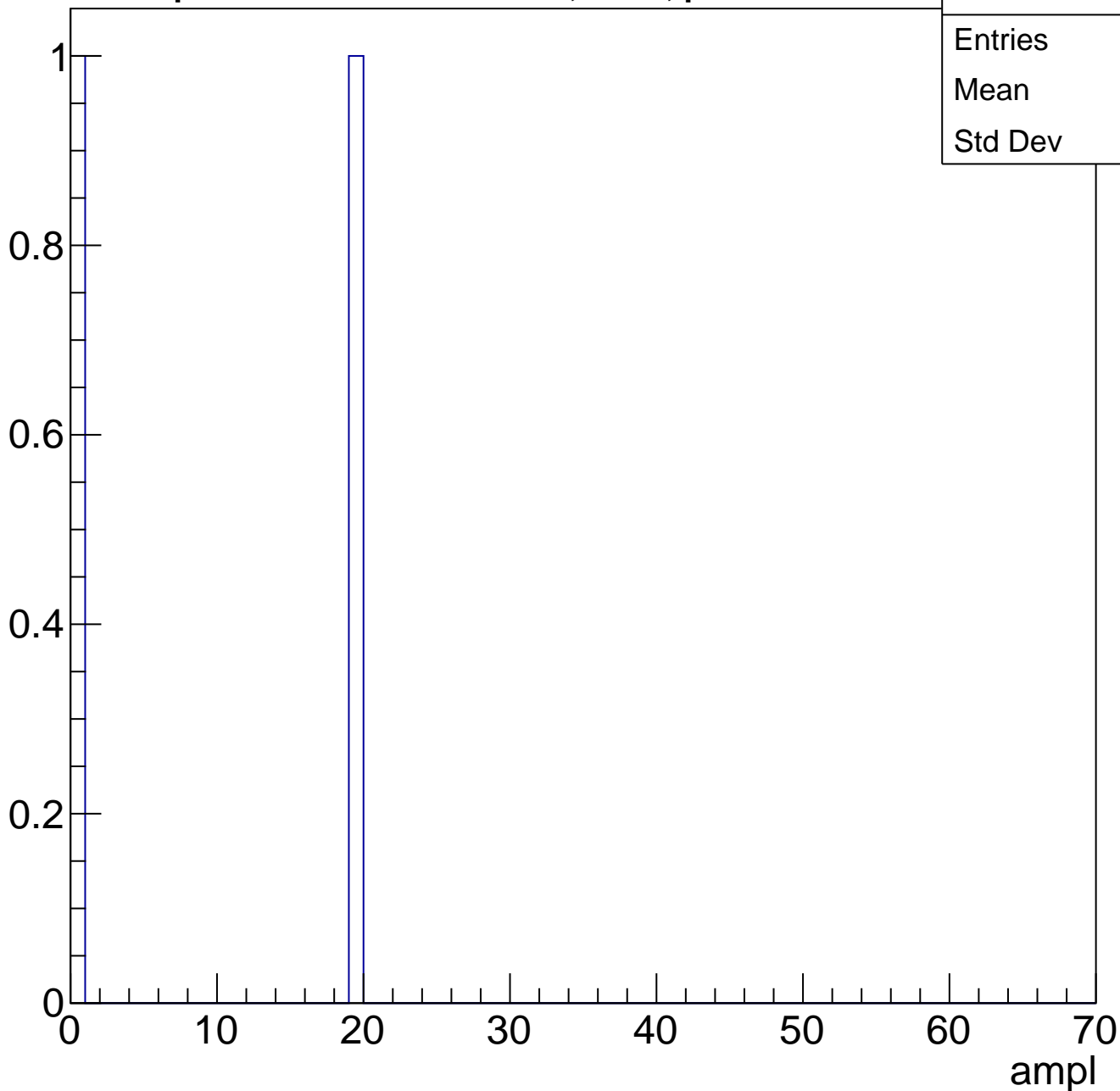
Entry



B1L100S, U10-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L100S, U10-ch92, adc0

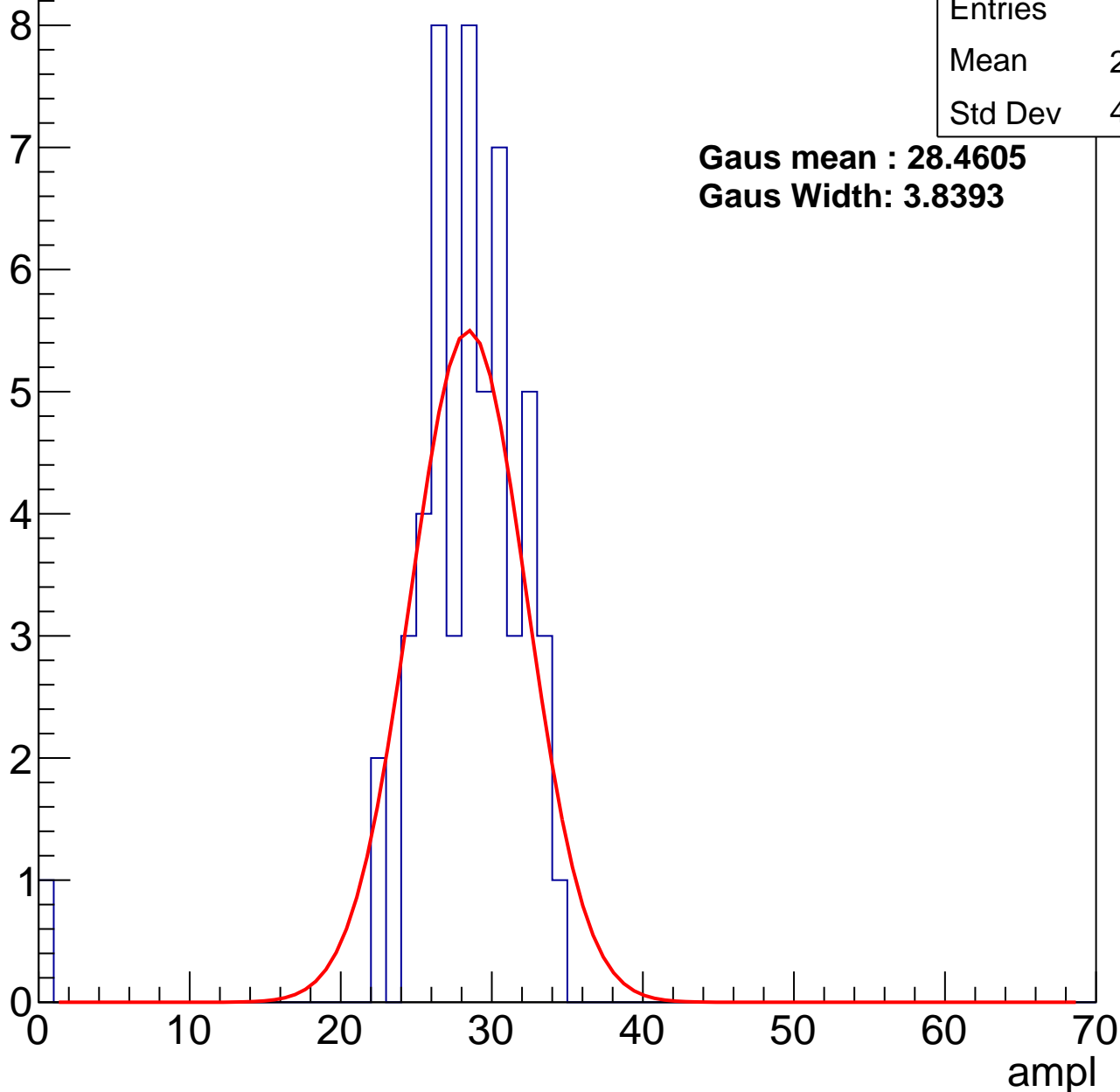
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	27.74
Std Dev	4.802

Gaus mean : 28.4605

Gaus Width: 3.8393



B1L100S, U10-ch92, adc1

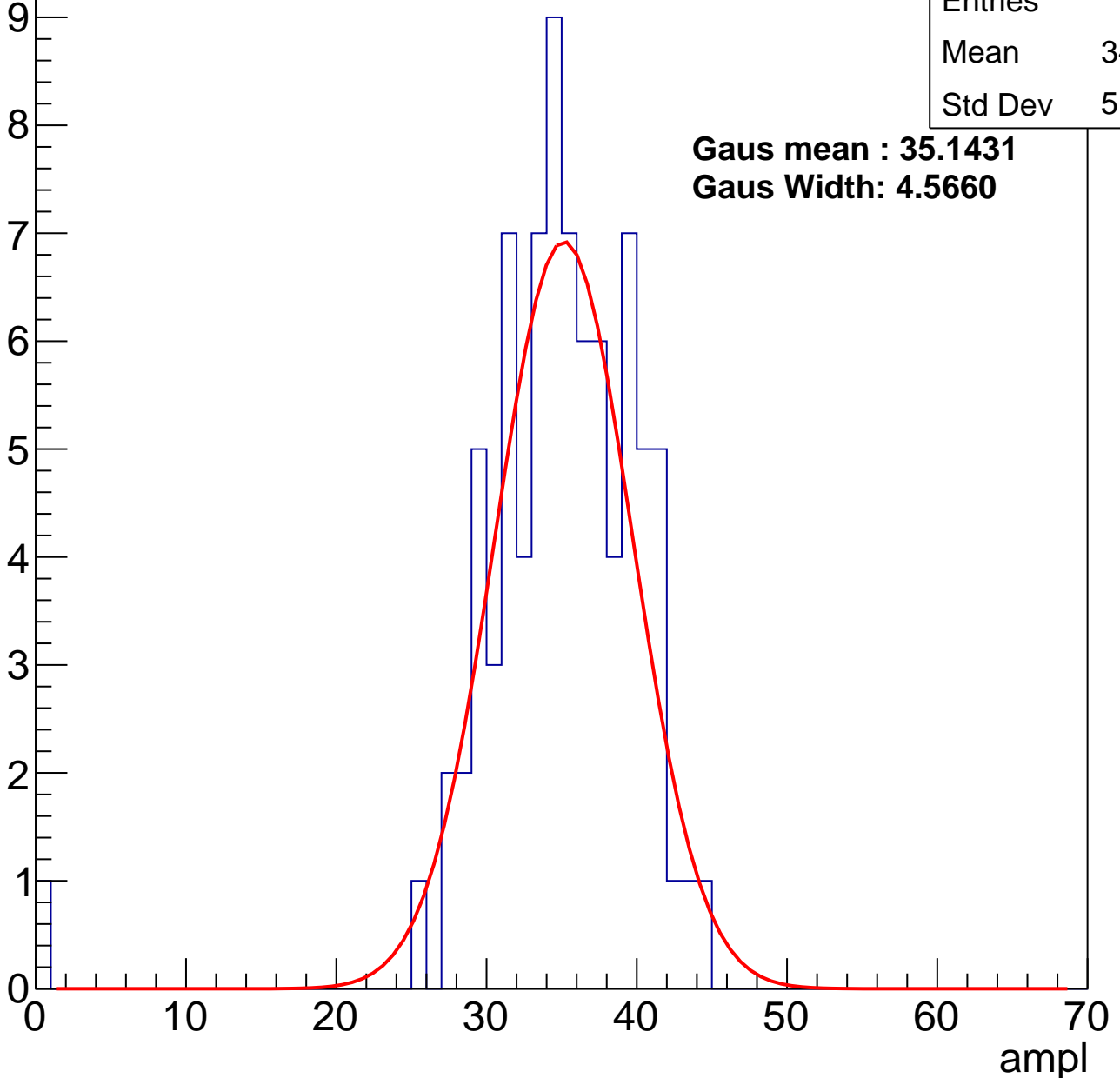
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	34.45
Std Dev	5.609

Gaus mean : 35.1431

Gaus Width: 4.5660



B1L100S, U10-ch92, adc2

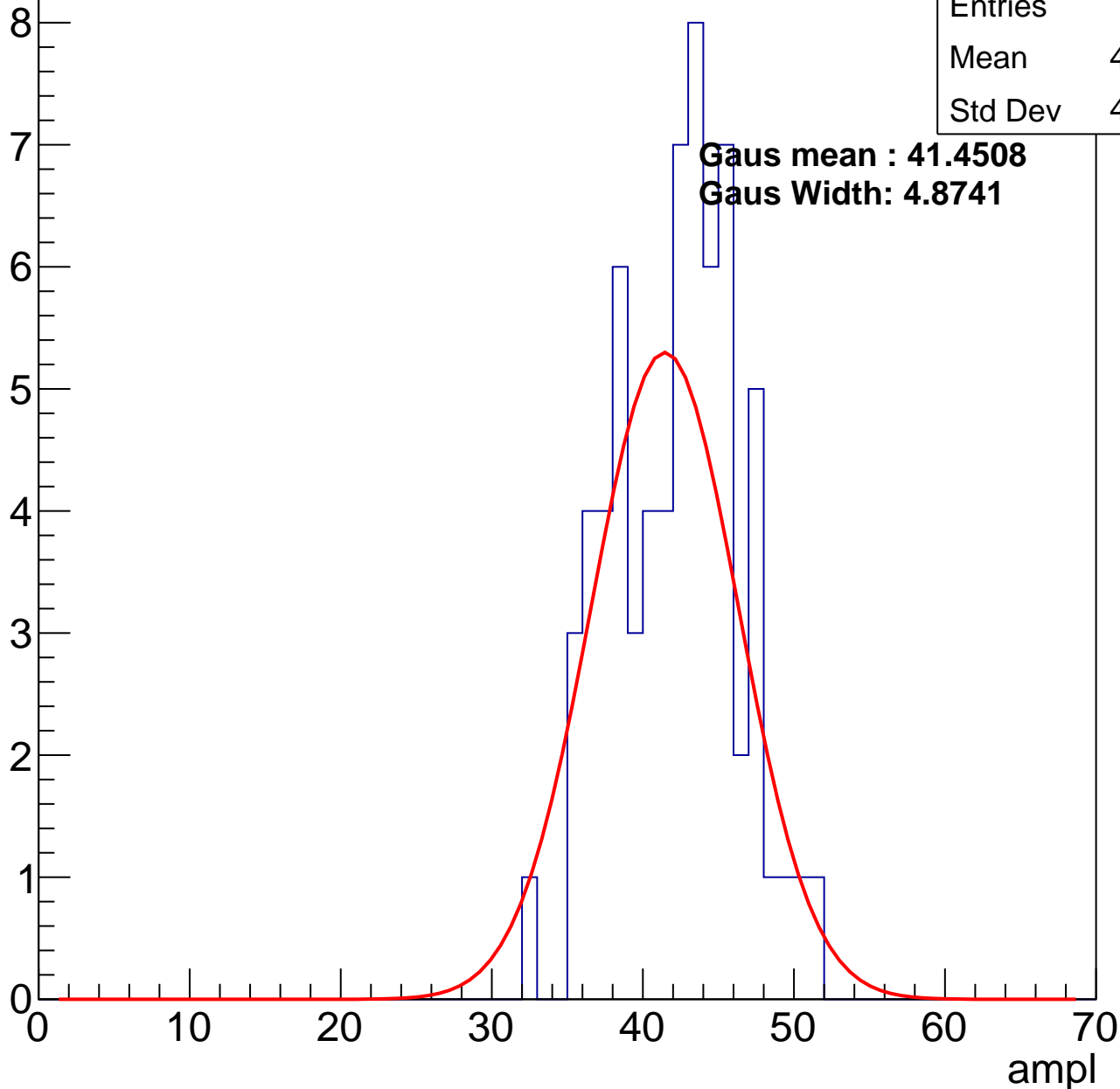
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	41.76
Std Dev	4.044

Gaus mean : 41.4508

Gaus Width: 4.8741

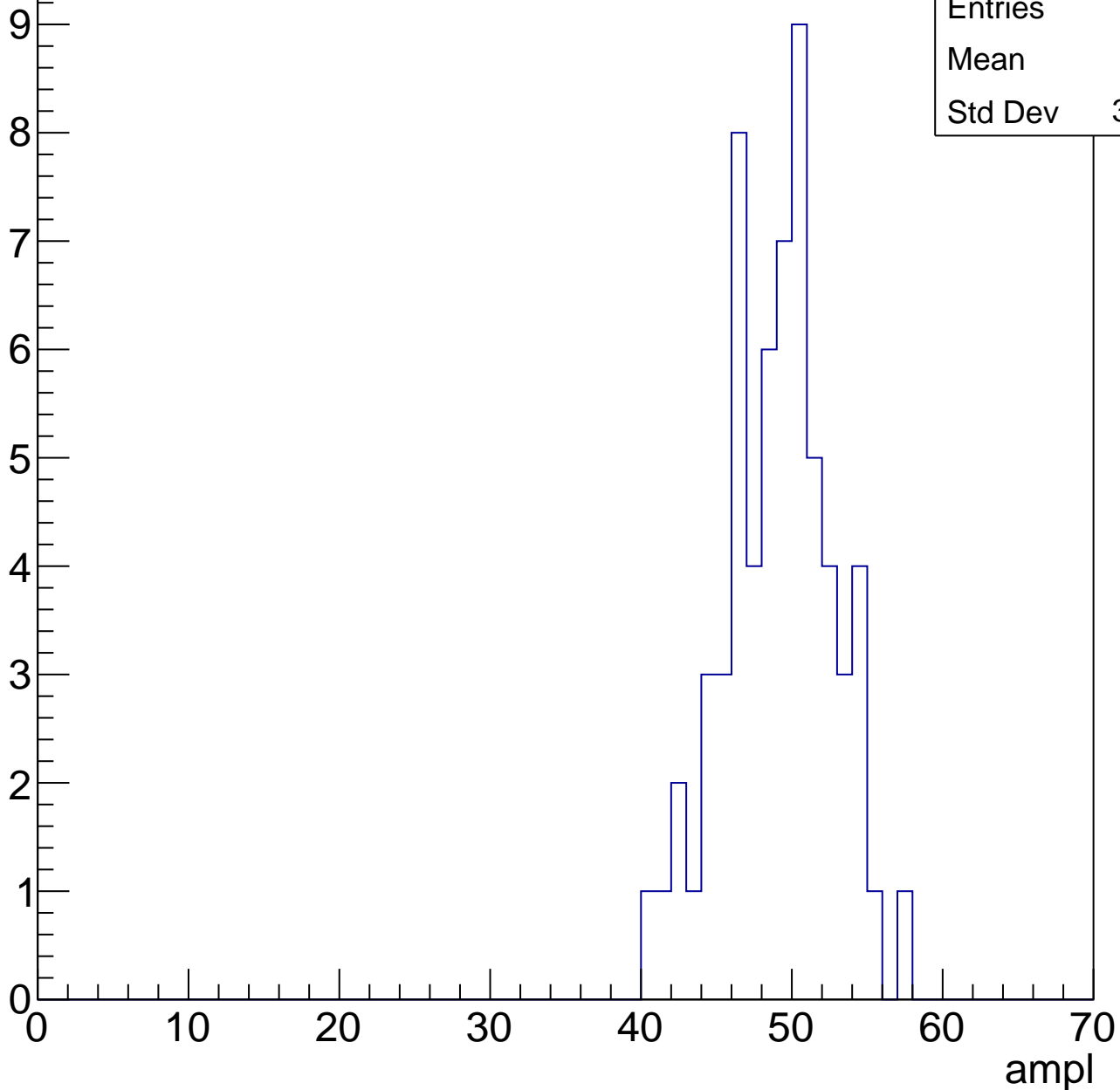


B1L100S, U10-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.6
Std Dev	3.561

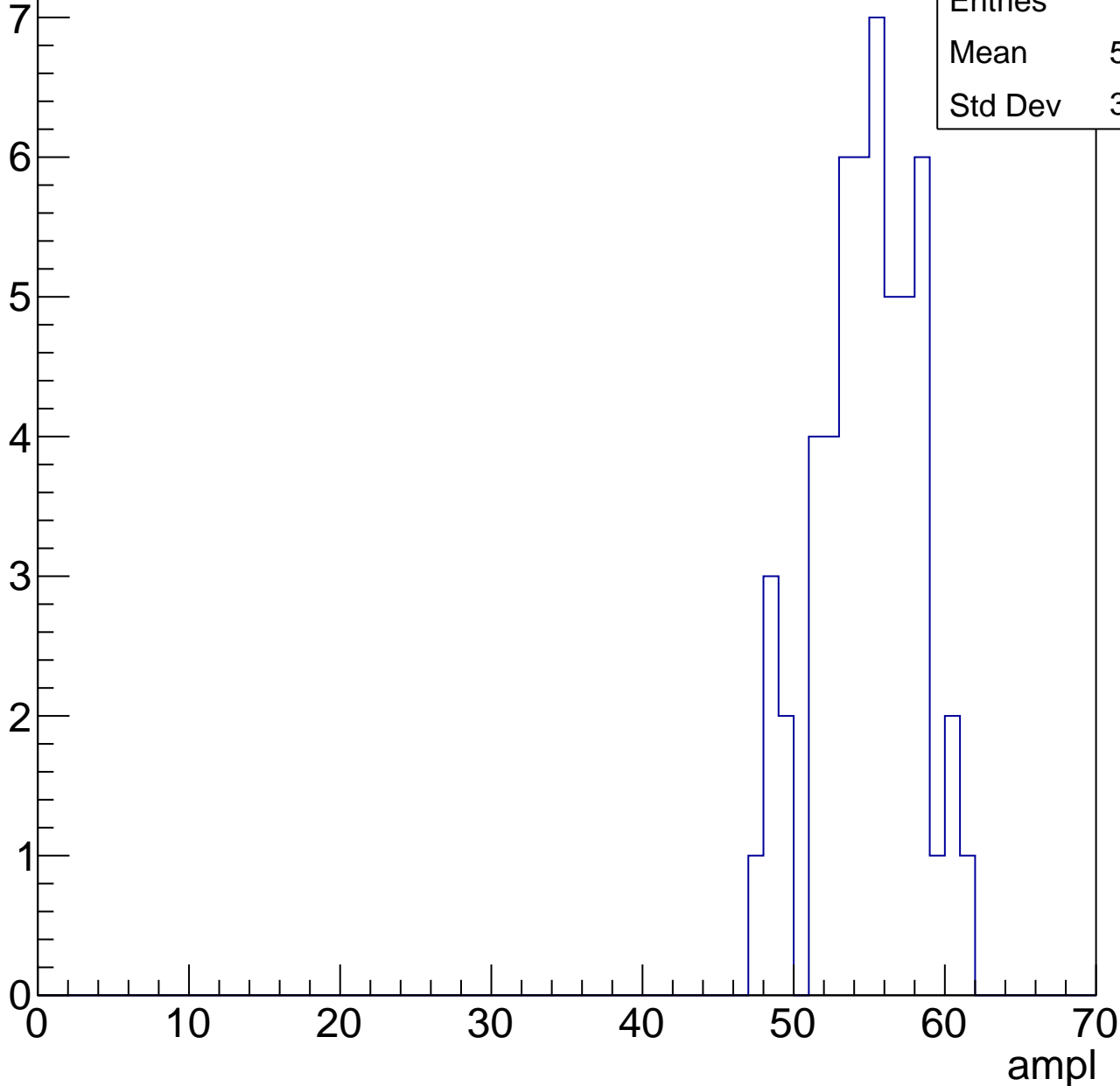


B1L100S, U10-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

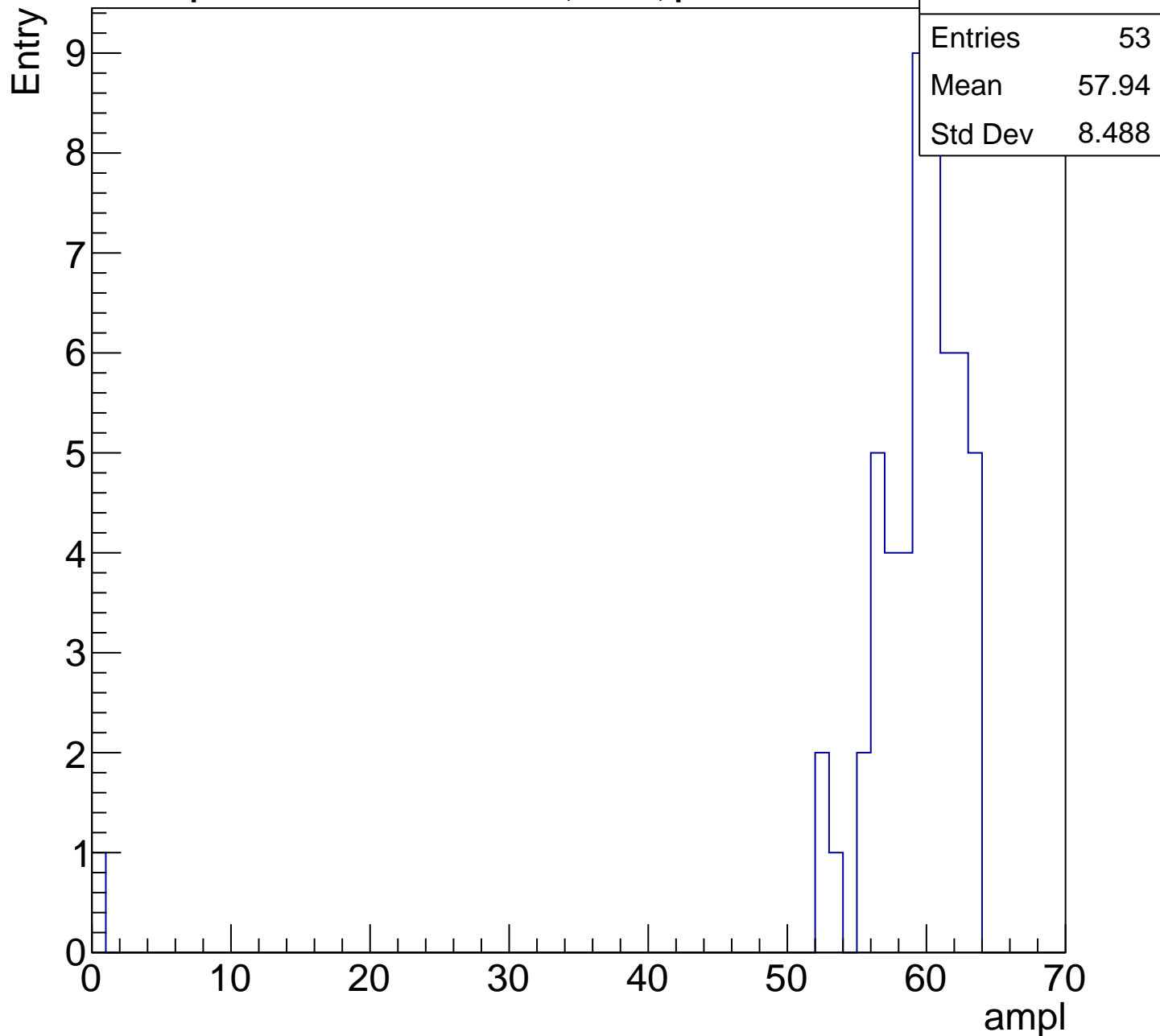
Entry

Entries	53
Mean	54.36
Std Dev	3.286



B1L100S, U10-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

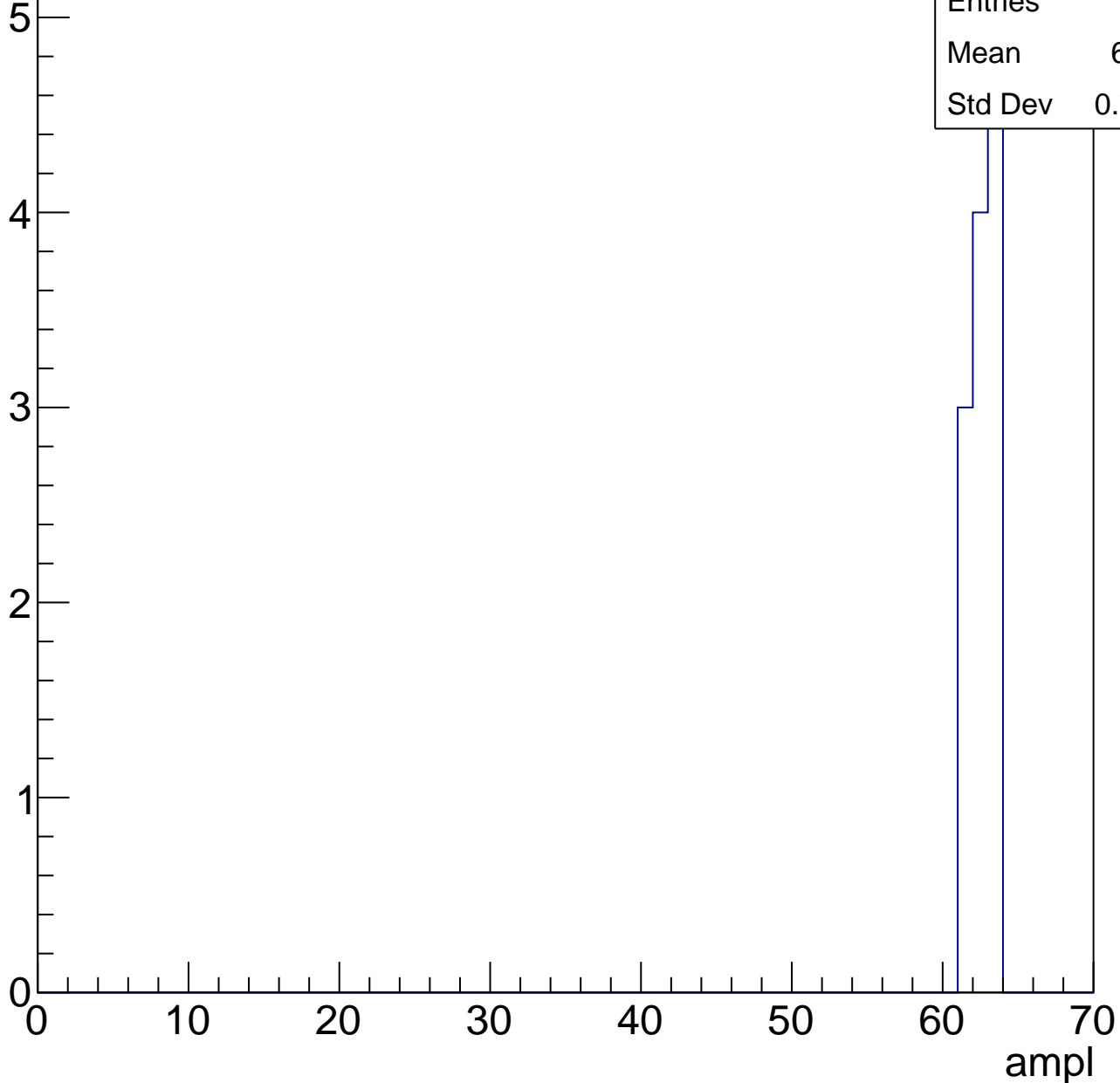


B1L100S, U10-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	62.17
Std Dev	0.7993



B1L100S, U10-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U10-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	27.91
Std Dev	3.211

Gaus mean : 28.1167

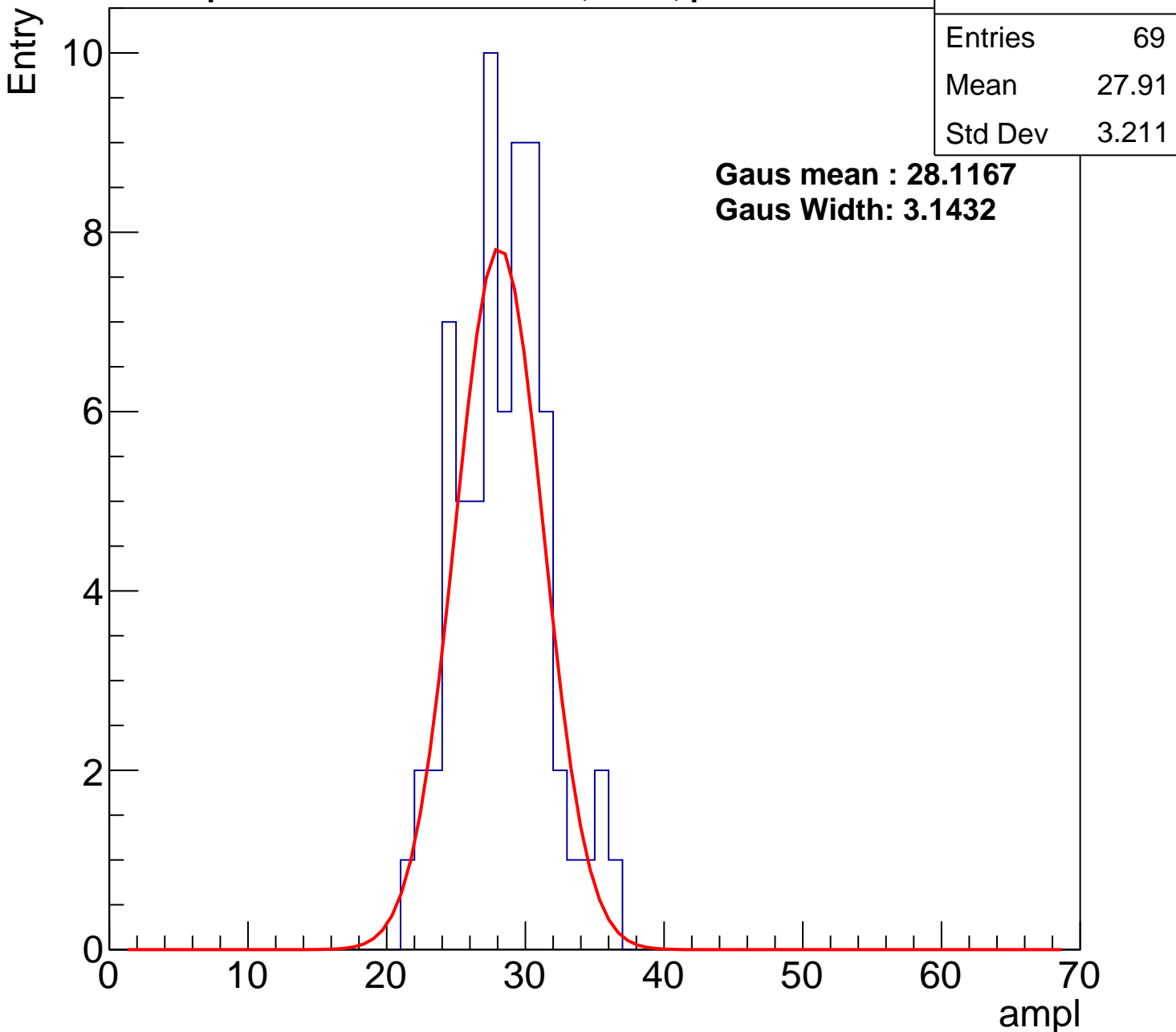
Gaus Width: 3.1432

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch93, adc1

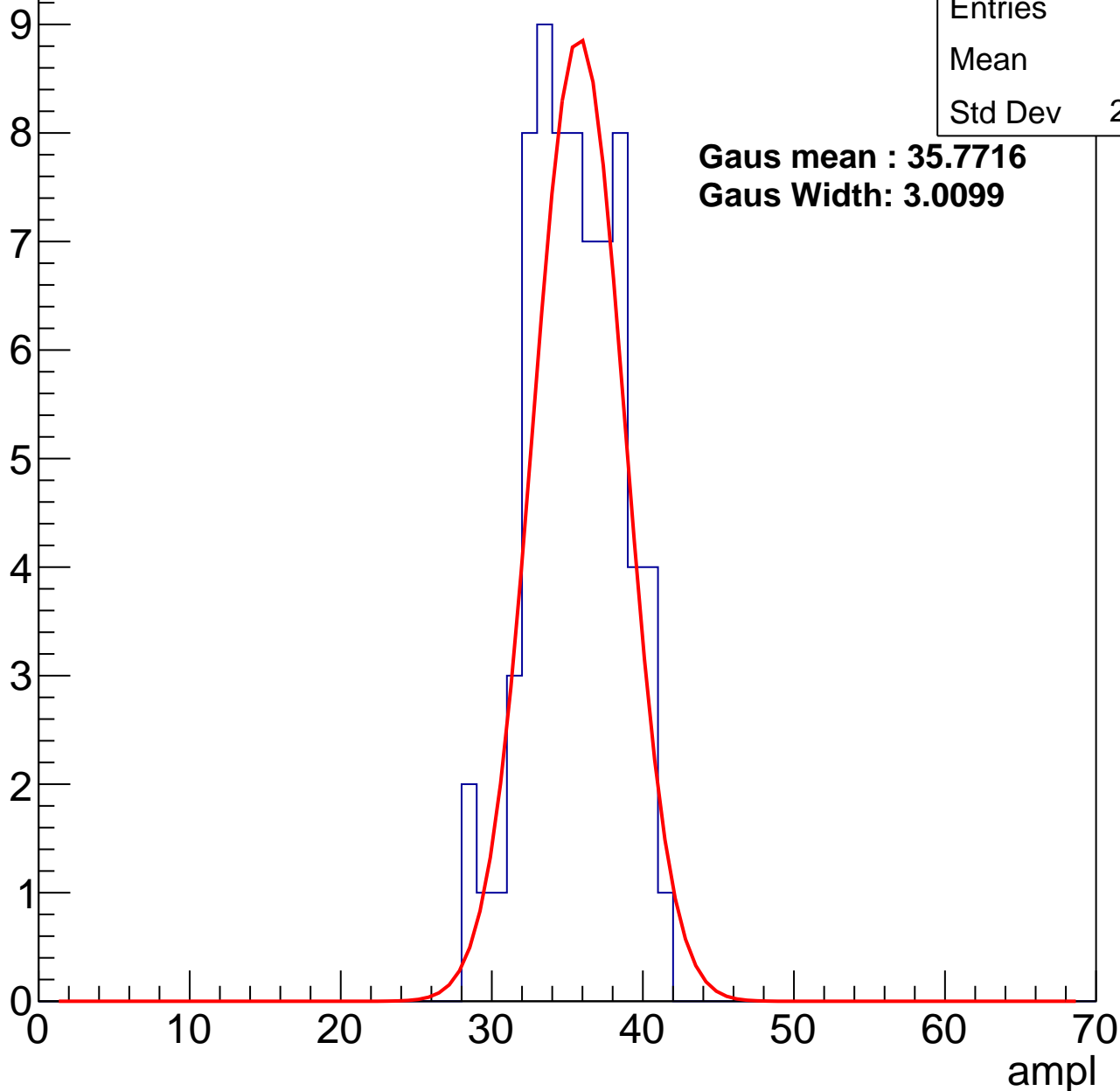
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35
Std Dev	2.979

Gaus mean : 35.7716

Gaus Width: 3.0099



B1L100S, U10-ch93, adc2

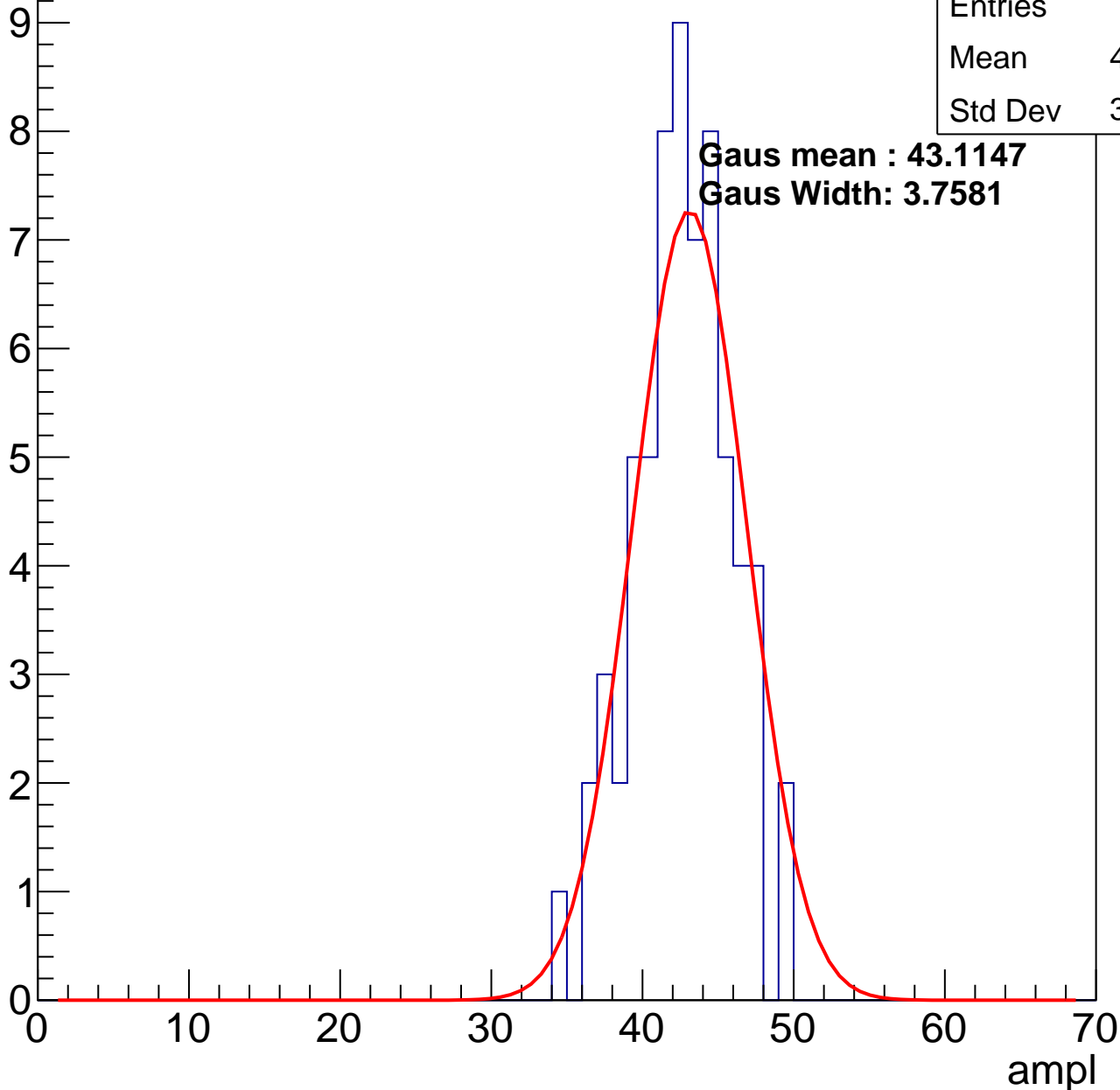
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.18
Std Dev	3.186

Gaus mean : 43.1147

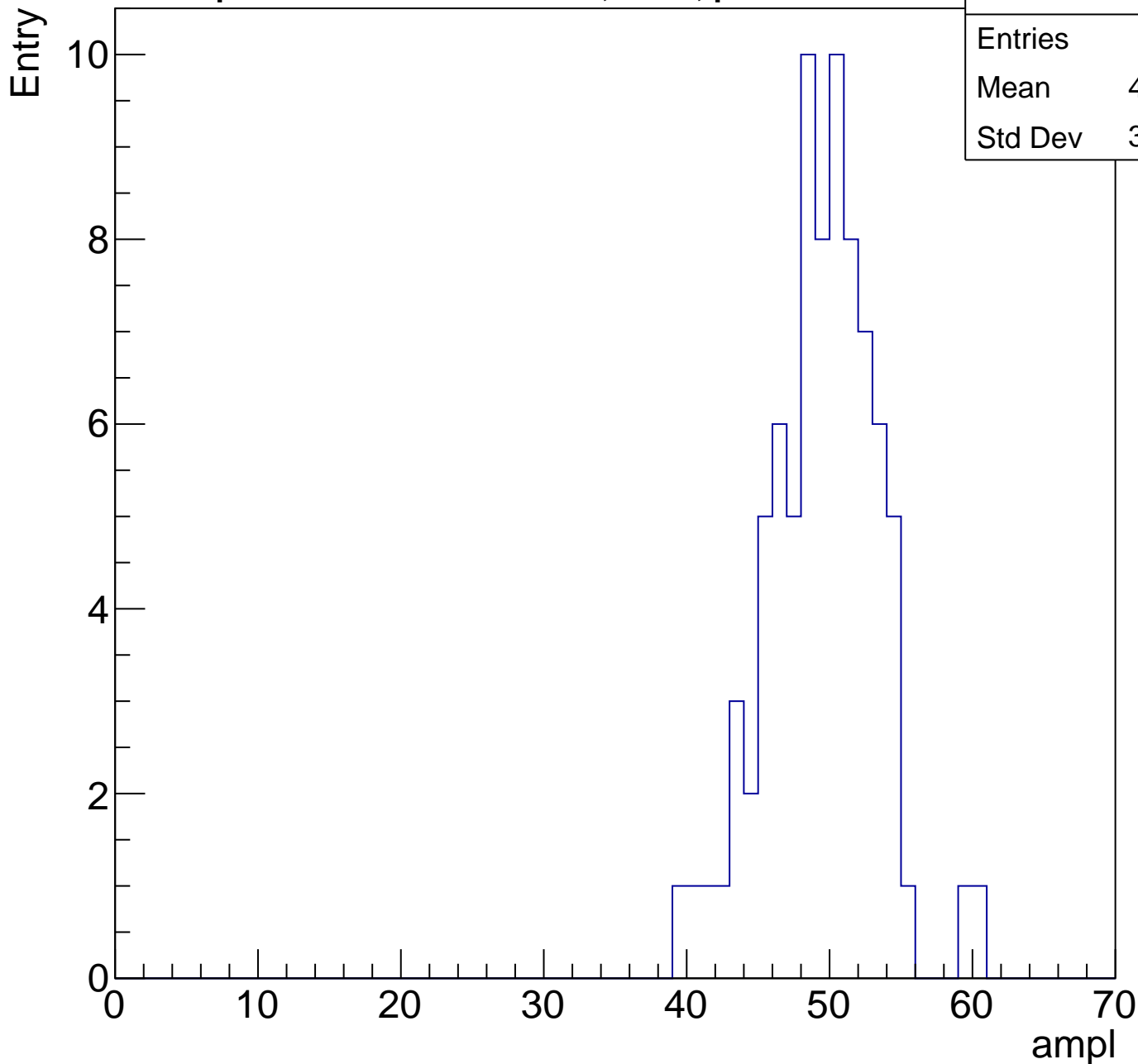
Gaus Width: 3.7581



B1L100S, U10-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	49.04
Std Dev	3.824

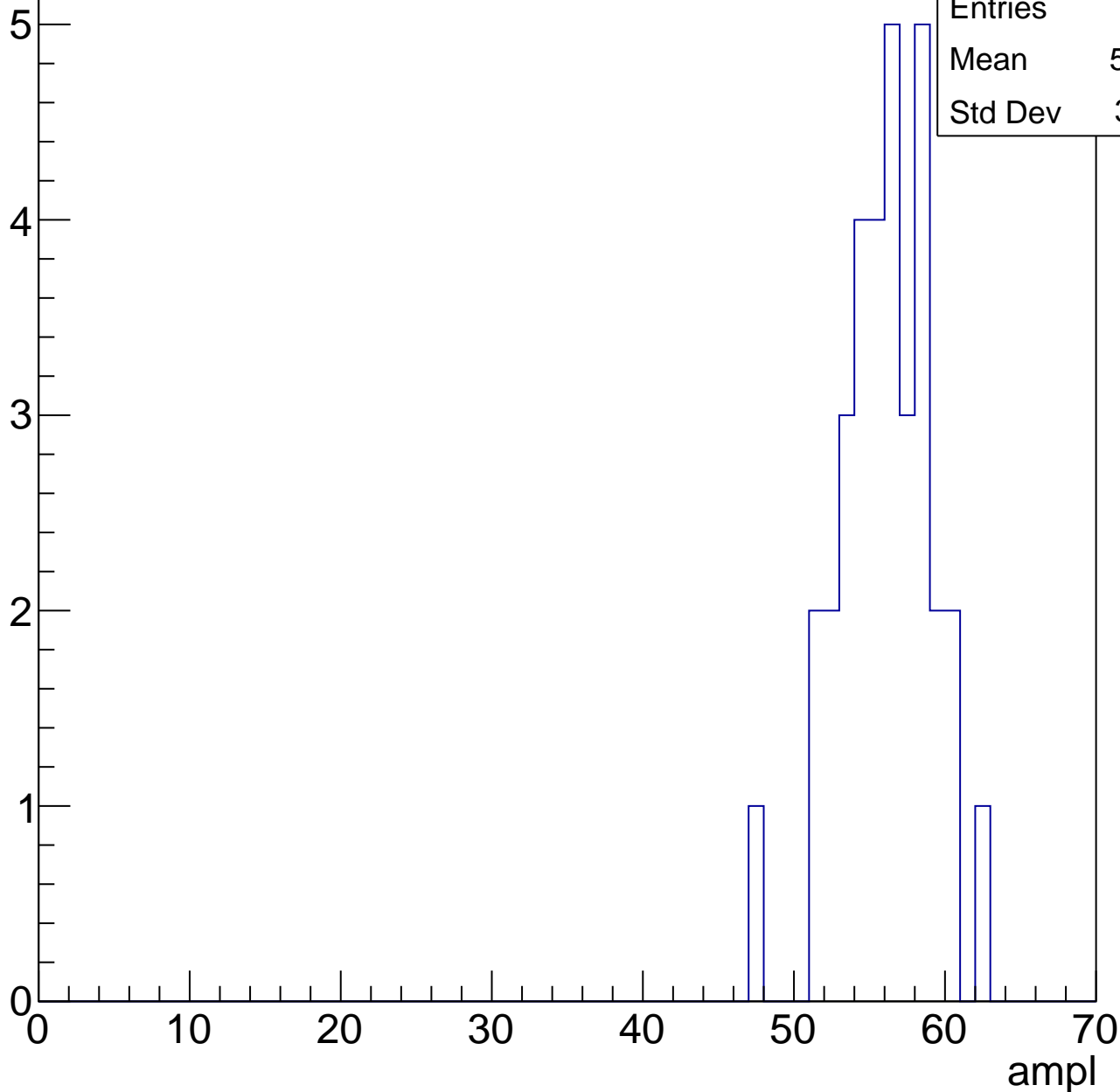


B1L100S, U10-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	55.56
Std Dev	3.031

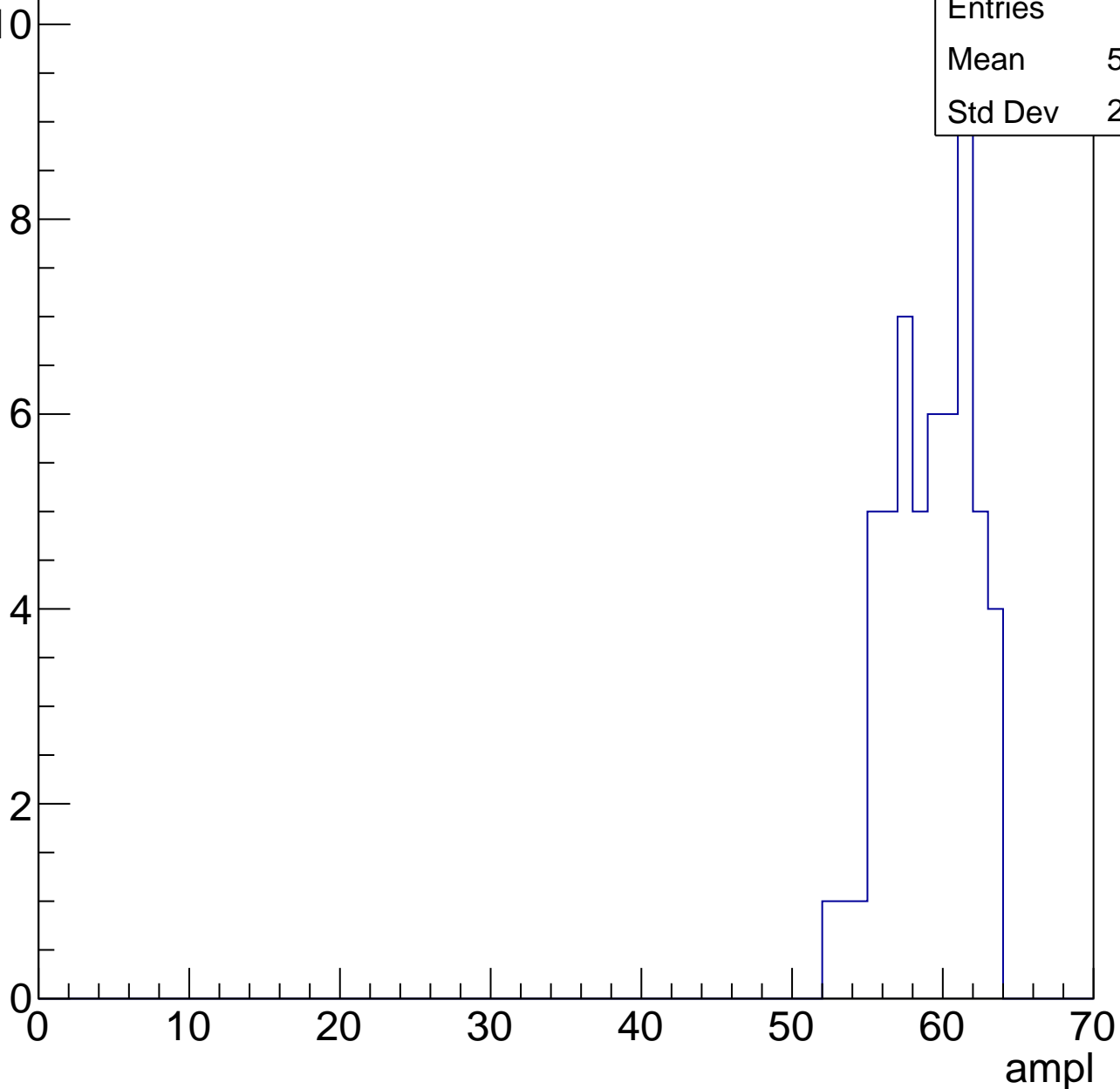


B1L100S, U10-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	58.73
Std Dev	2.735

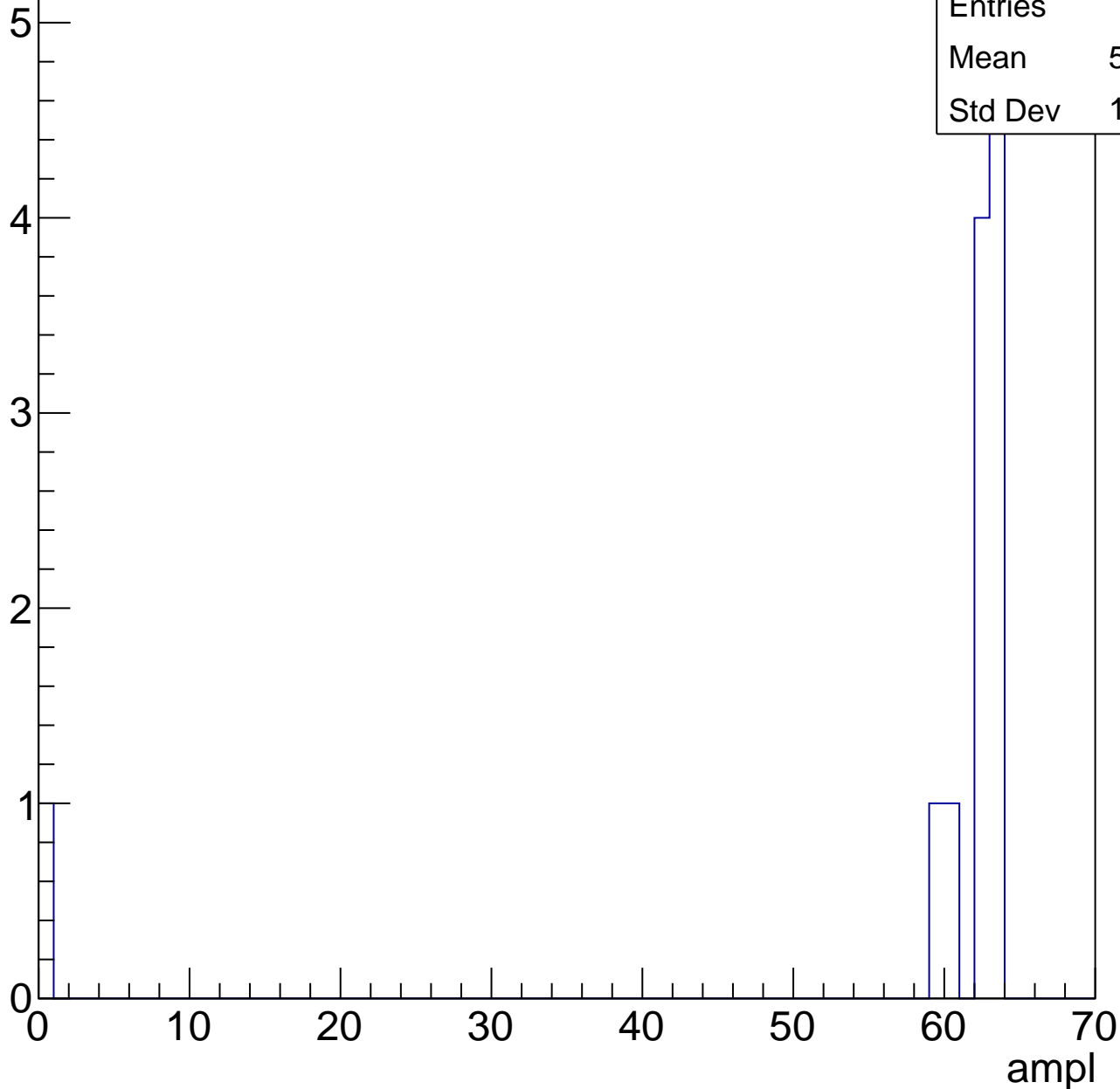


B1L100S, U10-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	56.83
Std Dev	17.18



B1L100S, U10-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch94, adc0

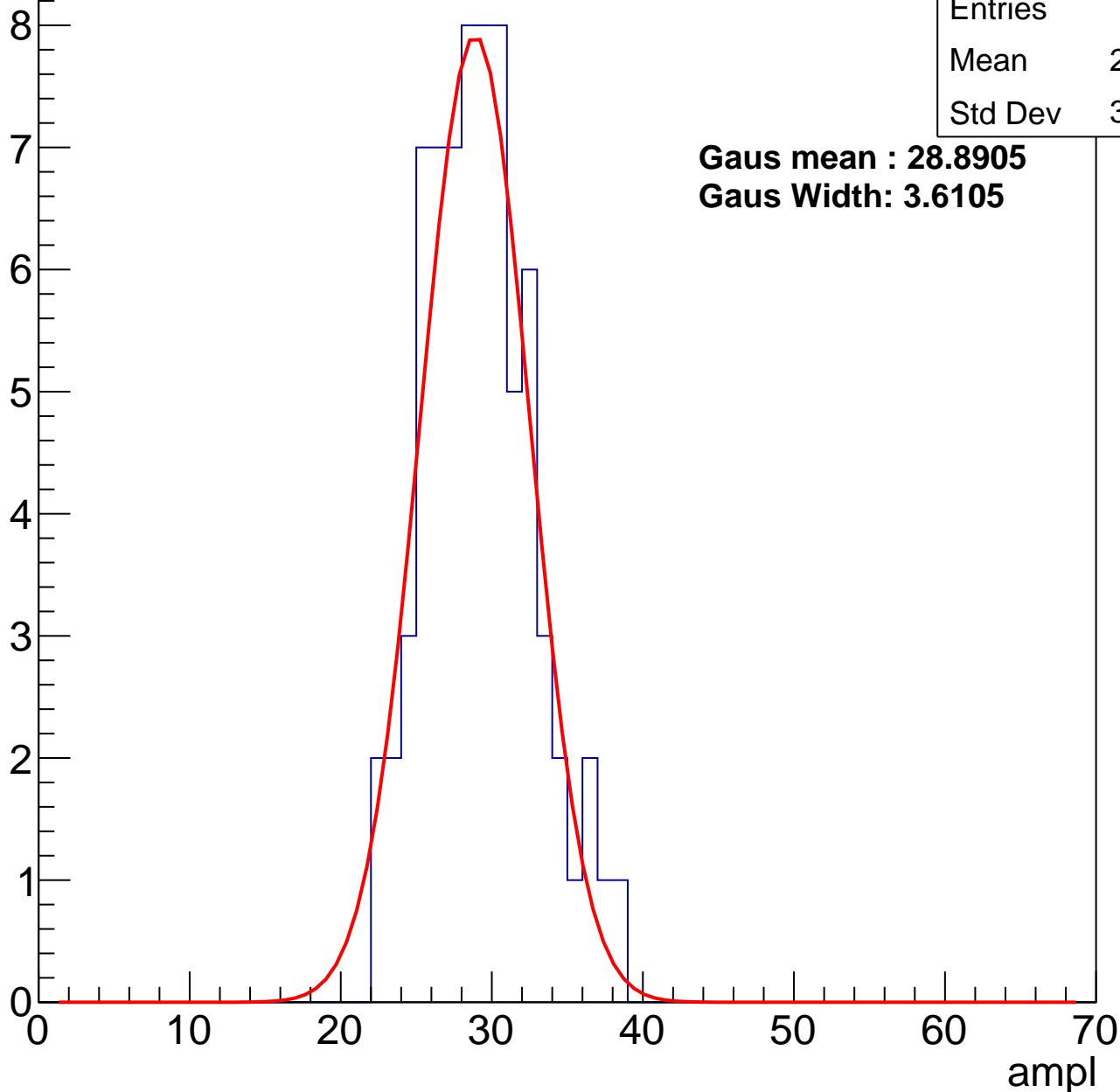
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.77
Std Dev	3.533

Gaus mean : 28.8905

Gaus Width: 3.6105



B1L100S, U10-ch94, adc1

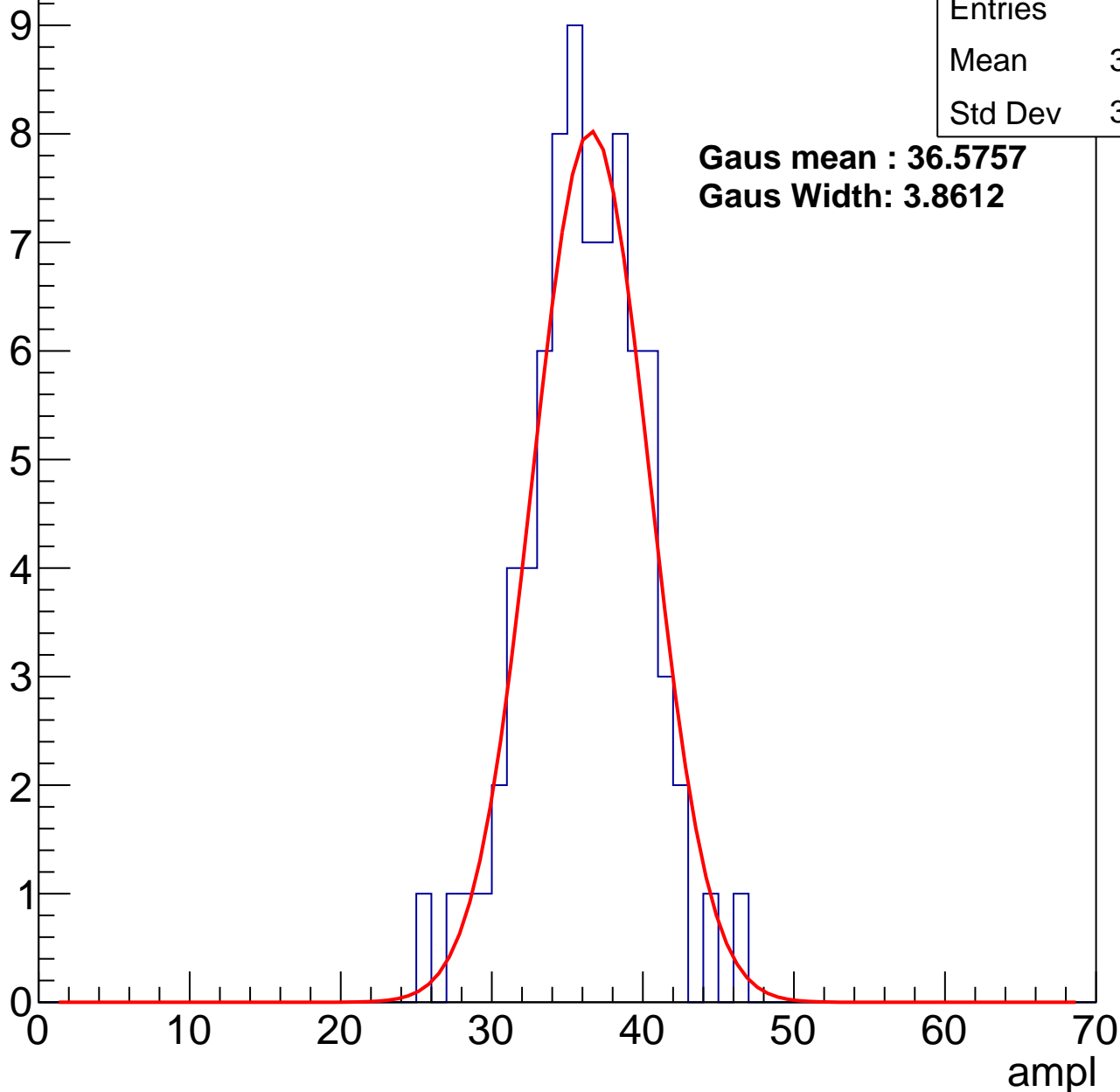
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	35.79
Std Dev	3.828

Gaus mean : 36.5757

Gaus Width: 3.8612



B1L100S, U10-ch94, adc2

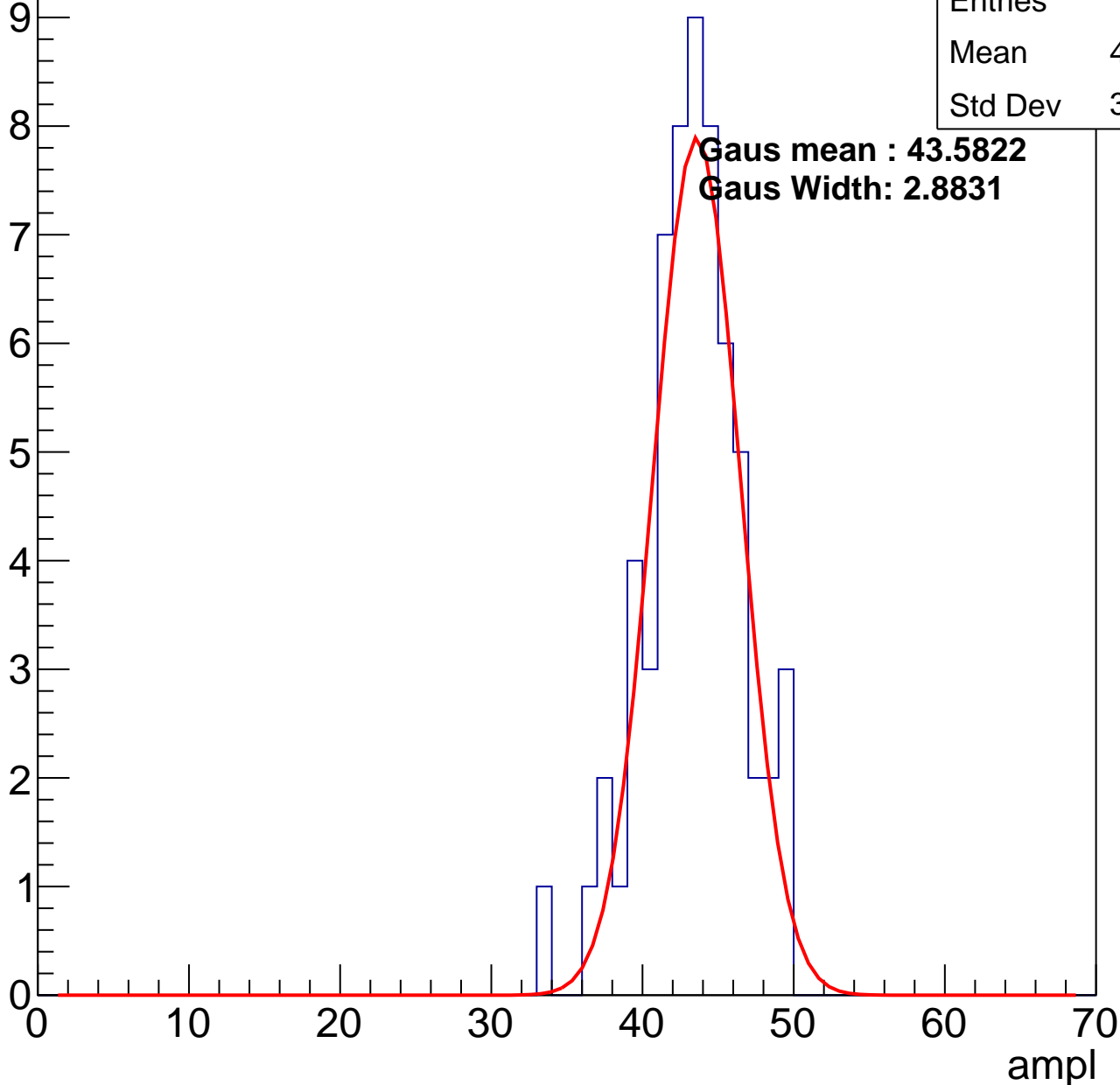
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.84
Std Dev	3.209

Gaus mean : 43.5822

Gaus Width: 2.8831

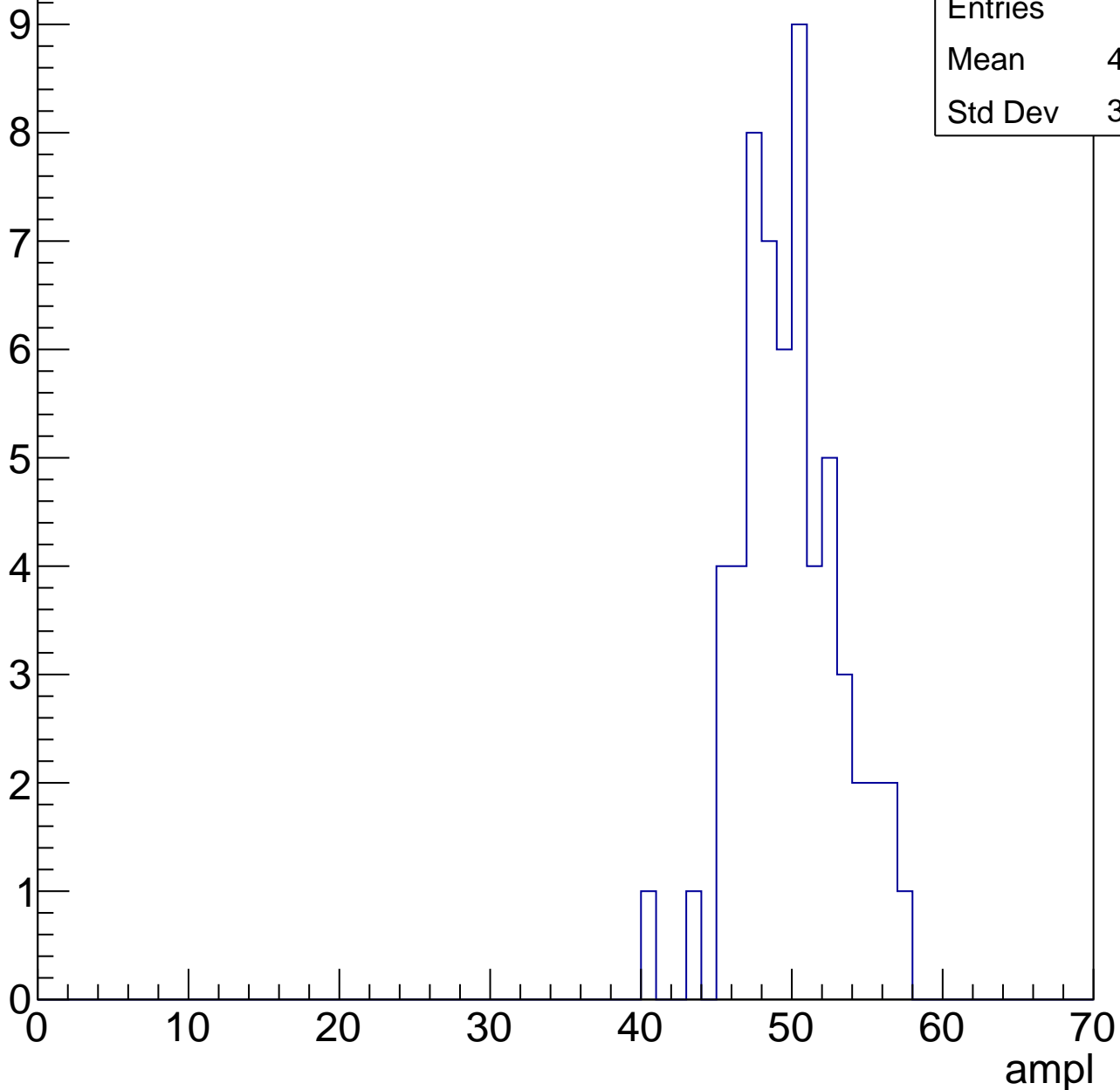


B1L100S, U10-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.37
Std Dev	3.319

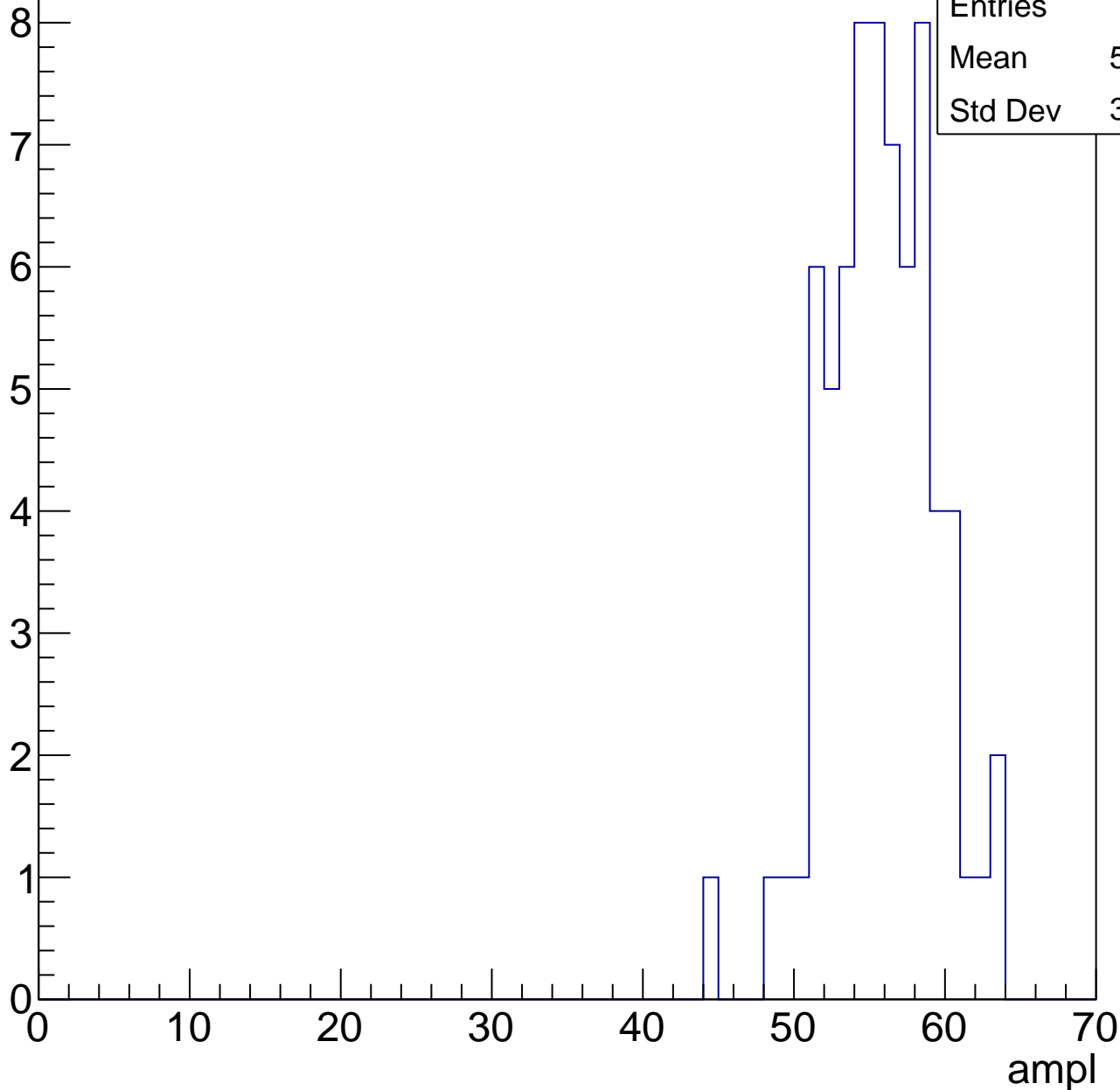


B1L100S, U10-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	55.29
Std Dev	3.542

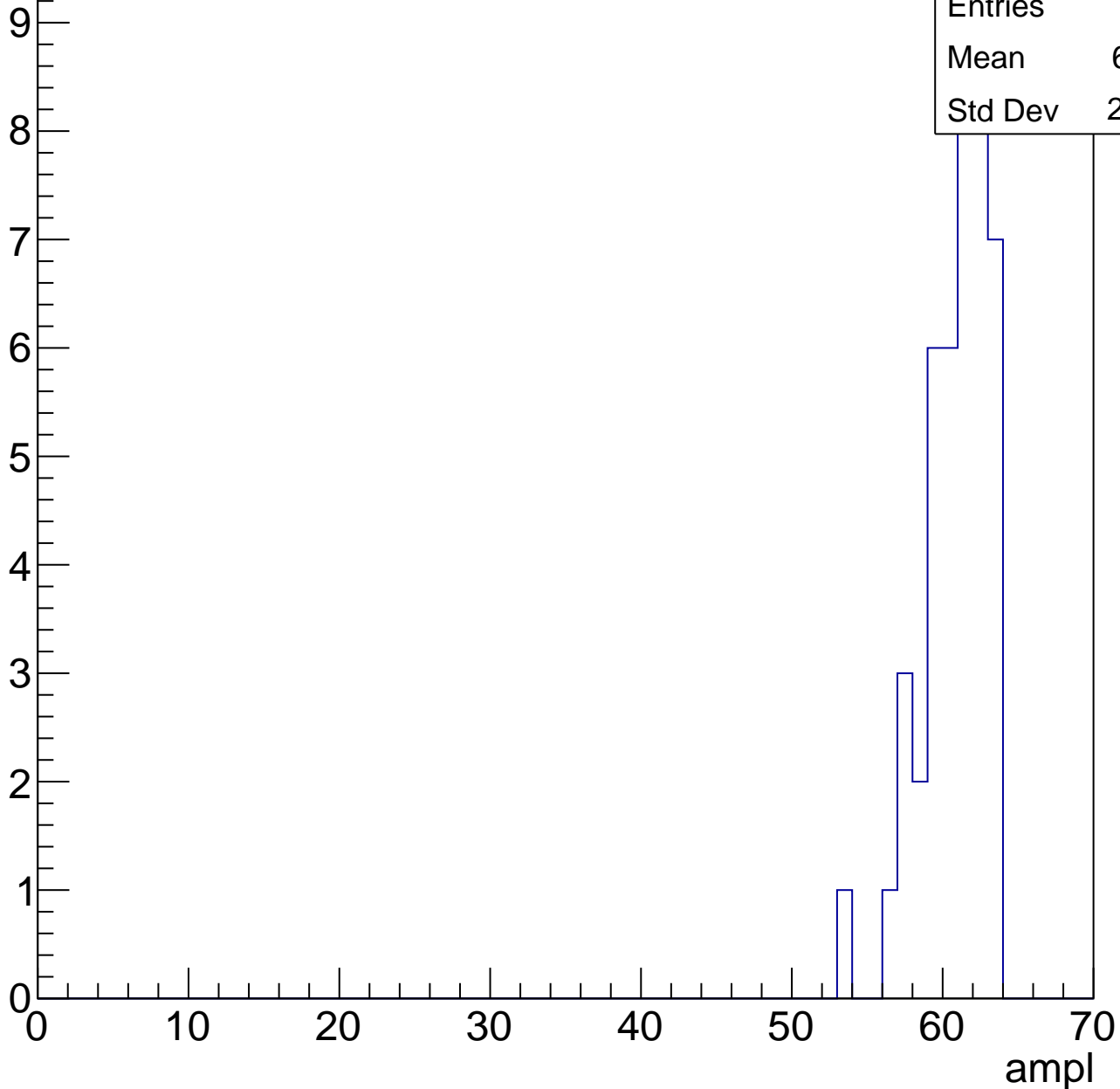


B1L100S, U10-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	60.41
Std Dev	2.167



B1L100S, U10-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch95, adc0

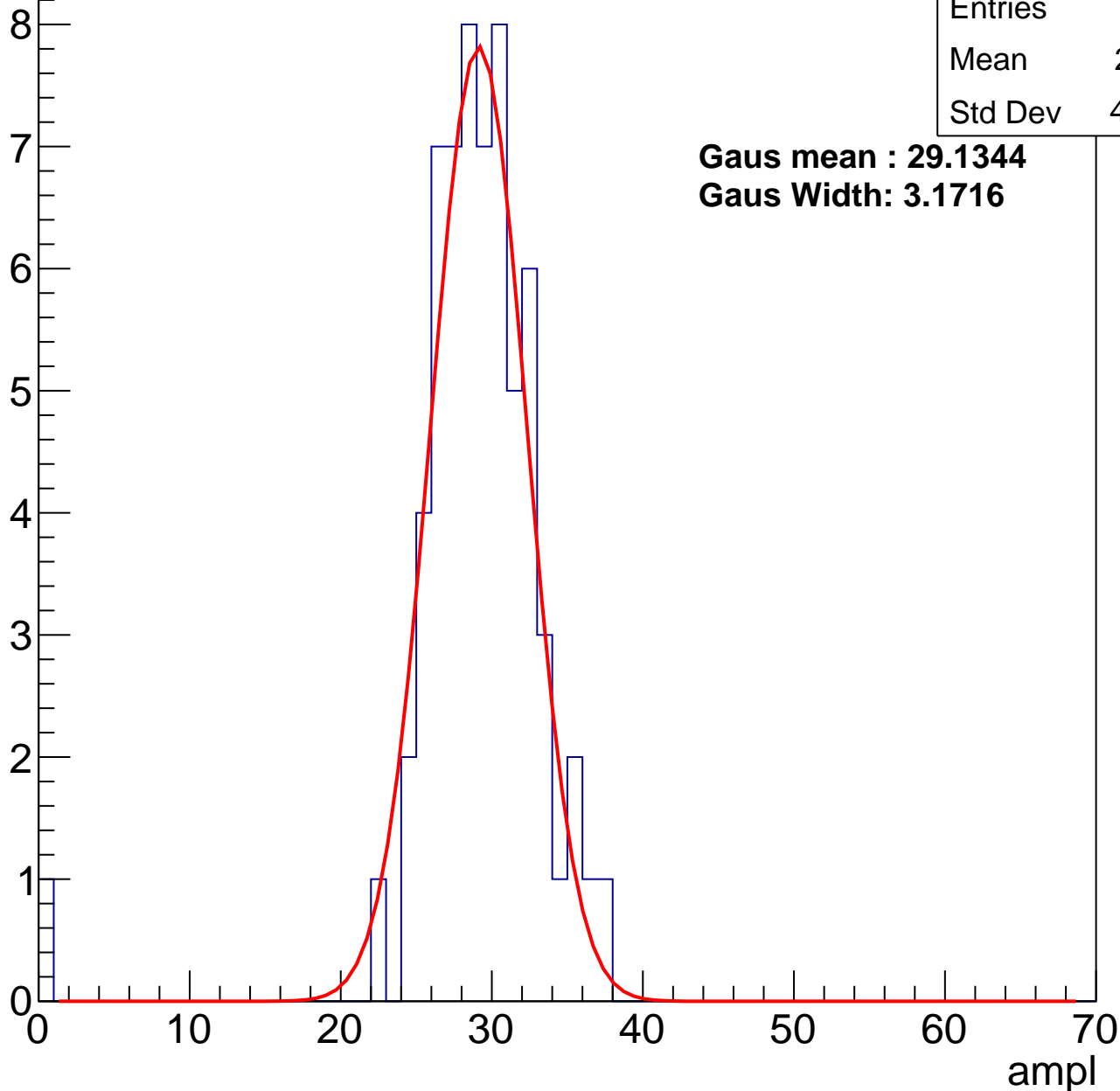
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.61
Std Dev	4.732

Gaus mean : 29.1344

Gaus Width: 3.1716



B1L100S, U10-ch95, adc1

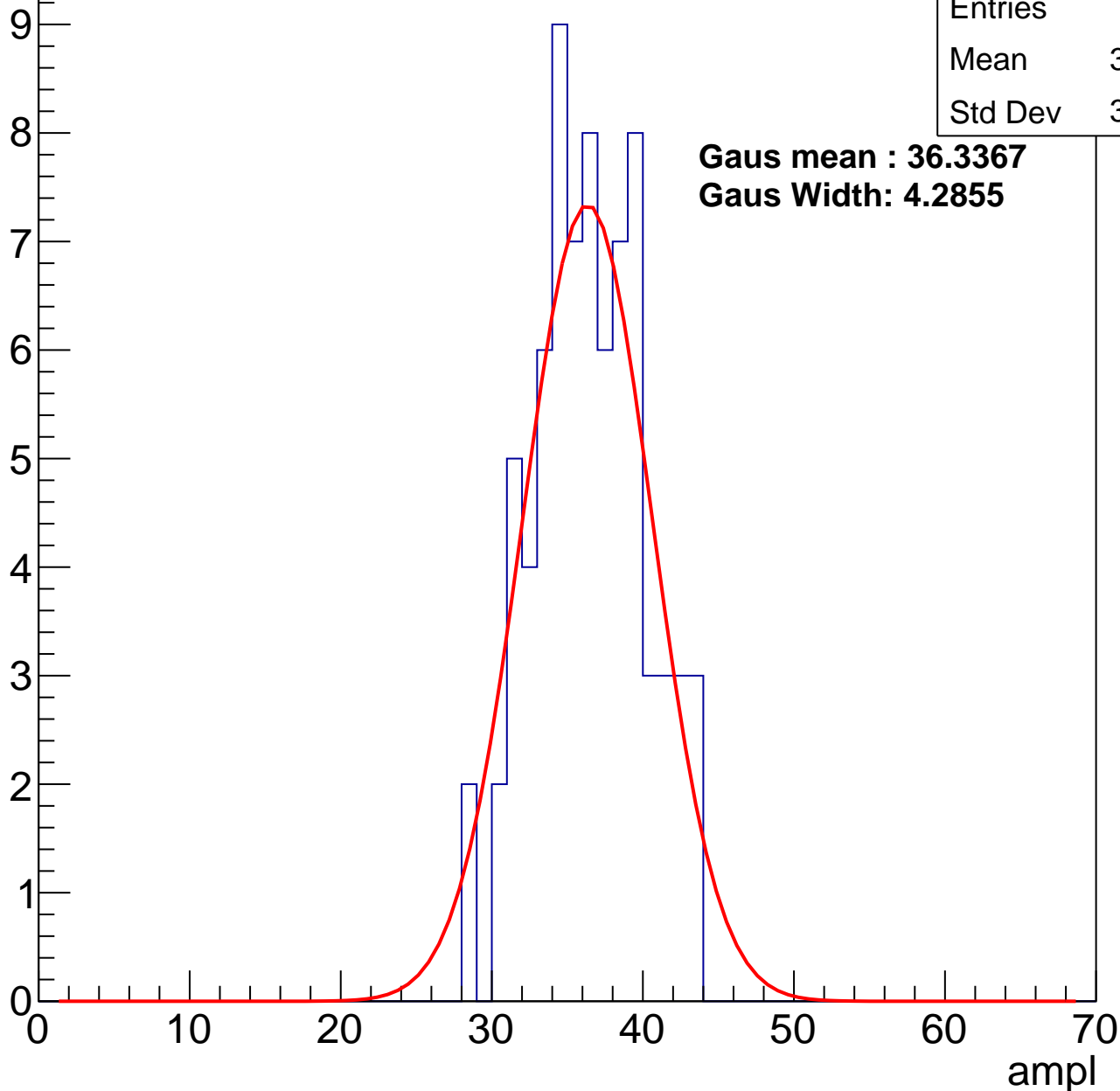
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	35.97
Std Dev	3.572

Gaus mean : 36.3367

Gaus Width: 4.2855



B1L100S, U10-ch95, adc2

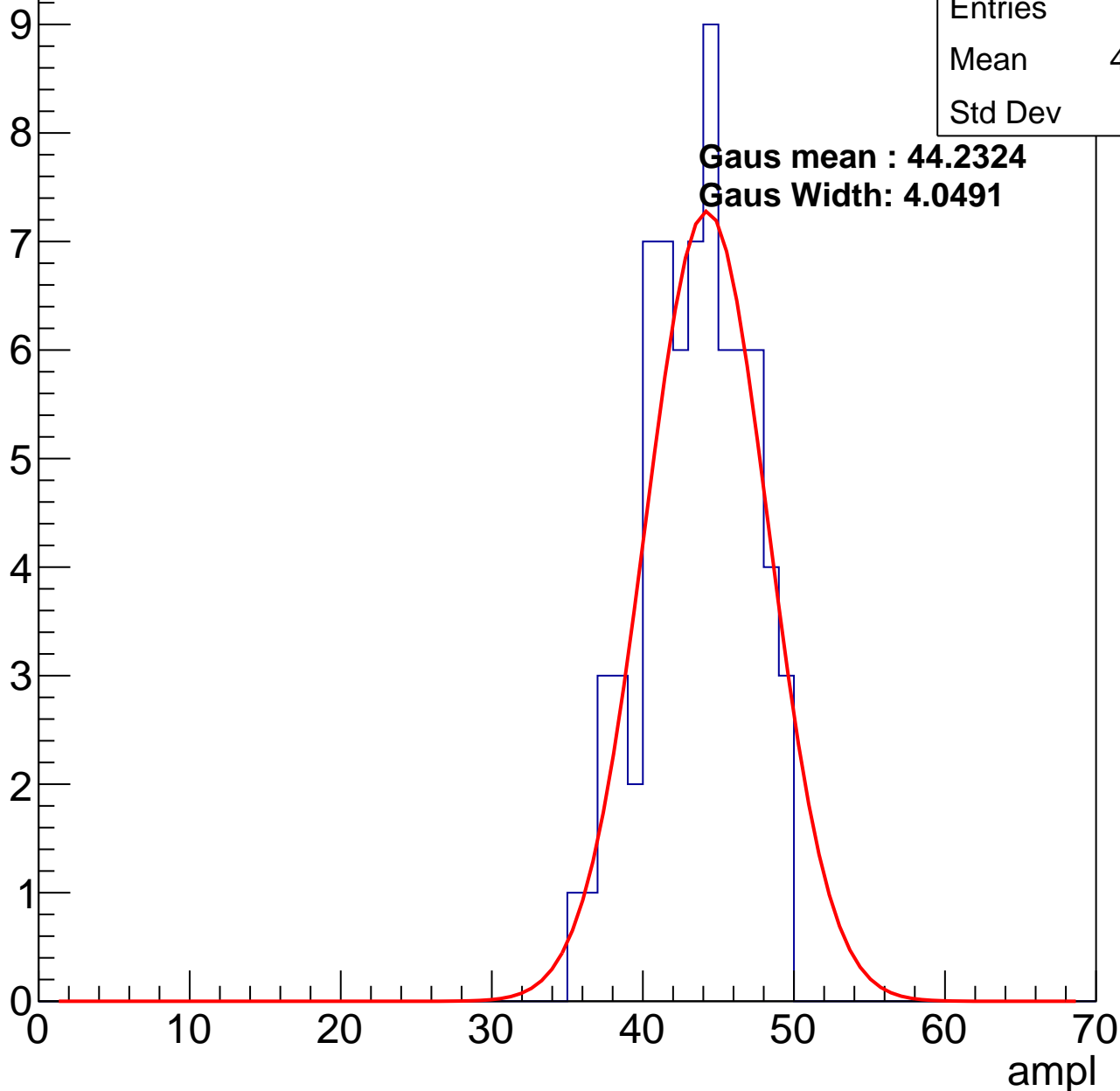
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.06
Std Dev	3.39

Gaus mean : 44.2324

Gaus Width: 4.0491

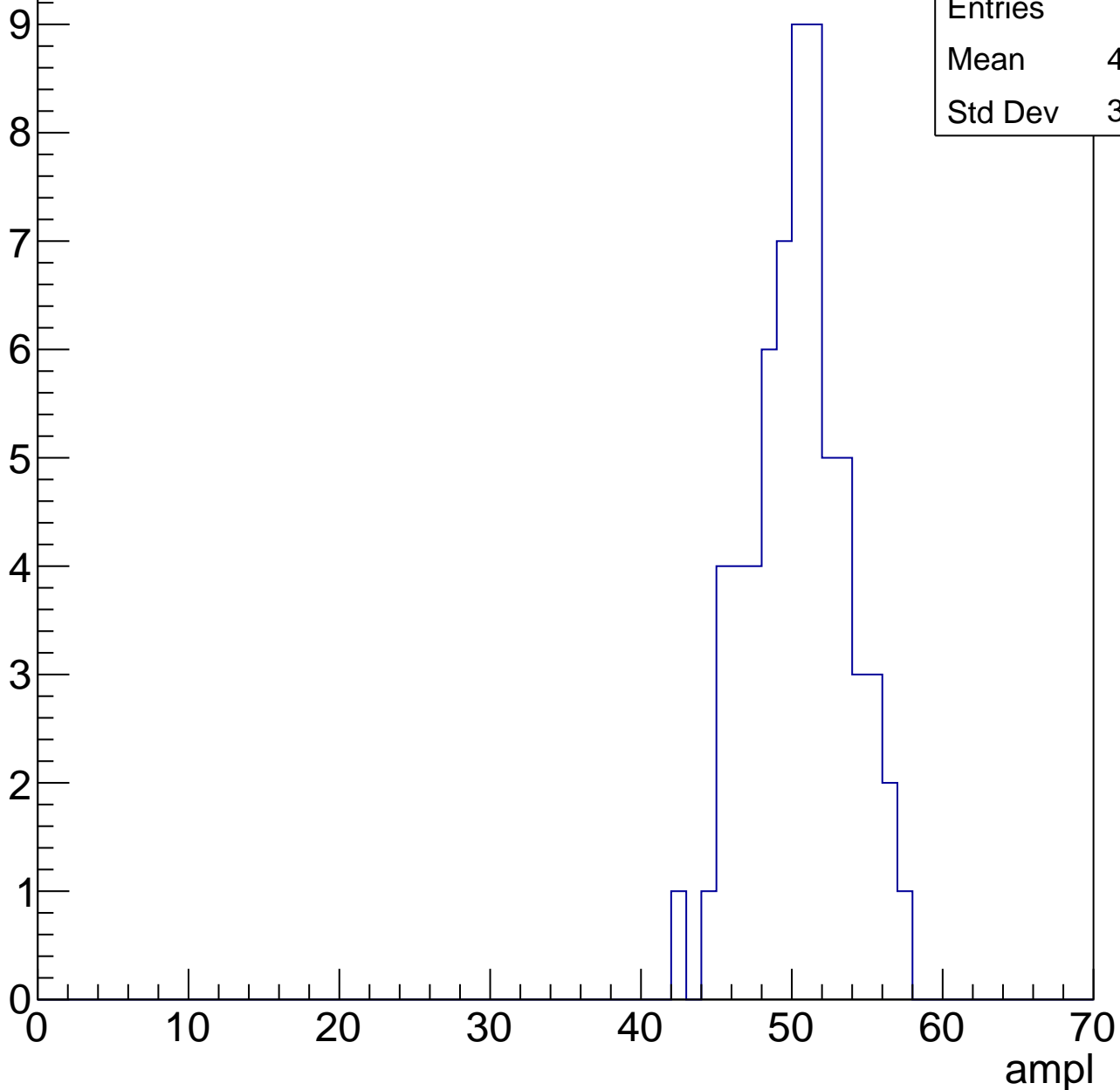


B1L100S, U10-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

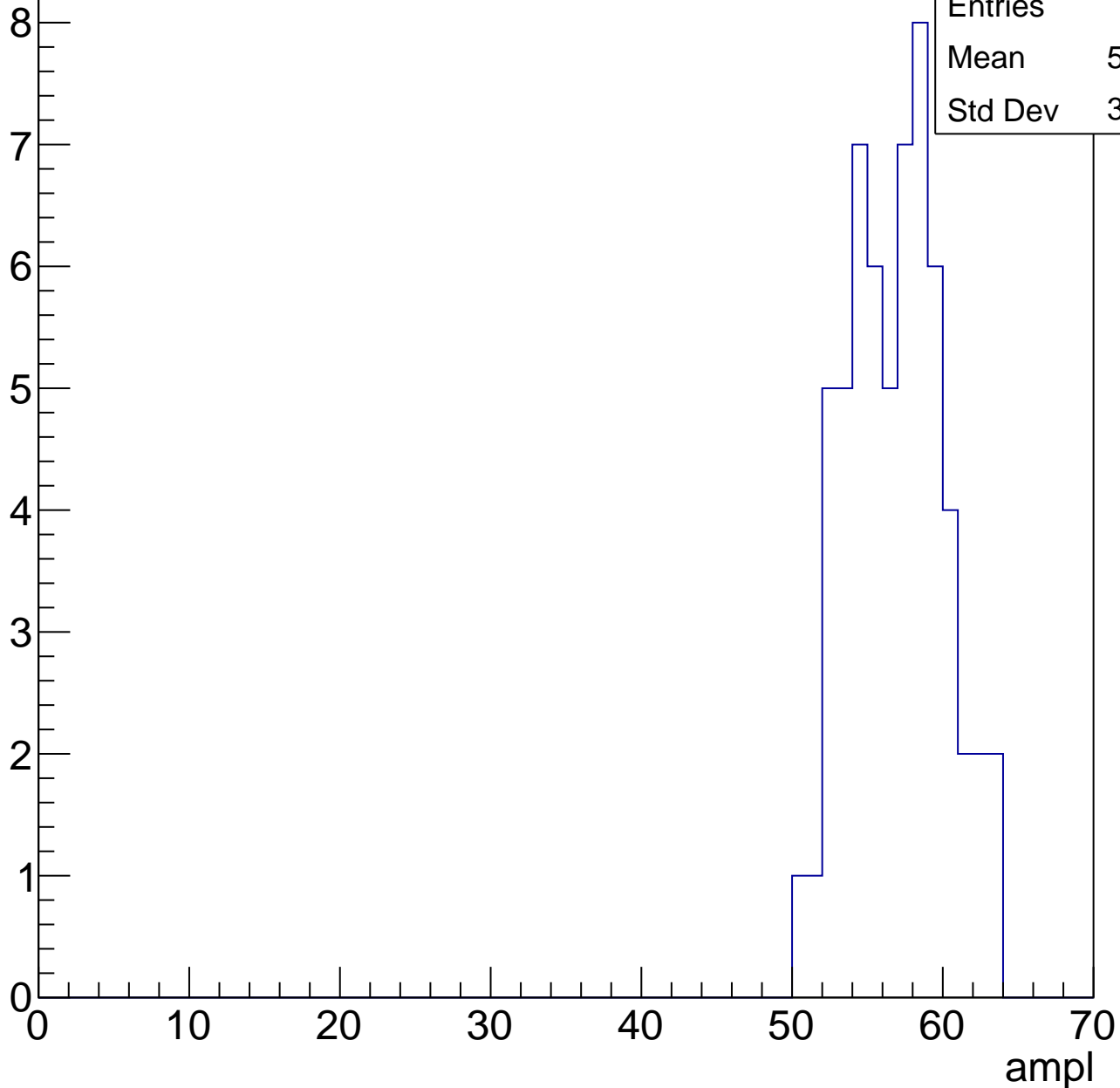
Entries	64
Mean	49.98
Std Dev	3.184



B1L100S, U10-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



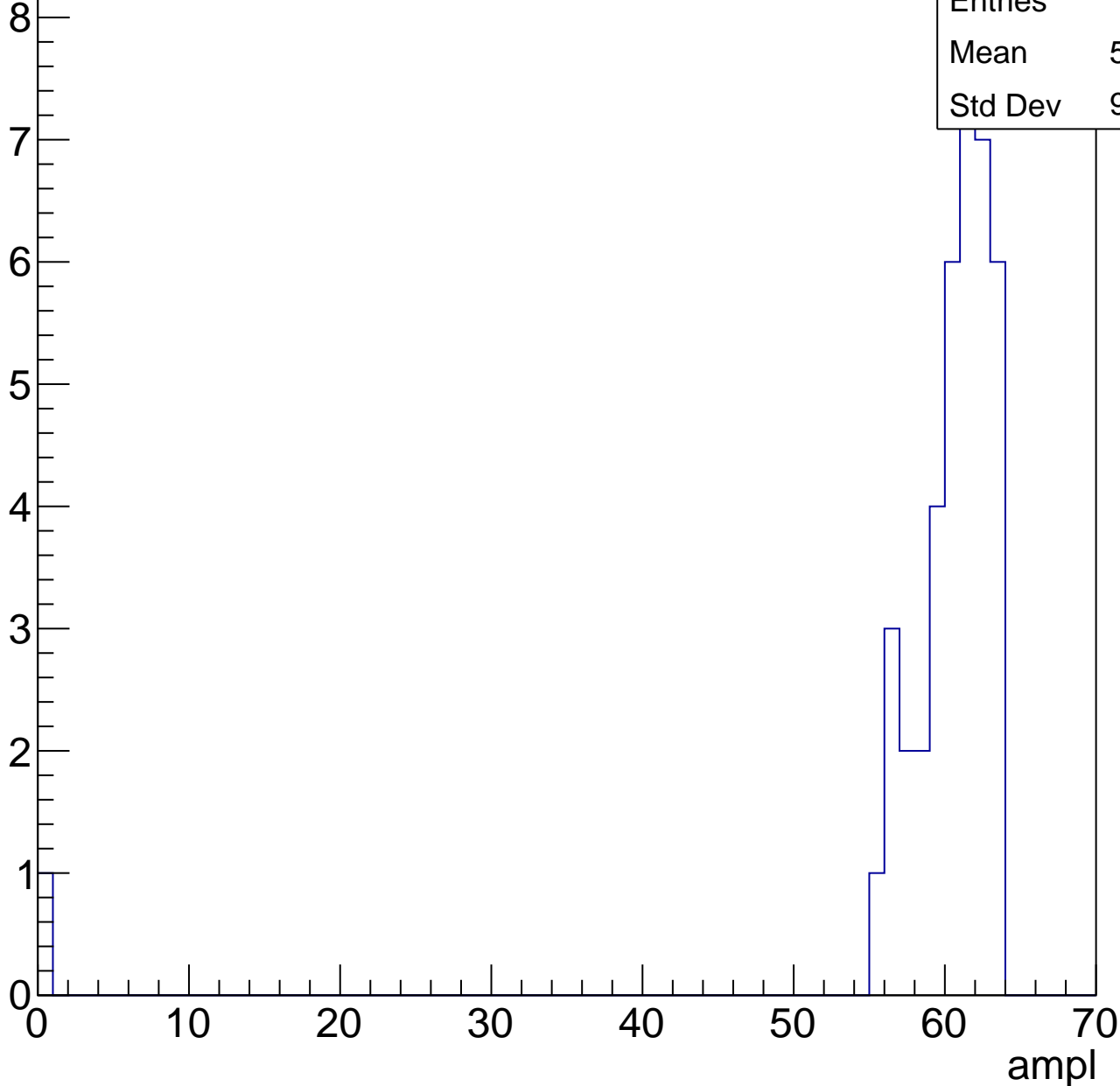
Entries	61
Mean	56.44
Std Dev	3.097

B1L100S, U10-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.73
Std Dev	9.654



B1L100S, U10-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U10-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch96, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	27.88
Std Dev	6.39

Gaus mean : 29.7721

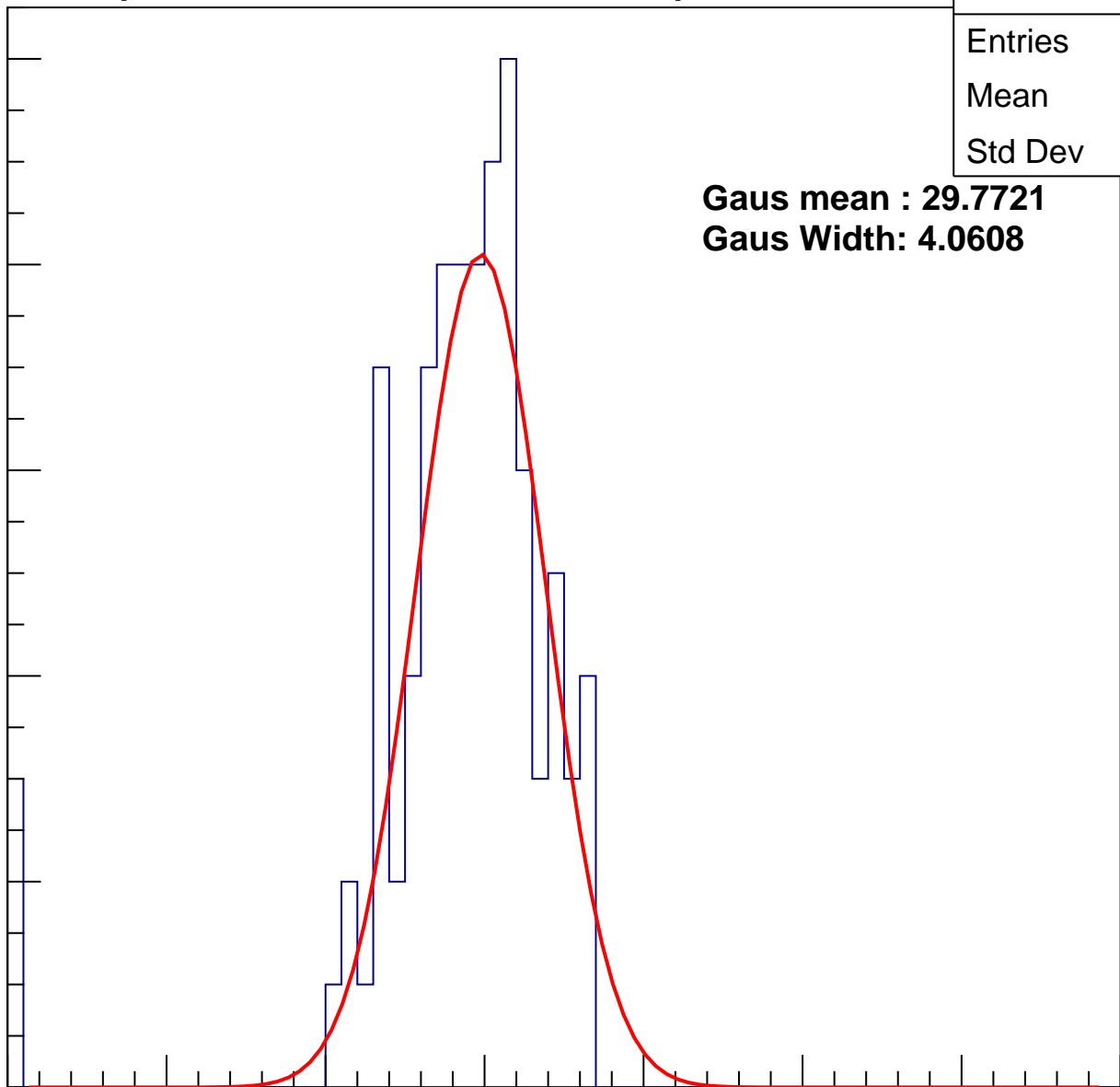
Gaus Width: 4.0608

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch96, adc1

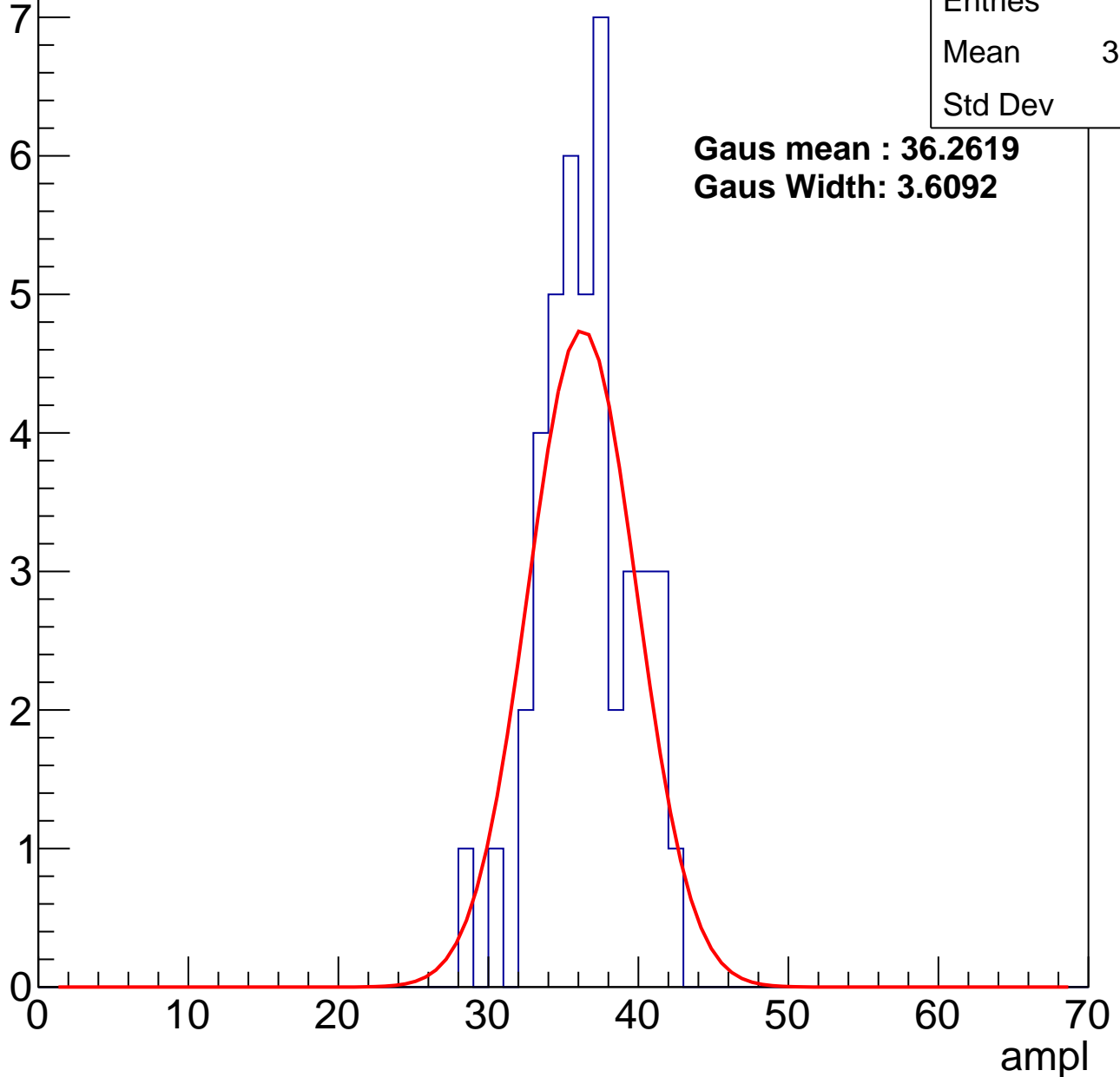
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	36.07
Std Dev	3.03

Gaus mean : 36.2619

Gaus Width: 3.6092



B1L100S, U10-ch96, adc2

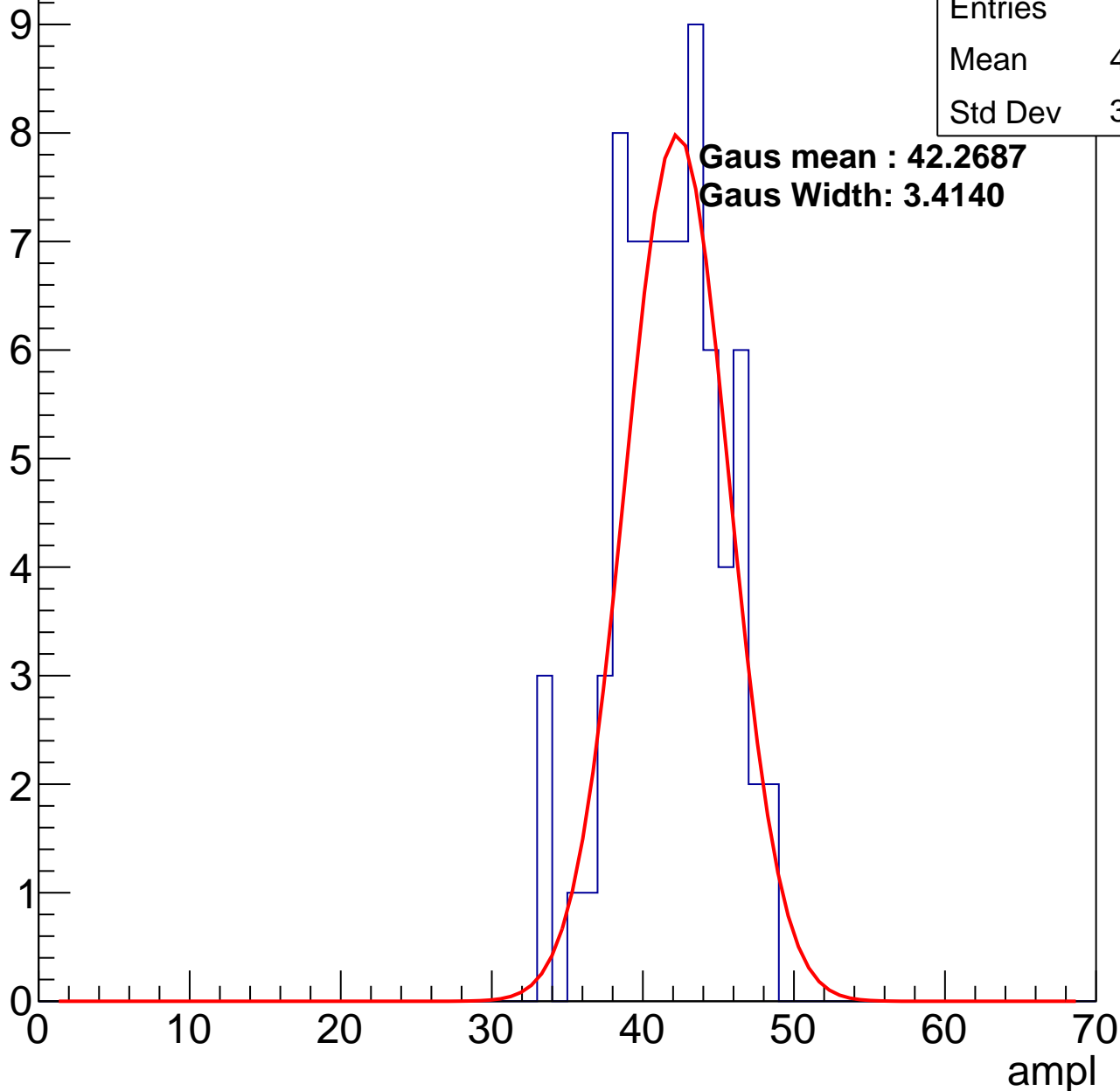
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	41.32
Std Dev	3.475

Gaus mean : 42.2687

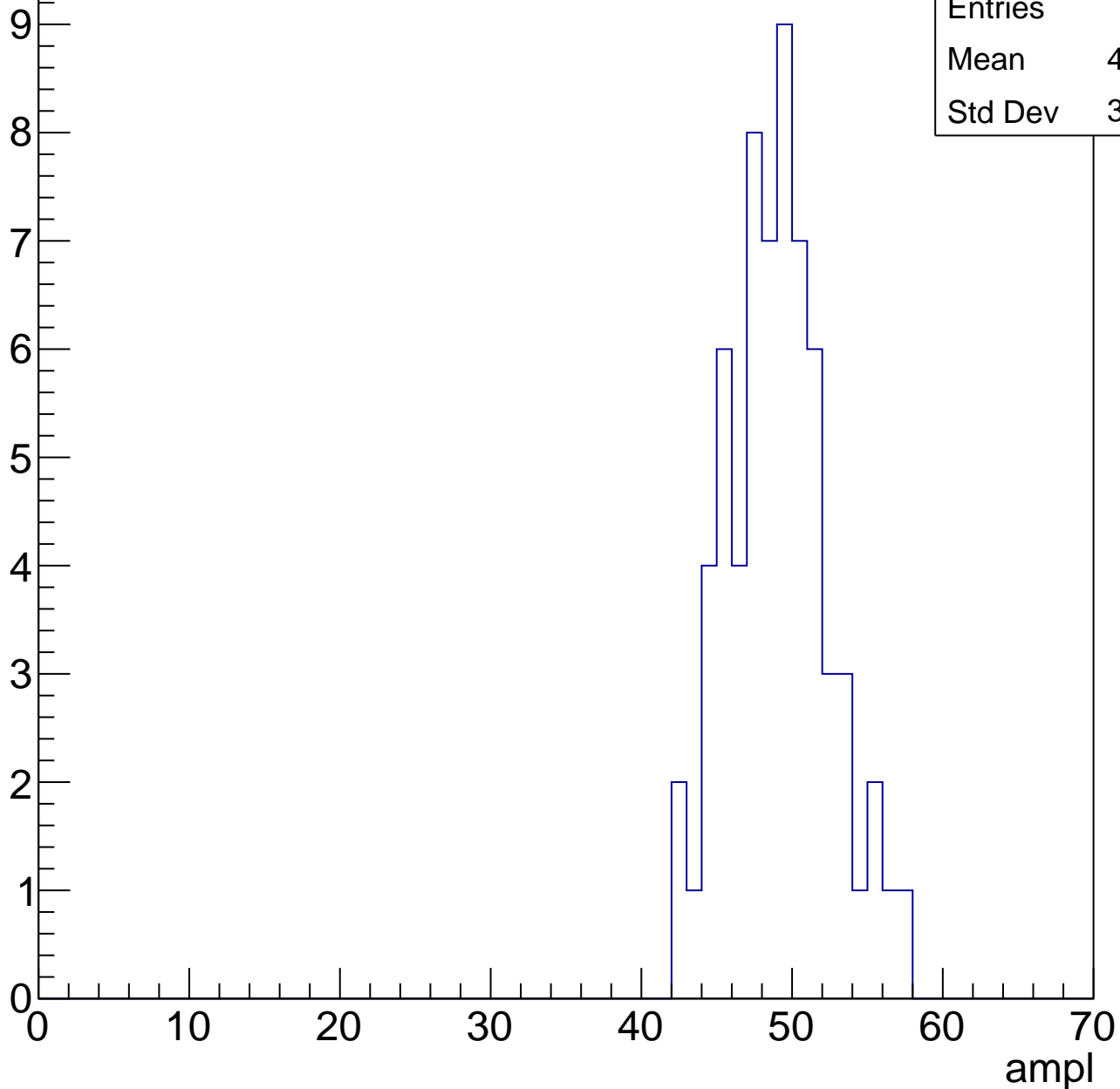
Gaus Width: 3.4140



B1L100S, U10-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



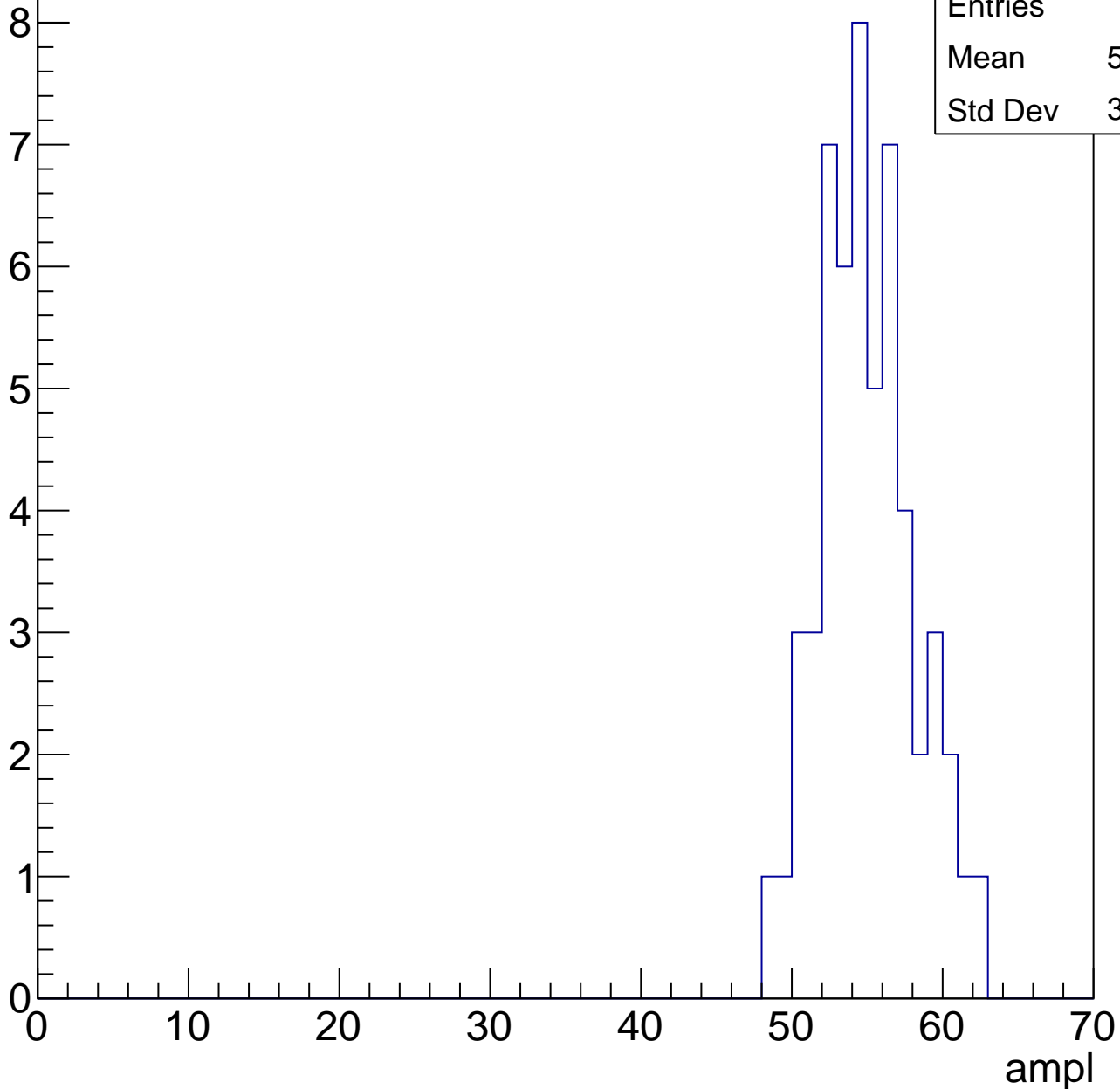
Entries	65
Mean	48.58
Std Dev	3.305

B1L100S, U10-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	54.54
Std Dev	3.089

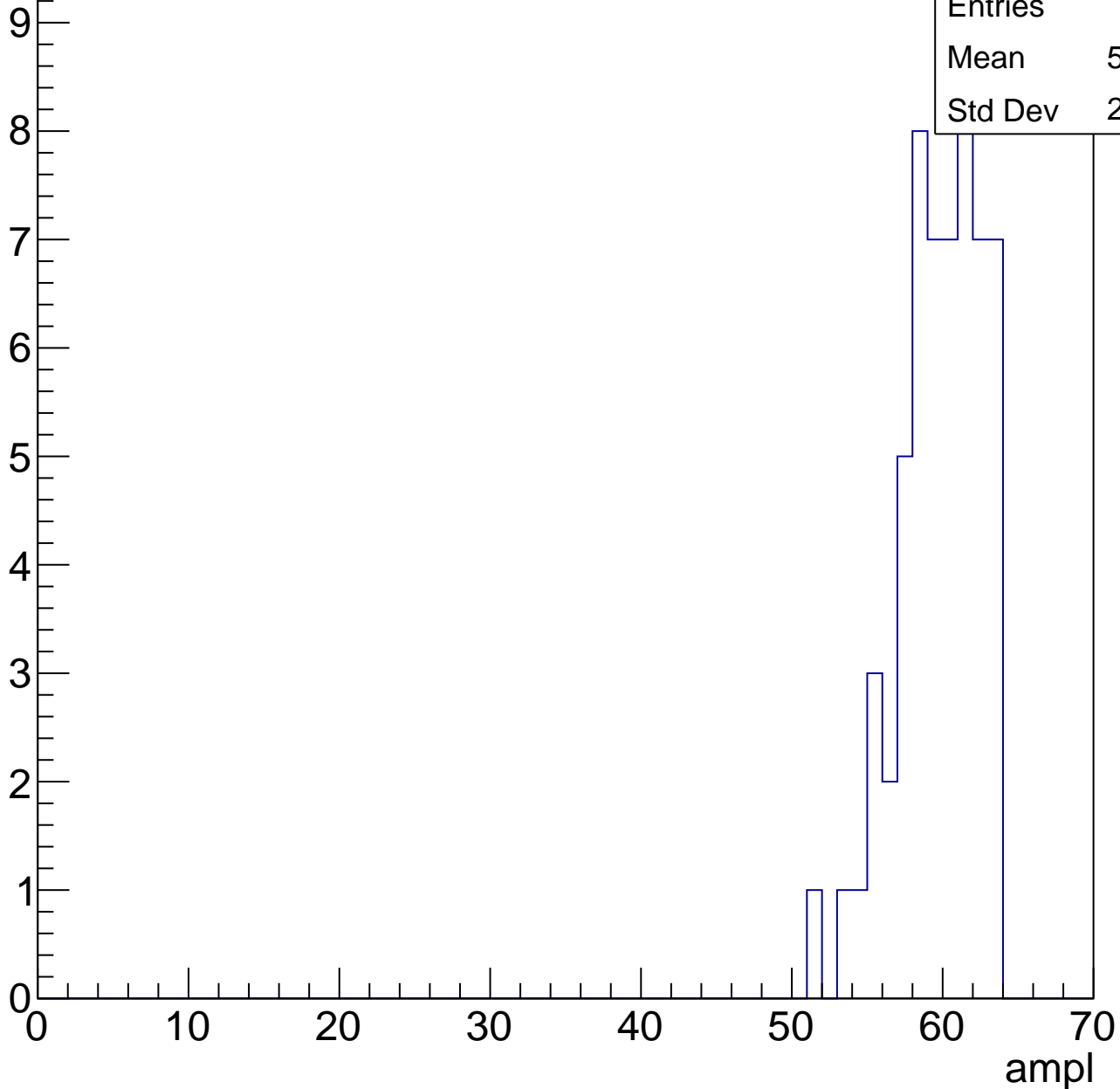


B1L100S, U10-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

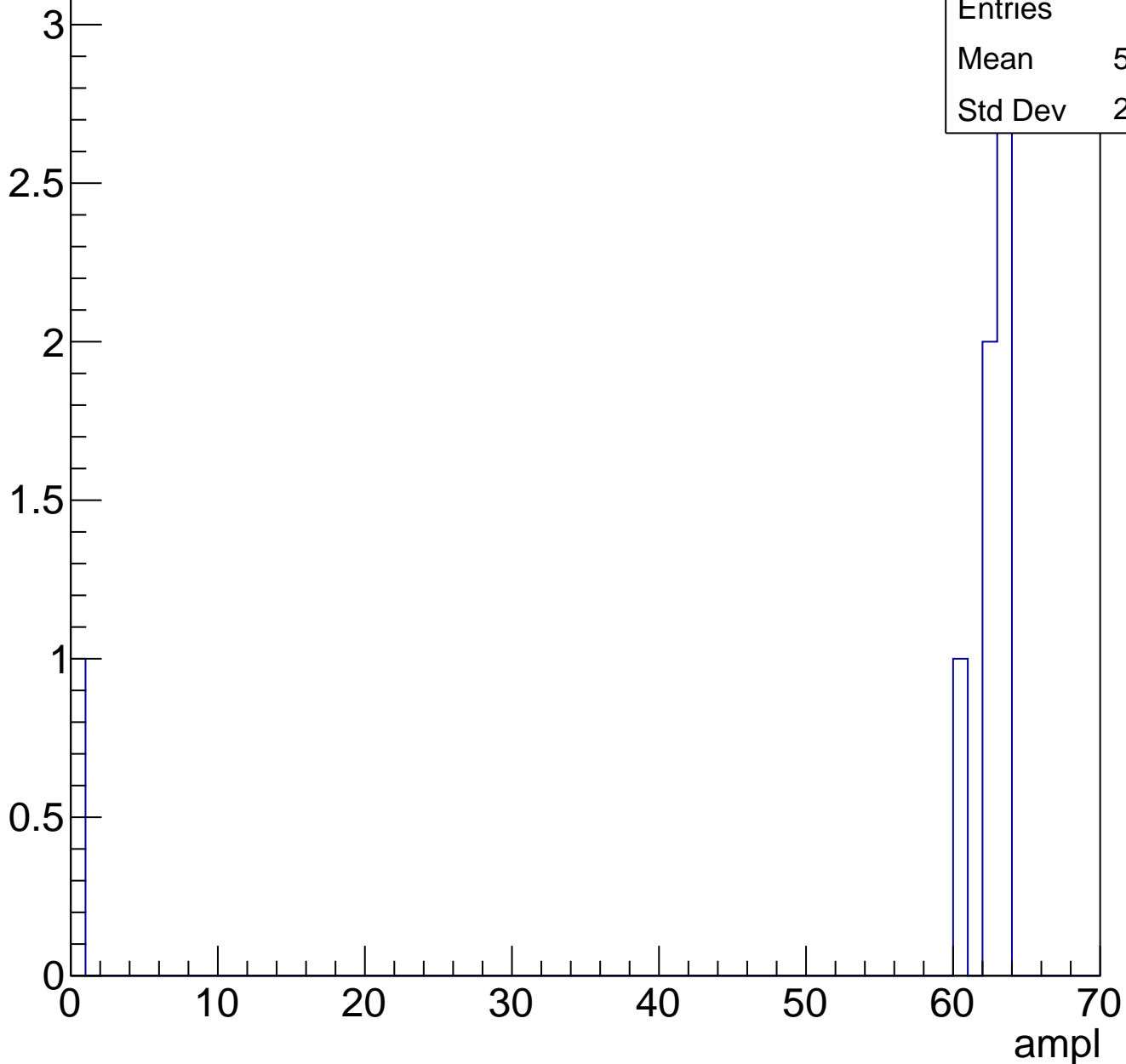
Entries	58
Mean	59.33
Std Dev	2.725



B1L100S, U10-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U10-ch97, adc0

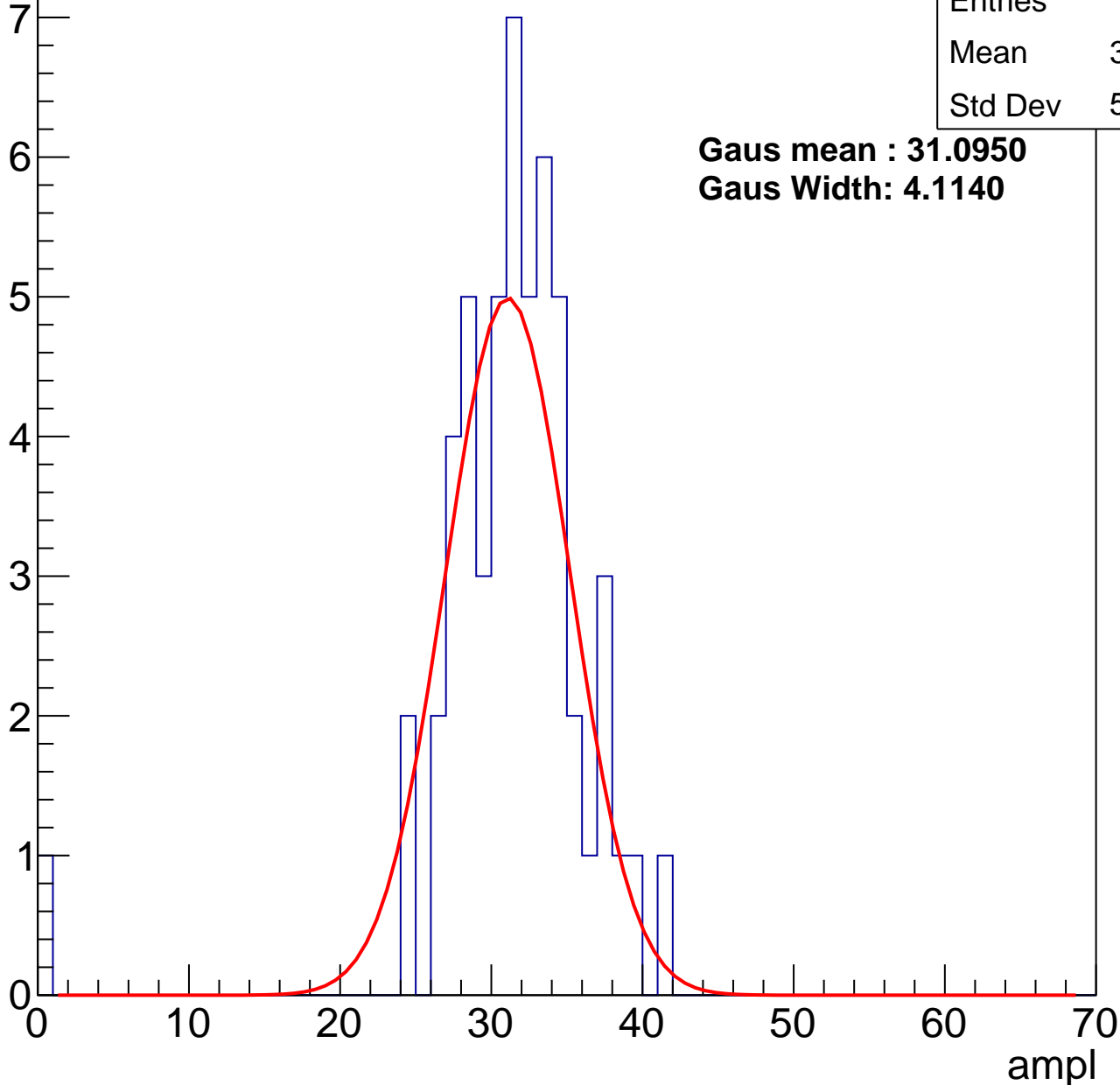
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	30.83
Std Dev	5.587

Gaus mean : 31.0950

Gaus Width: 4.1140



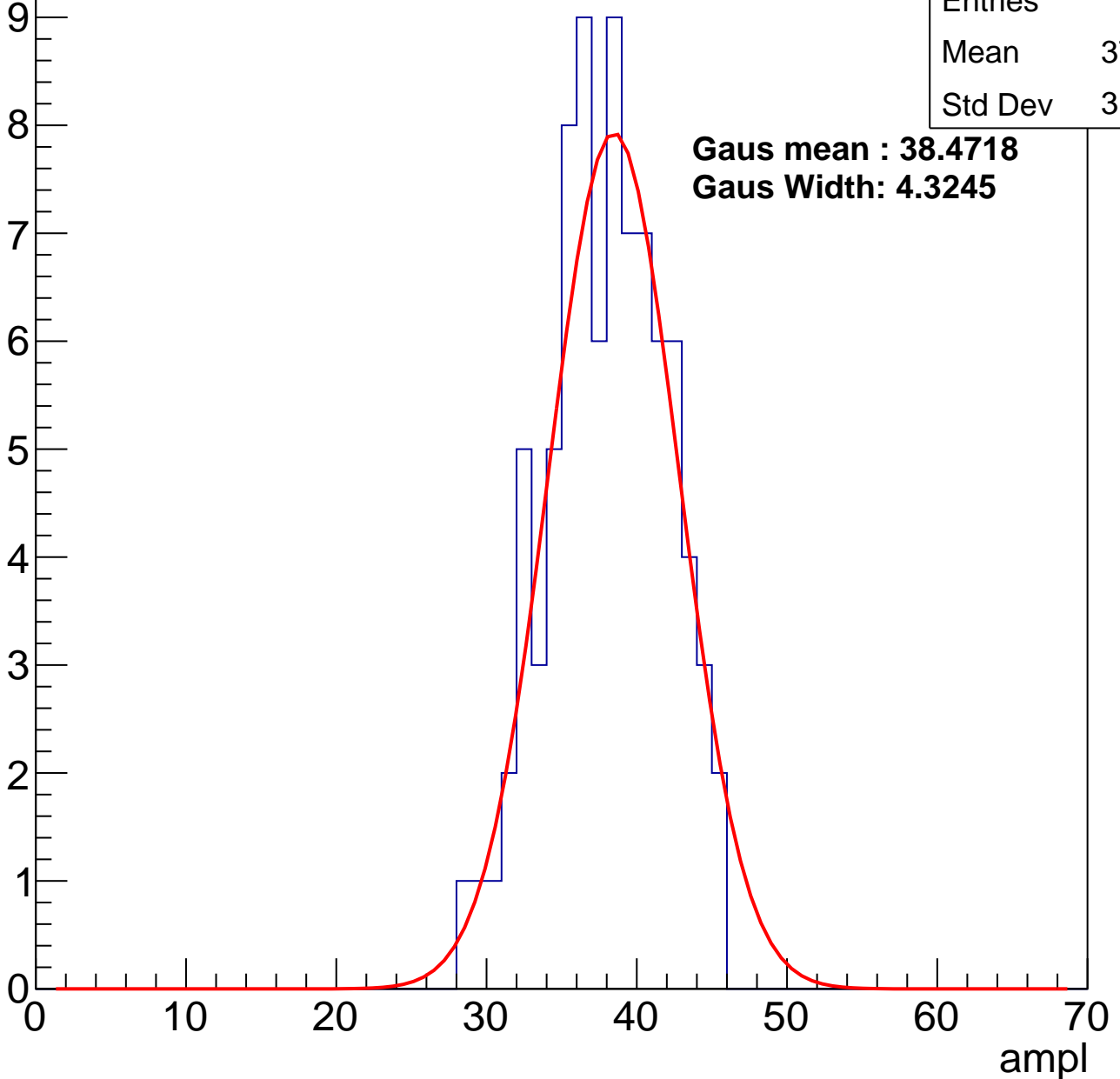
B1L100S, U10-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	37.54
Std Dev	3.858

Gaus mean : 38.4718
Gaus Width: 4.3245



B1L100S, U10-ch97, adc2

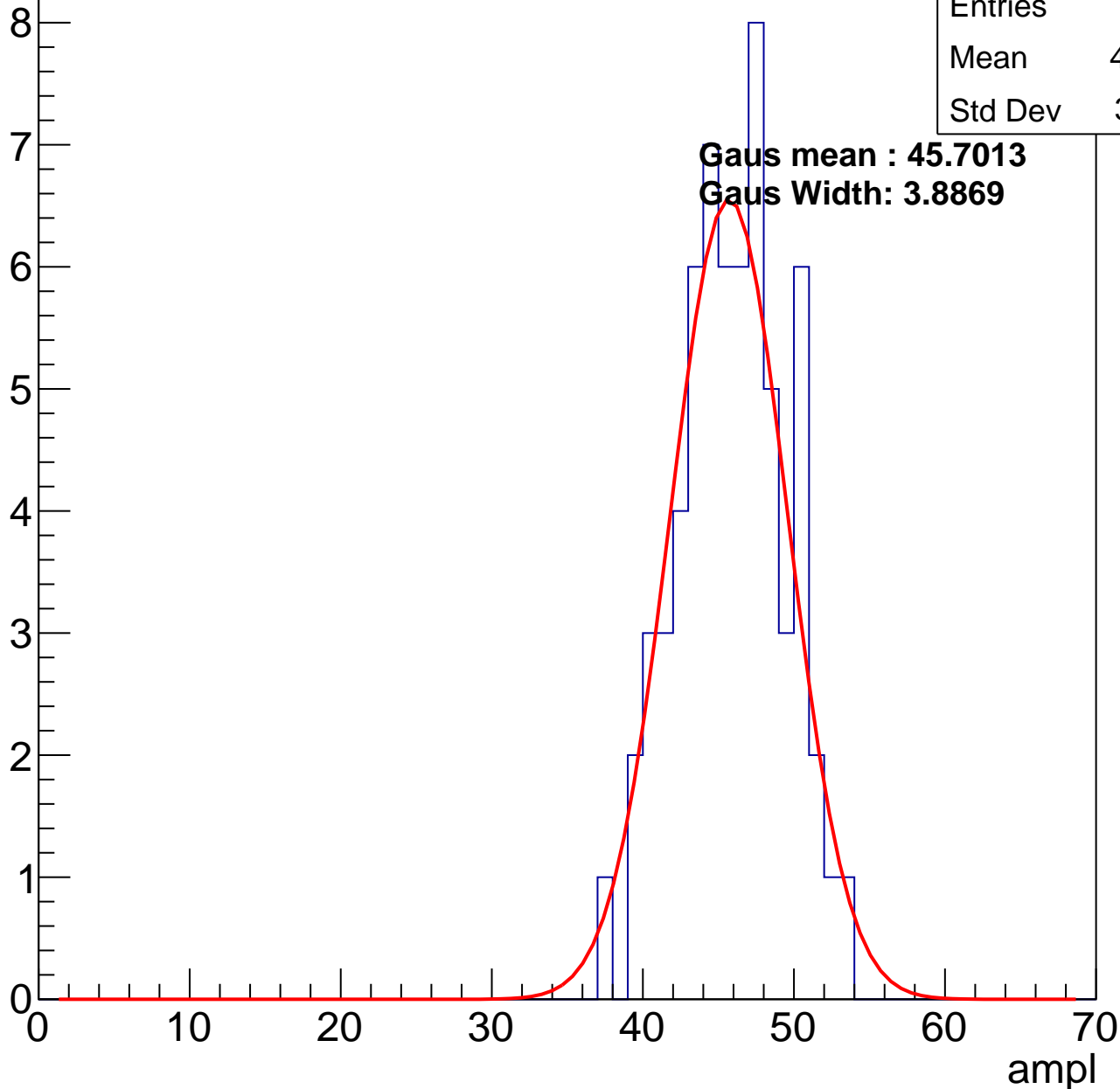
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	45.44
Std Dev	3.491

Gaus mean : 45.7013

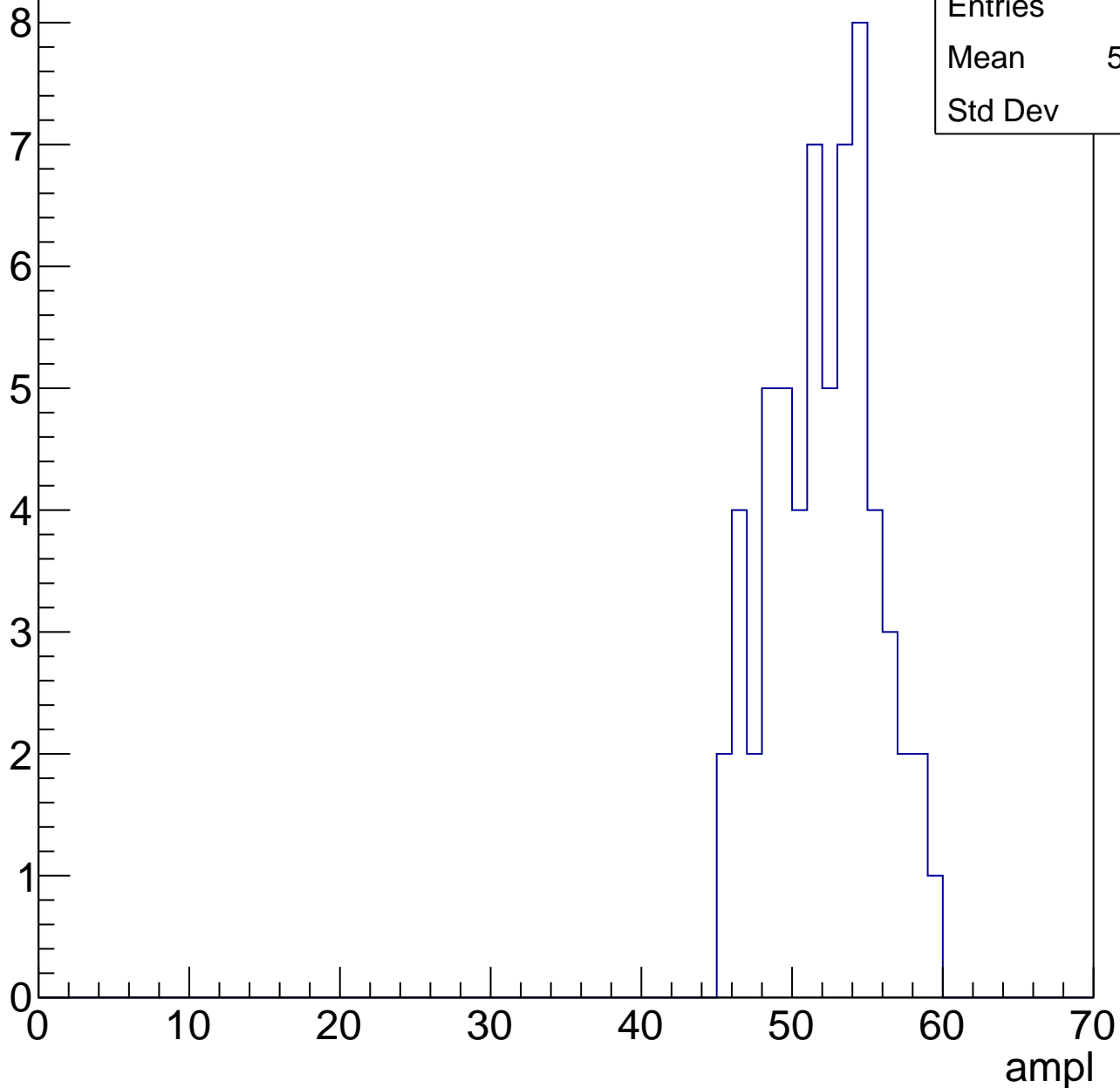
Gaus Width: 3.8869



B1L100S, U10-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

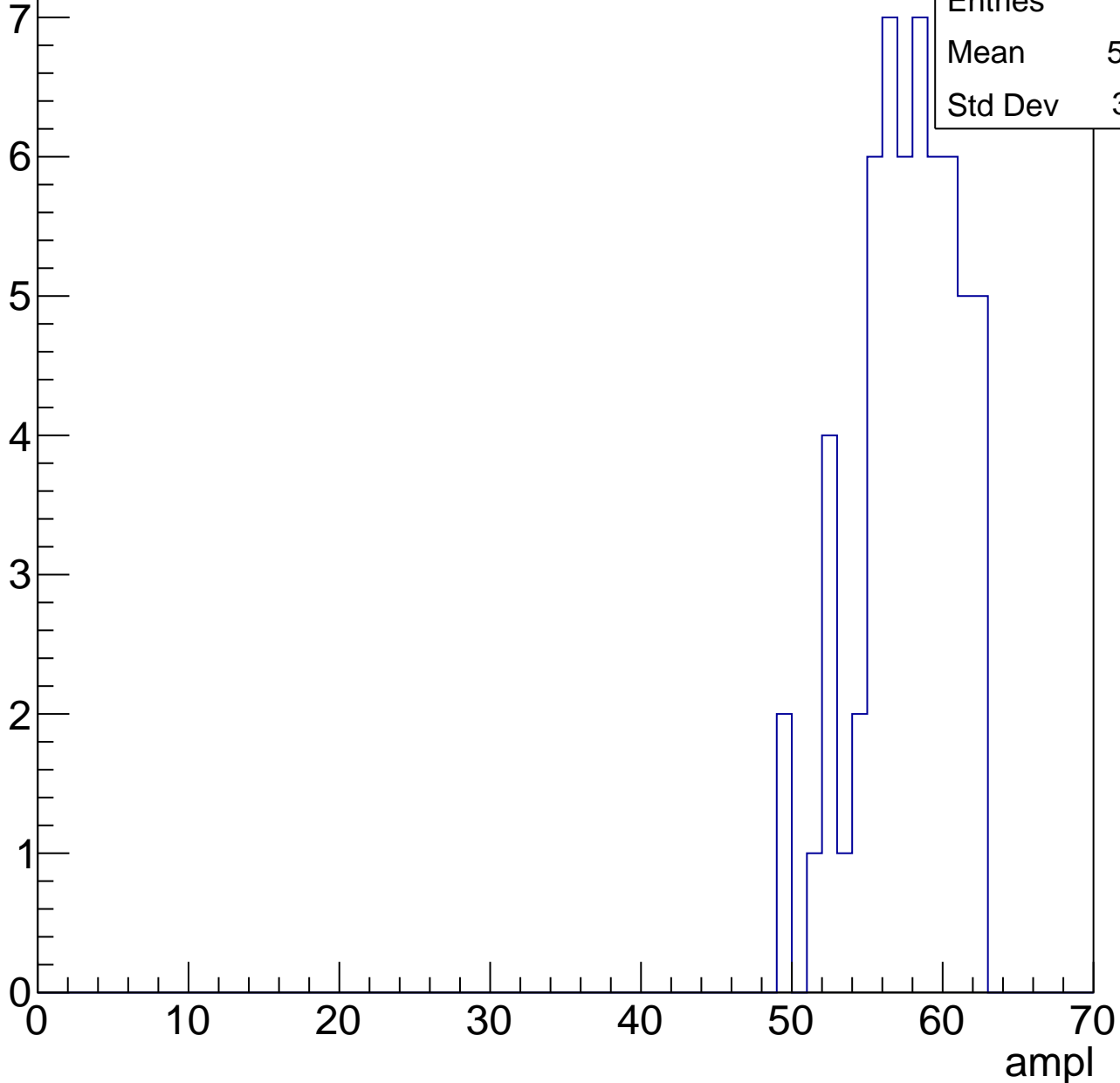


B1L100S, U10-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	57.19
Std Dev	3.261

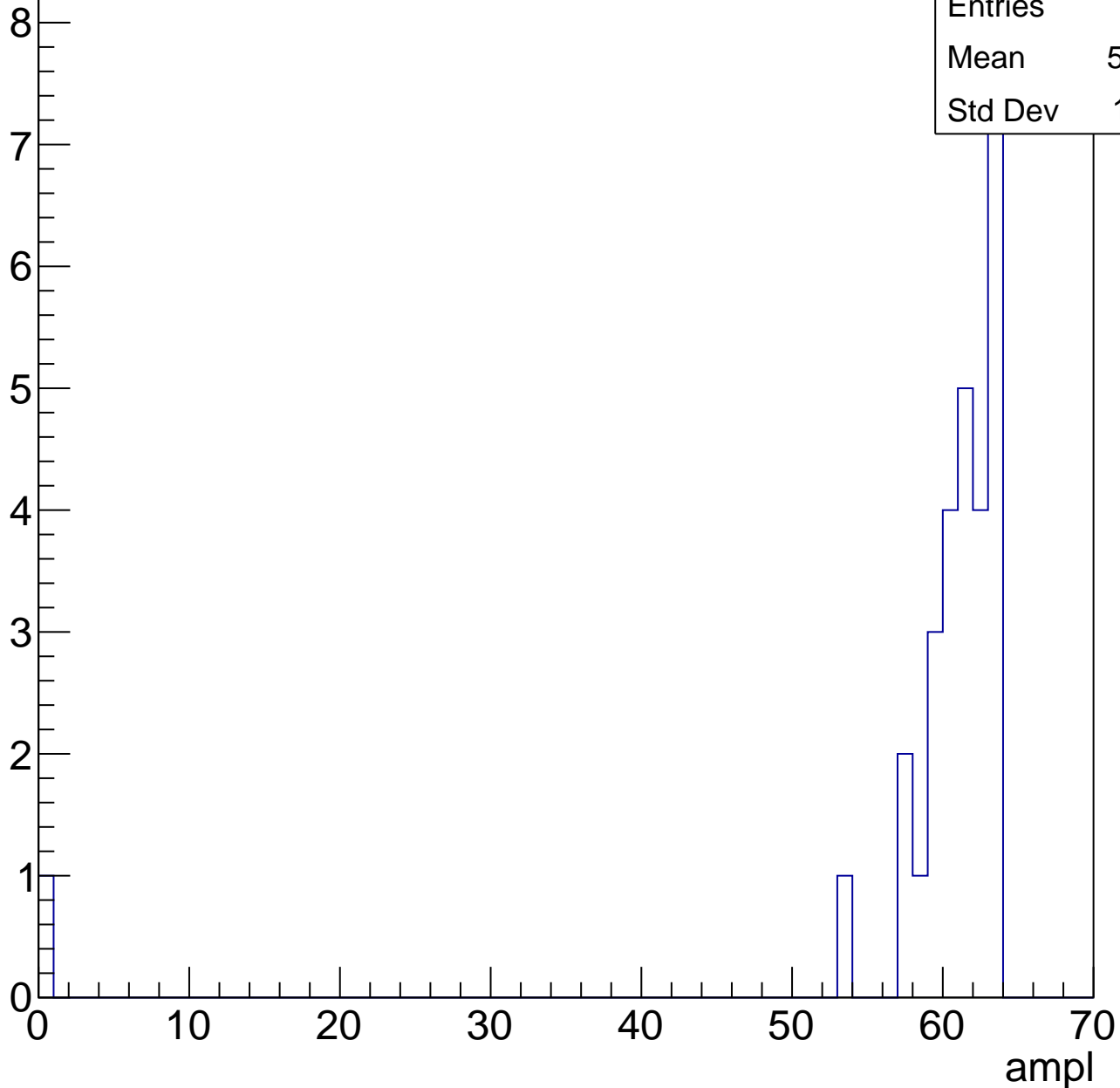


B1L100S, U10-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.59
Std Dev	11.31



B1L100S, U10-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch98, adc0

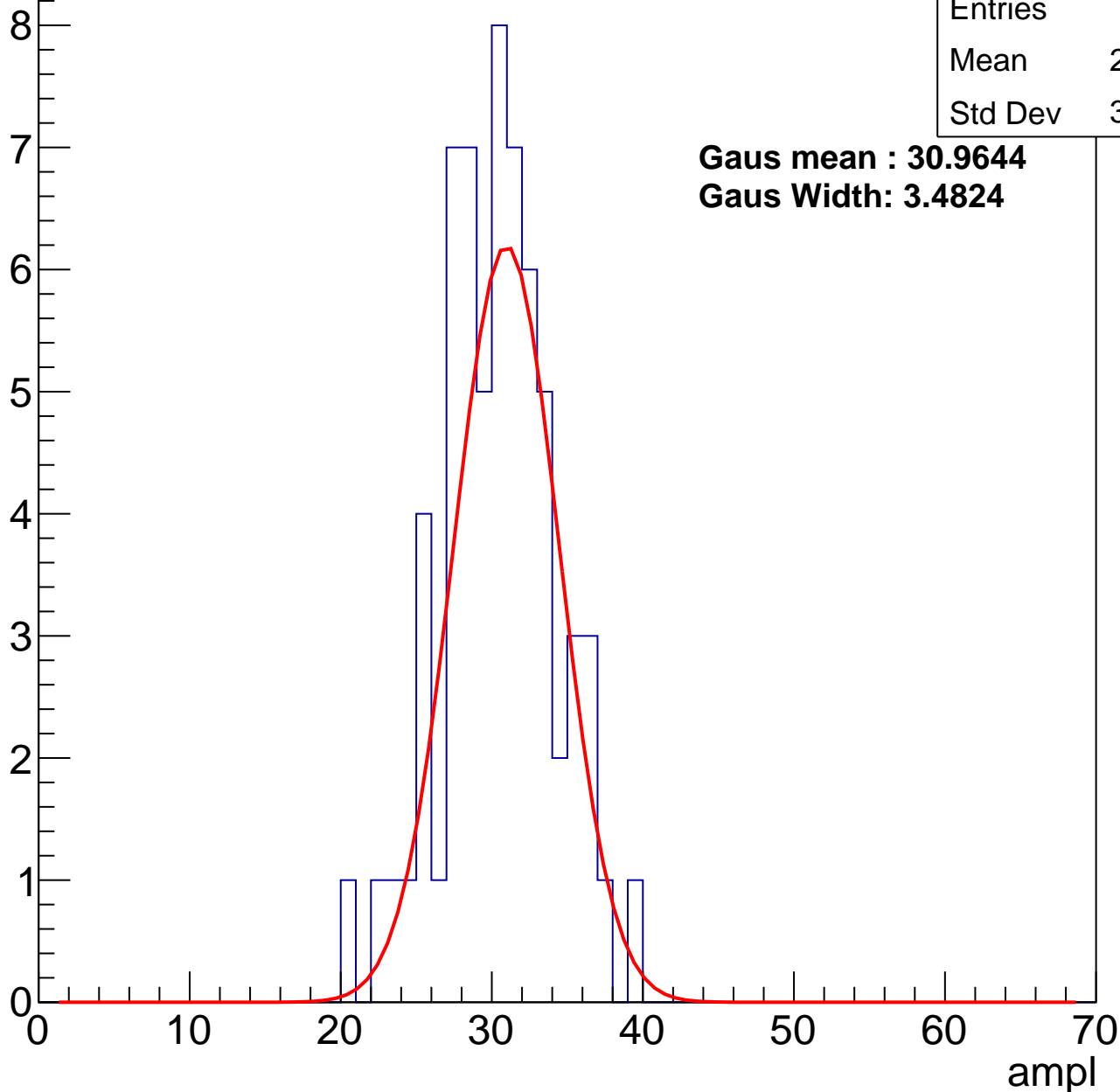
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.94
Std Dev	3.712

Gaus mean : 30.9644

Gaus Width: 3.4824



B1L100S, U10-ch98, adc1

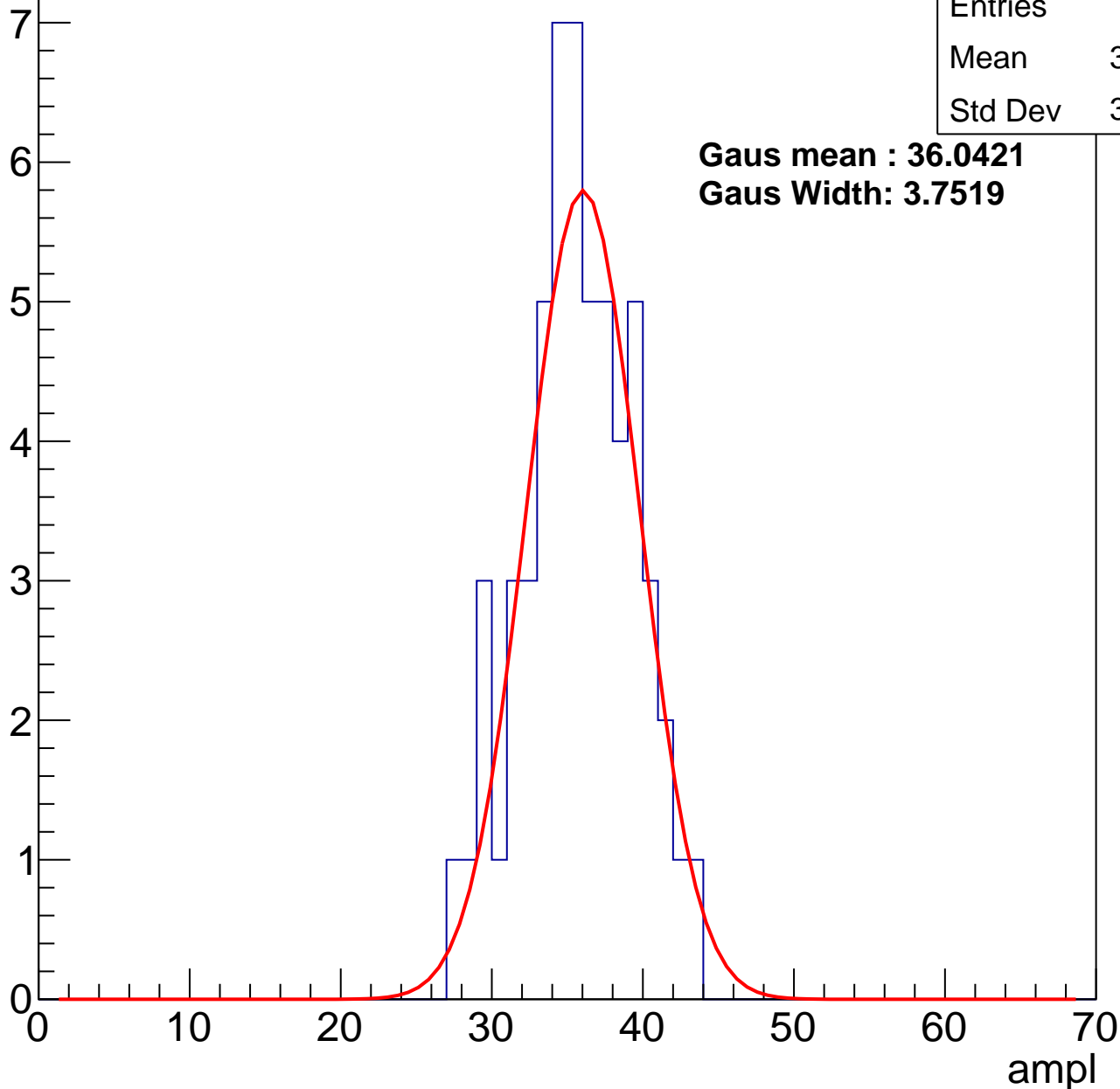
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	35.23
Std Dev	3.618

Gaus mean : 36.0421

Gaus Width: 3.7519



B1L100S, U10-ch98, adc2

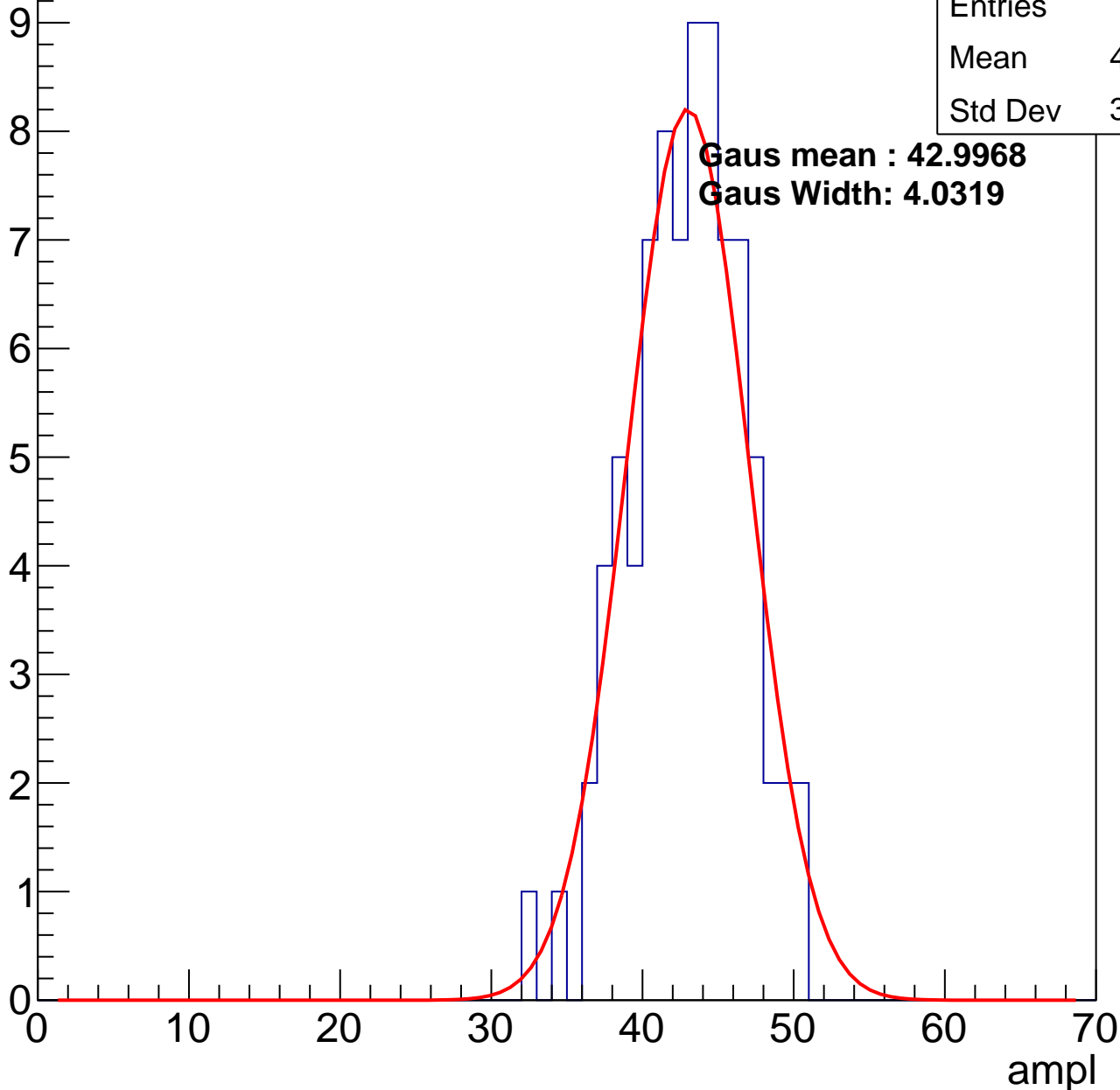
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	42.48
Std Dev	3.683

Gaus mean : 42.9968

Gaus Width: 4.0319

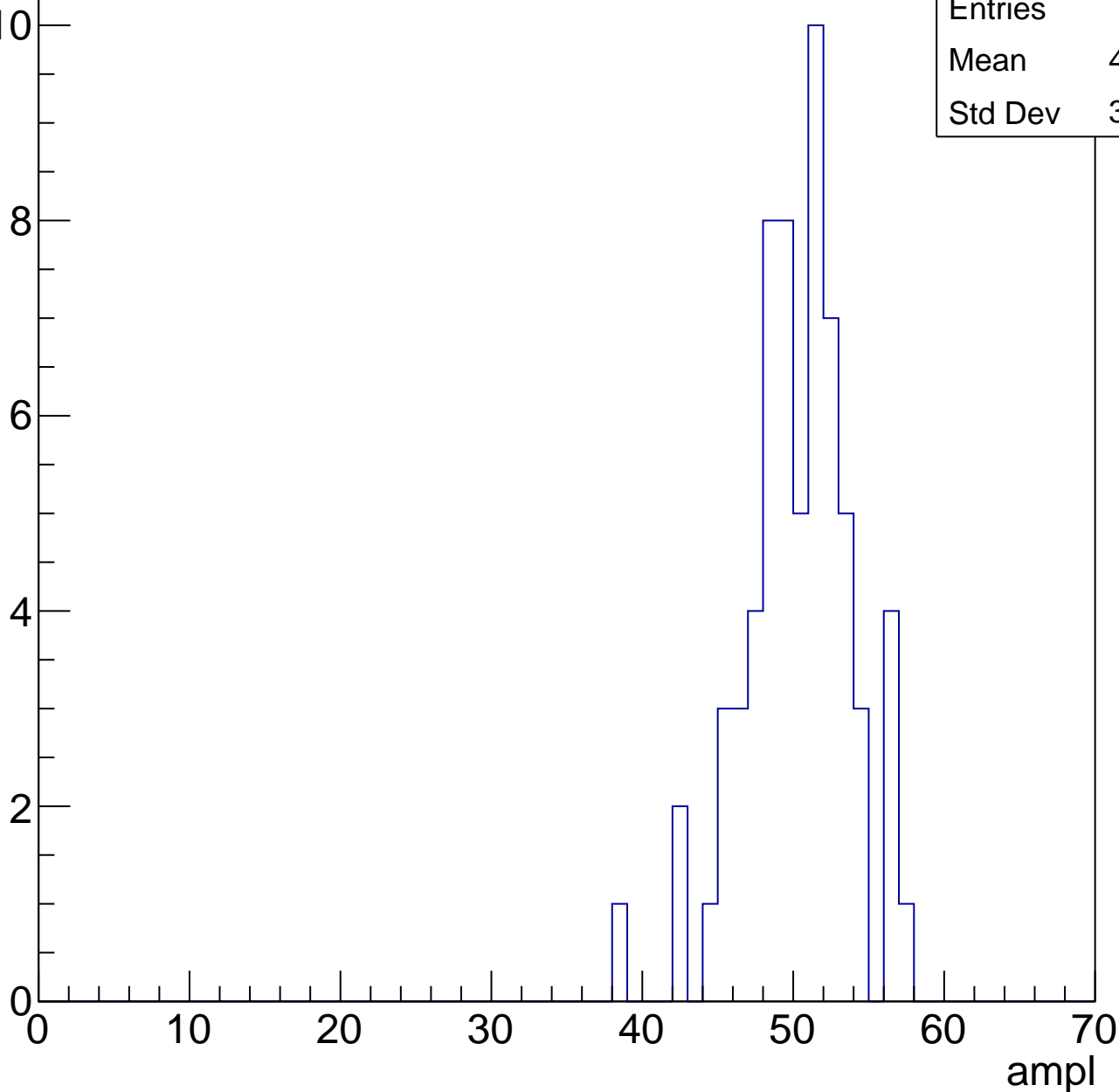


B1L100S, U10-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.77
Std Dev	3.568

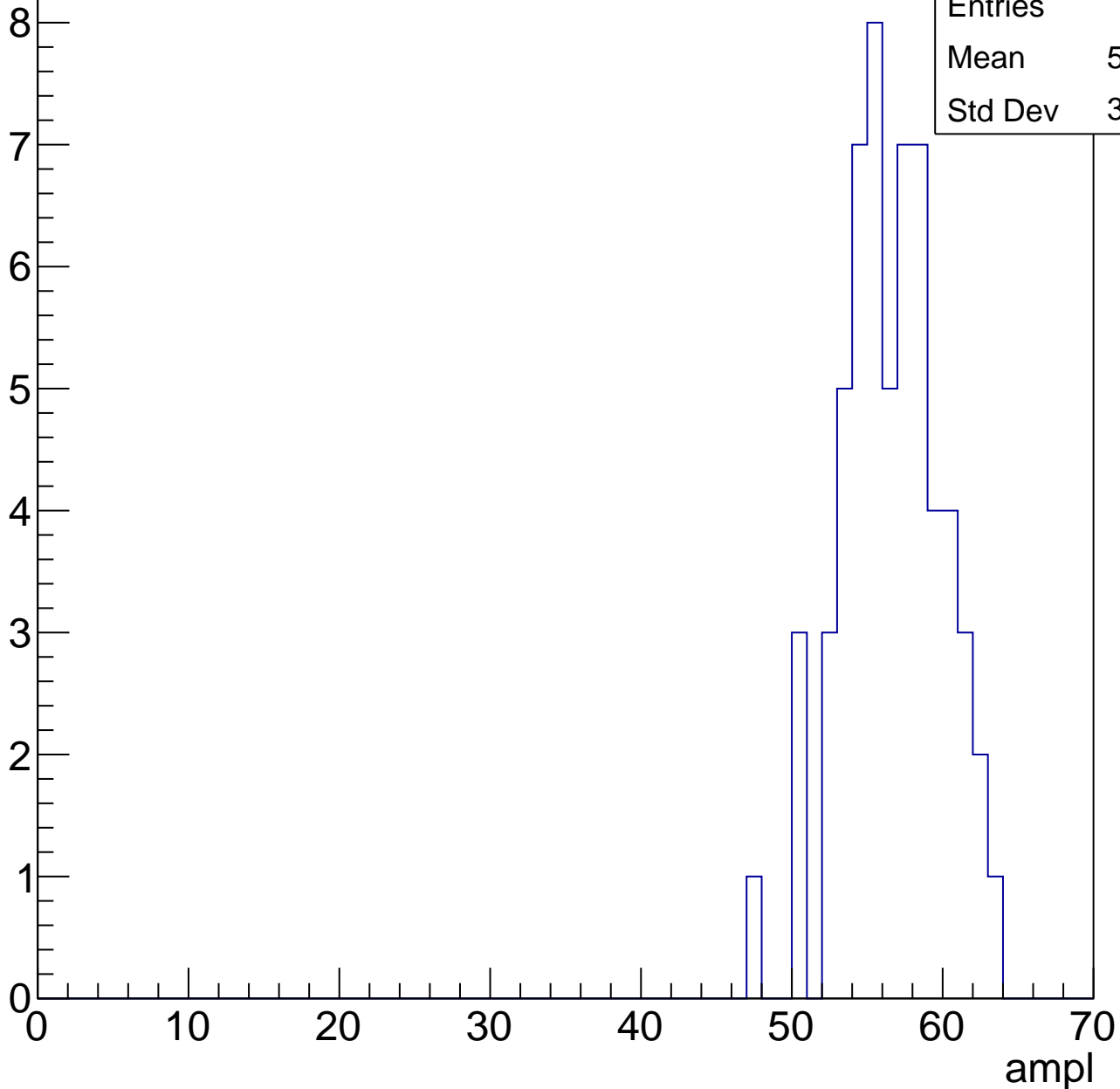


B1L100S, U10-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.12
Std Dev	3.287



B1L100S, U10-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	58.71
Std Dev	9.529

ampl

0

10

20

30

40

50

60

70

B1L100S, U10-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch99, adc0

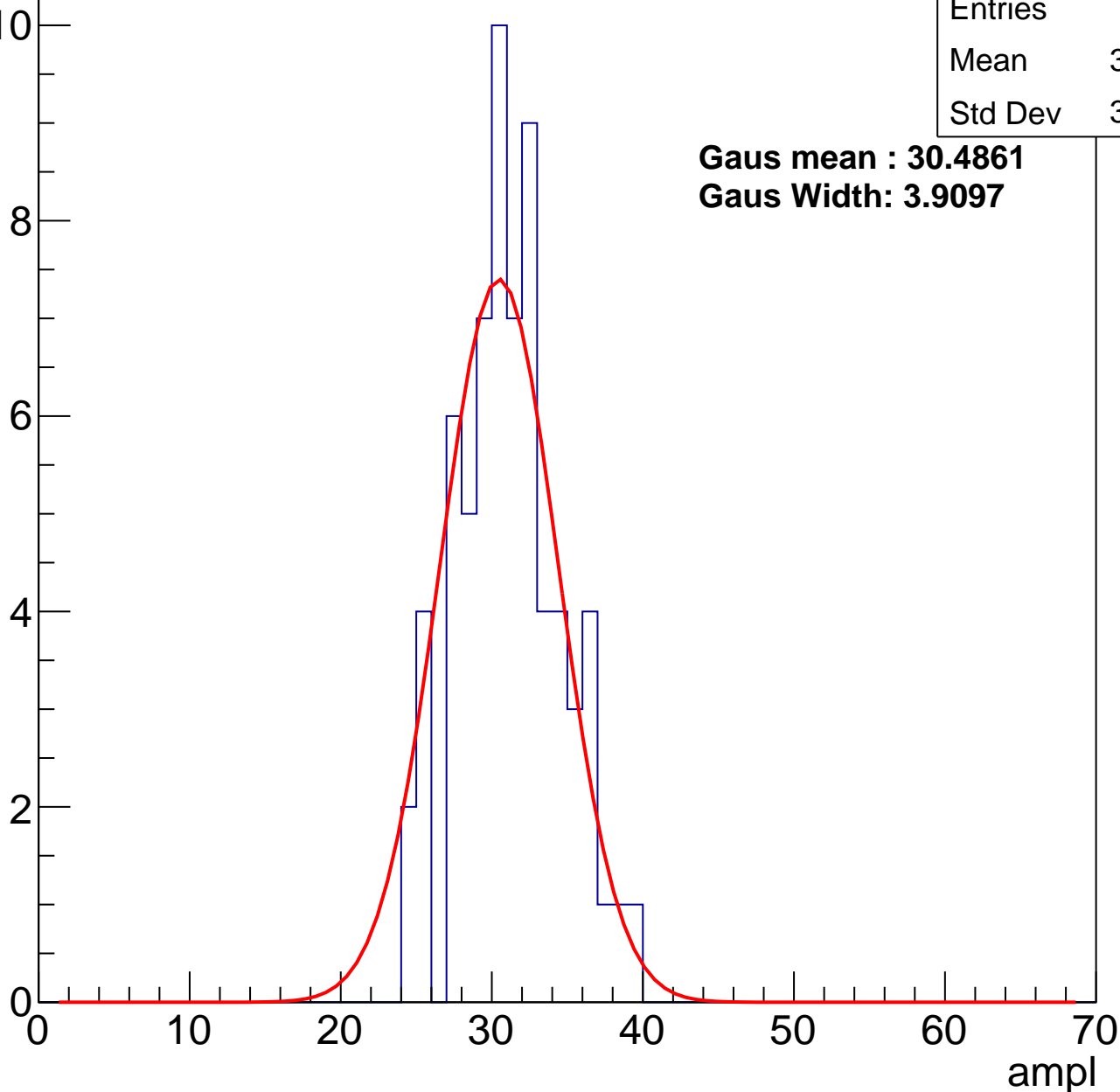
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.72
Std Dev	3.373

Gaus mean : 30.4861

Gaus Width: 3.9097



B1L100S, U10-ch99, adc1

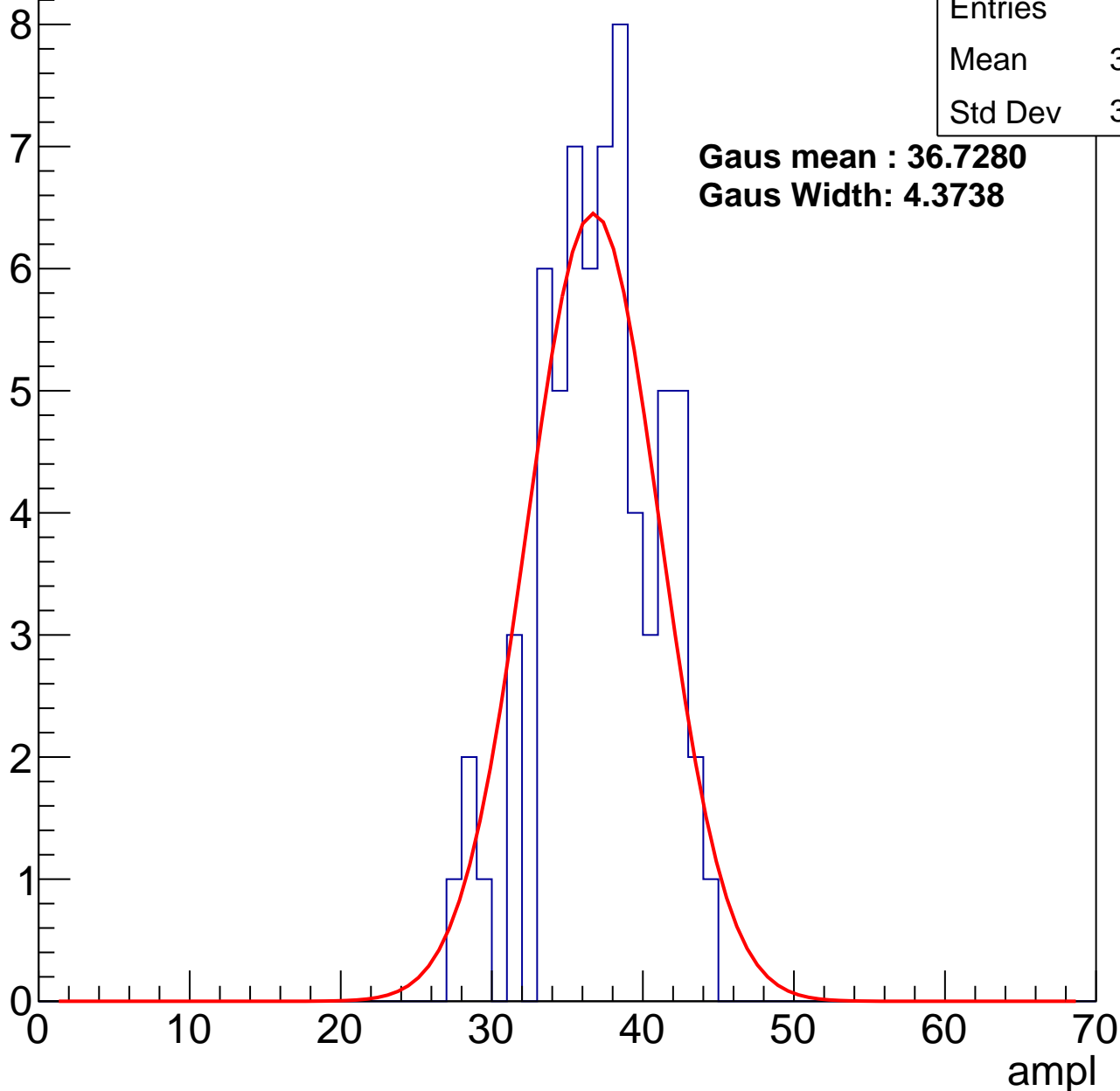
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.64
Std Dev	3.848

Gaus mean : 36.7280

Gaus Width: 4.3738



B1L100S, U10-ch99, adc2

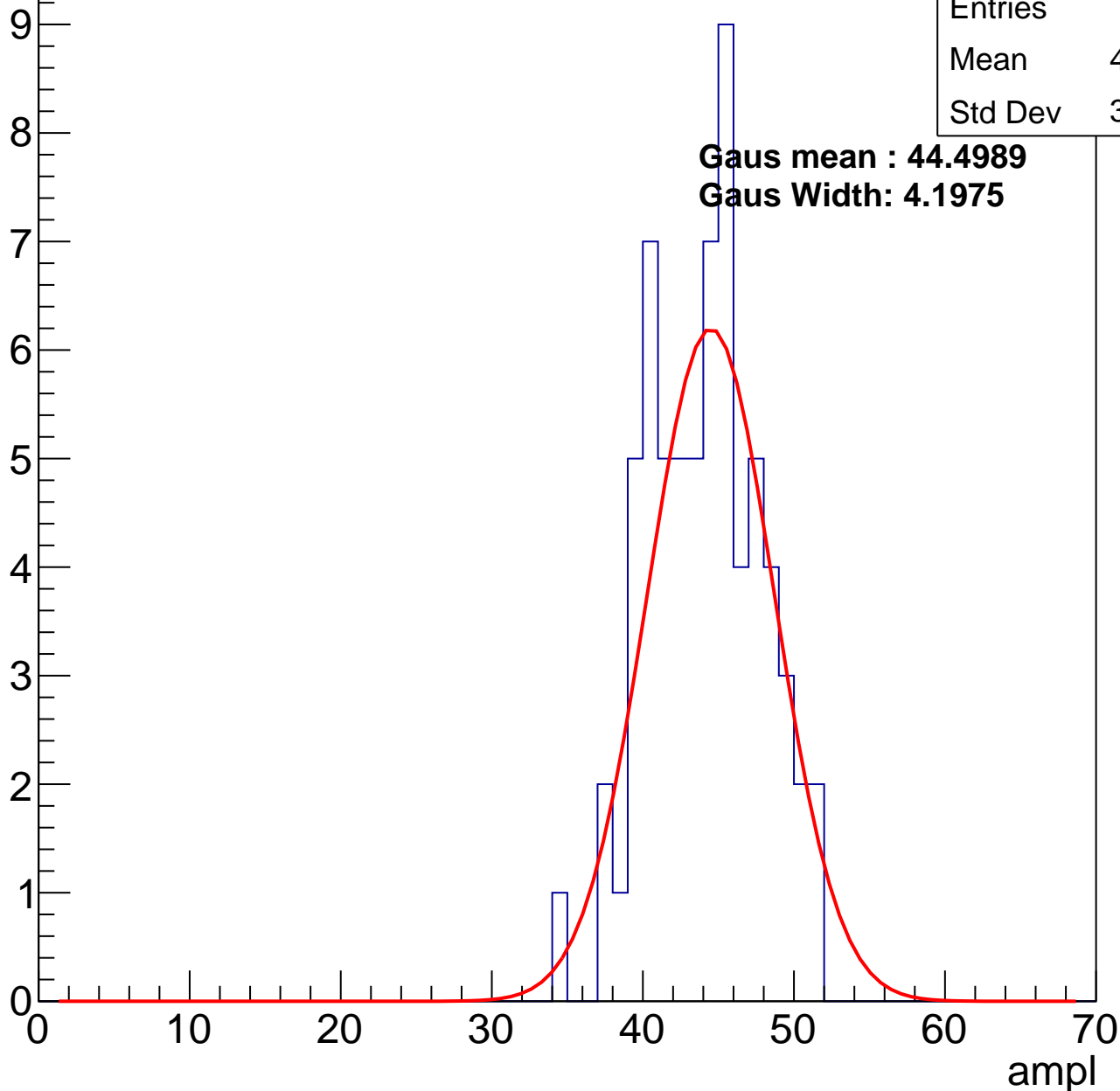
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.64
Std Dev	3.688

Gaus mean : 44.4989

Gaus Width: 4.1975

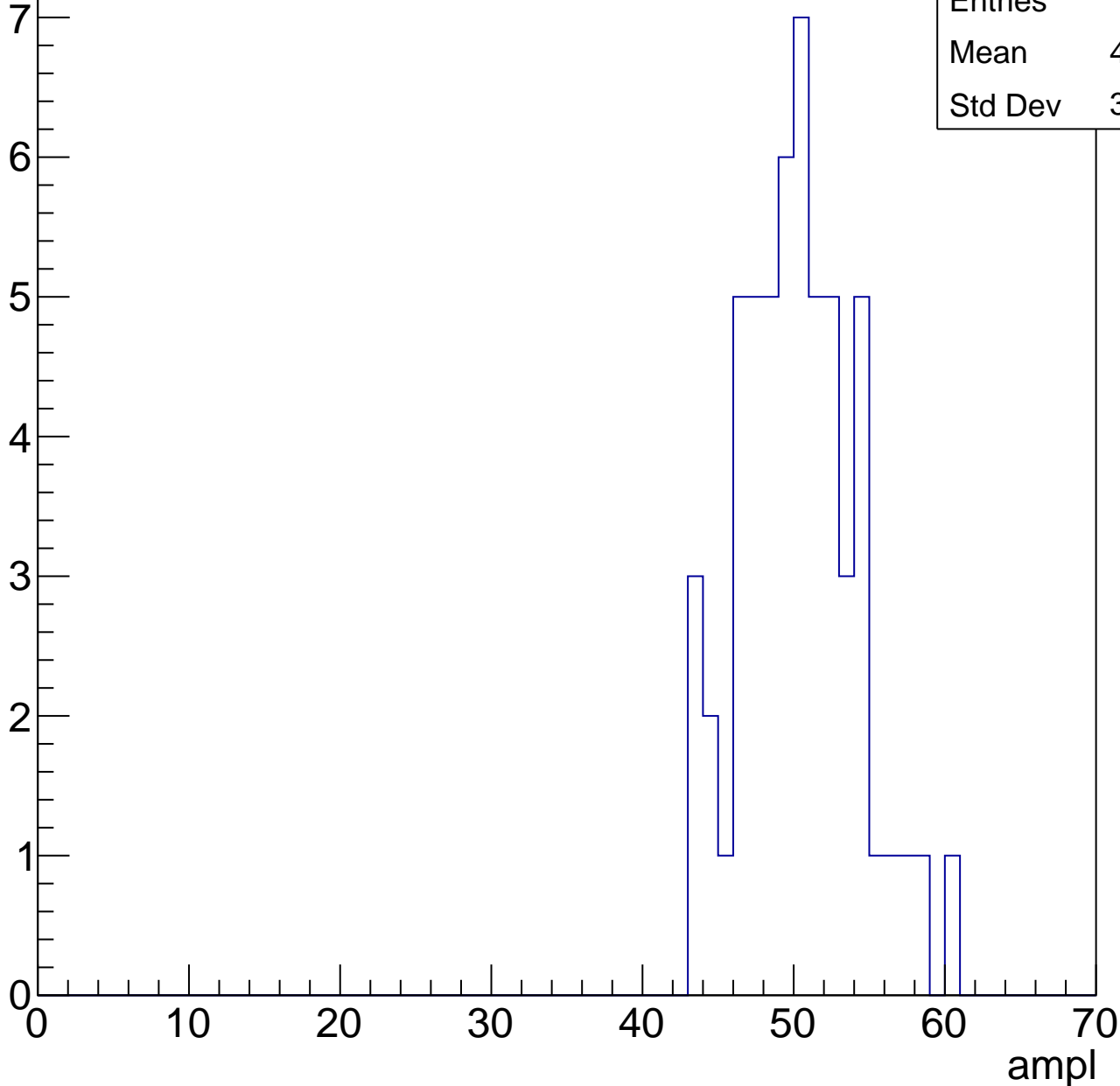


B1L100S, U10-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	49.84
Std Dev	3.745

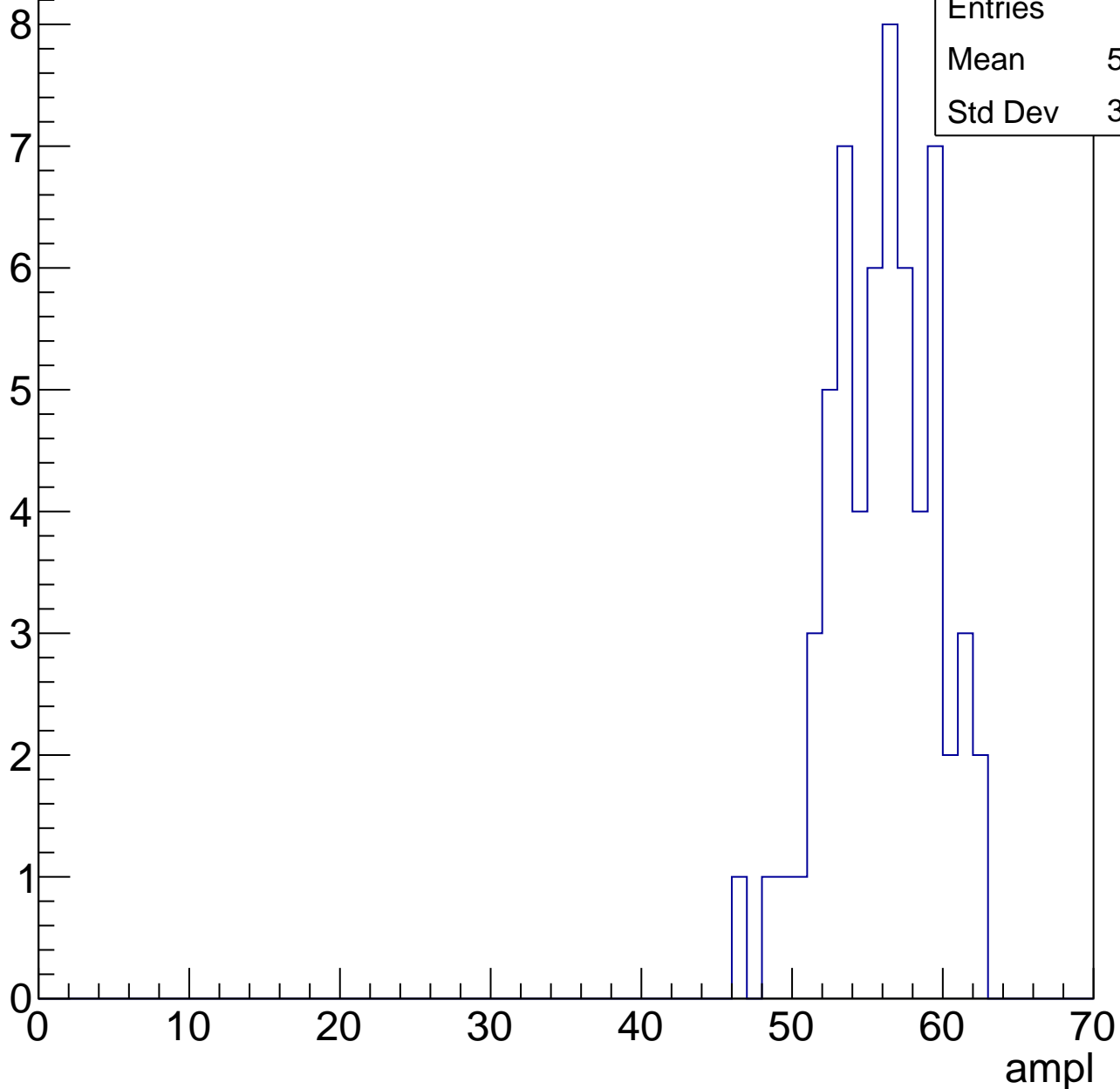


B1L100S, U10-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	55.49
Std Dev	3.476

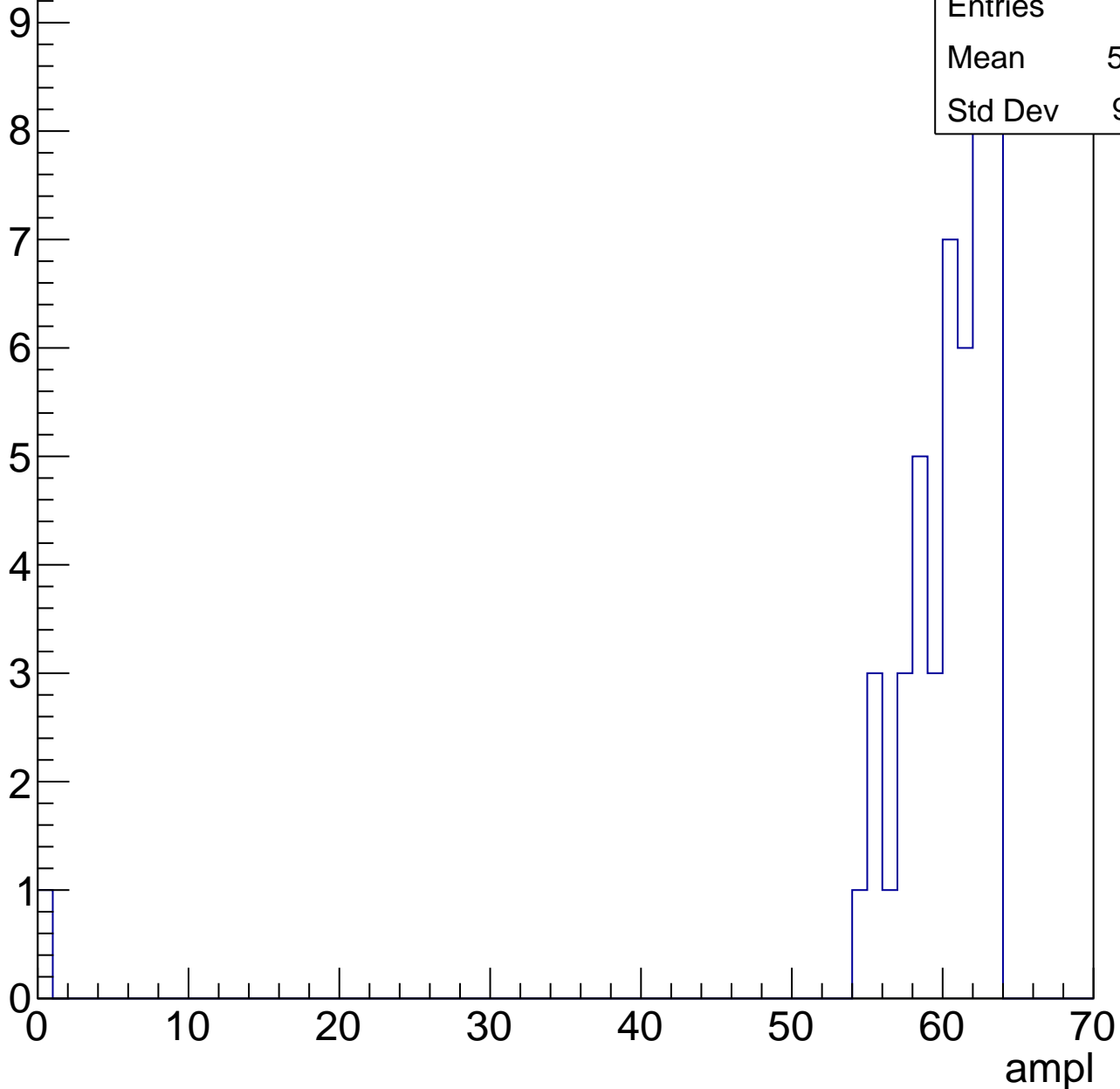


B1L100S, U10-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.77
Std Dev	9.021



B1L100S, U10-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L100S, U10-ch100, adc0

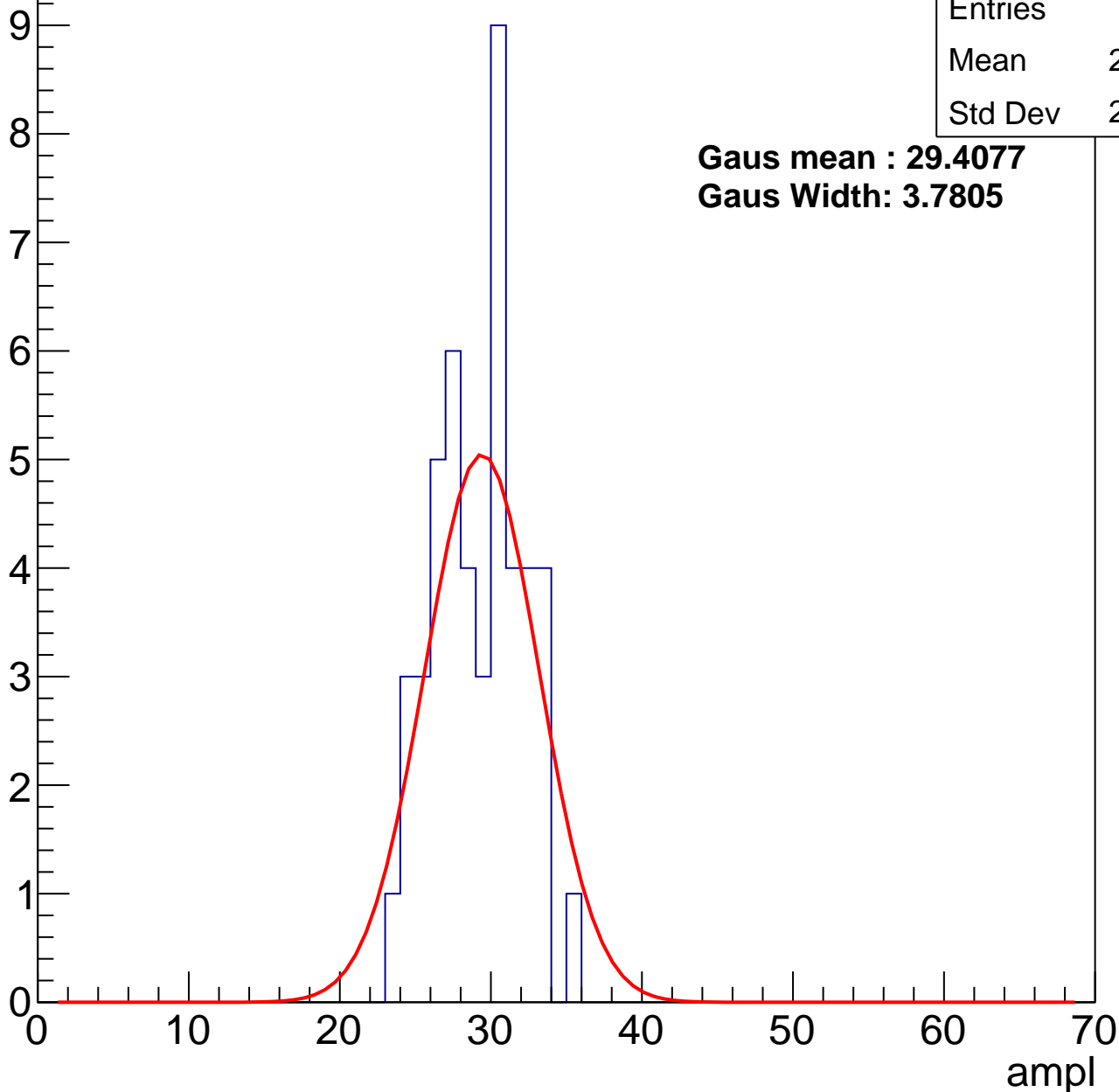
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	28.72
Std Dev	2.878

Gaus mean : 29.4077

Gaus Width: 3.7805



B1L100S, U10-ch100, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	35.08
Std Dev	3.595

Gaus mean : 35.4016

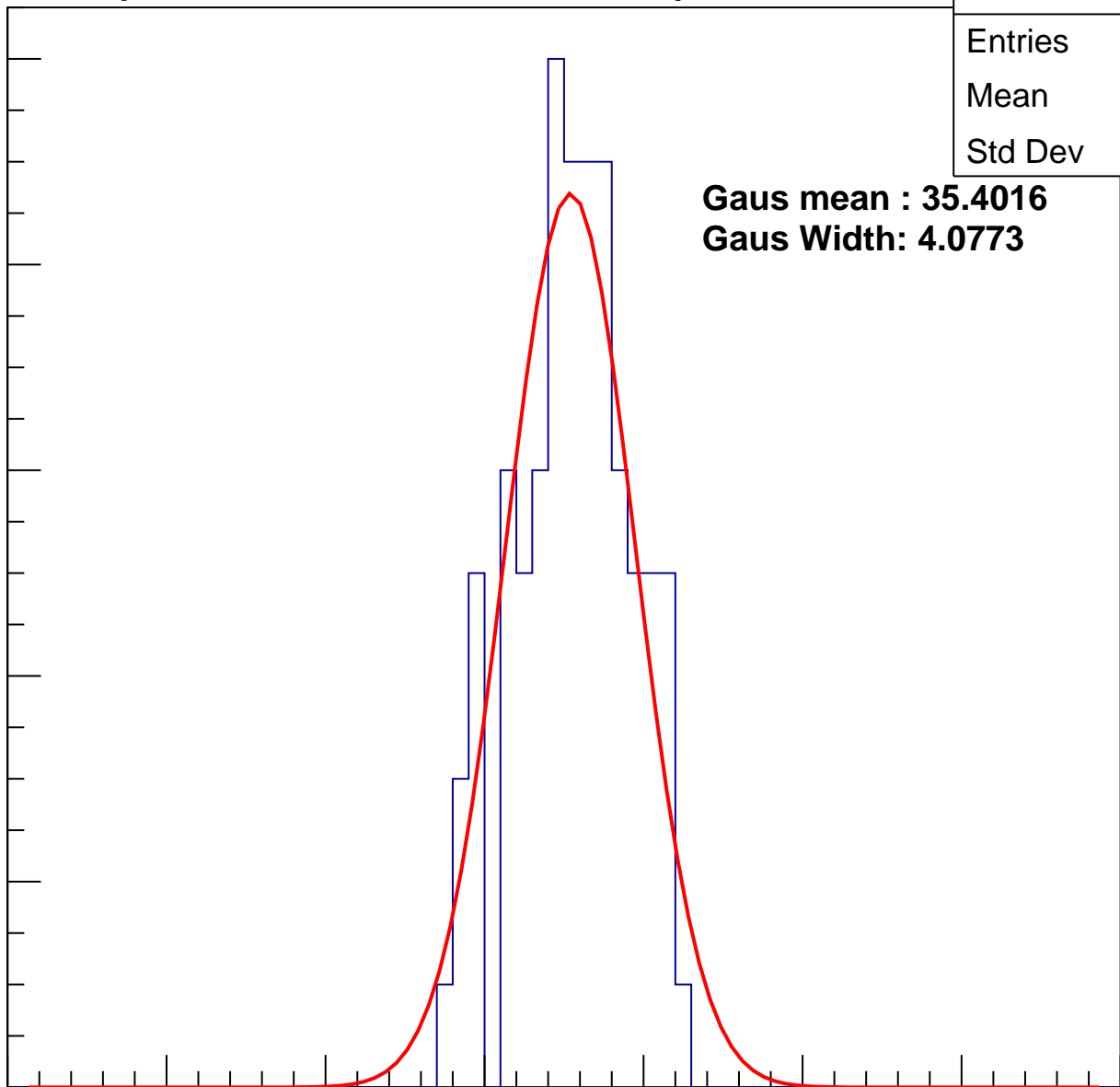
Gaus Width: 4.0773

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch100, adc2

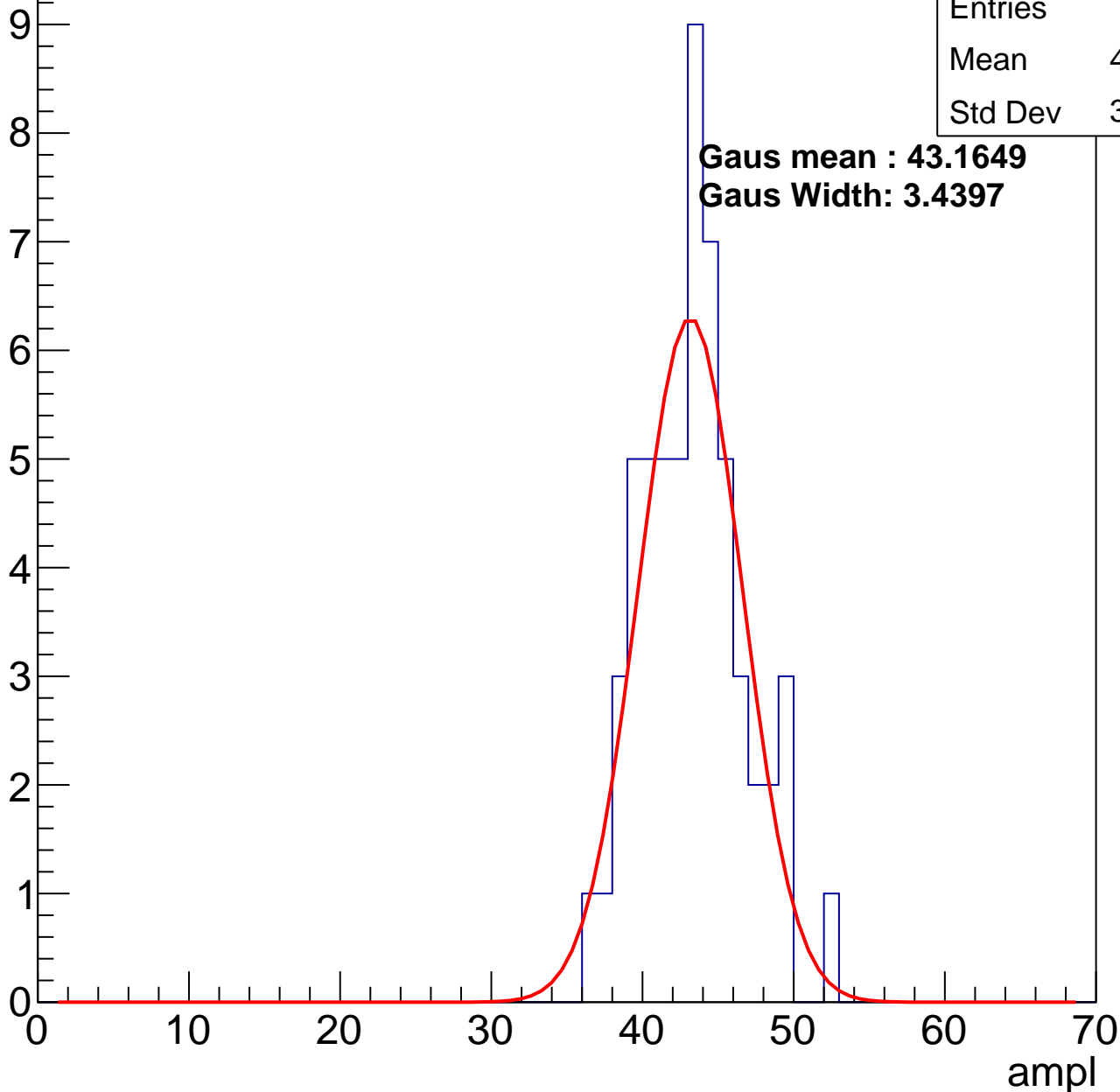
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.88
Std Dev	3.335

Gaus mean : 43.1649

Gaus Width: 3.4397

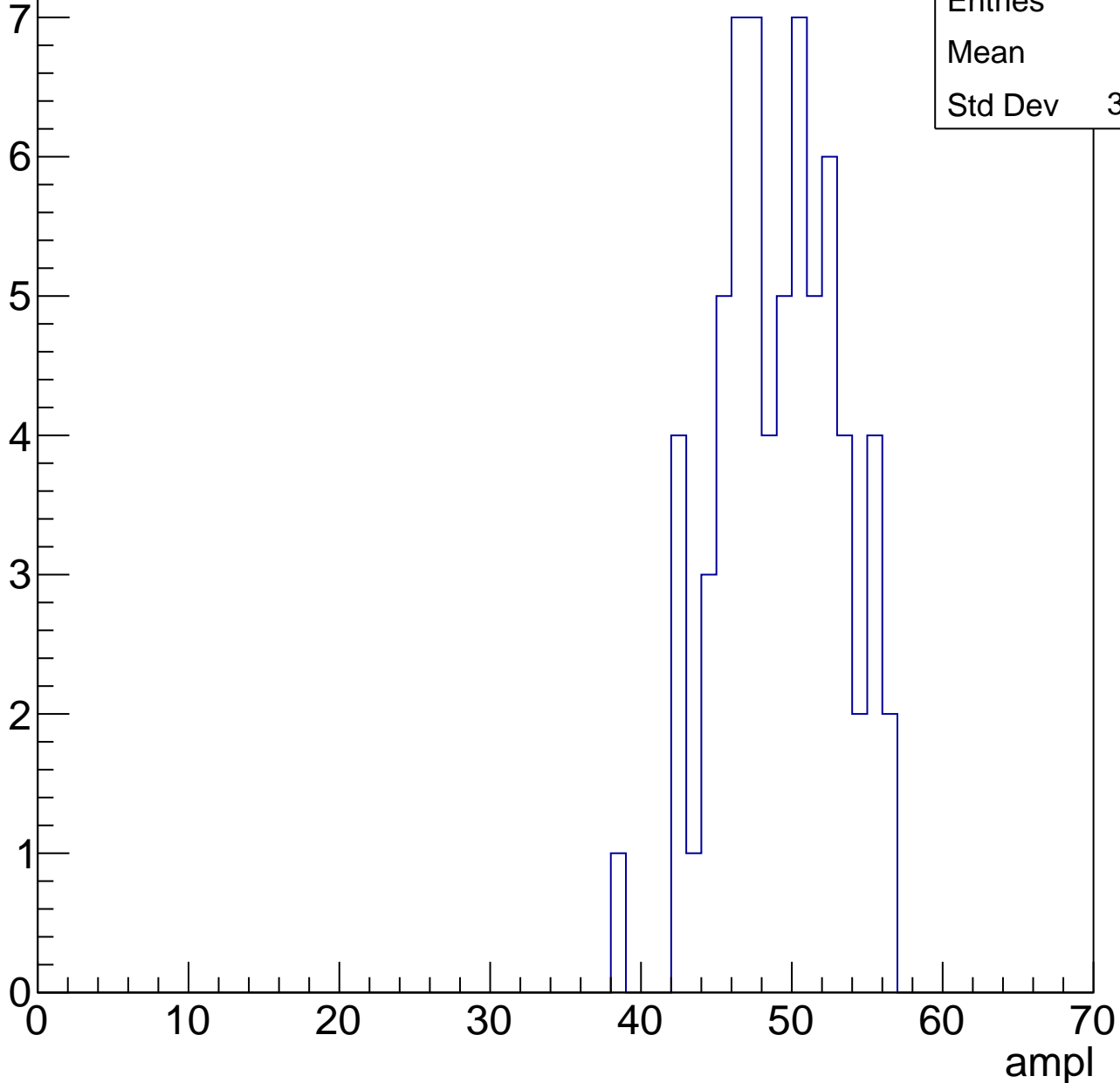


B1L100S, U10-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	48.7
Std Dev	3.929

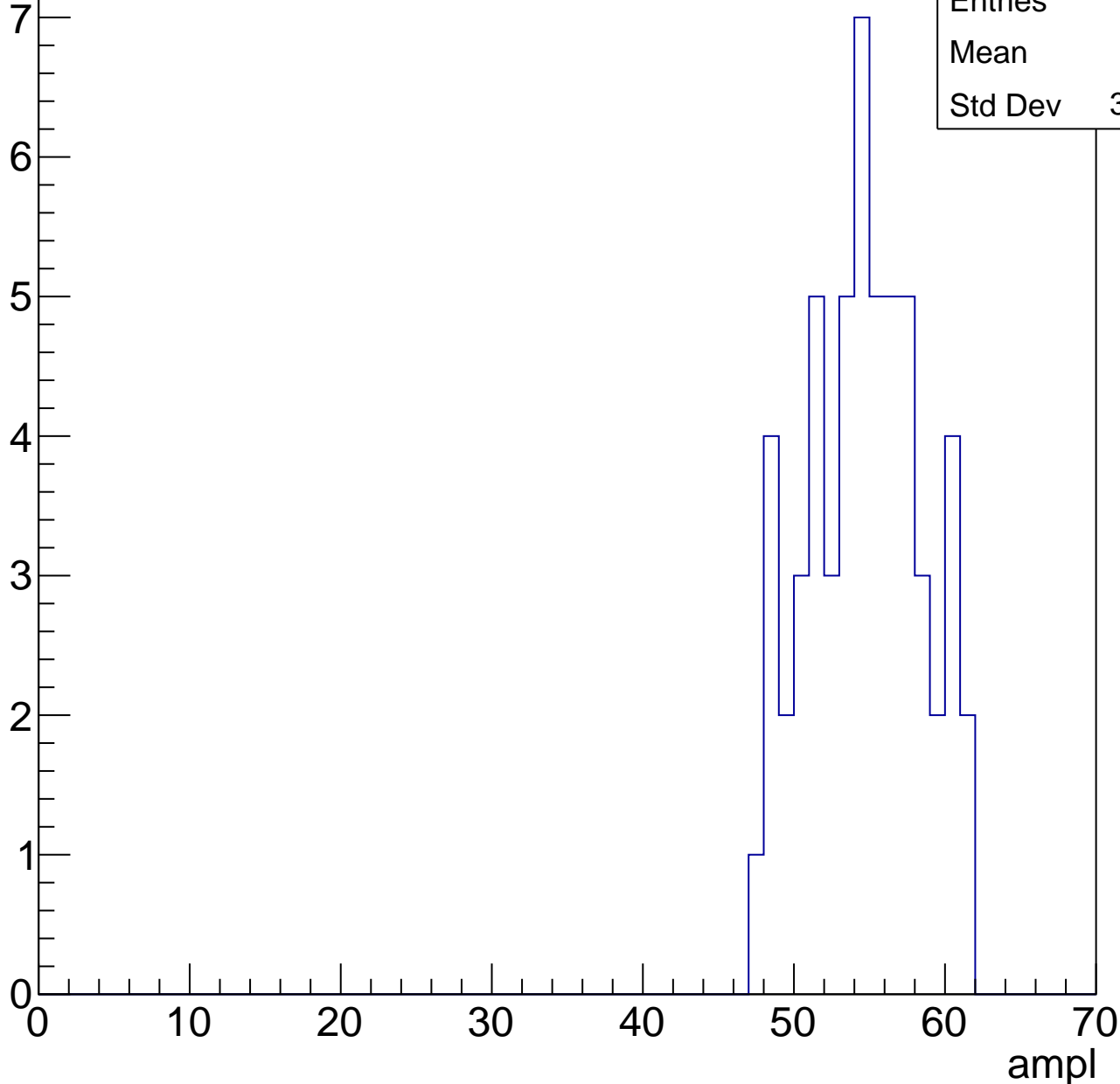


B1L100S, U10-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	54.2
Std Dev	3.686

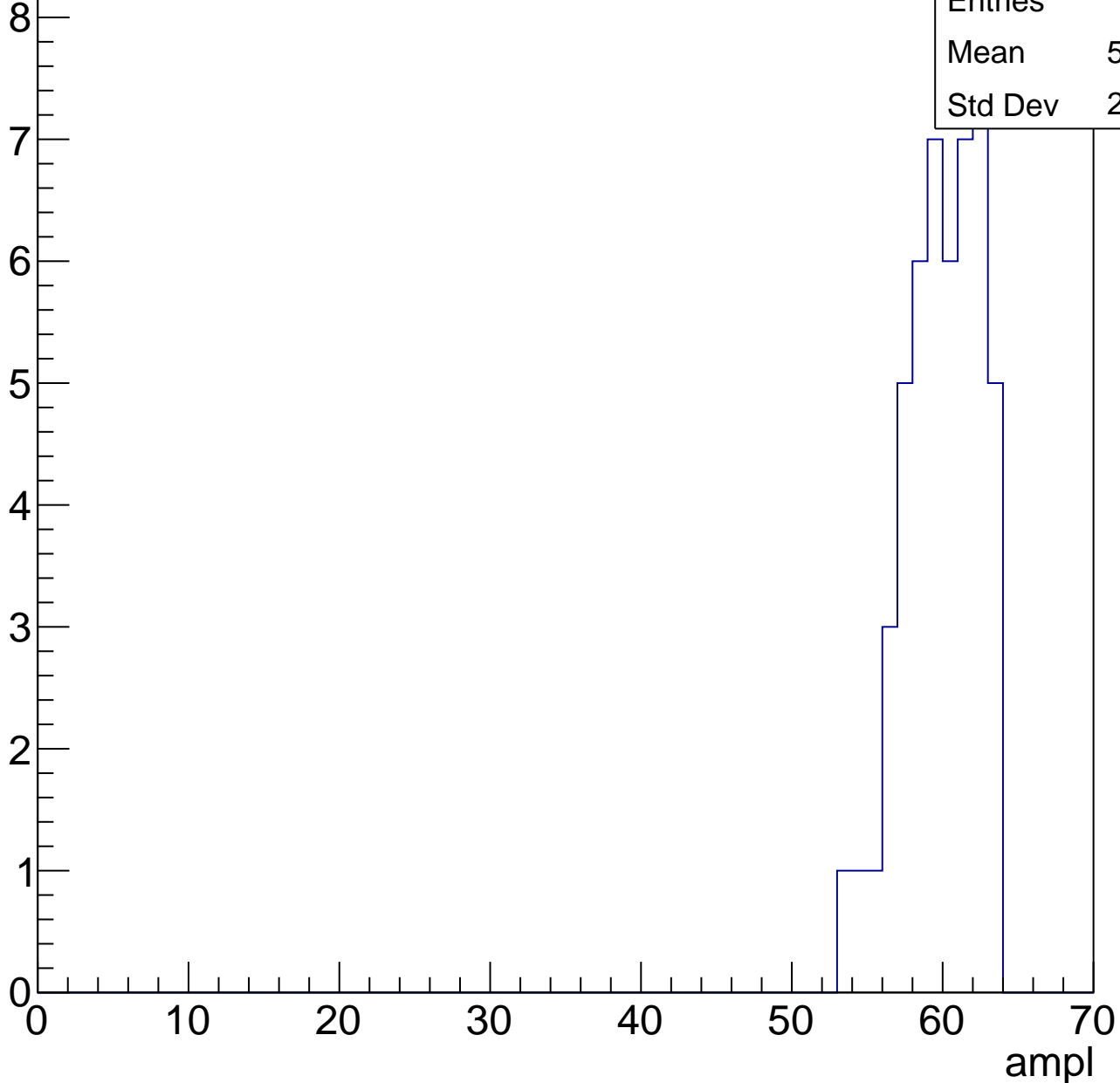


B1L100S, U10-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

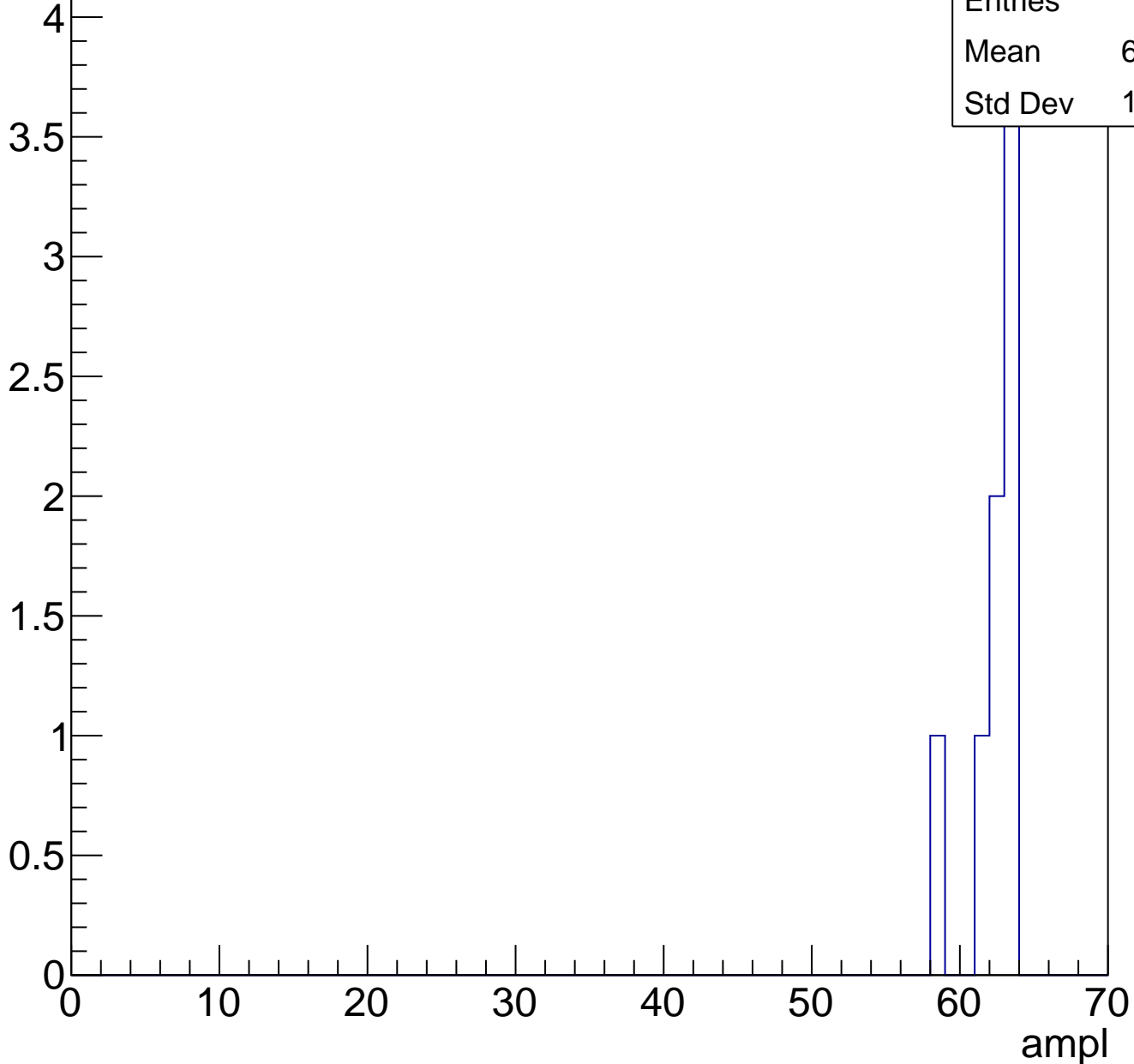
Entries	50
Mean	59.48
Std Dev	2.468



B1L100S, U10-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

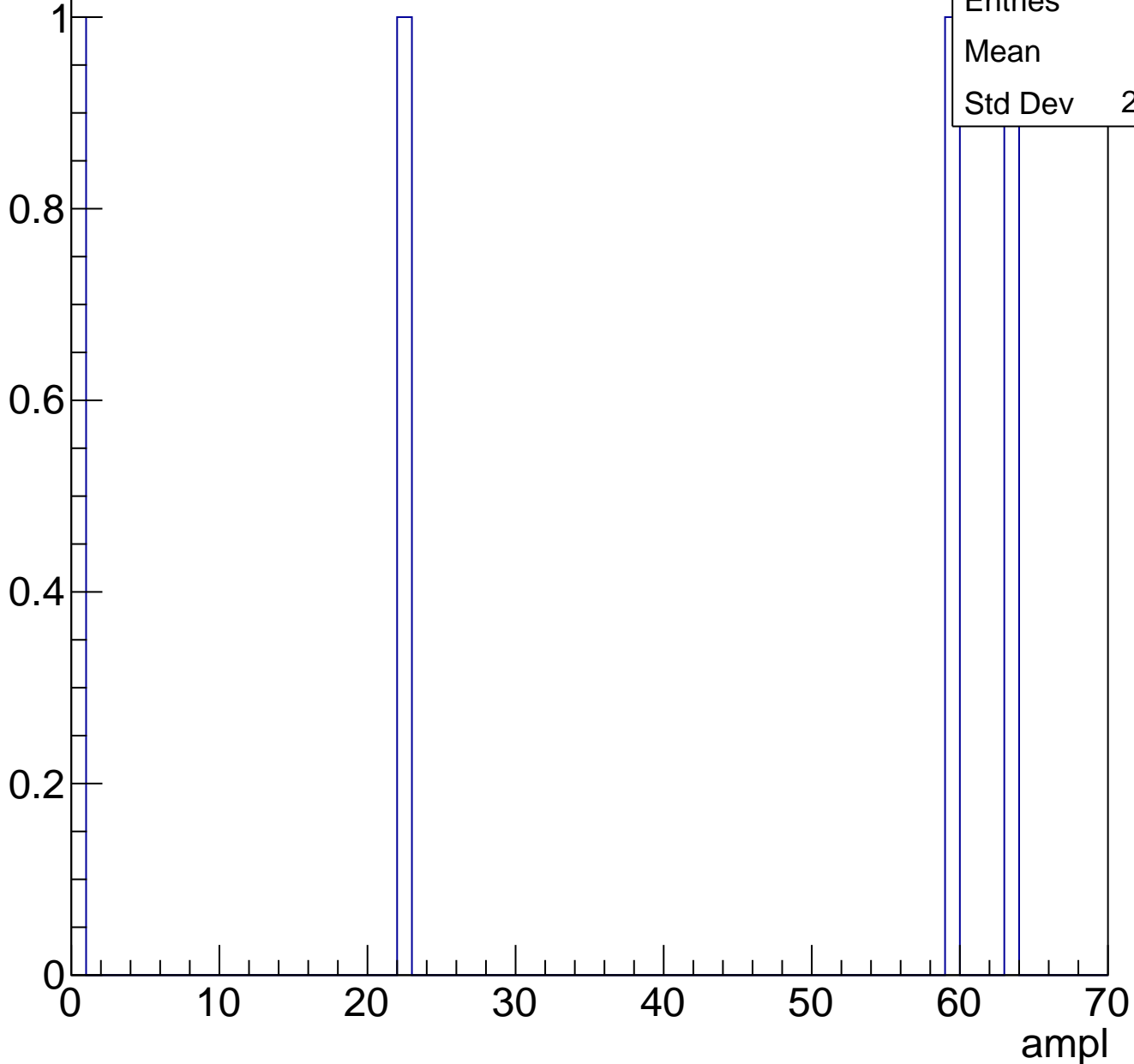
Entry



B1L100S, U10-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch101, adc0

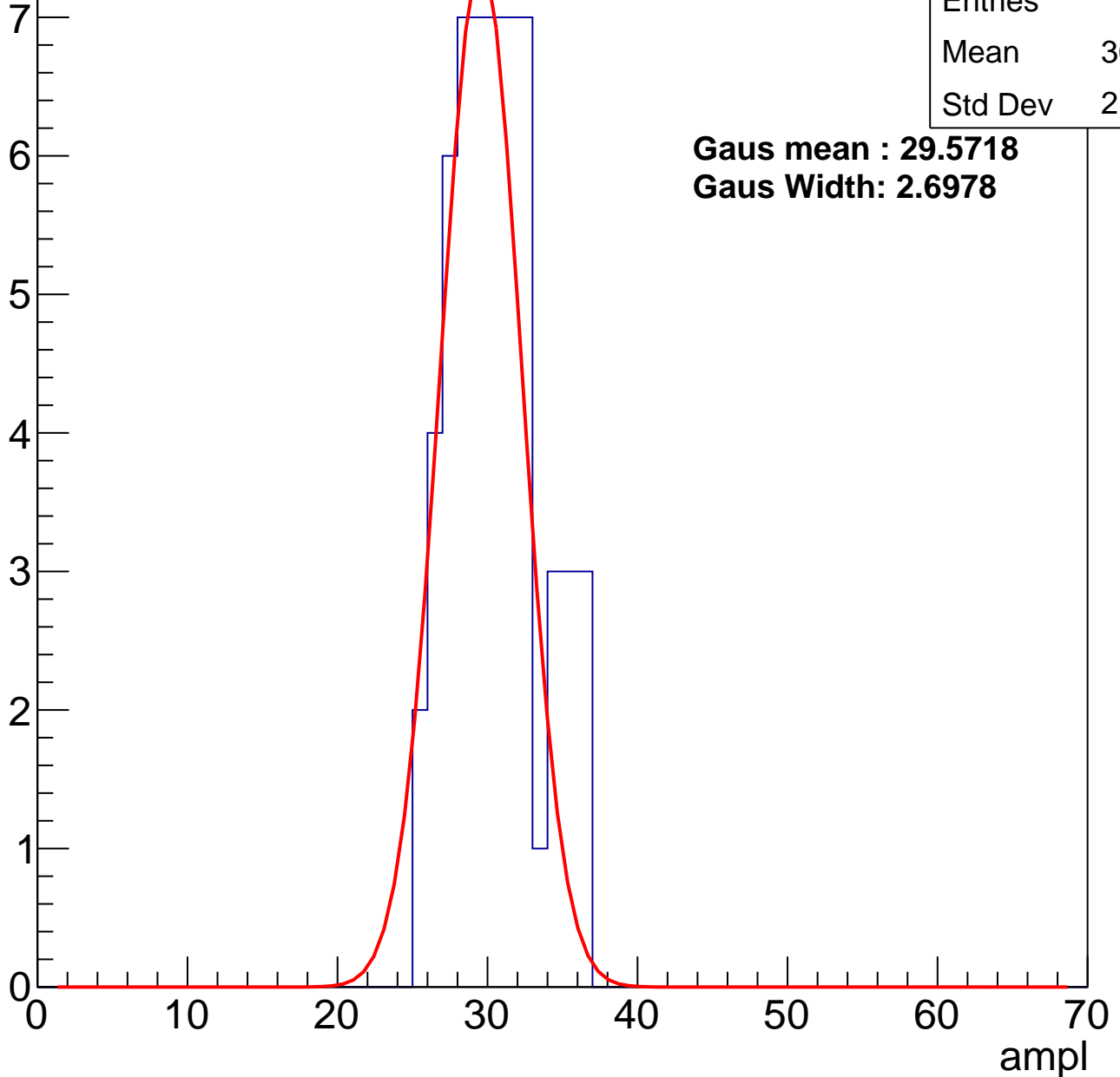
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	30.07
Std Dev	2.895

Gaus mean : 29.5718

Gaus Width: 2.6978



B1L100S, U10-ch101, adc1

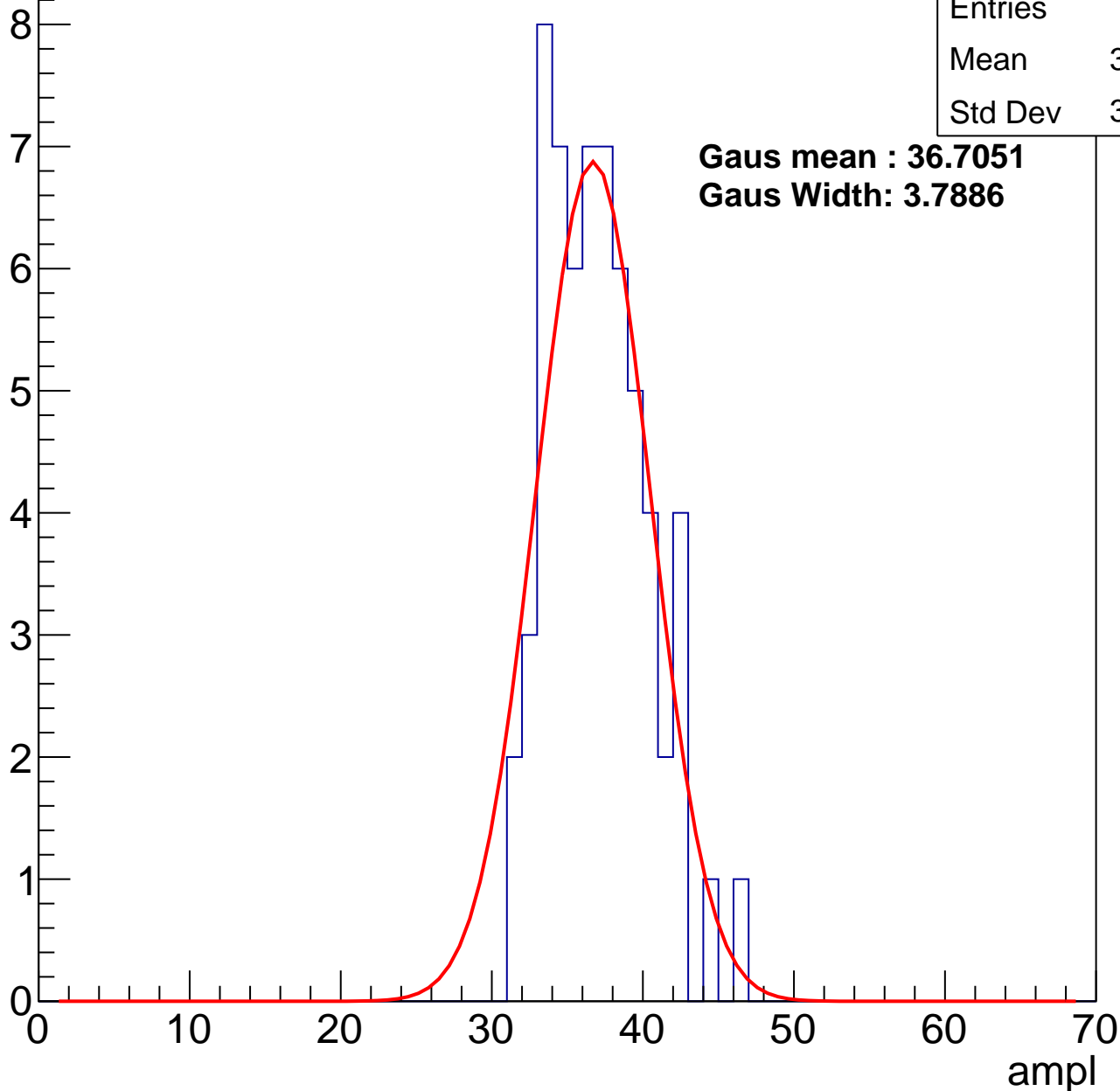
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	36.57
Std Dev	3.289

Gaus mean : 36.7051

Gaus Width: 3.7886



B1L100S, U10-ch101, adc2

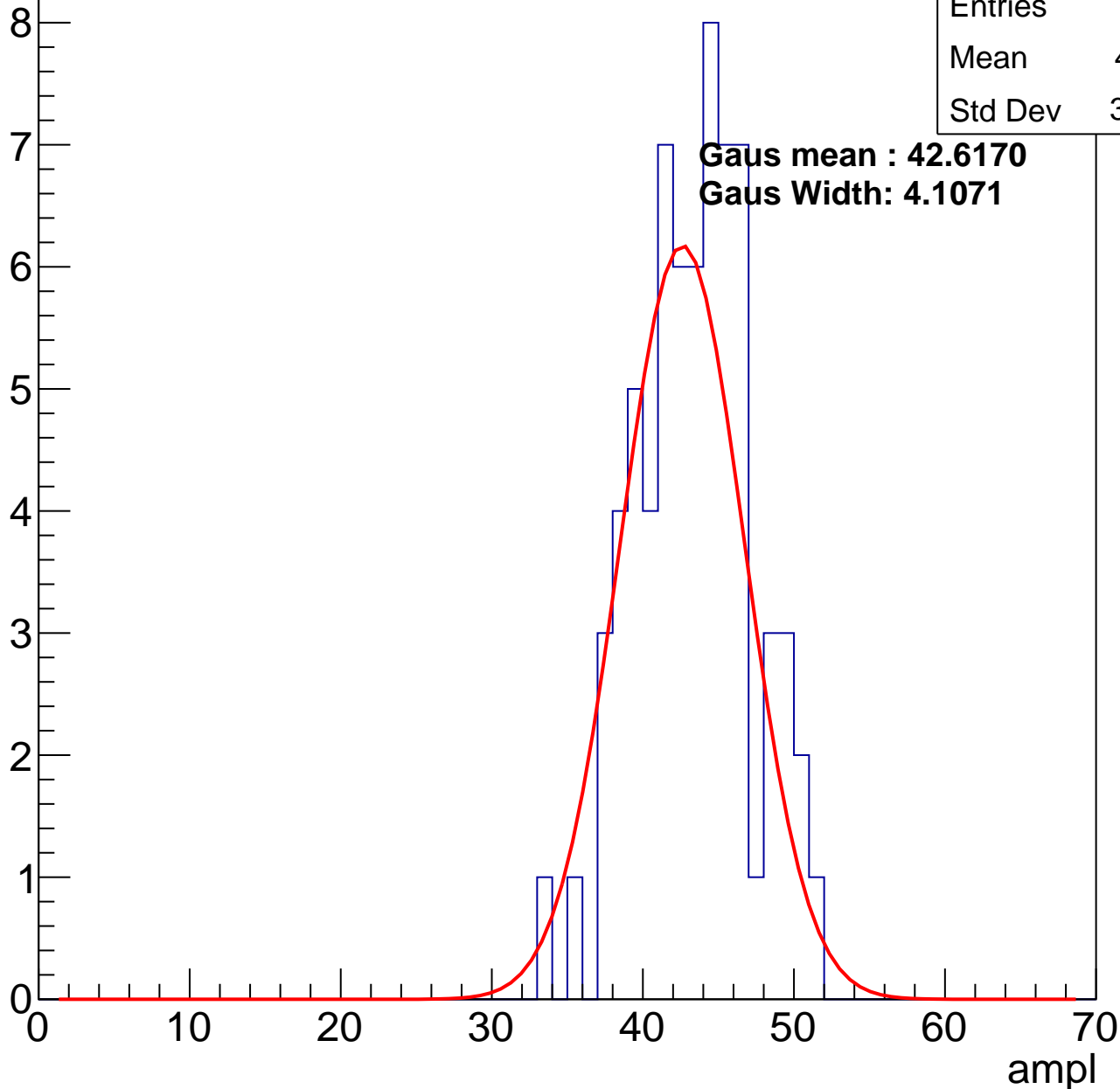
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.91
Std Dev	3.772

Gaus mean : 42.6170

Gaus Width: 4.1071

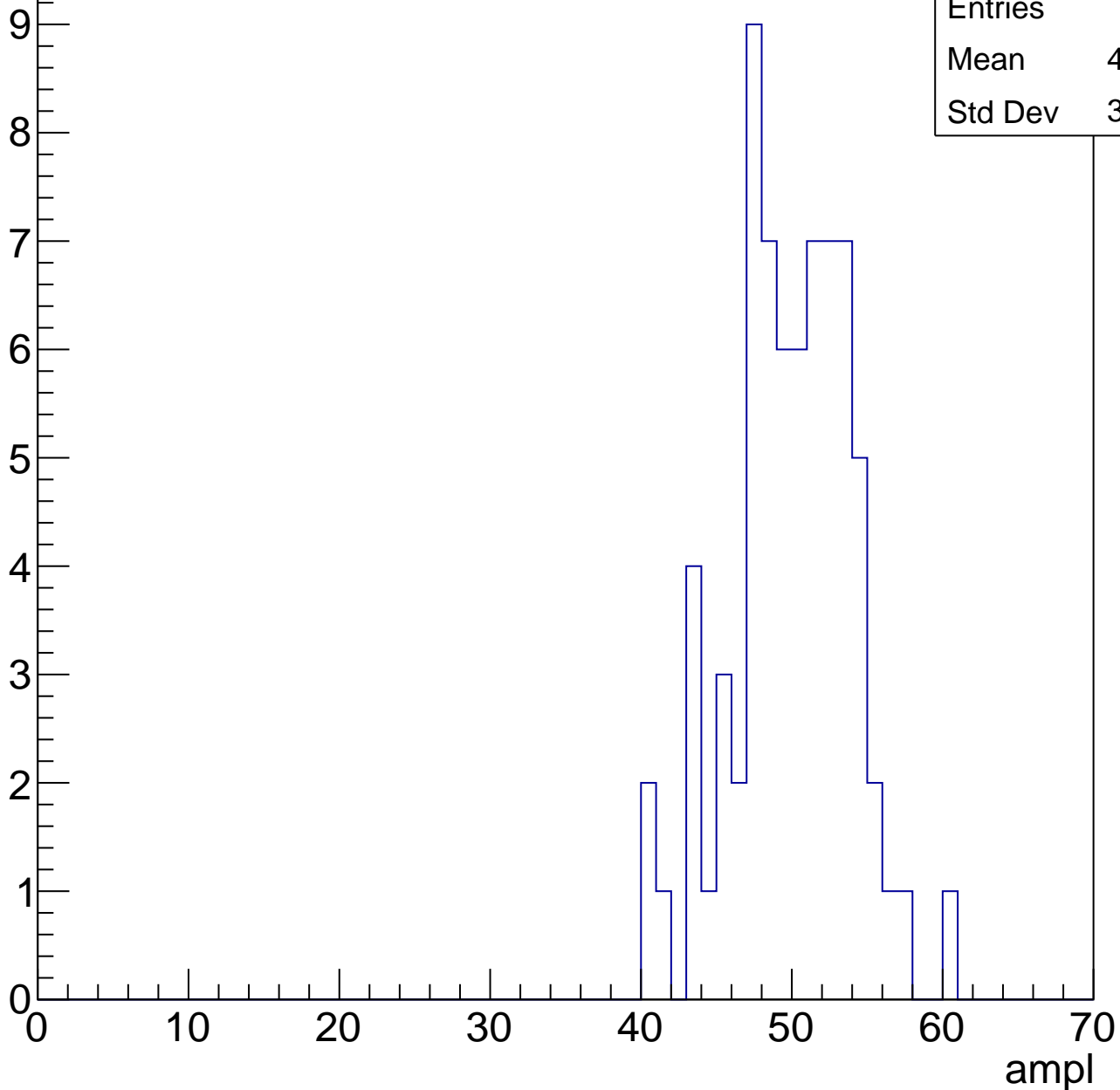


B1L100S, U10-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

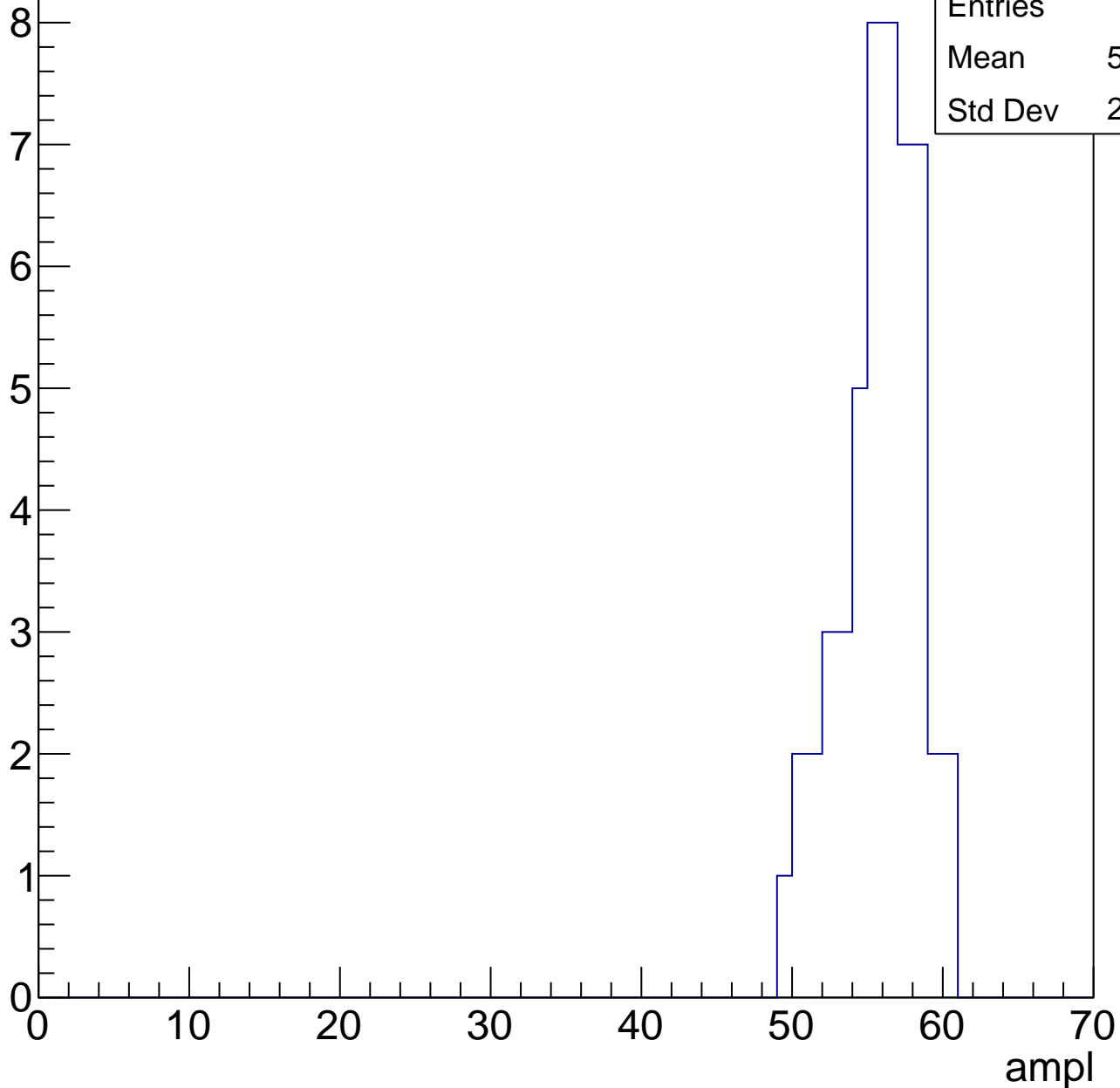
Entries	72
Mean	49.47
Std Dev	3.958



B1L100S, U10-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

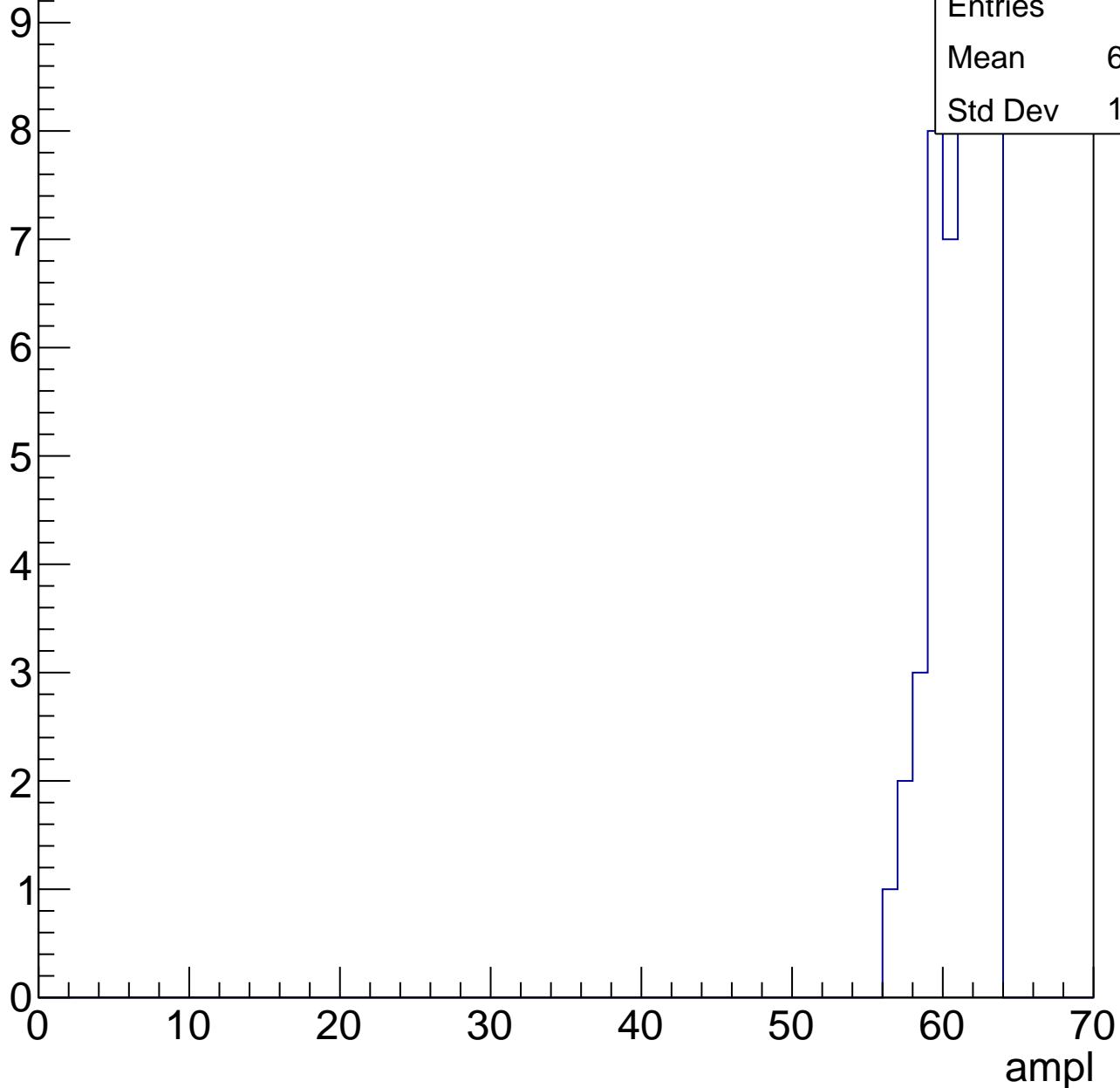


Entries	50
Mean	55.34
Std Dev	2.597

B1L100S, U10-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

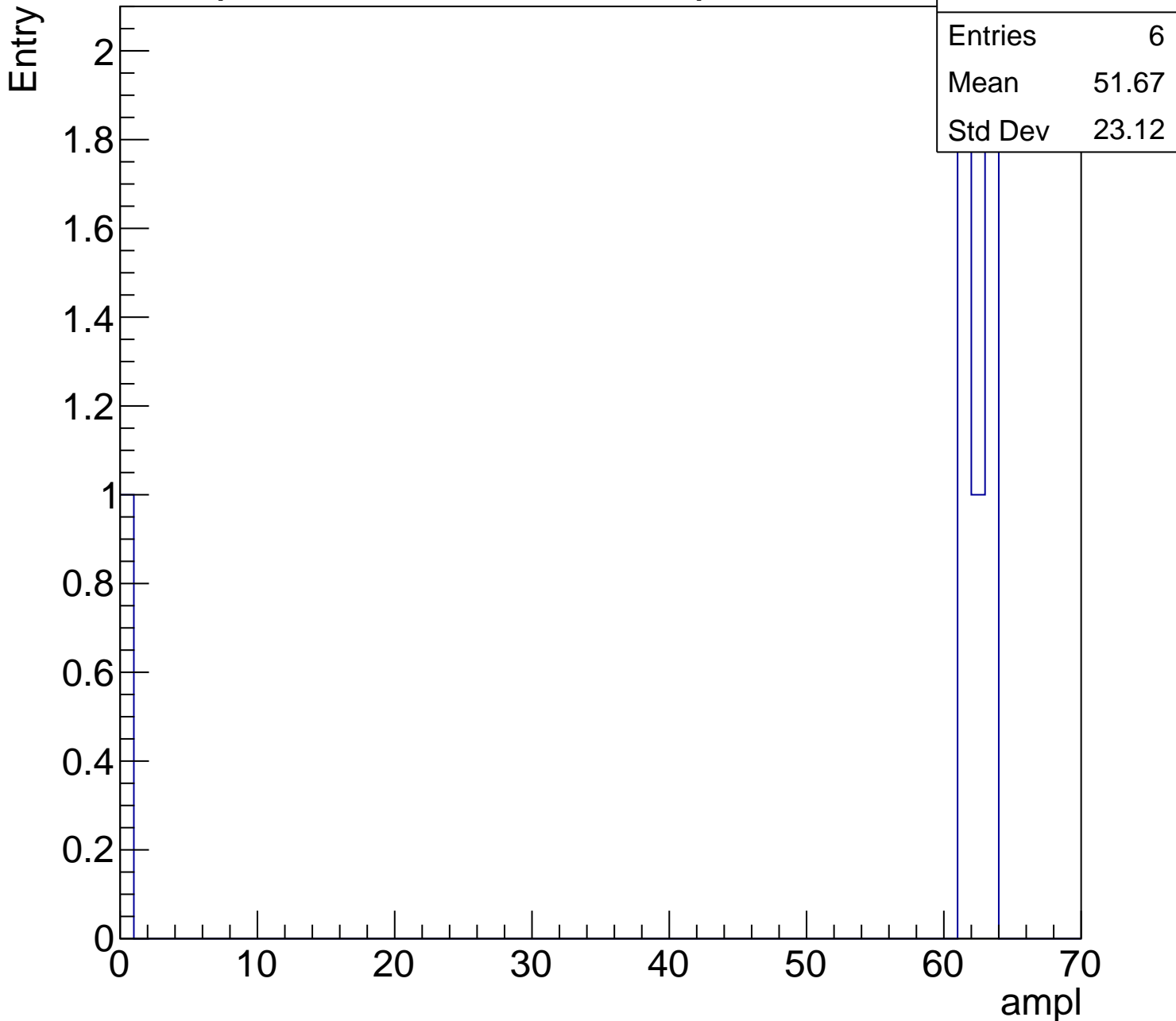
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.12

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch102, adc0

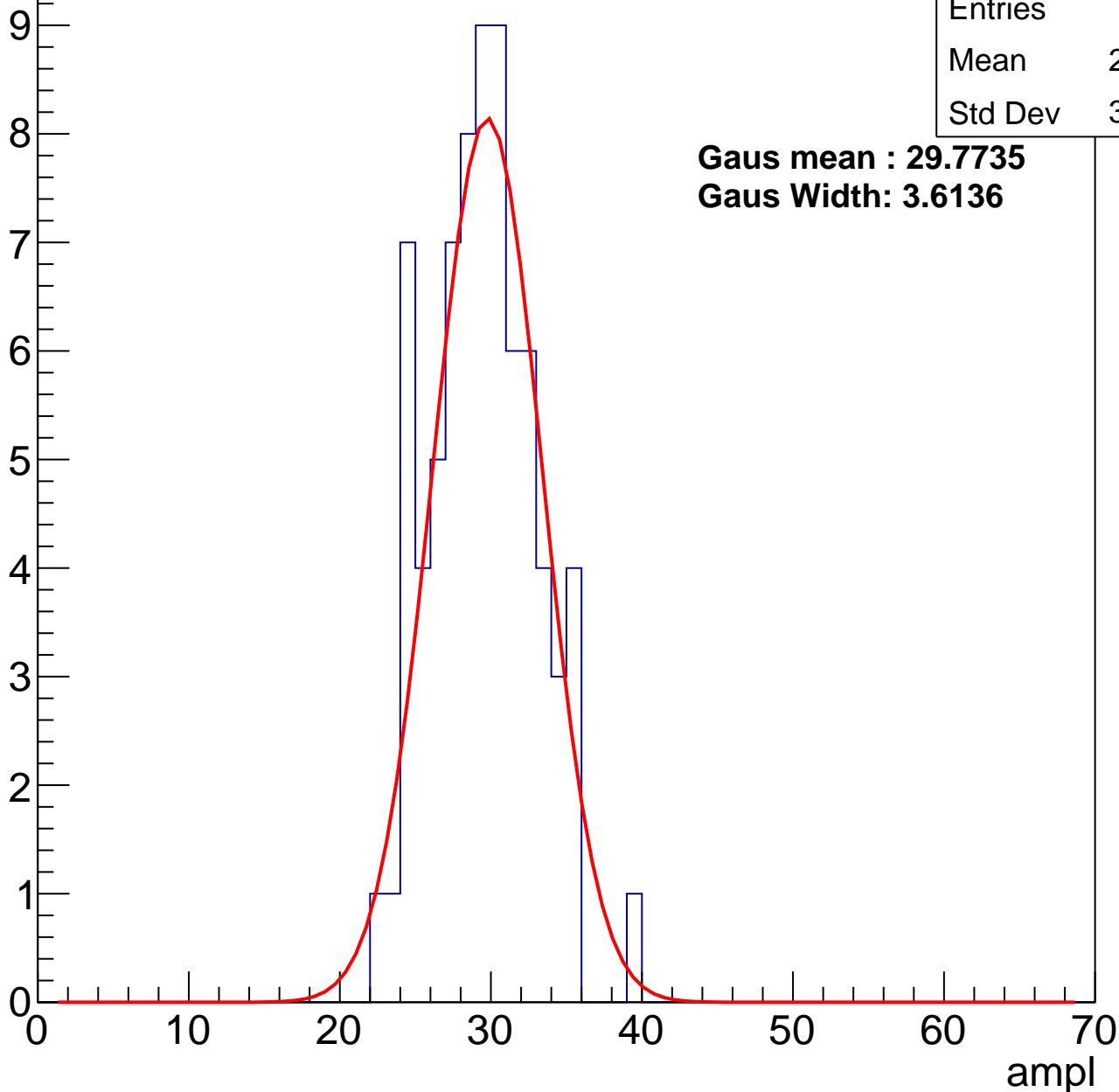
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.04
Std Dev	3.415

Gaus mean : 29.7735

Gaus Width: 3.6136



B1L100S, U10-ch102, adc1

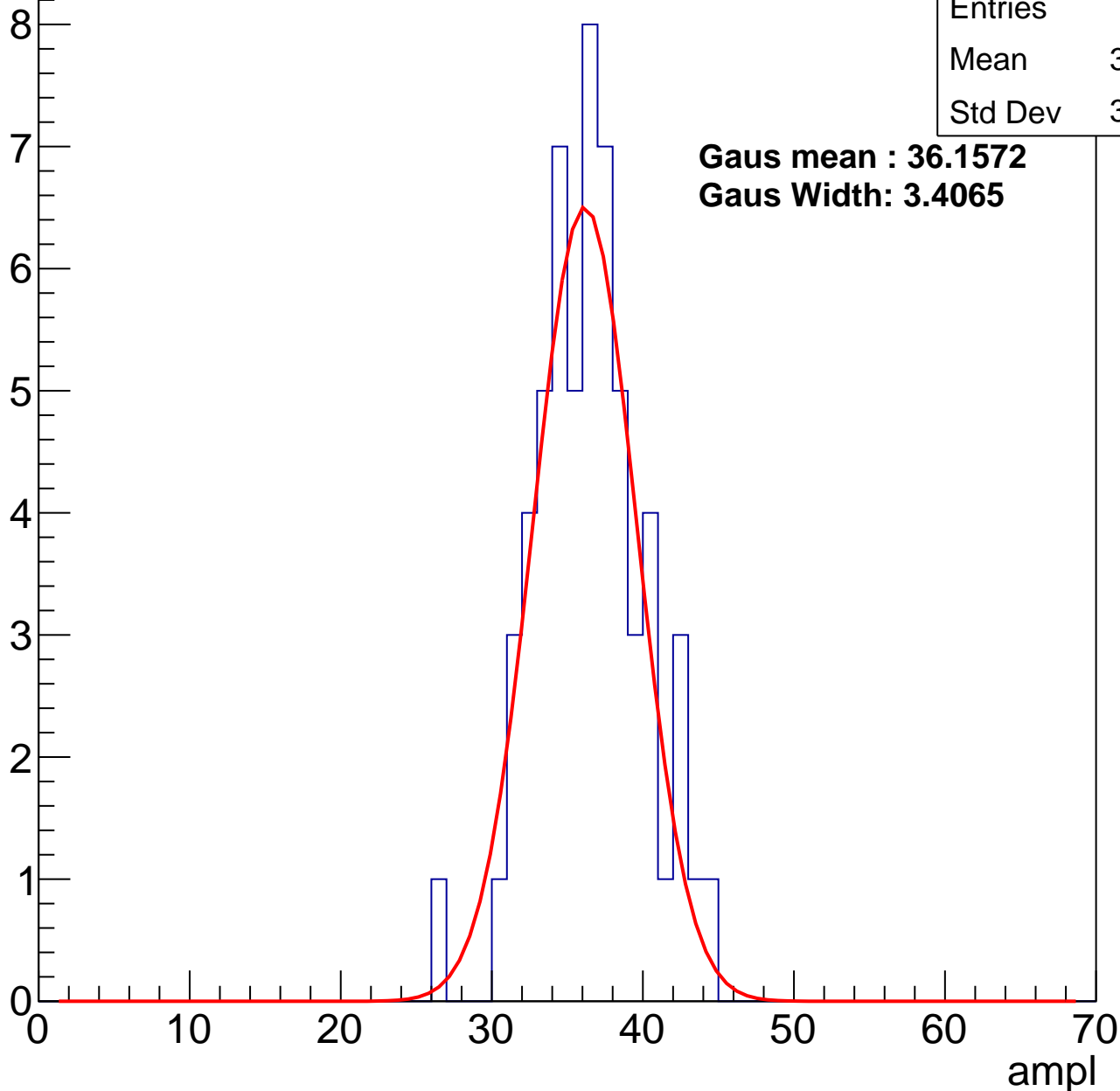
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.98
Std Dev	3.486

Gaus mean : 36.1572

Gaus Width: 3.4065



B1L100S, U10-ch102, adc2

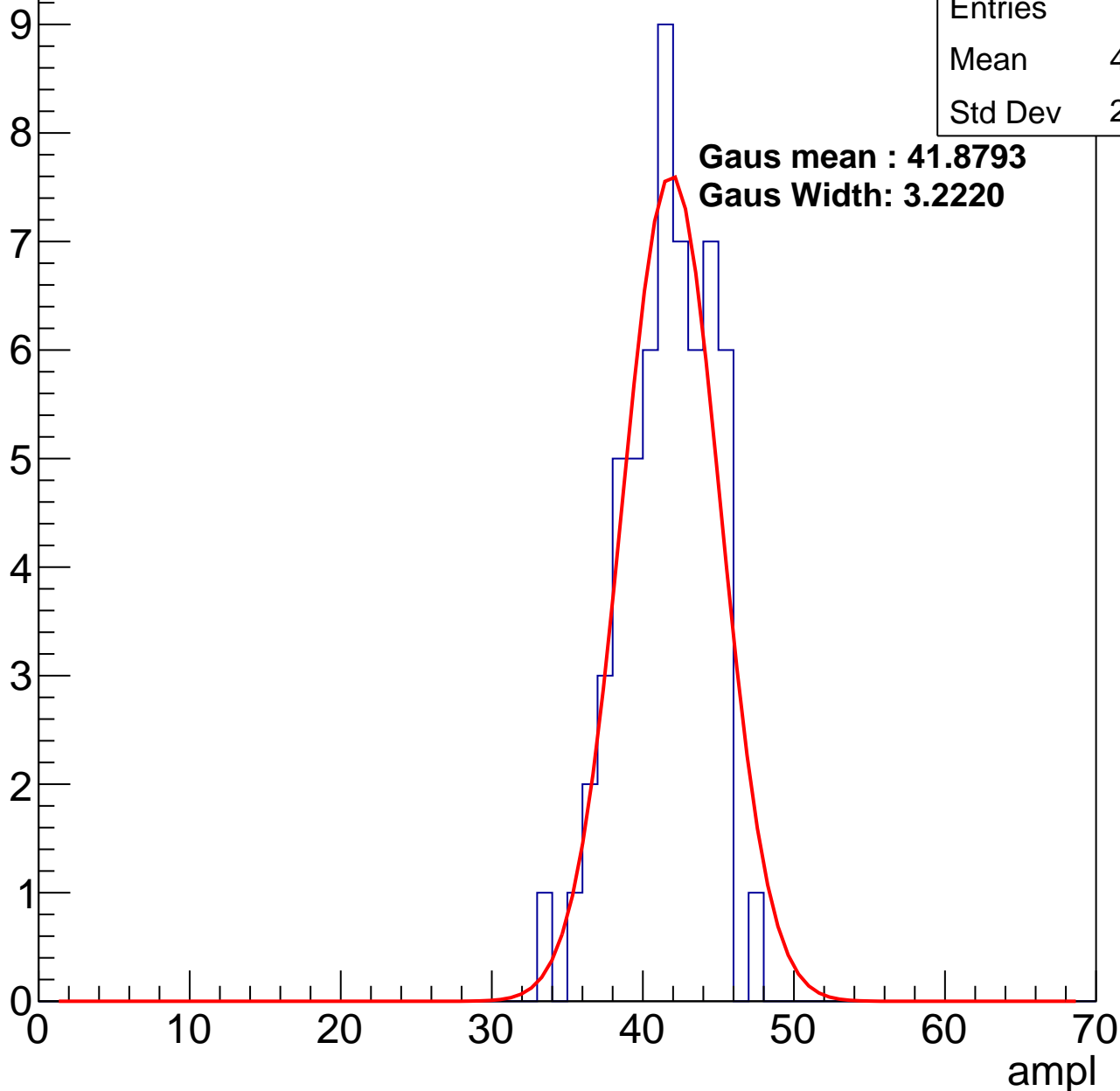
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	41.05
Std Dev	2.896

Gaus mean : 41.8793

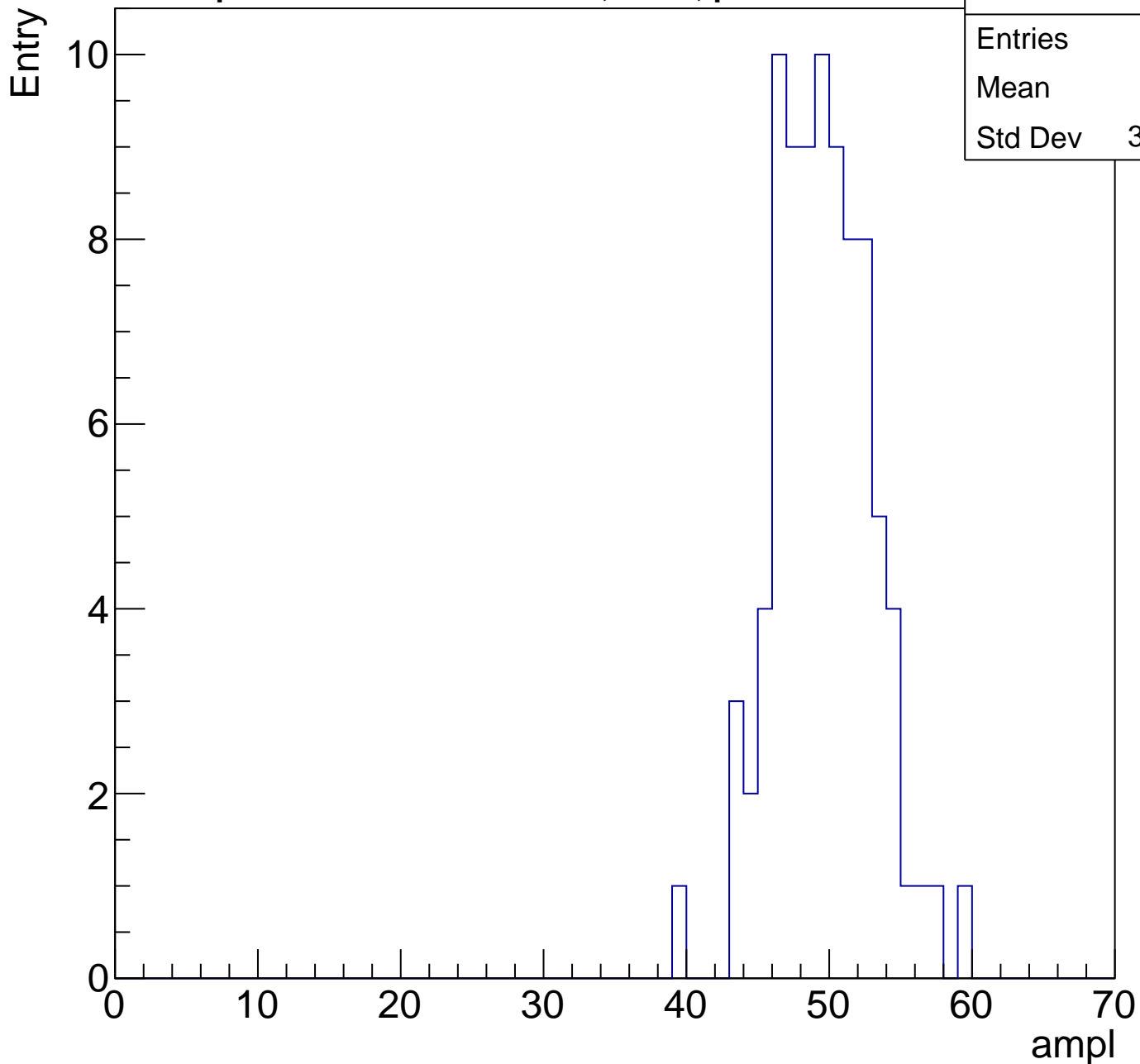
Gaus Width: 3.2220



B1L100S, U10-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	49.1
Std Dev	3.407

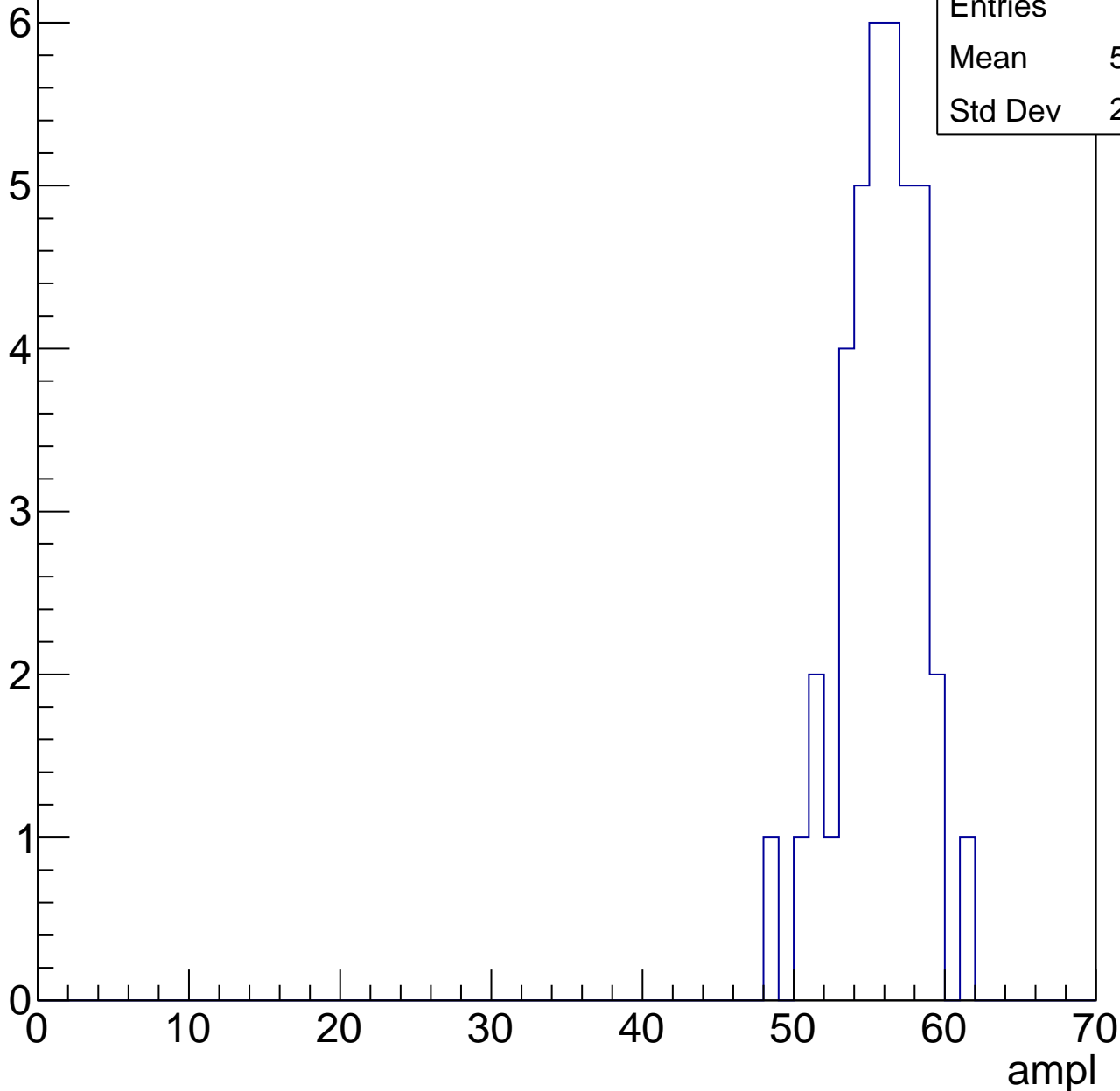


B1L100S, U10-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	55.23
Std Dev	2.645

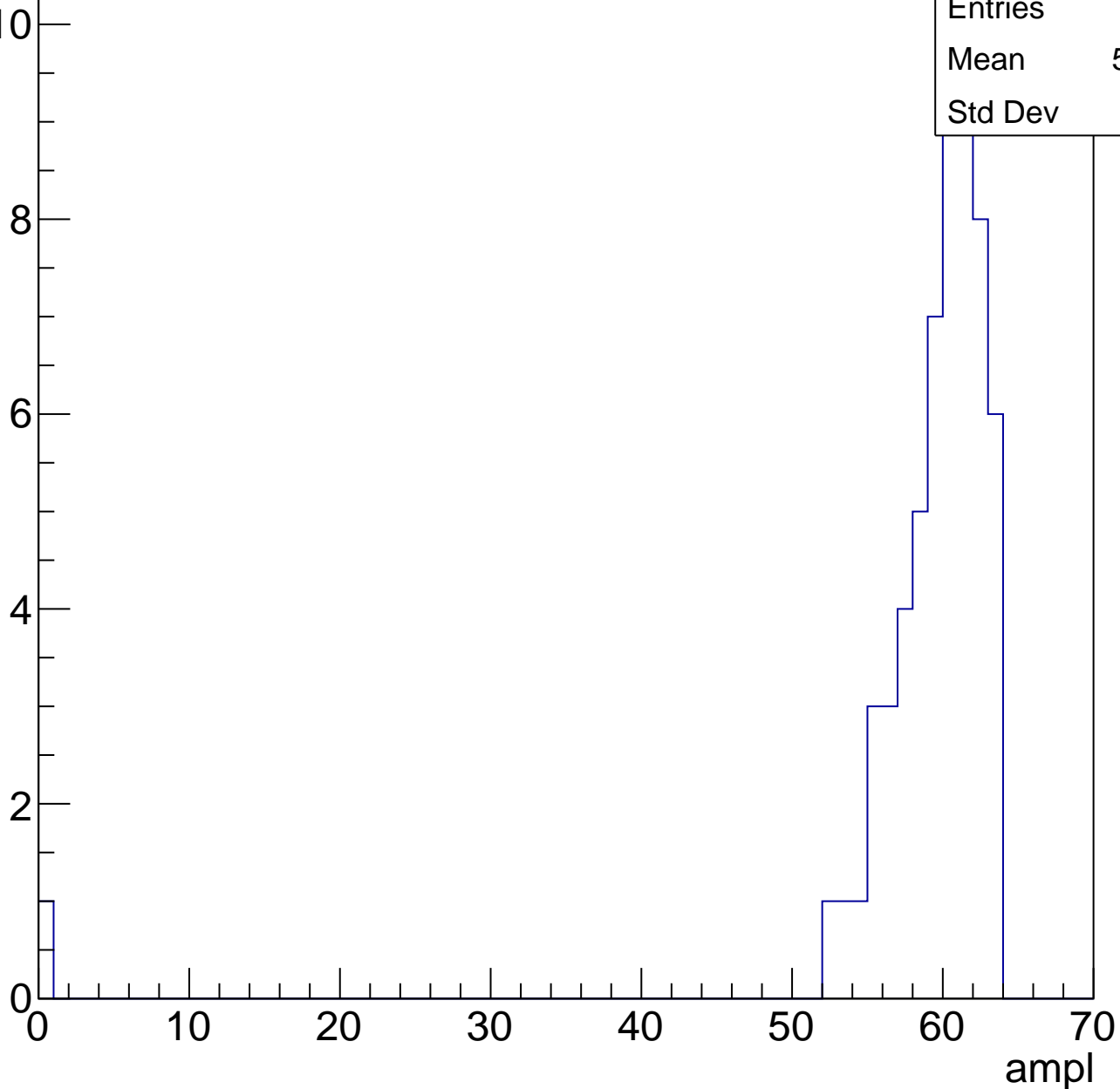


B1L100S, U10-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

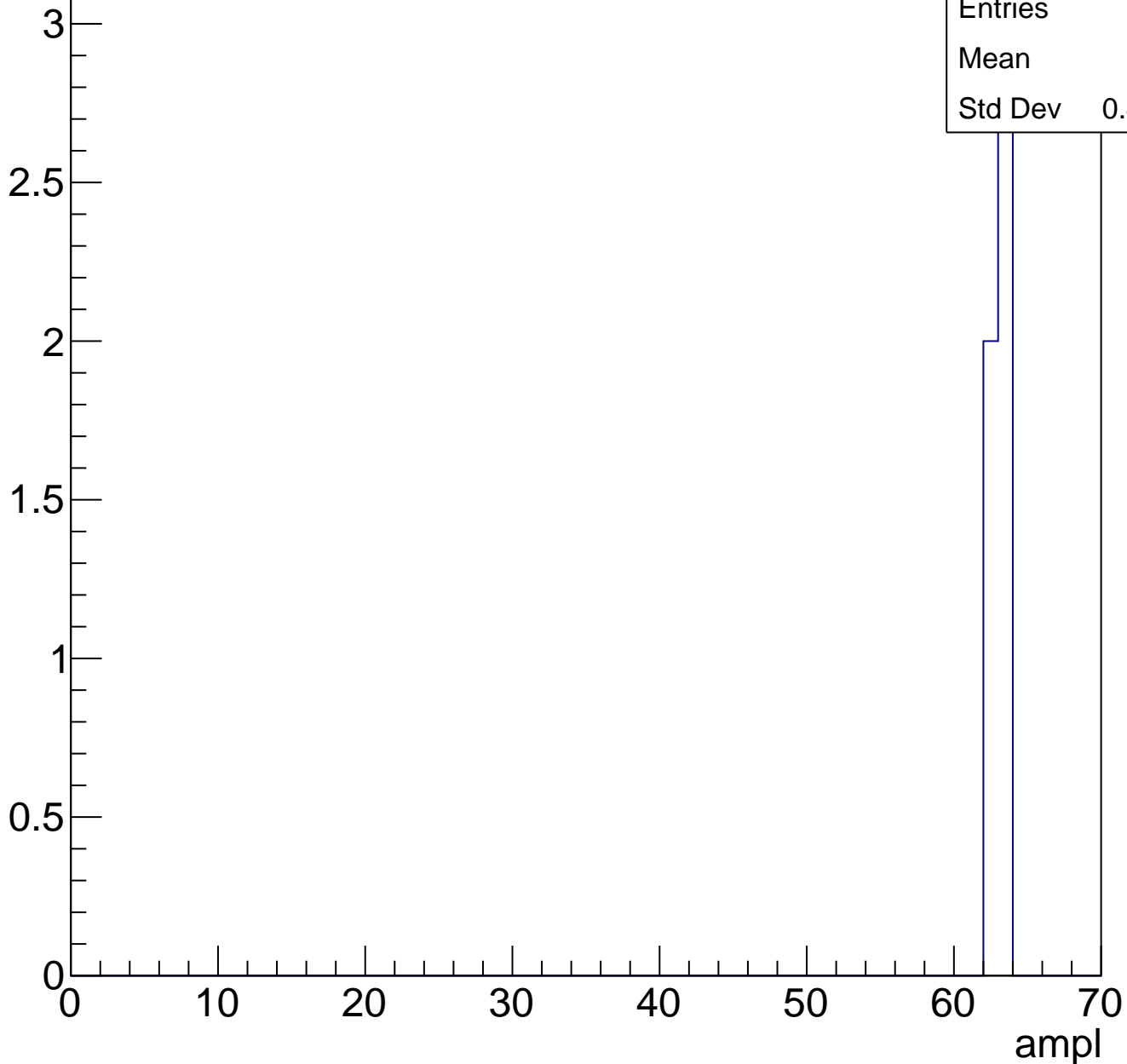
Entries	59
Mean	58.41
Std Dev	8.11



B1L100S, U10-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

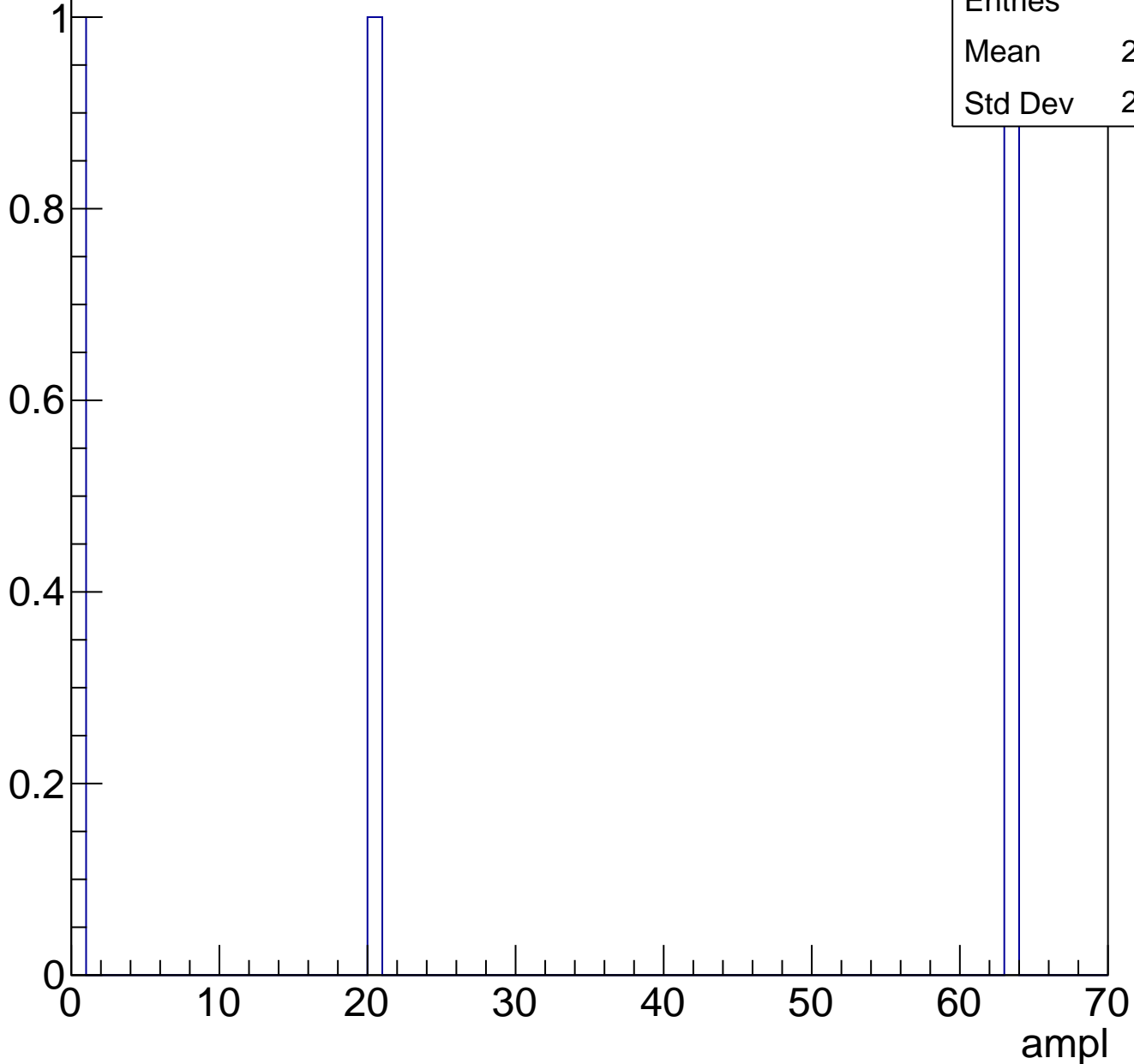
Entry



B1L100S, U10-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch103, adc0

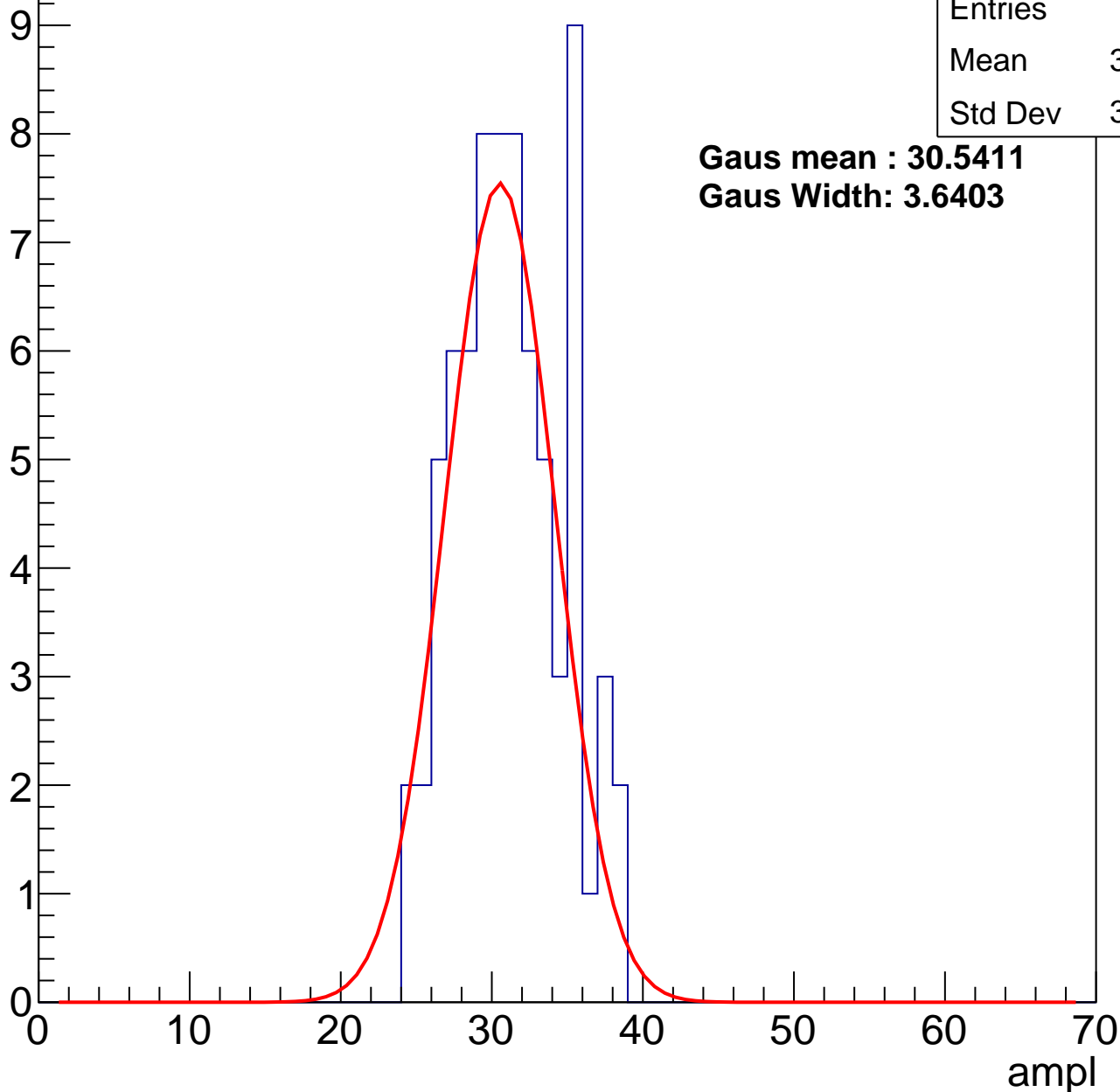
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.74
Std Dev	3.503

Gaus mean : 30.5411

Gaus Width: 3.6403



B1L100S, U10-ch103, adc1

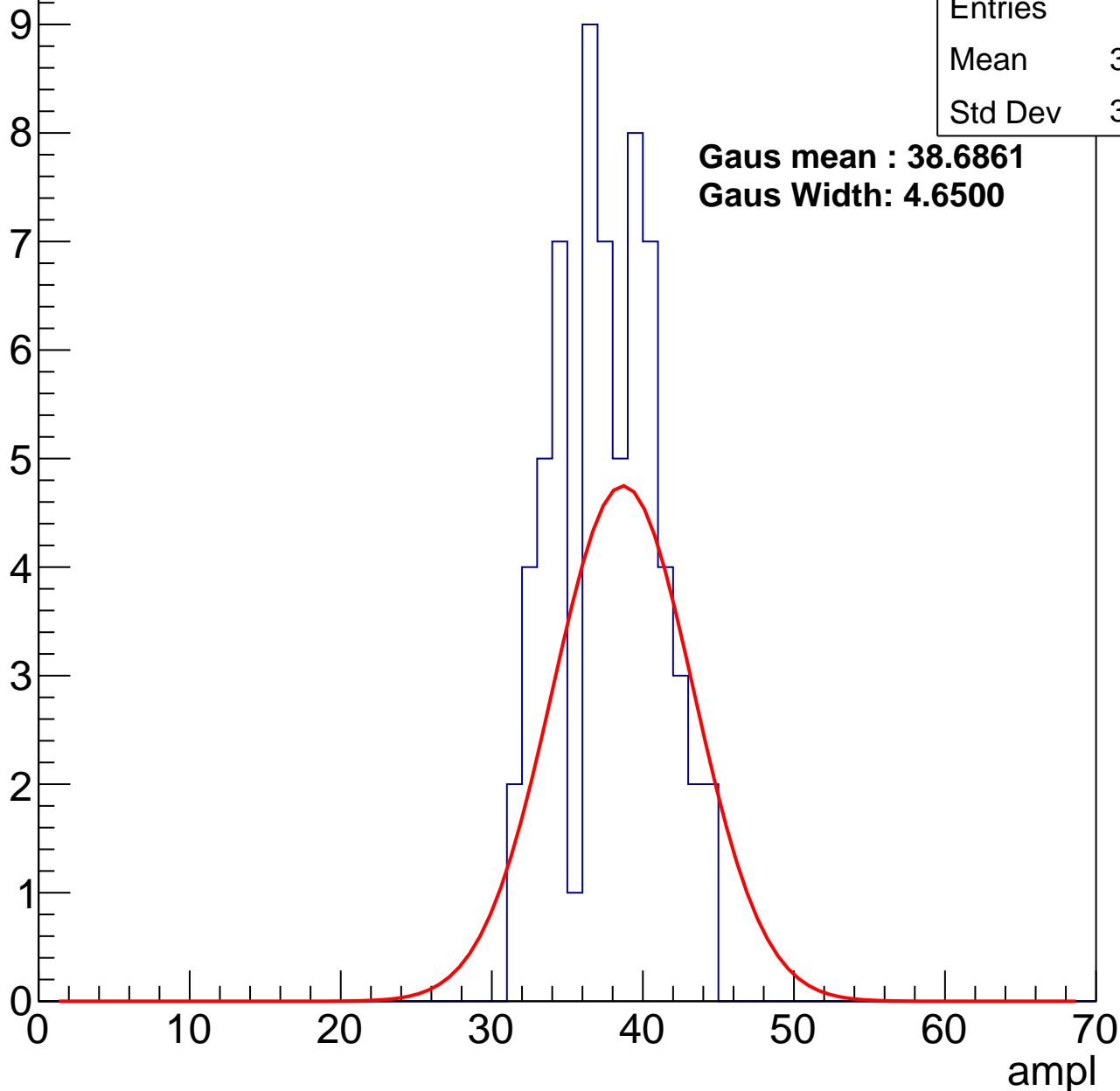
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.23
Std Dev	3.334

Gaus mean : 38.6861

Gaus Width: 4.6500



B1L100S, U10-ch103, adc2

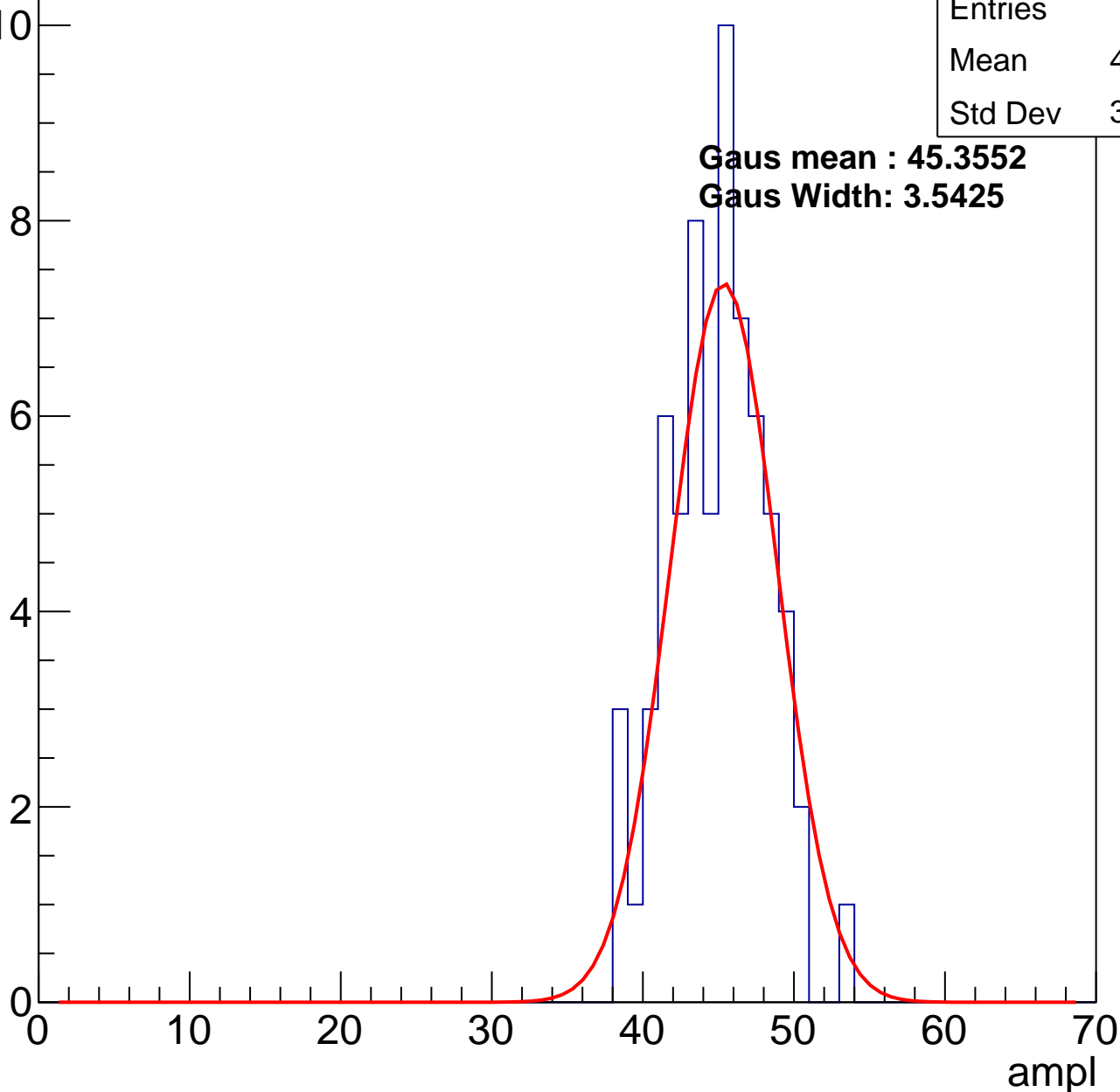
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.48
Std Dev	3.192

Gaus mean : 45.3552

Gaus Width: 3.5425

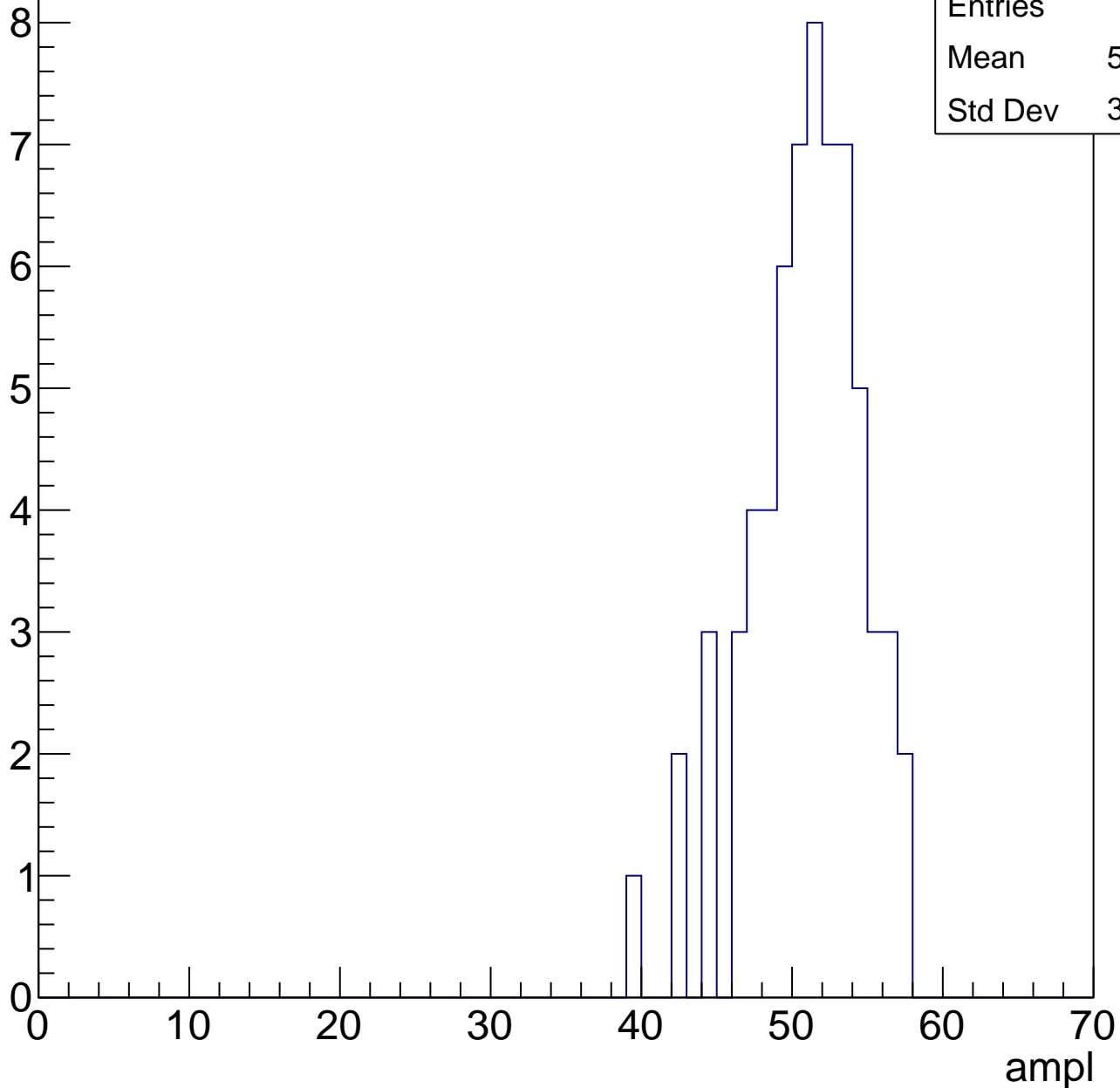


B1L100S, U10-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.42
Std Dev	3.745

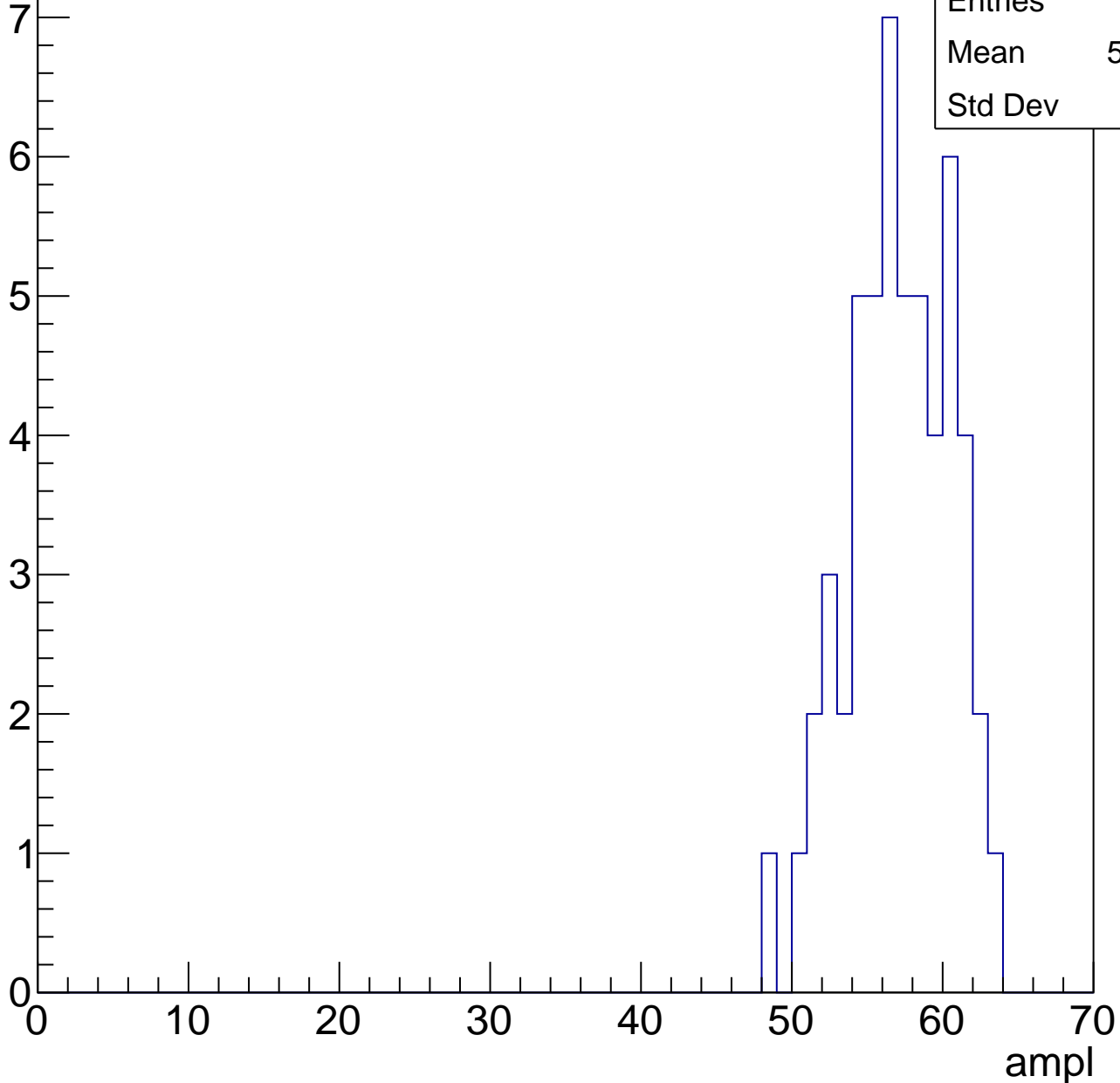


B1L100S, U10-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	56.62
Std Dev	3.36

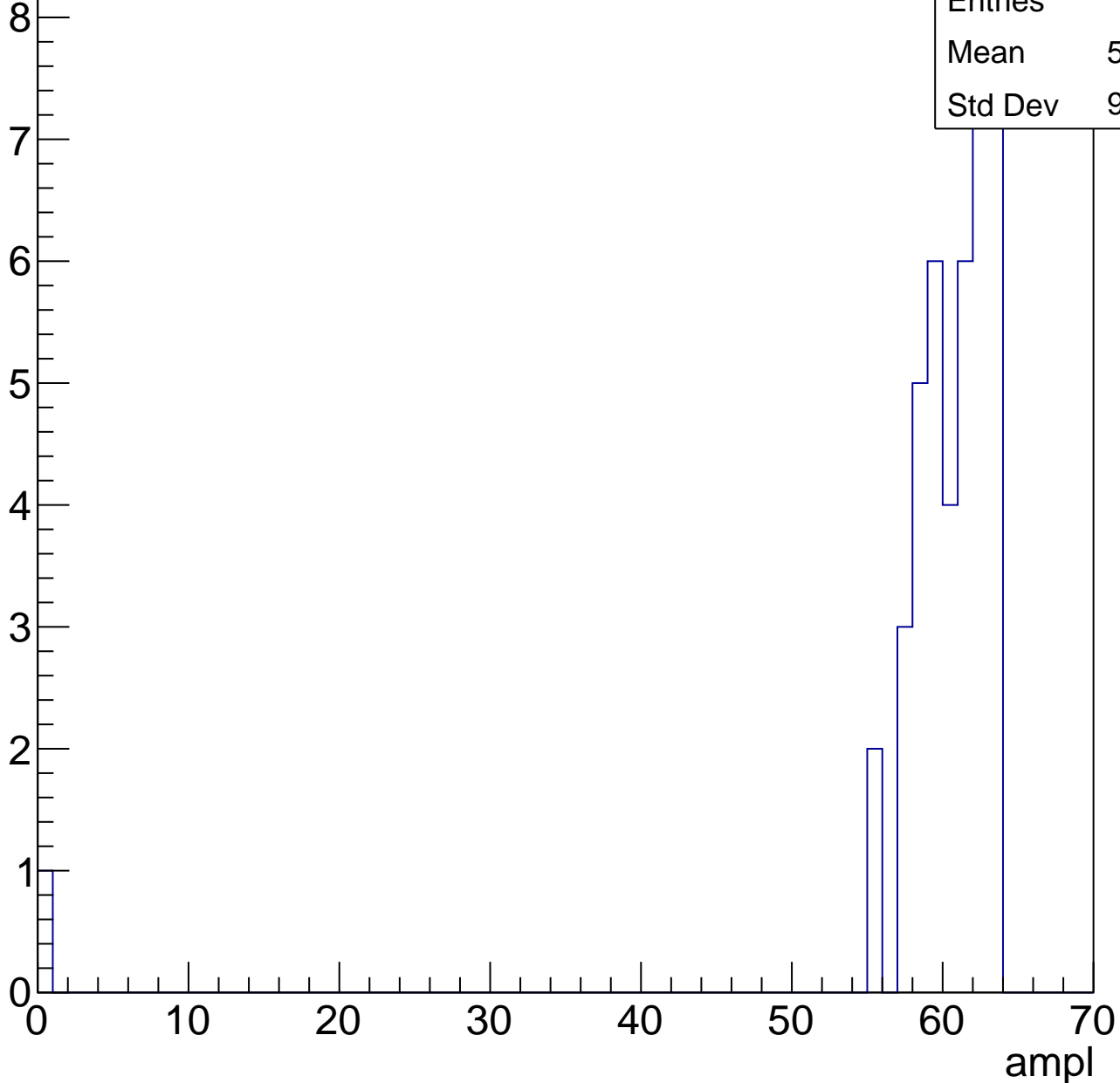


B1L100S, U10-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	58.86
Std Dev	9.348



B1L100S, U10-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U10-ch104, adc0

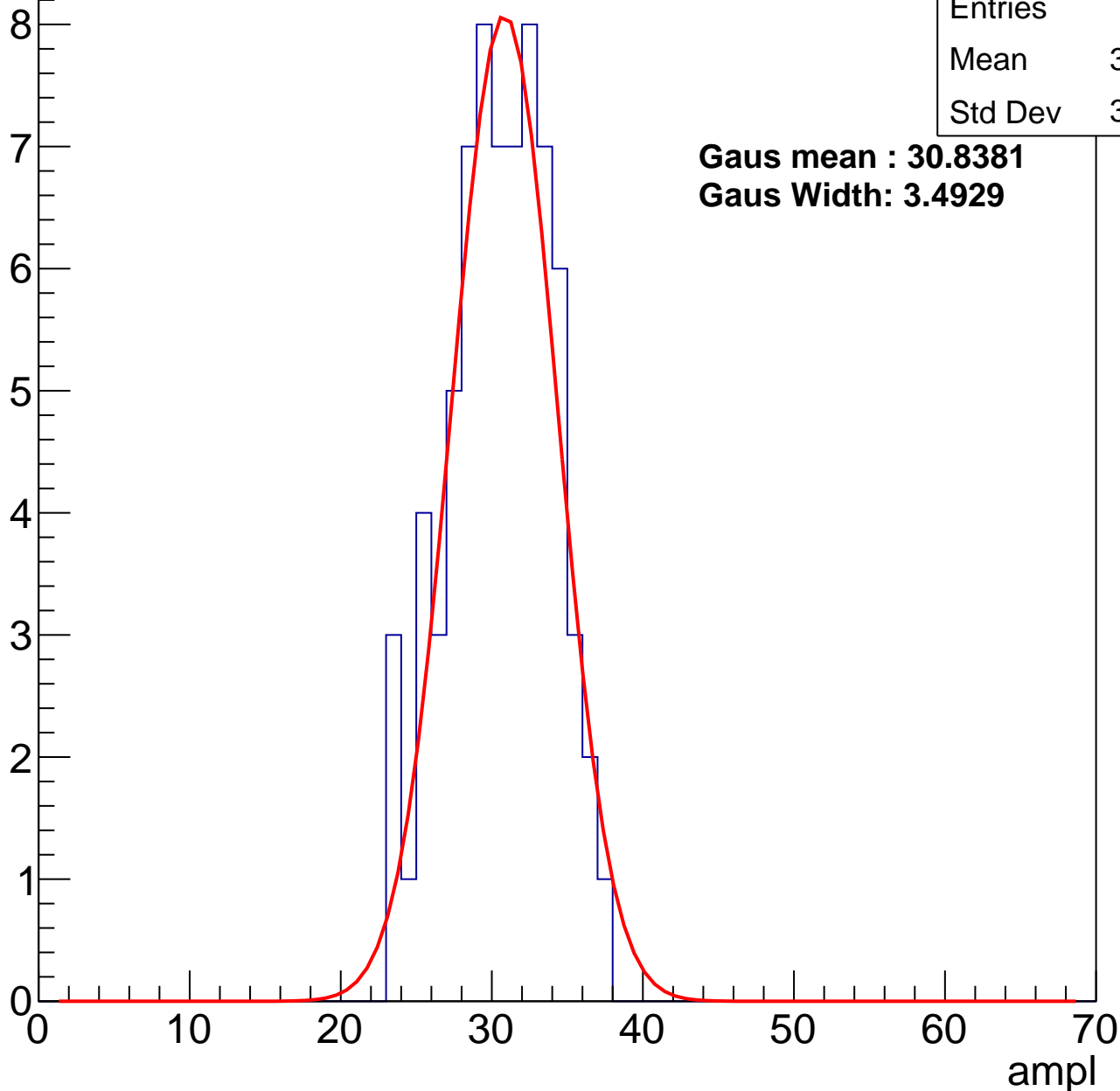
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.08
Std Dev	3.345

Gaus mean : 30.8381

Gaus Width: 3.4929



B1L100S, U10-ch104, adc1

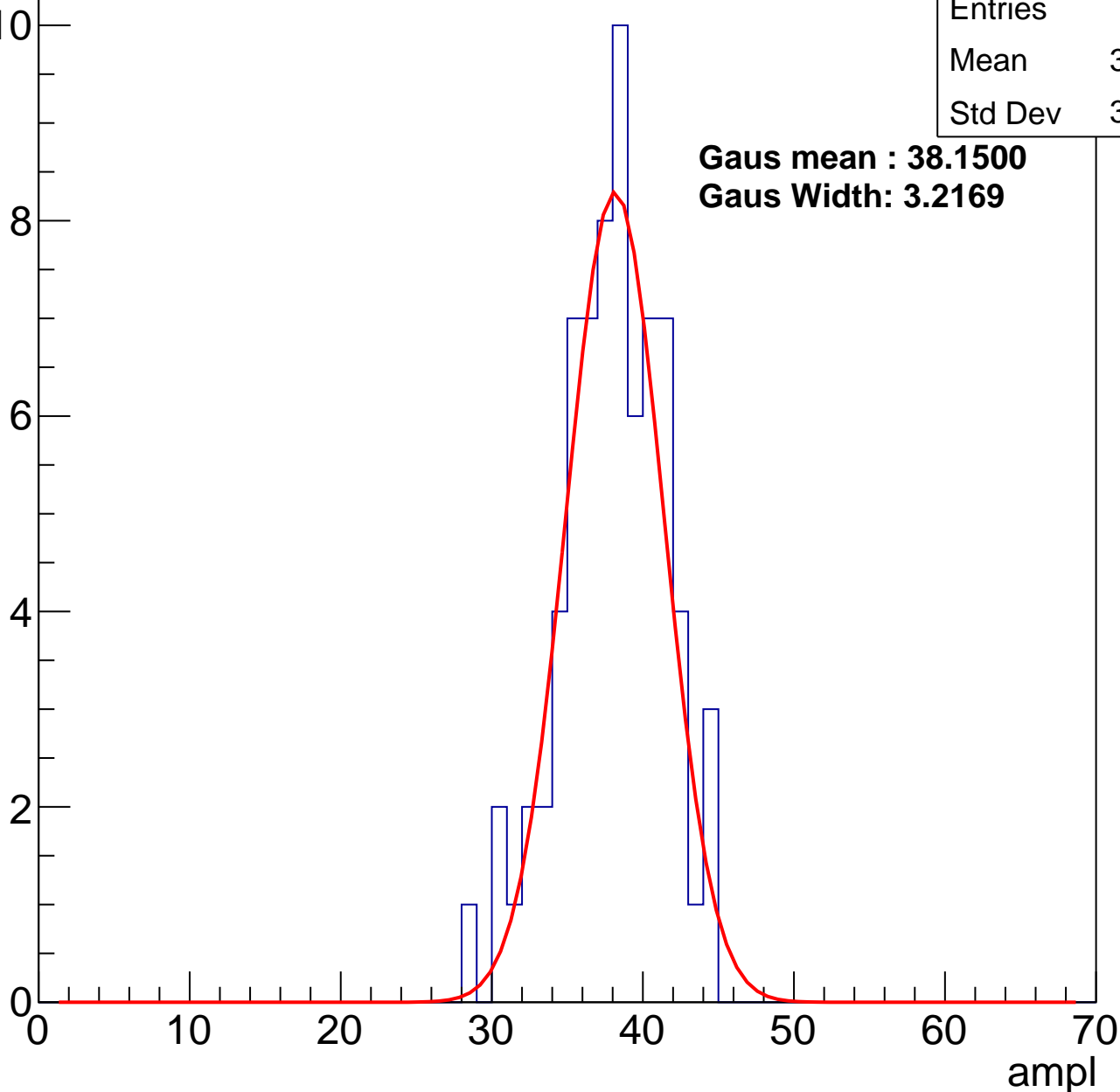
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.53
Std Dev	3.416

Gaus mean : 38.1500

Gaus Width: 3.2169



B1L100S, U10-ch104, adc2

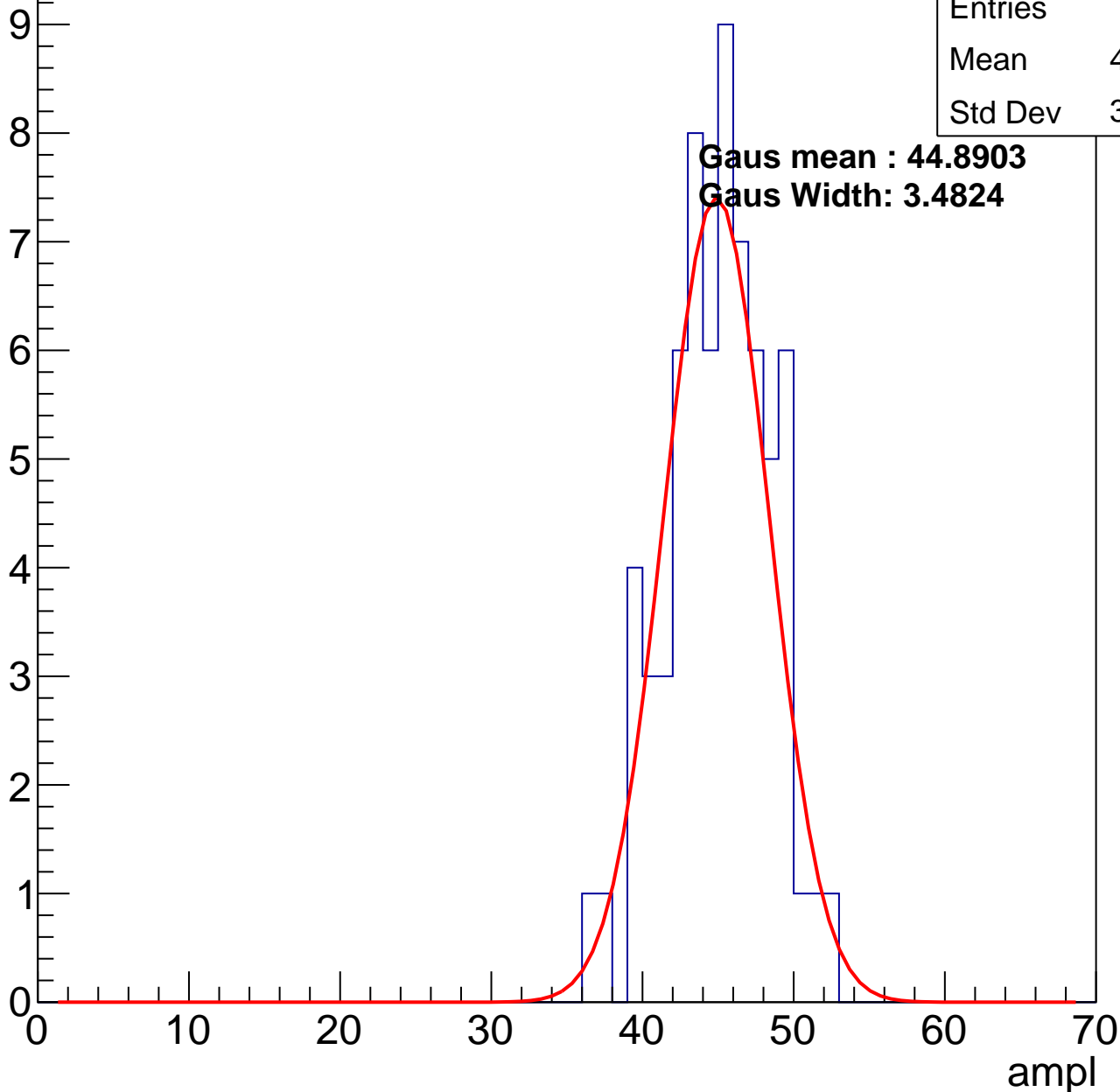
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.53
Std Dev	3.367

Gaus mean : 44.8903

Gaus Width: 3.4824

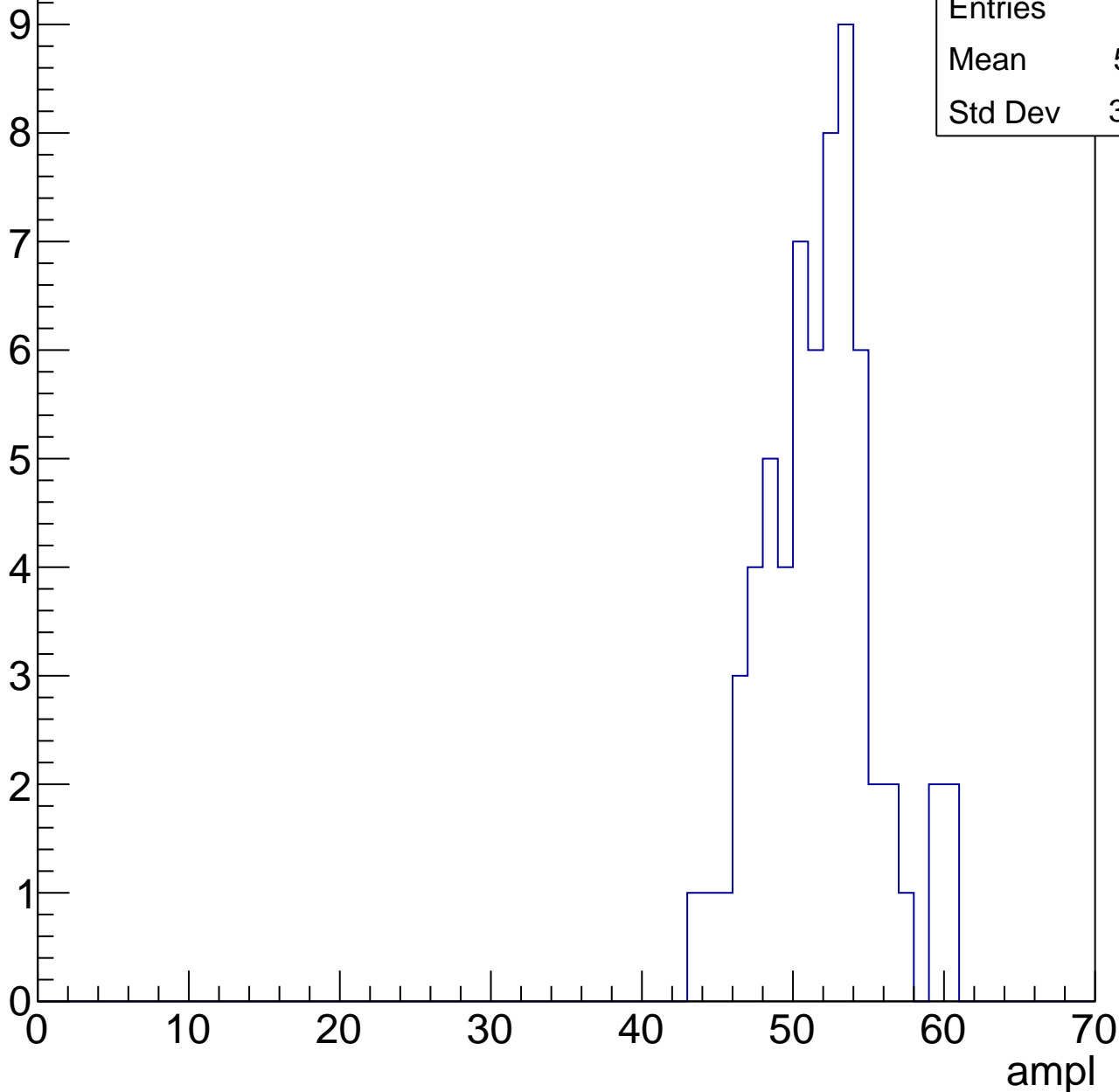


B1L100S, U10-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	51.31
Std Dev	3.657

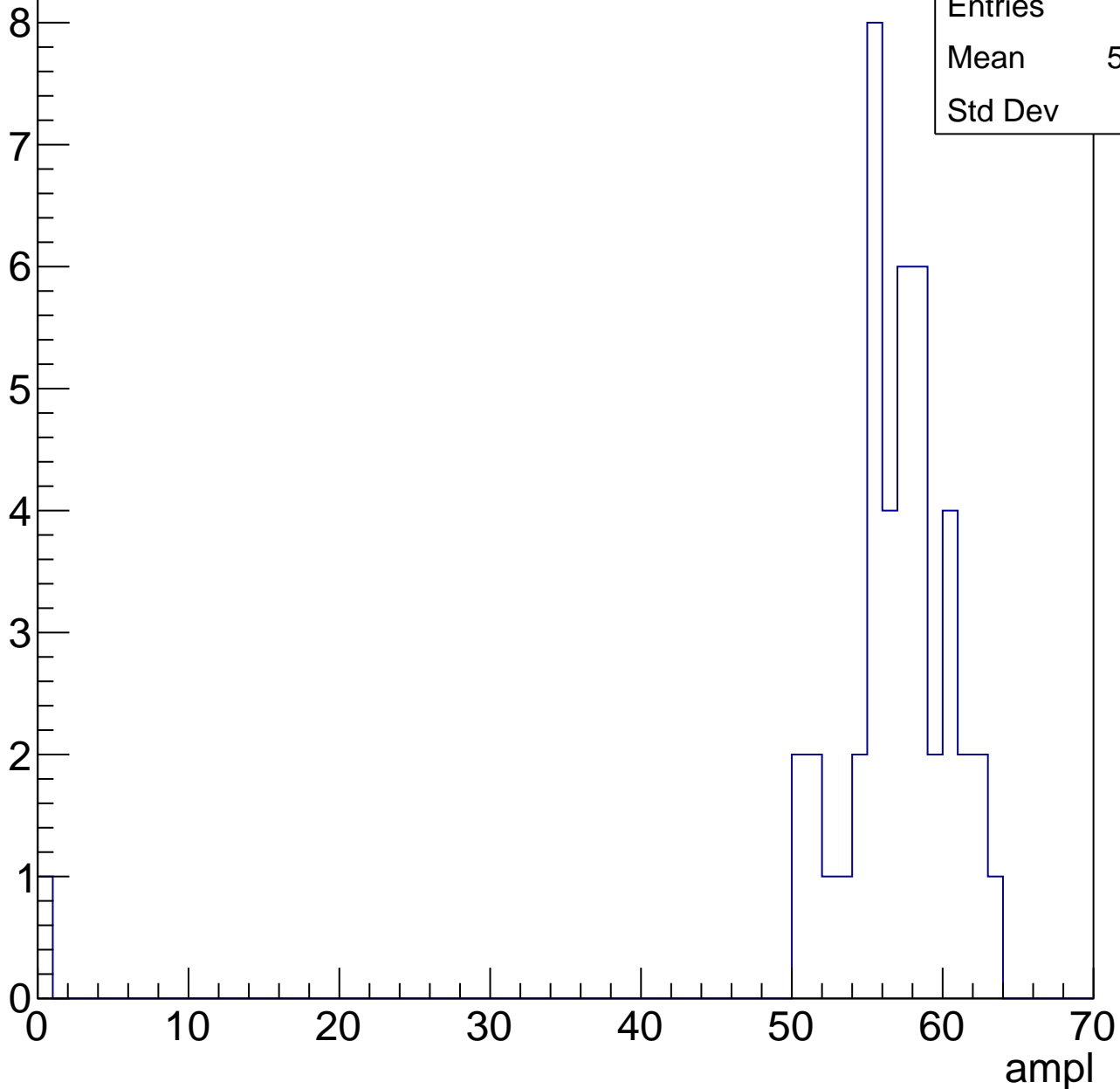


B1L100S, U10-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	55.36
Std Dev	9

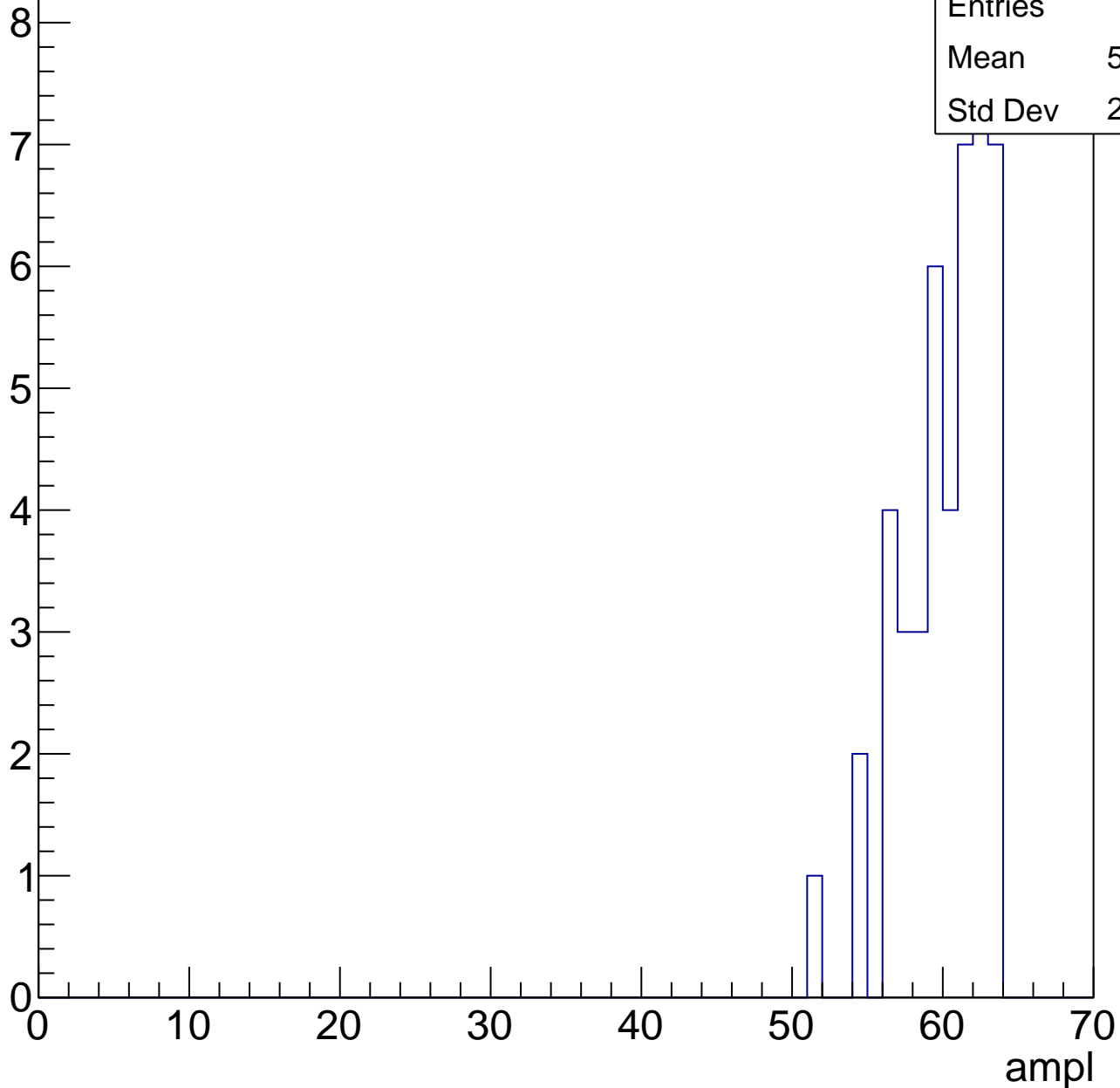


B1L100S, U10-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	59.69
Std Dev	2.827



B1L100S, U10-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

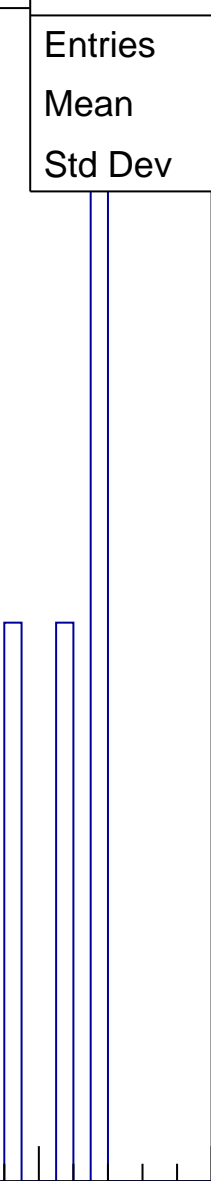
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	2.046

0 10 20 30 40 50 60 70

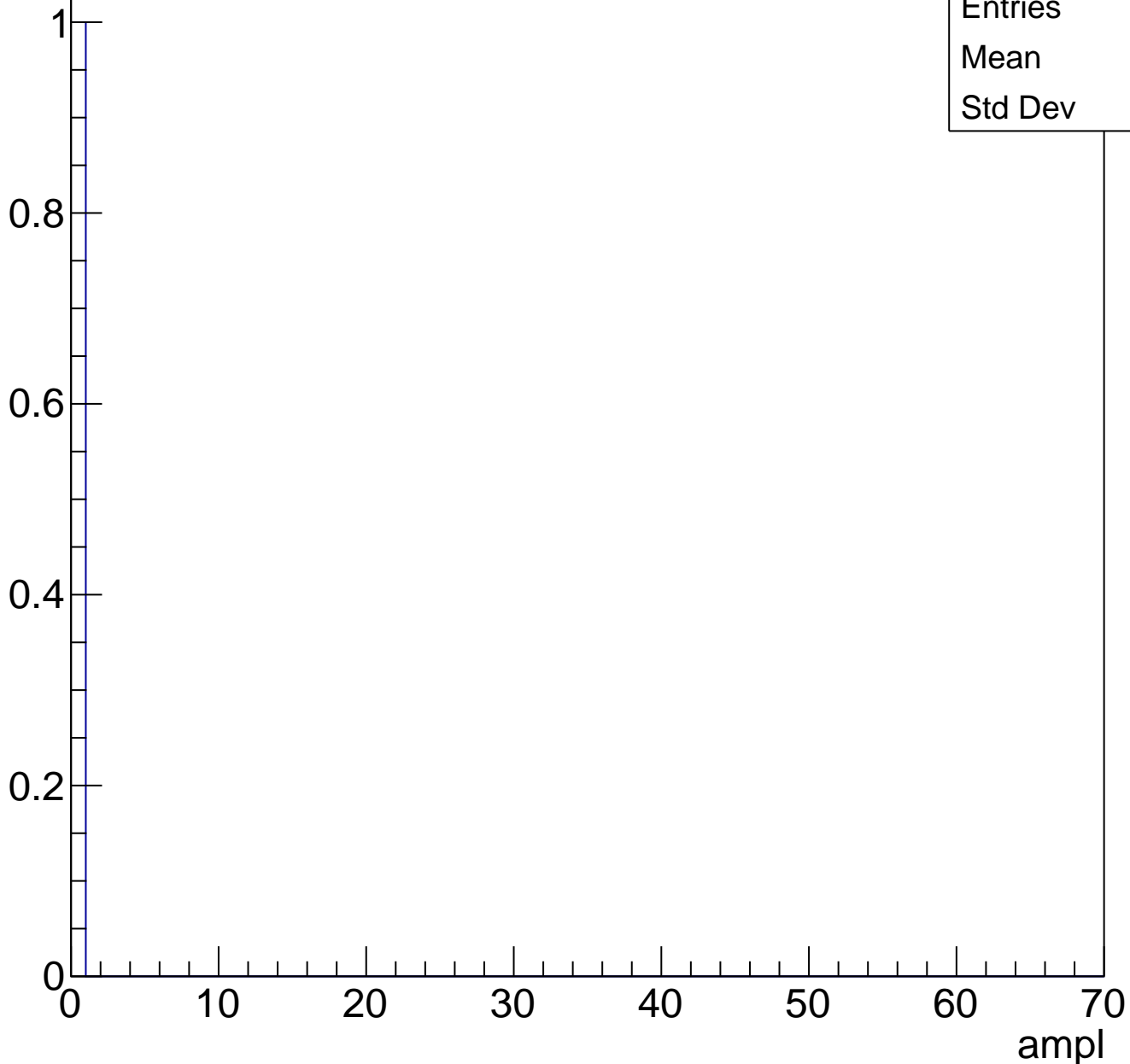
ampl



B1L100S, U10-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch105, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	30.84
Std Dev	3.602

Gaus mean : 31.2782

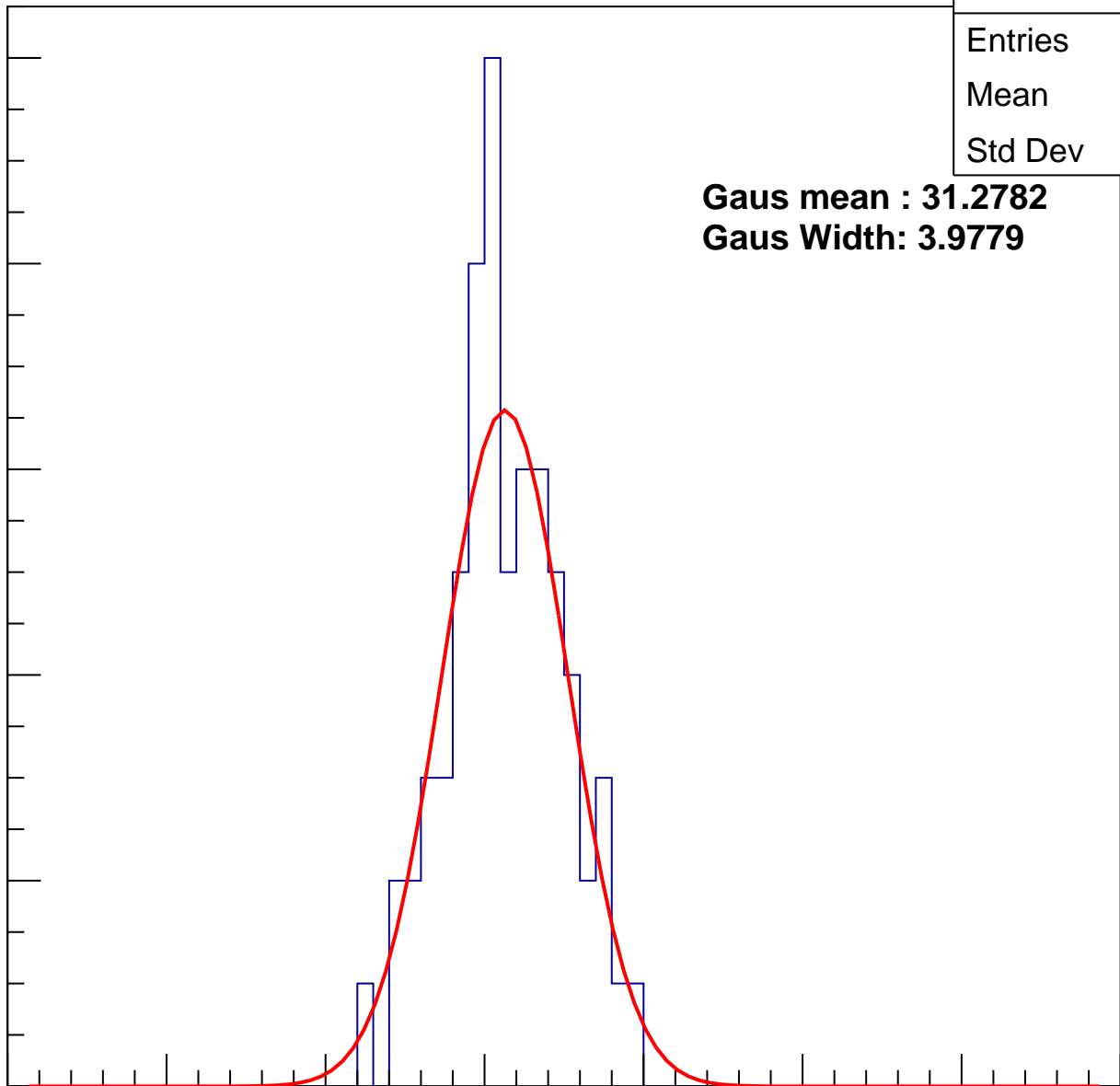
Gaus Width: 3.9779

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch105, adc1

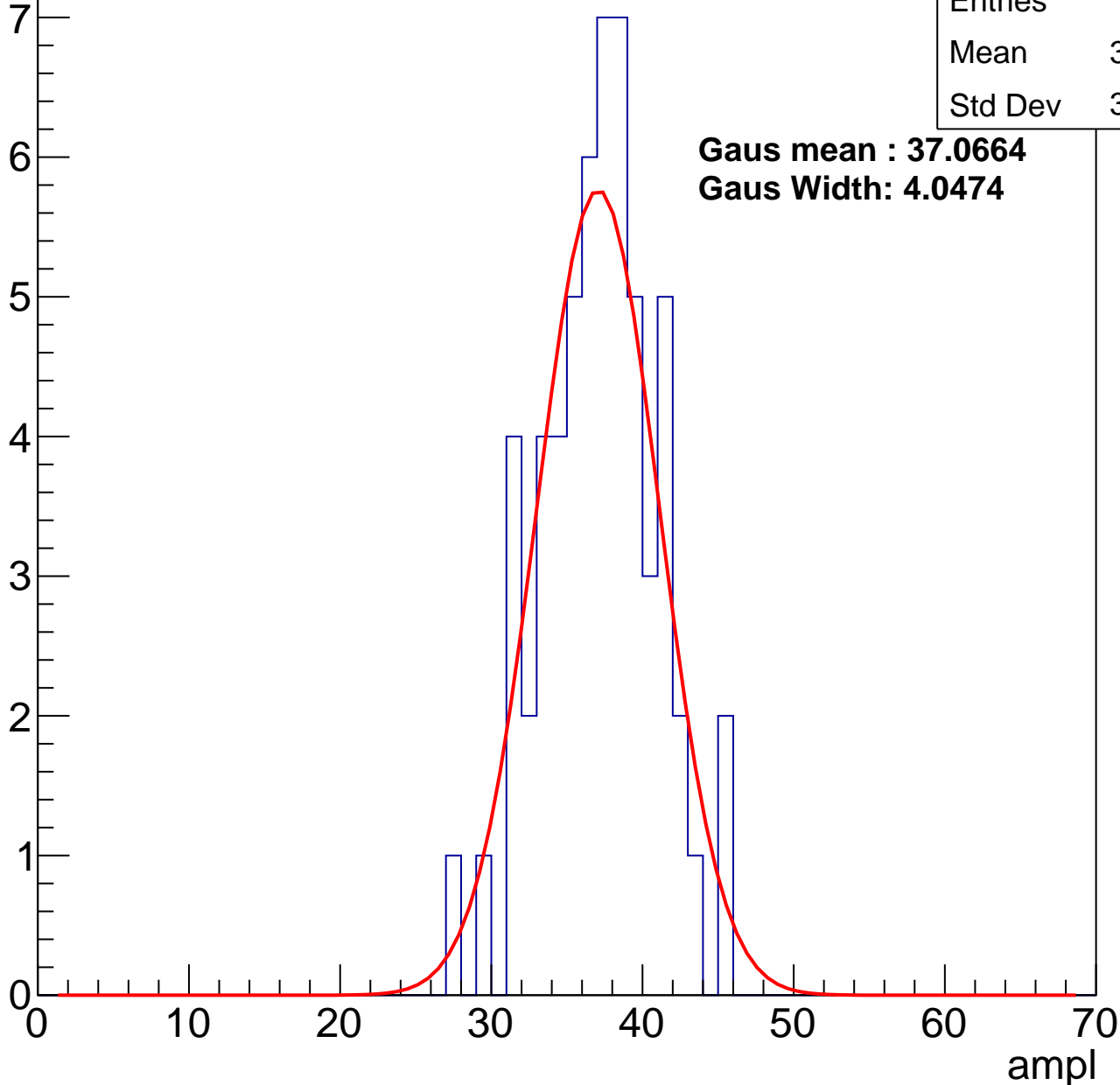
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.69
Std Dev	3.743

Gaus mean : 37.0664

Gaus Width: 4.0474



B1L100S, U10-ch105, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	43.77
Std Dev	4.126

Gaus mean : 43.7101

Gaus Width: 4.3444

Entry

10

8

6

4

2

0

0

10

20

30

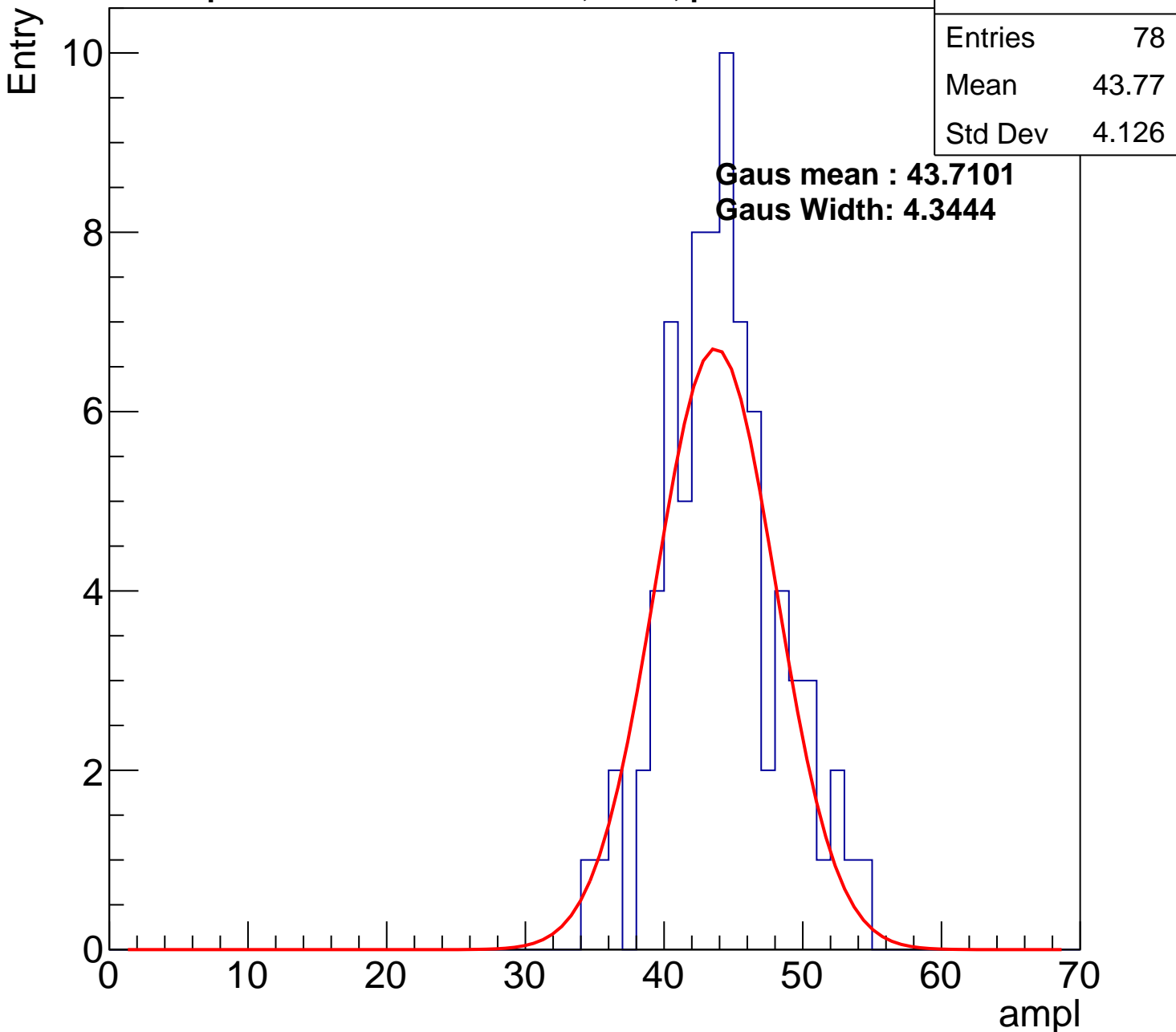
40

50

60

70

ampl

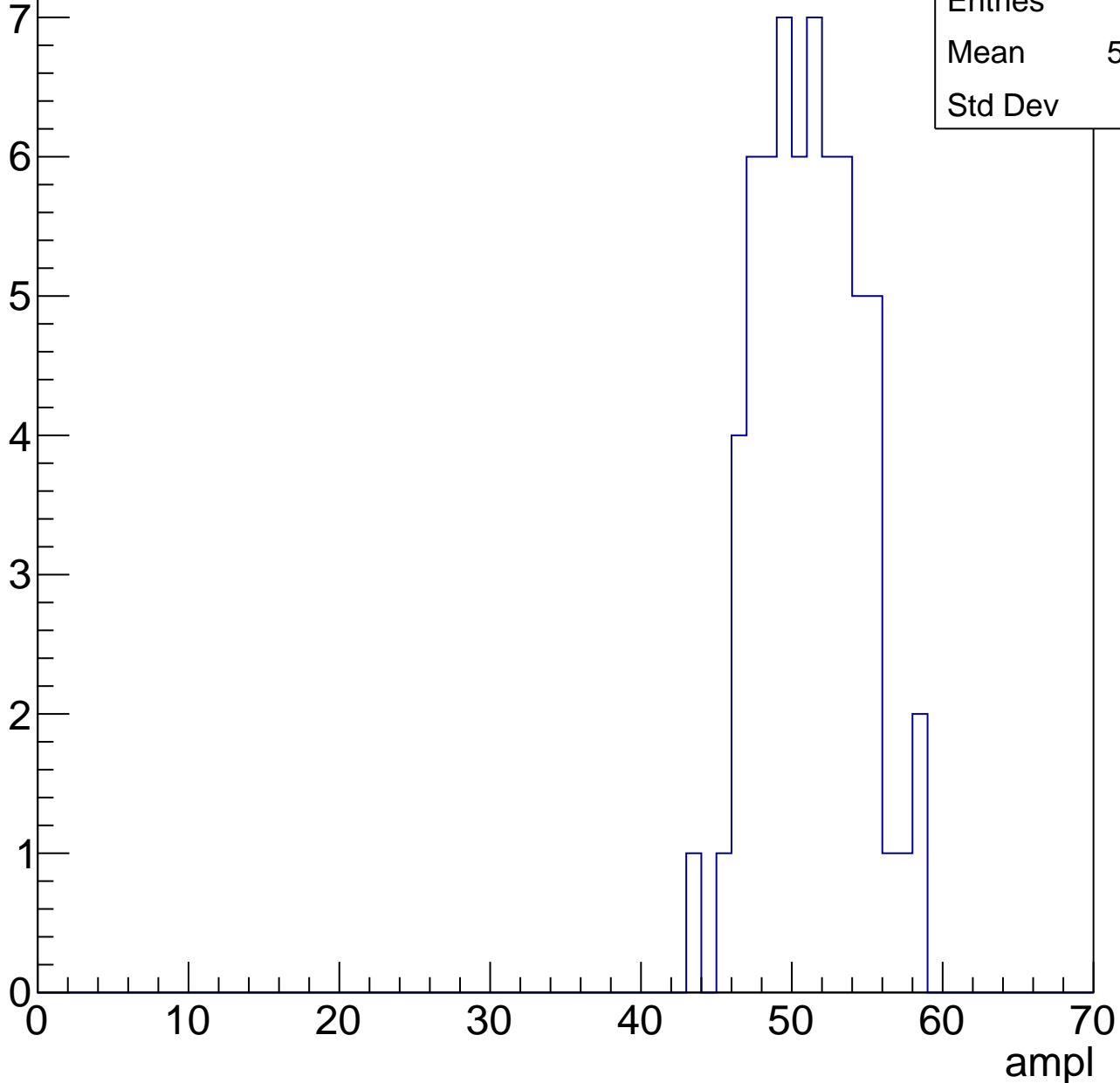


B1L100S, U10-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	50.72
Std Dev	3.29

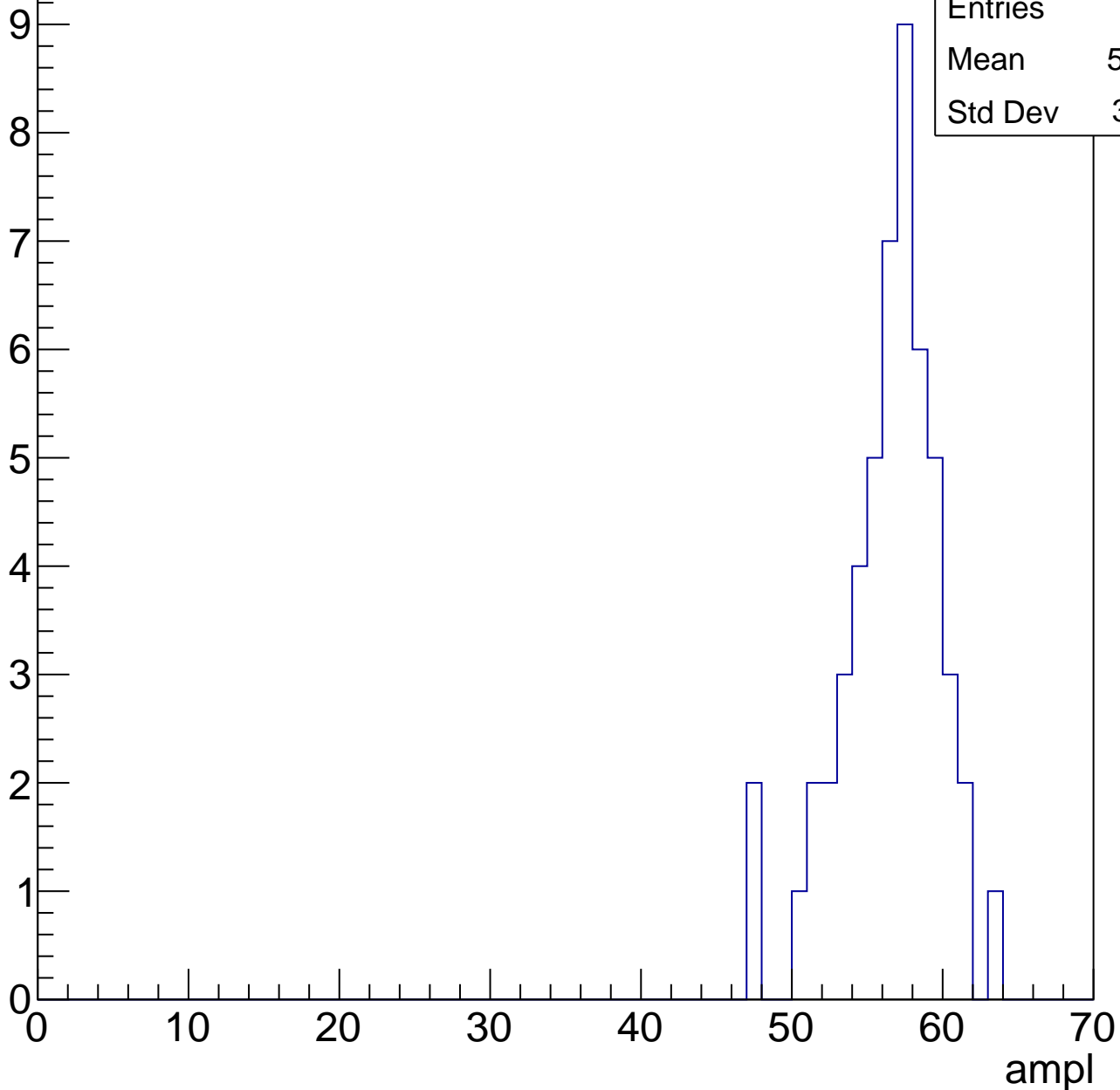


B1L100S, U10-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.02
Std Dev	3.261



B1L100S, U10-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	42
Mean	59.38
Std Dev	9.446

Entry

10

8

6

4

2

0

0

10

20

30

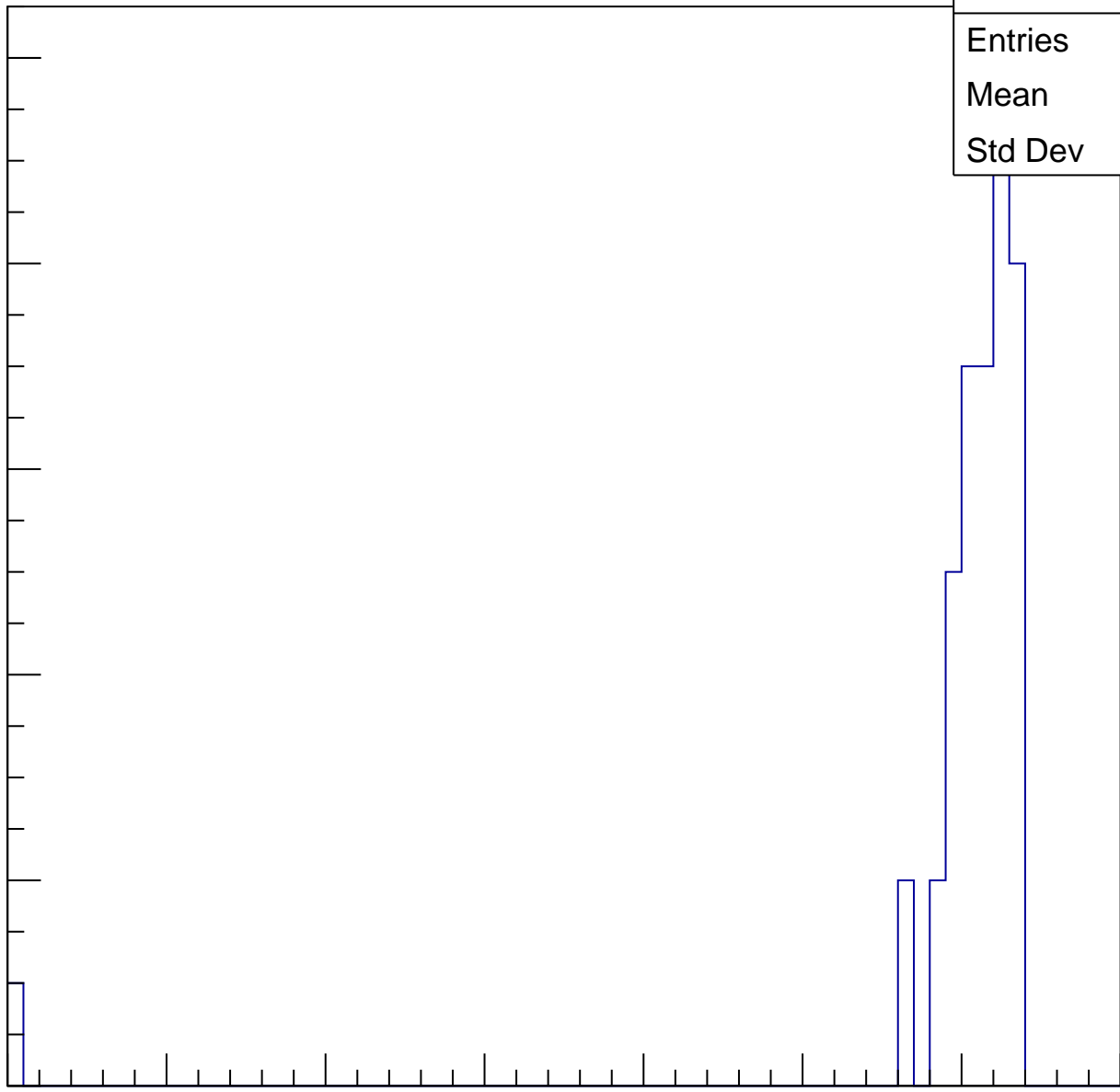
40

50

60

70

ampl



B1L100S, U10-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch106, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	28.39
Std Dev	4.809

Gaus mean : 29.5429

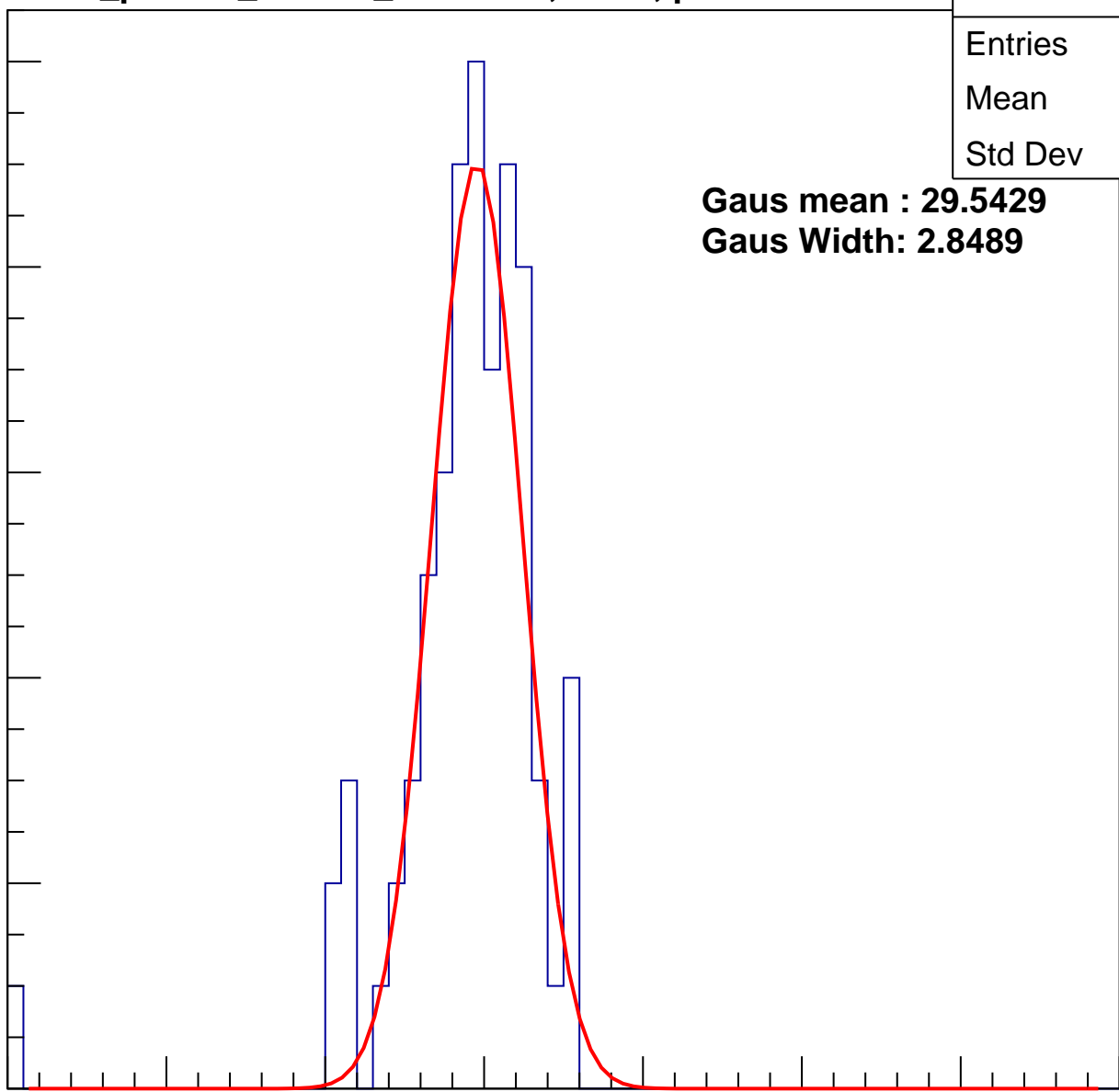
Gaus Width: 2.8489

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch106, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	36.28
Std Dev	3.114

Gaus mean : 37.0207

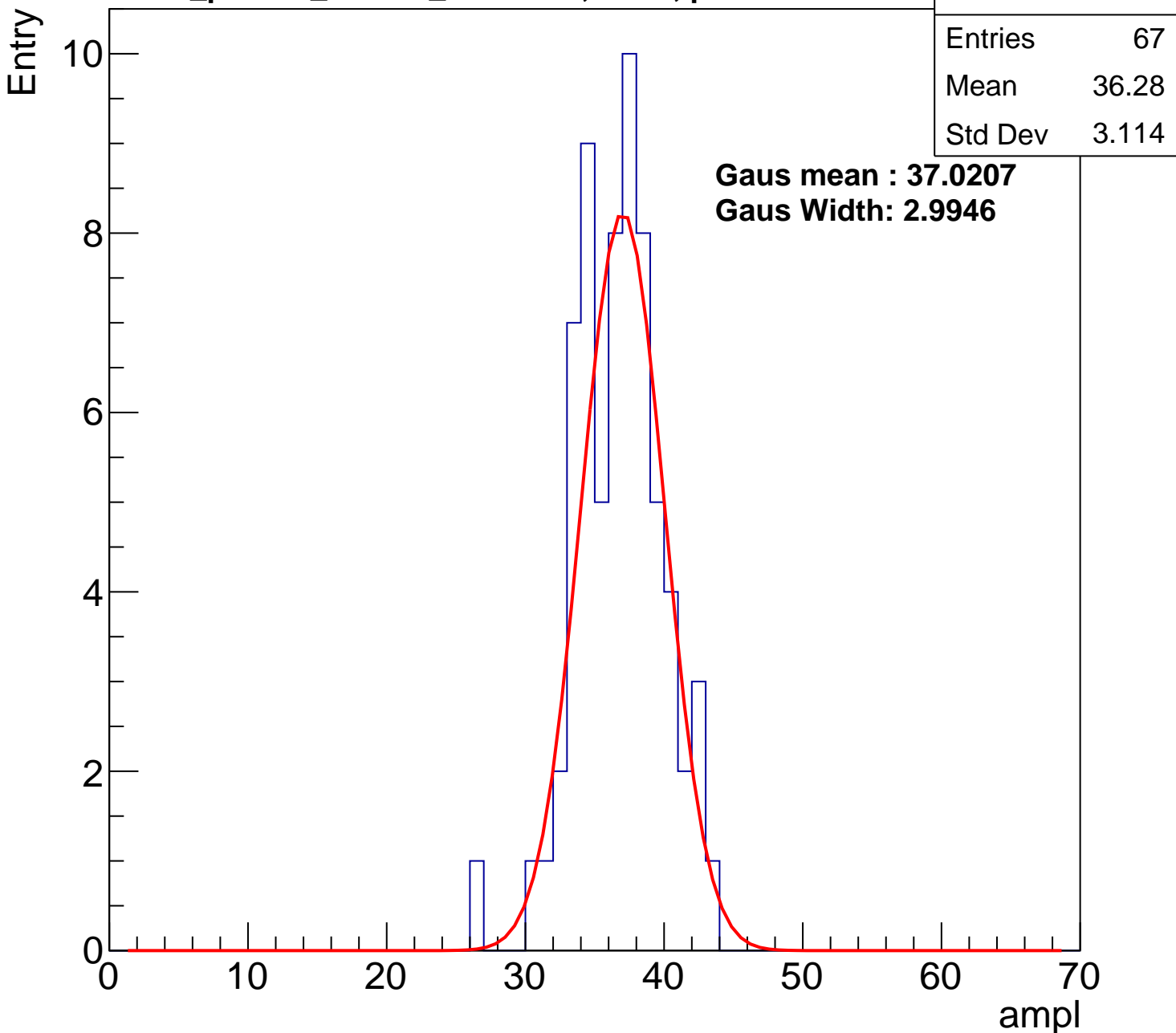
Gaus Width: 2.9946

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch106, adc2

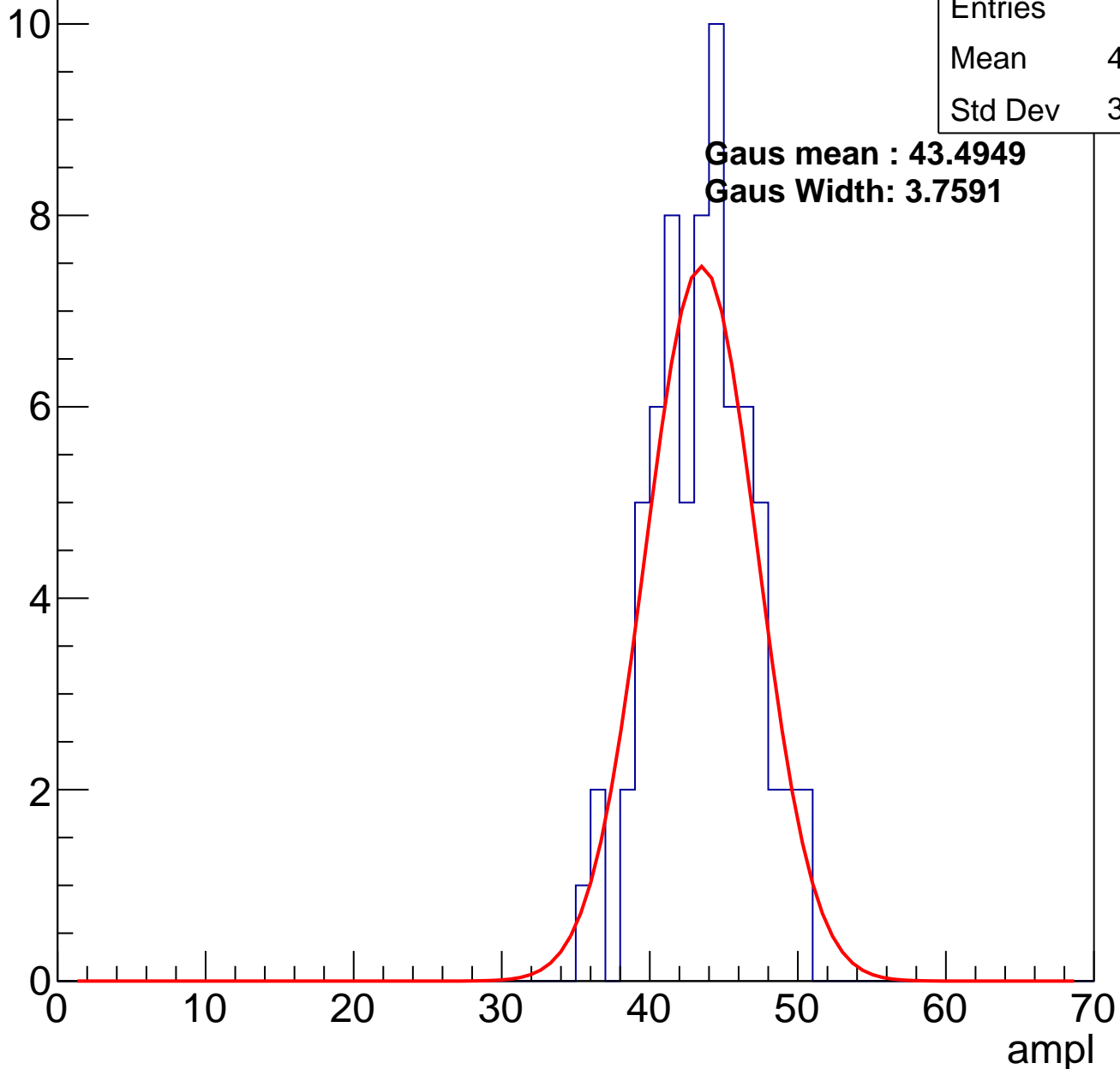
calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	43.07
Std Dev	3.318

Gaus mean : 43.4949

Gaus Width: 3.7591

Entry

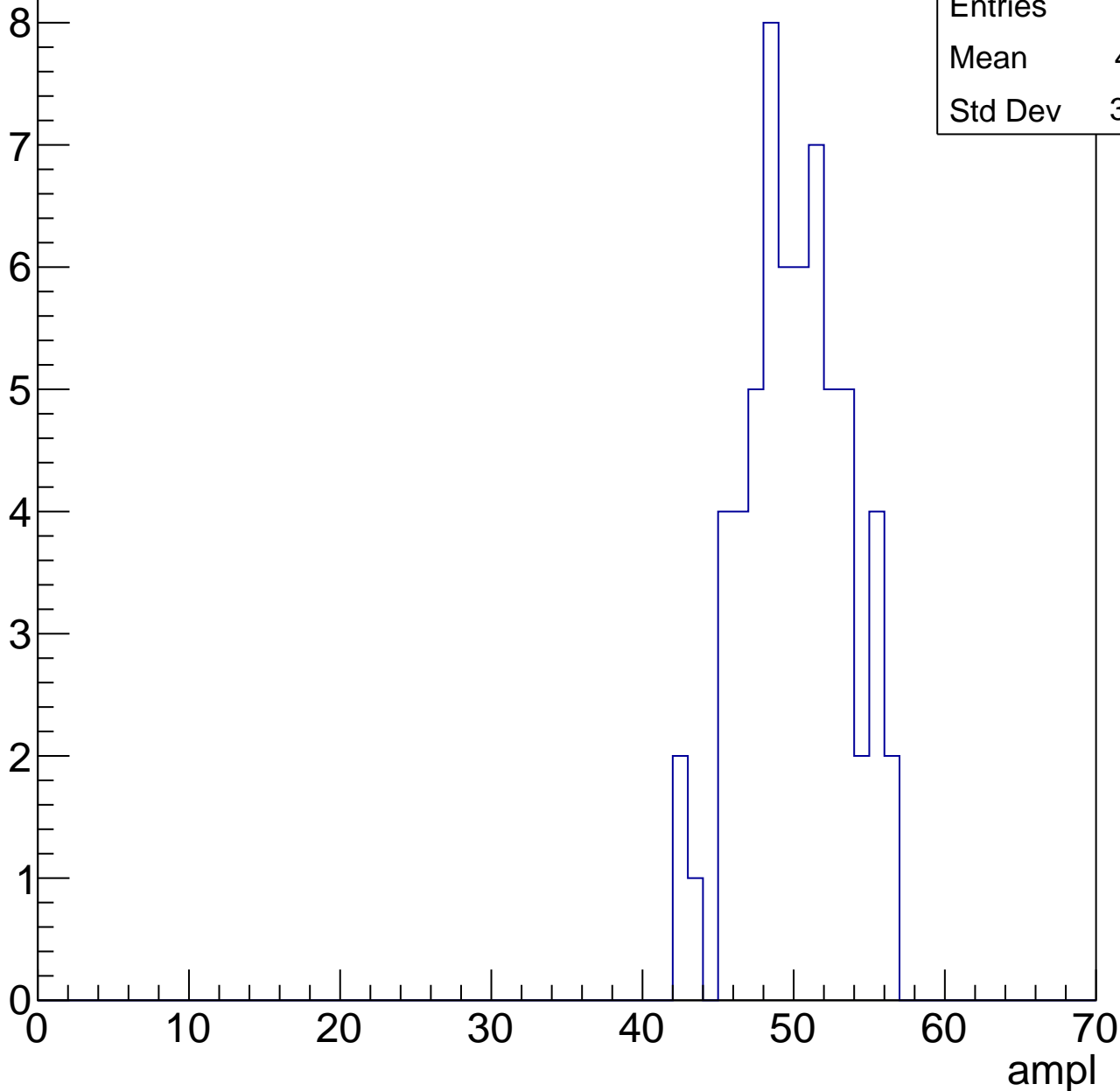


B1L100S, U10-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

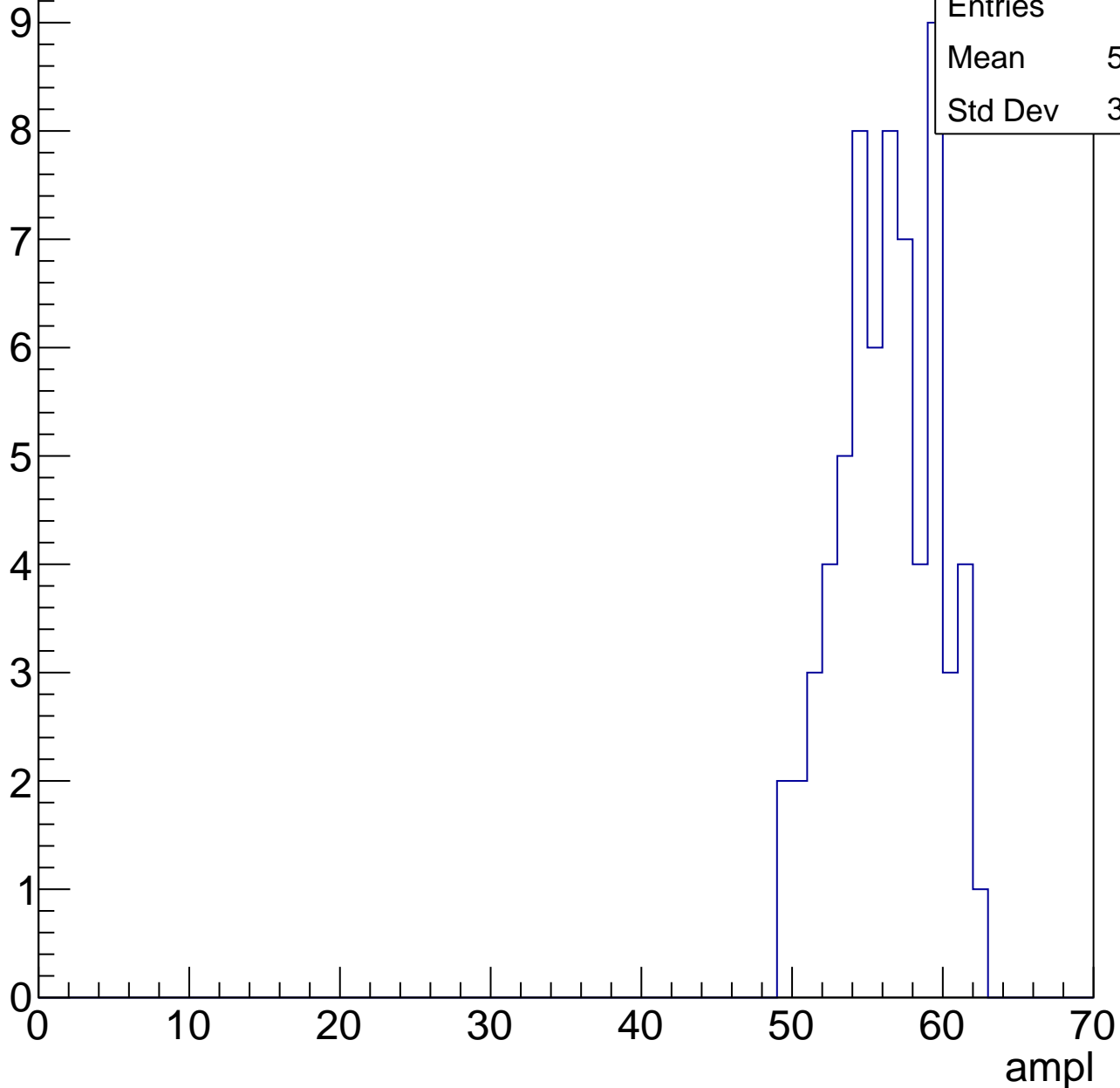
Entries	61
Mean	49.61
Std Dev	3.365



B1L100S, U10-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

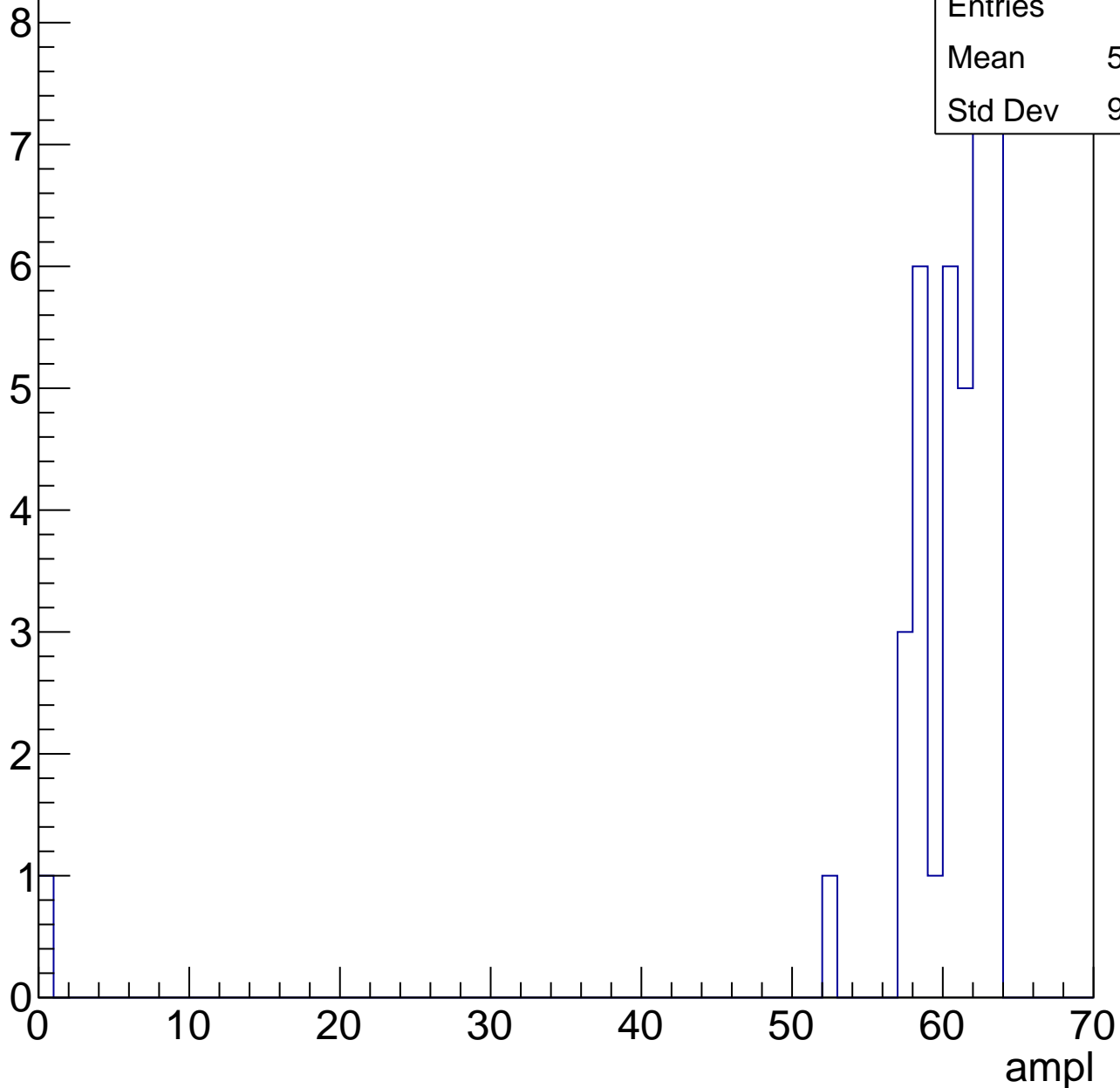


B1L100S, U10-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	58.85
Std Dev	9.836



B1L100S, U10-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

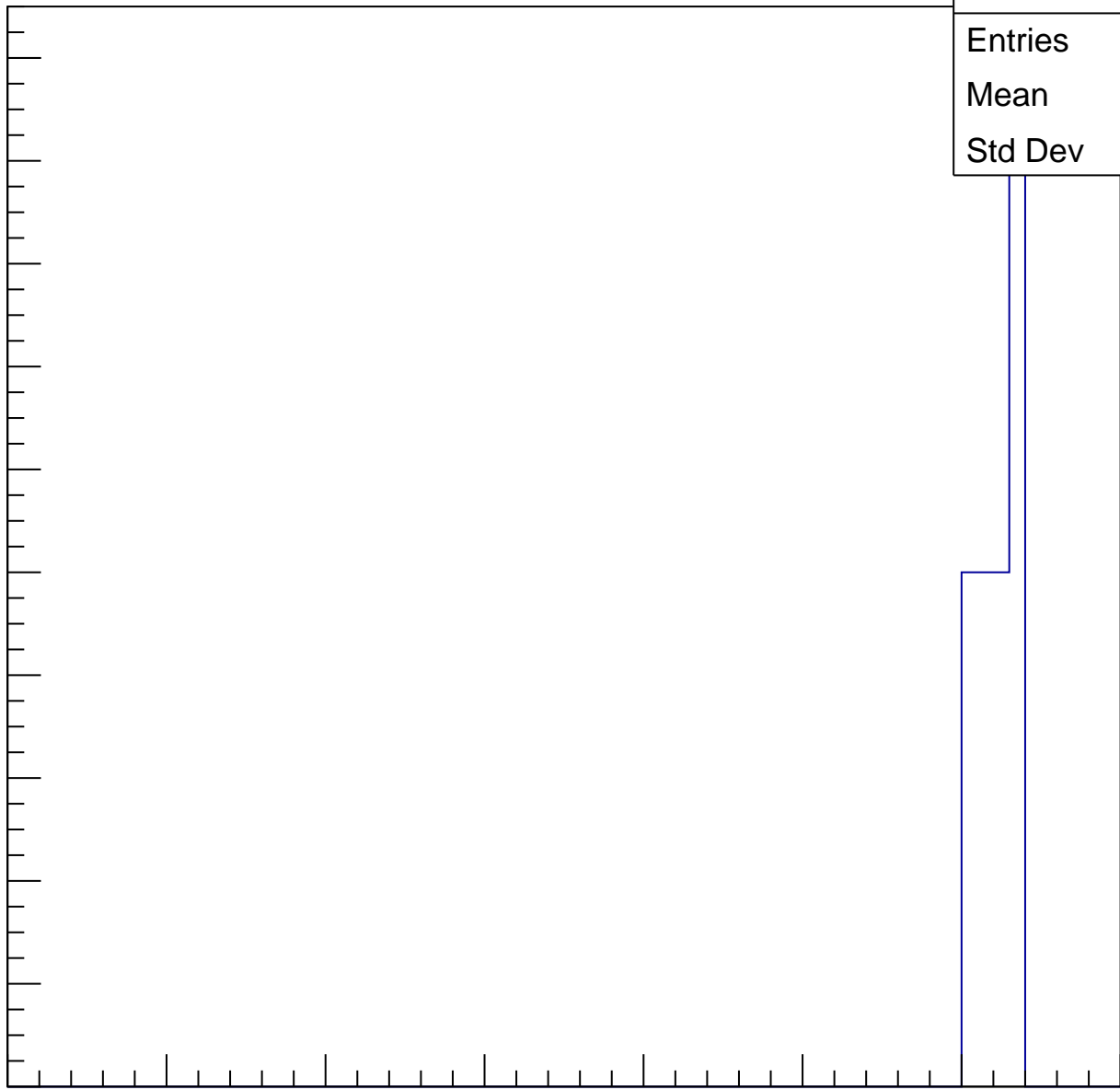
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch107, adc0

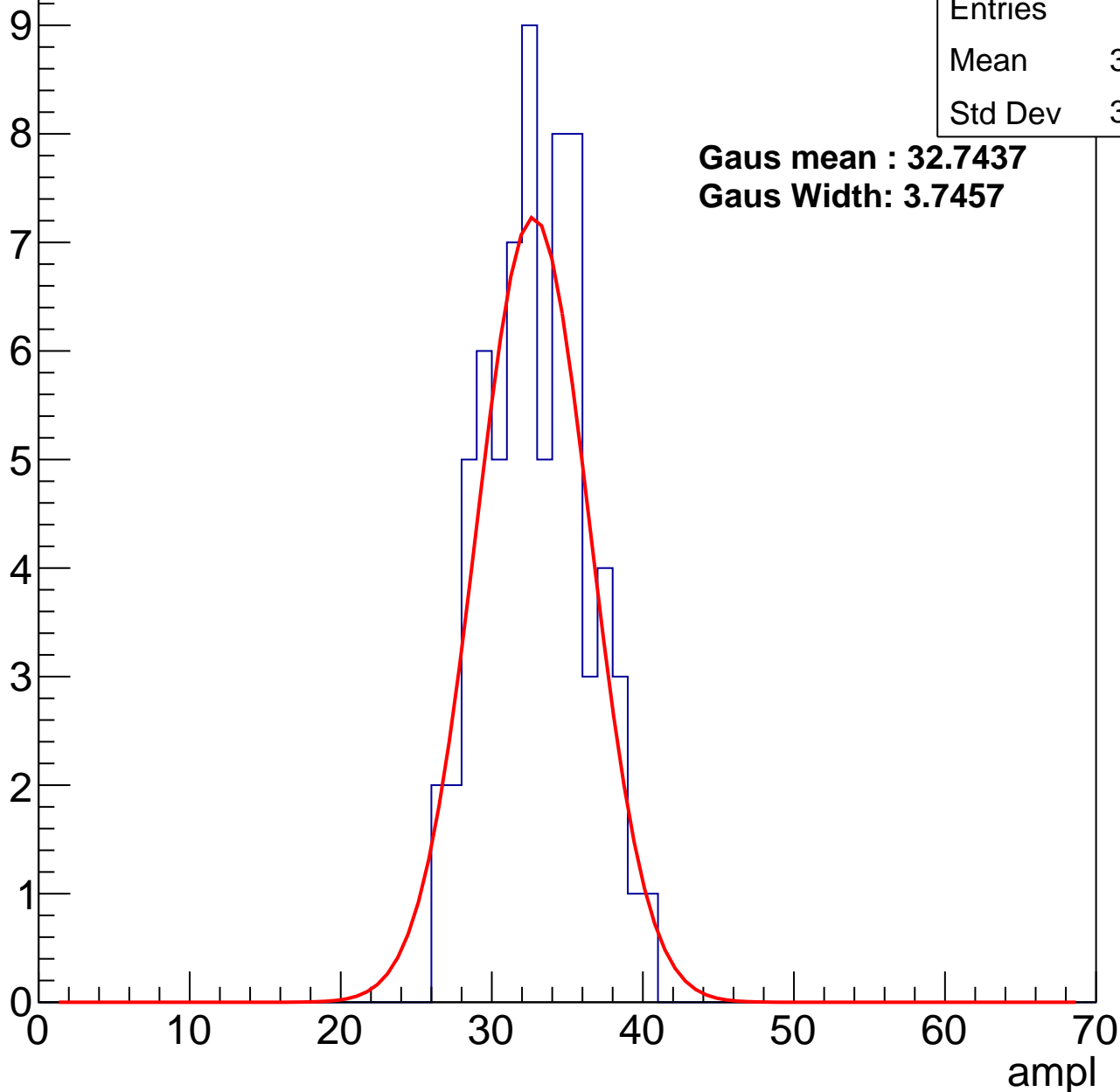
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	32.48
Std Dev	3.286

Gaus mean : 32.7437

Gaus Width: 3.7457



B1L100S, U10-ch107, adc1

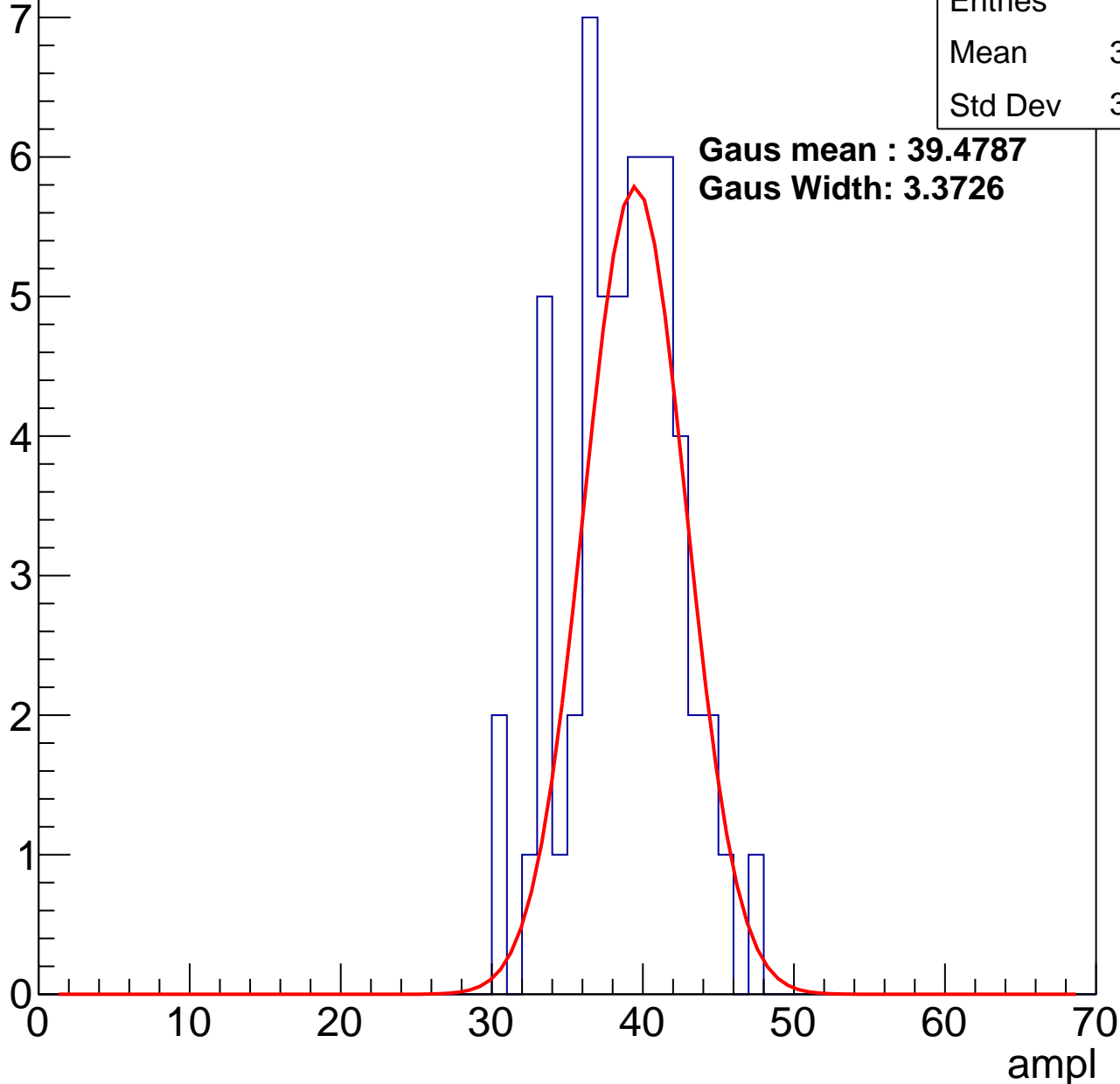
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	38.25
Std Dev	3.666

Gaus mean : 39.4787

Gaus Width: 3.3726



B1L100S, U10-ch107, adc2

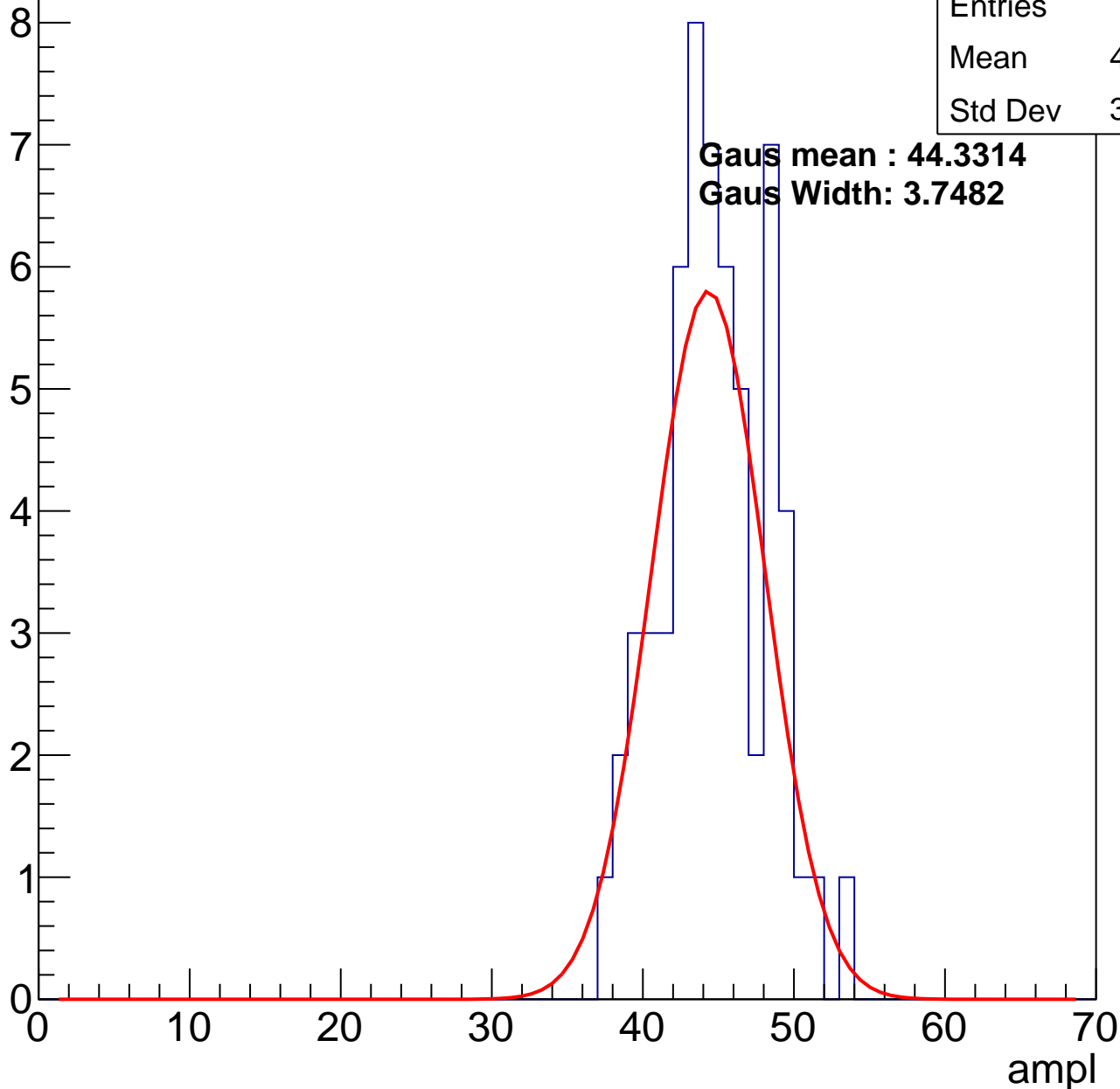
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.28
Std Dev	3.465

Gaus mean : 44.3314

Gaus Width: 3.7482

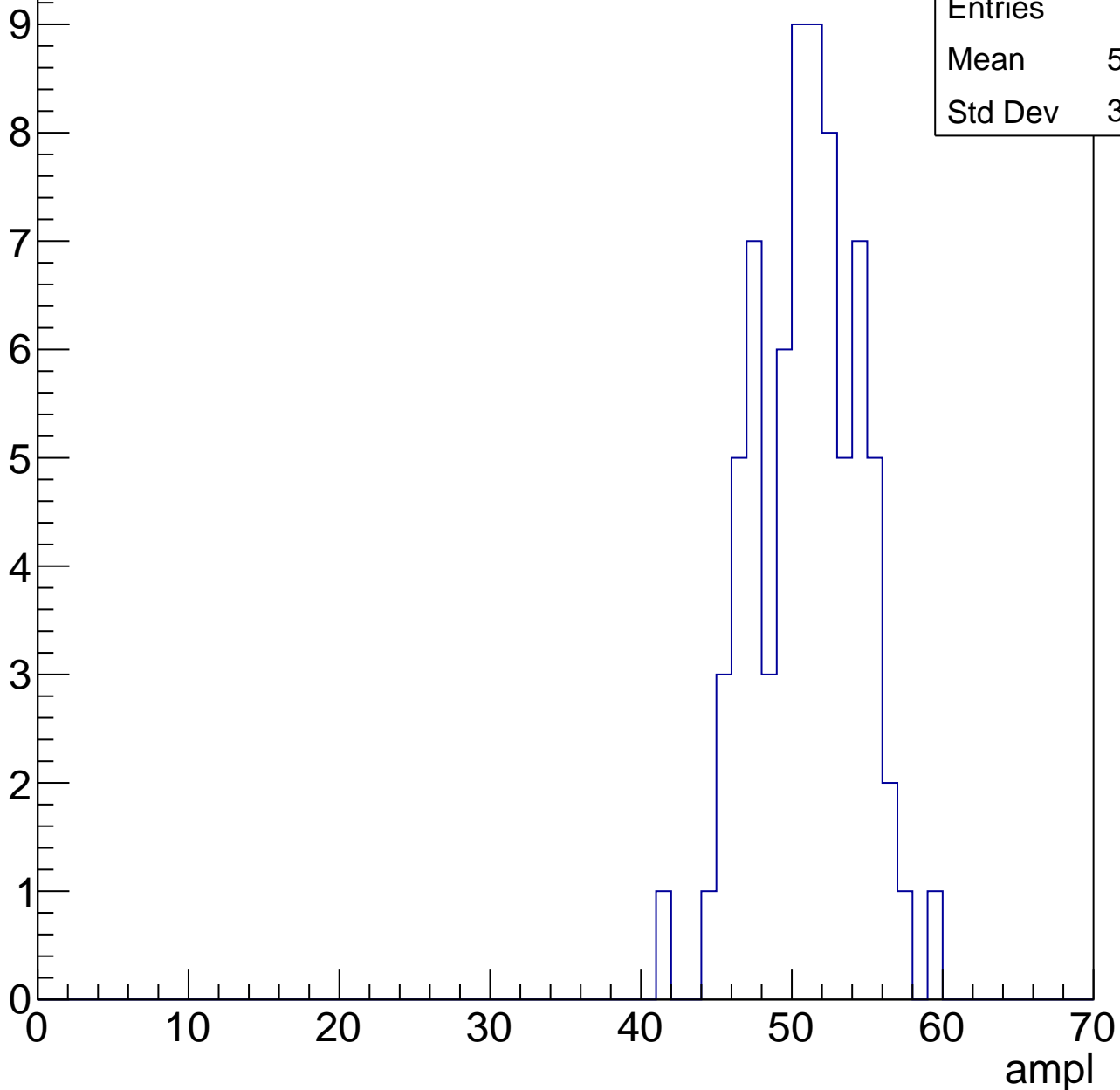


B1L100S, U10-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.52
Std Dev	3.429

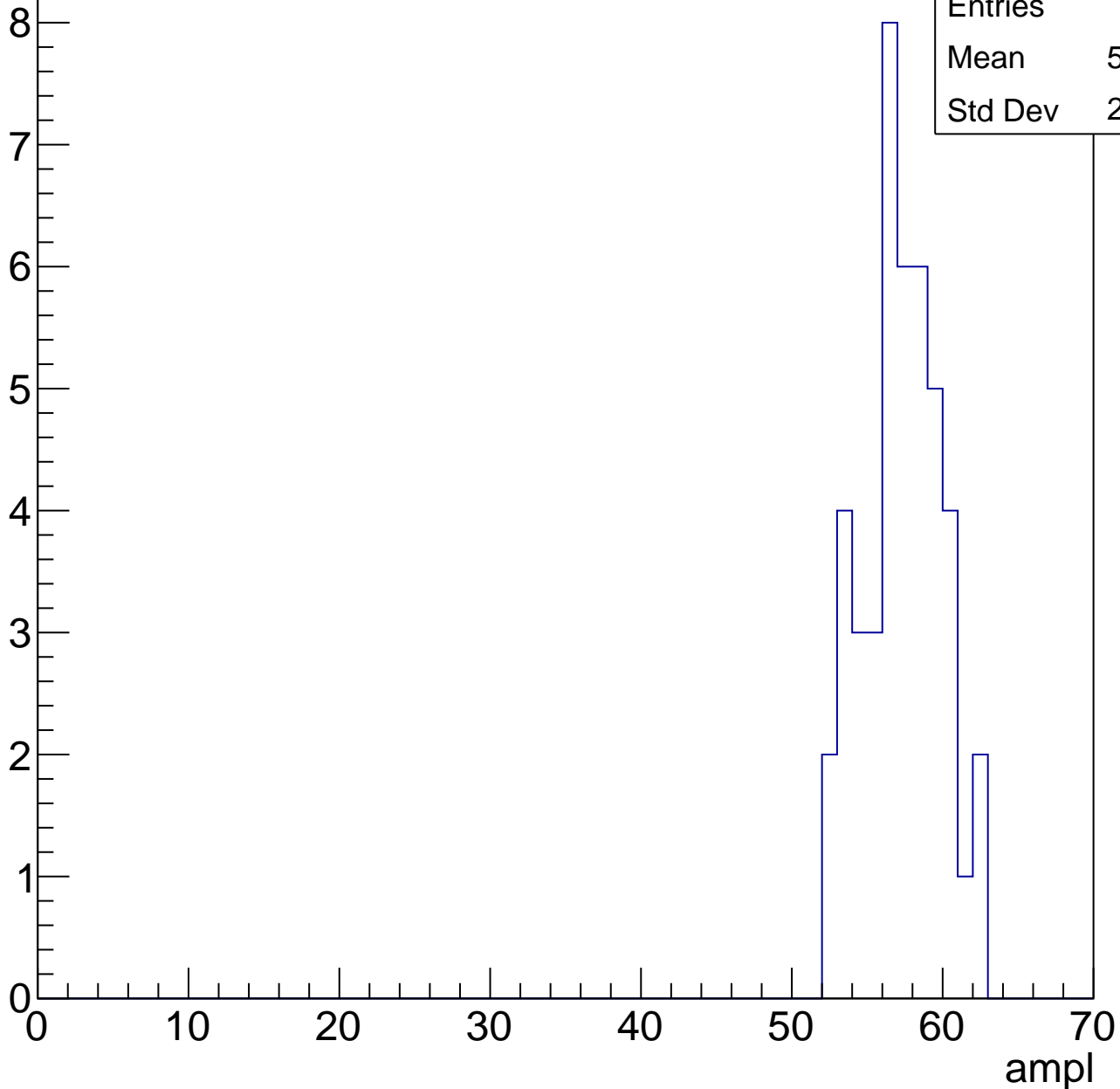


B1L100S, U10-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	56.84
Std Dev	2.558

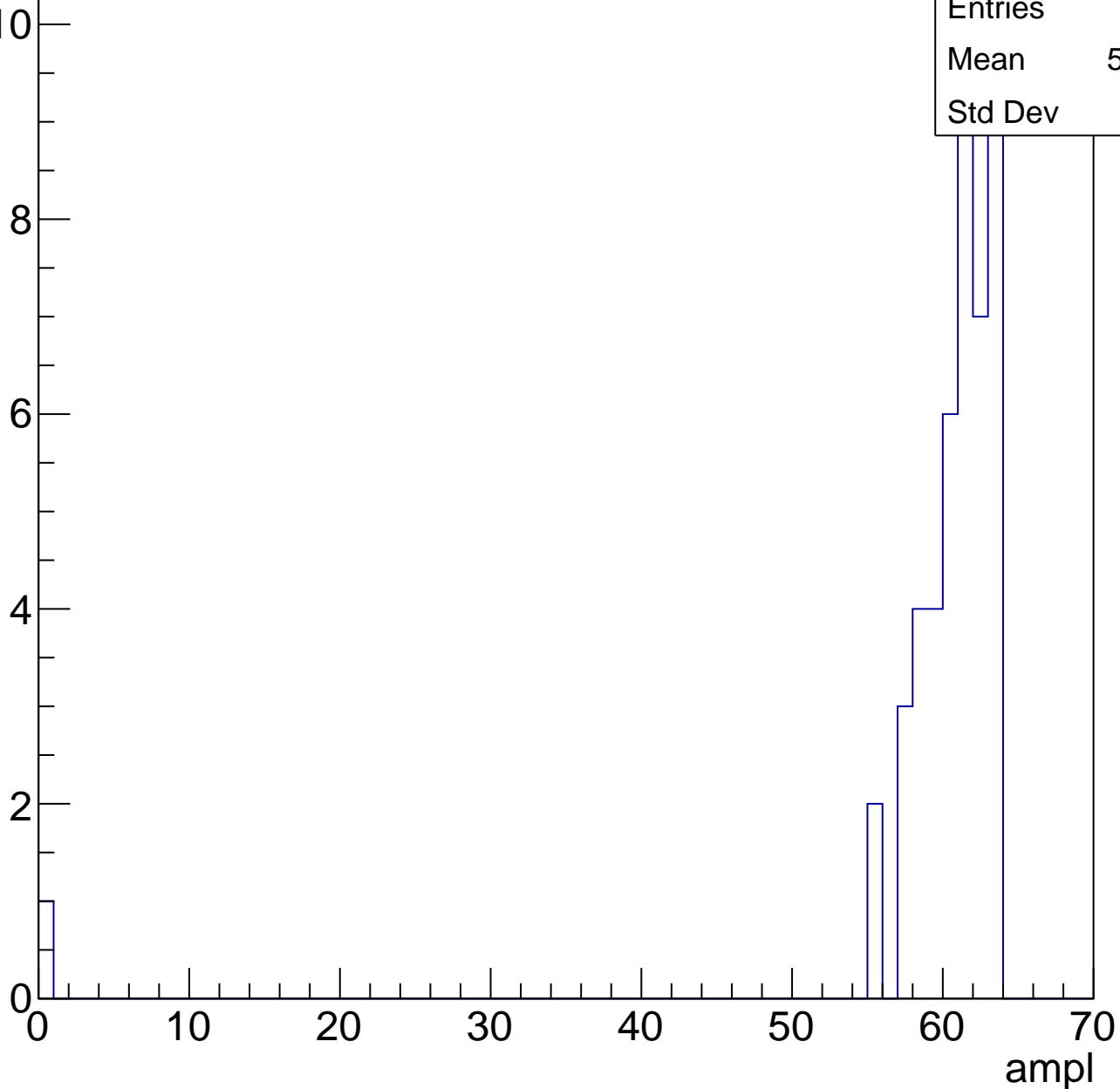


B1L100S, U10-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.17
Std Dev	9.08



B1L100S, U10-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U10-ch108, adc0

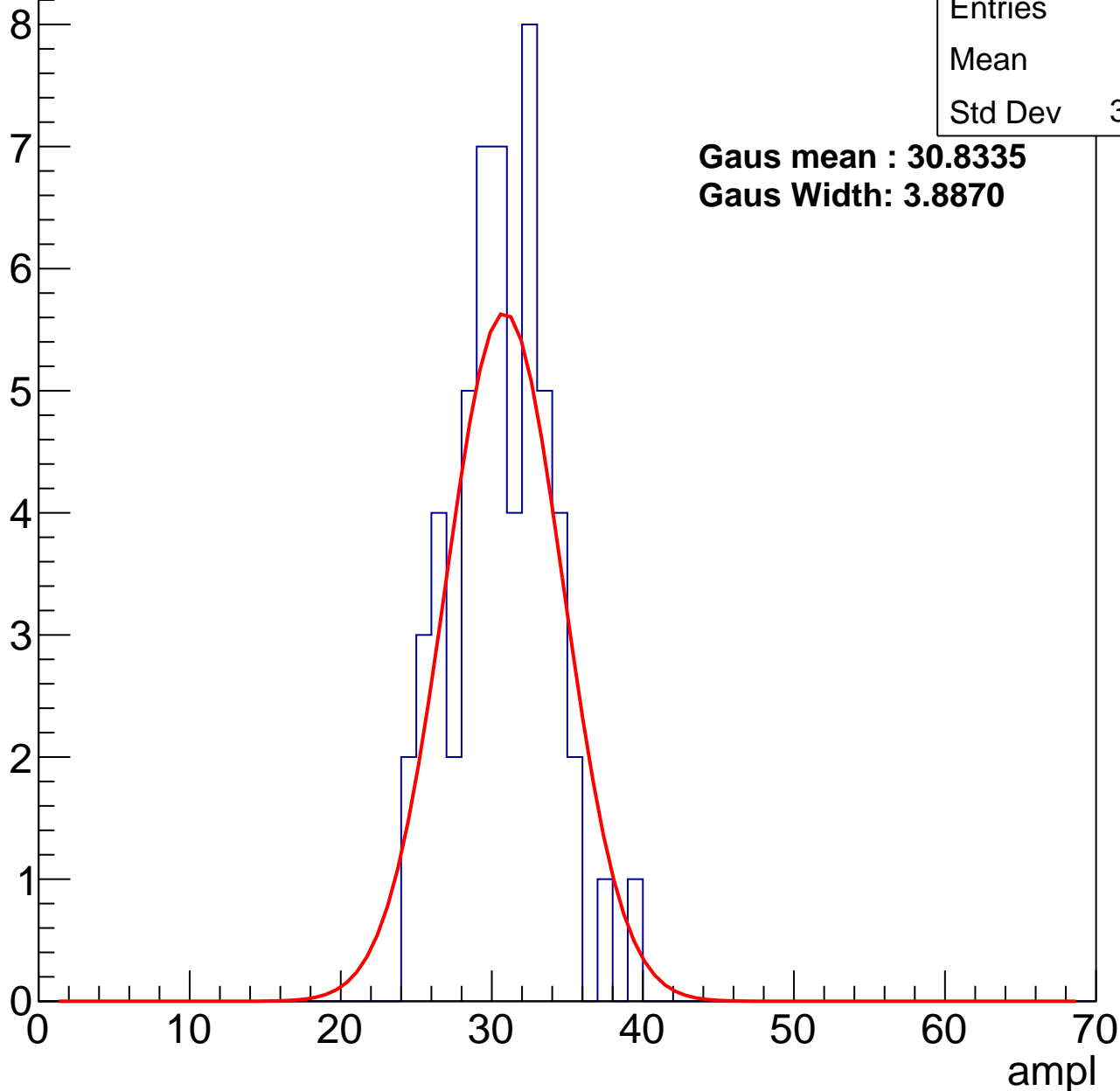
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	30.2
Std Dev	3.244

Gaus mean : 30.8335

Gaus Width: 3.8870



B1L100S, U10-ch108, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.35
Std Dev	3.61

Gaus mean : 37.1874

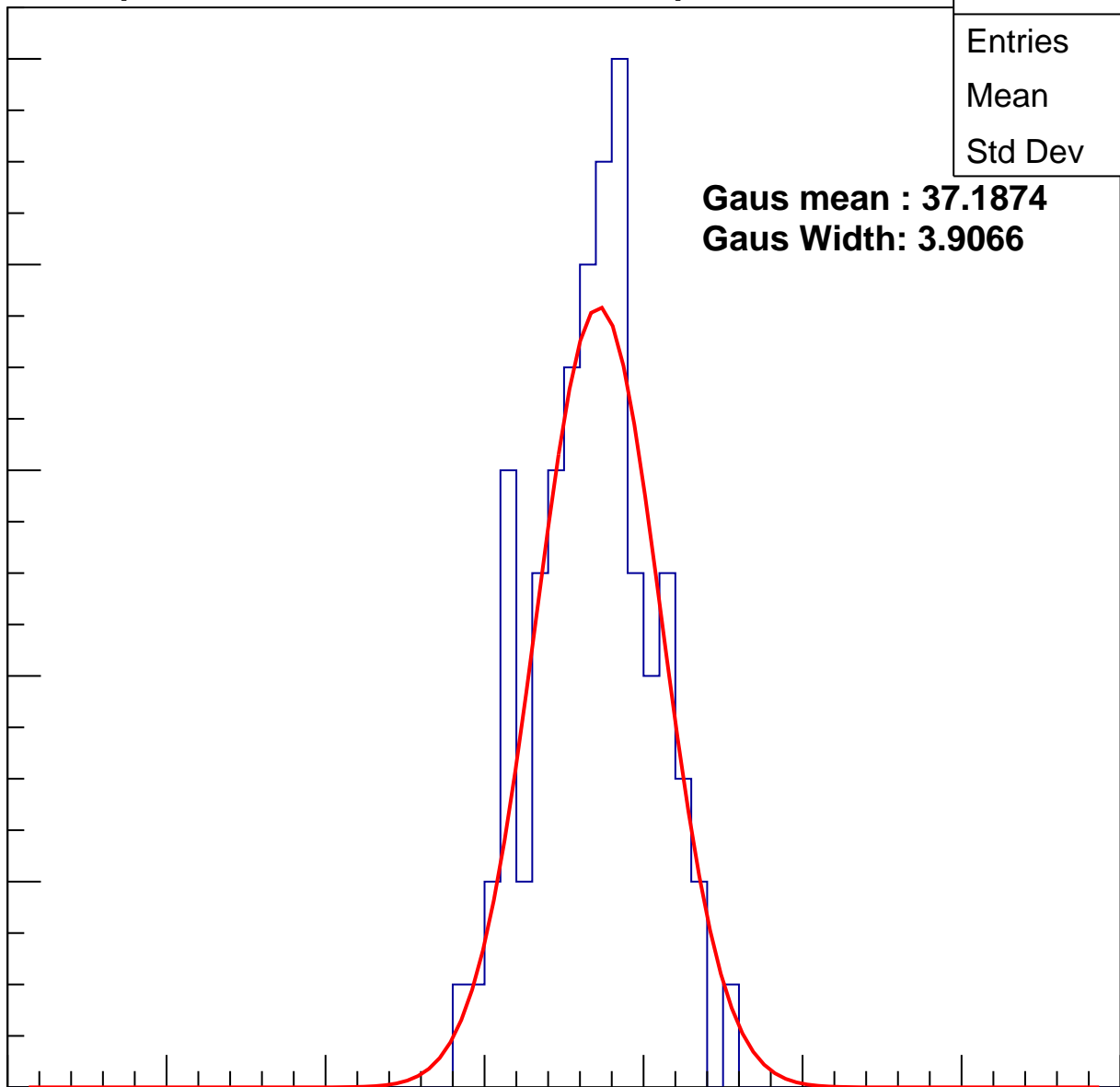
Gaus Width: 3.9066

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch108, adc2

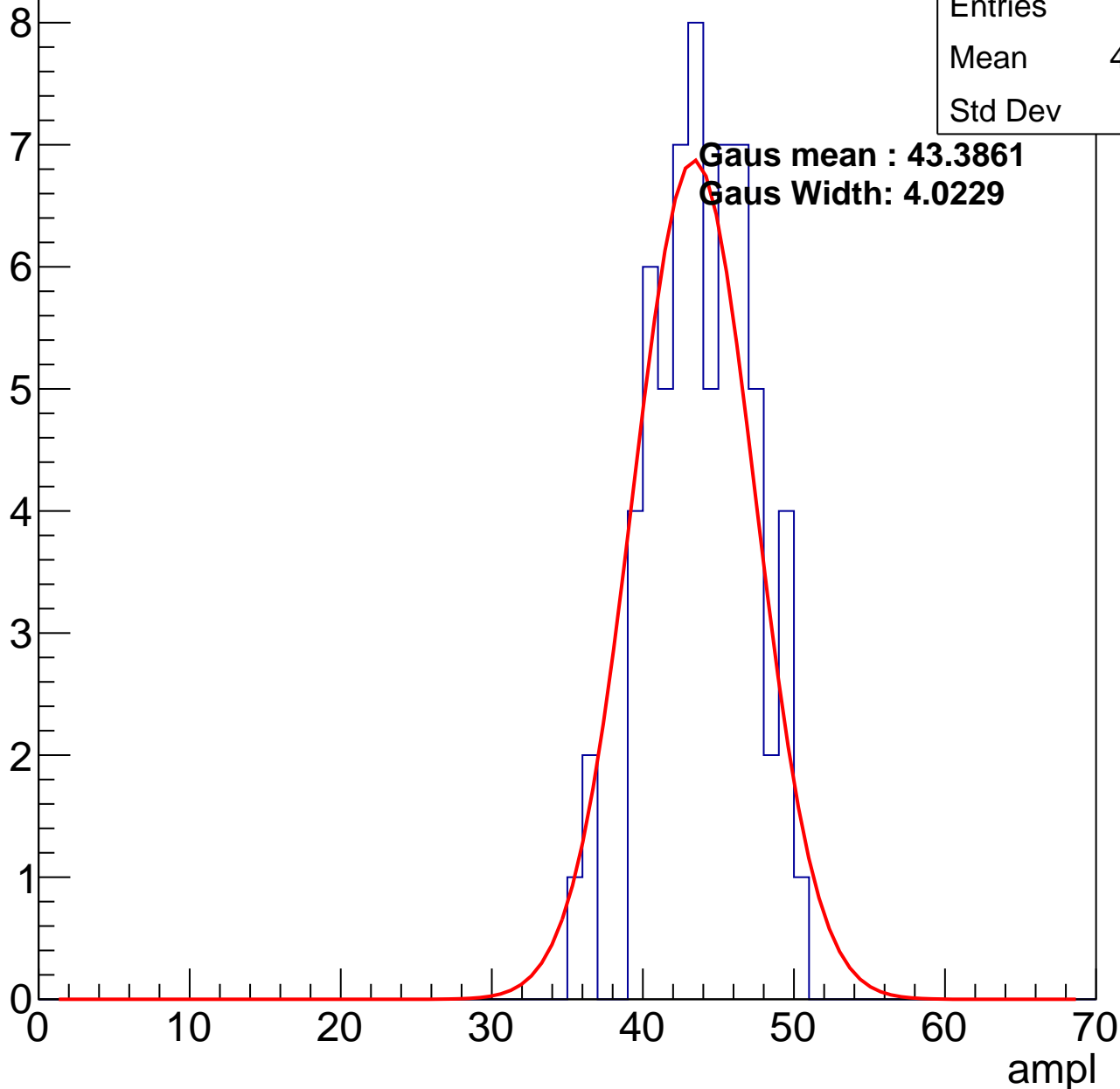
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.44
Std Dev	3.34

Gaus mean : 43.3861

Gaus Width: 4.0229

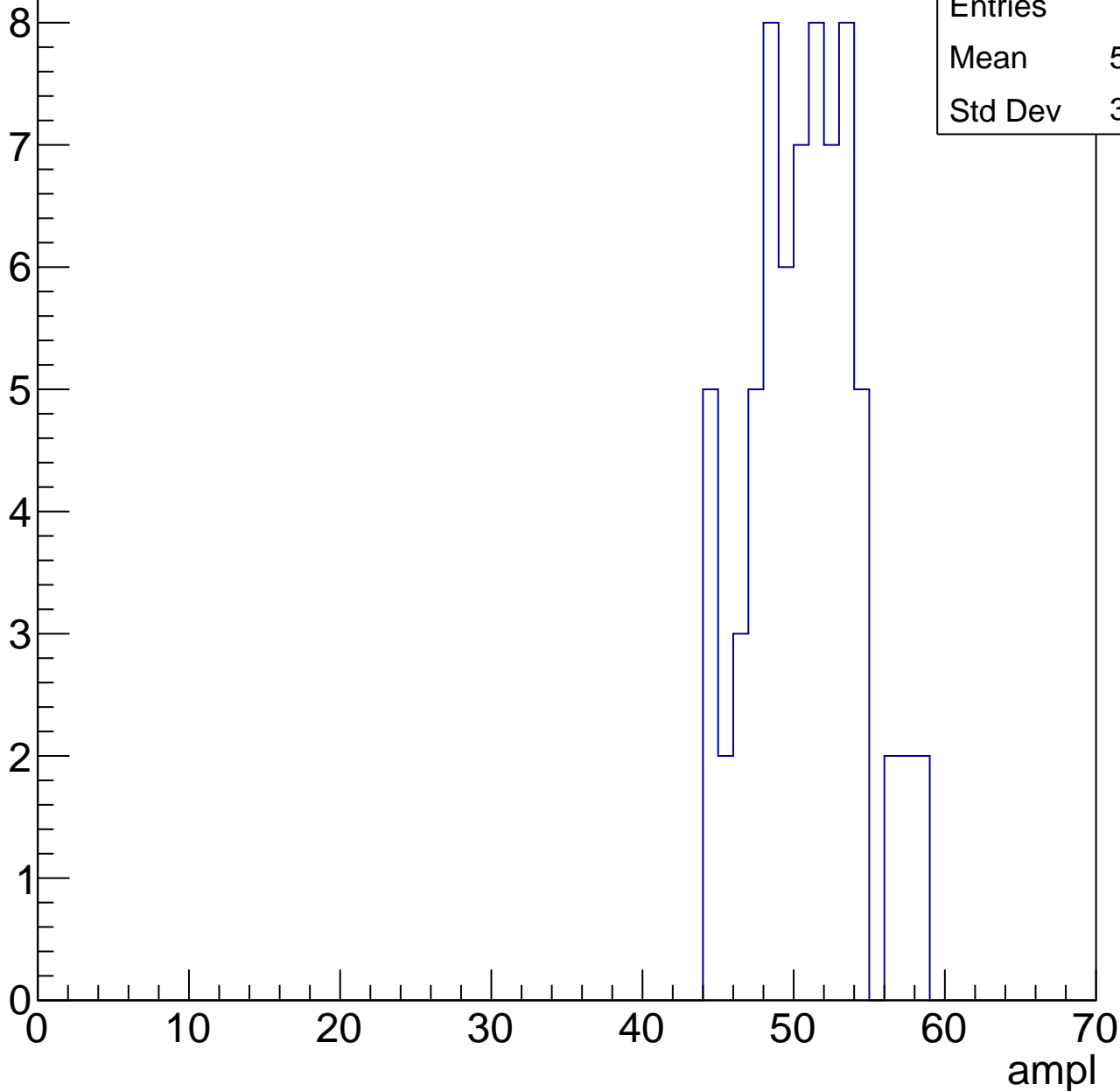


B1L100S, U10-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

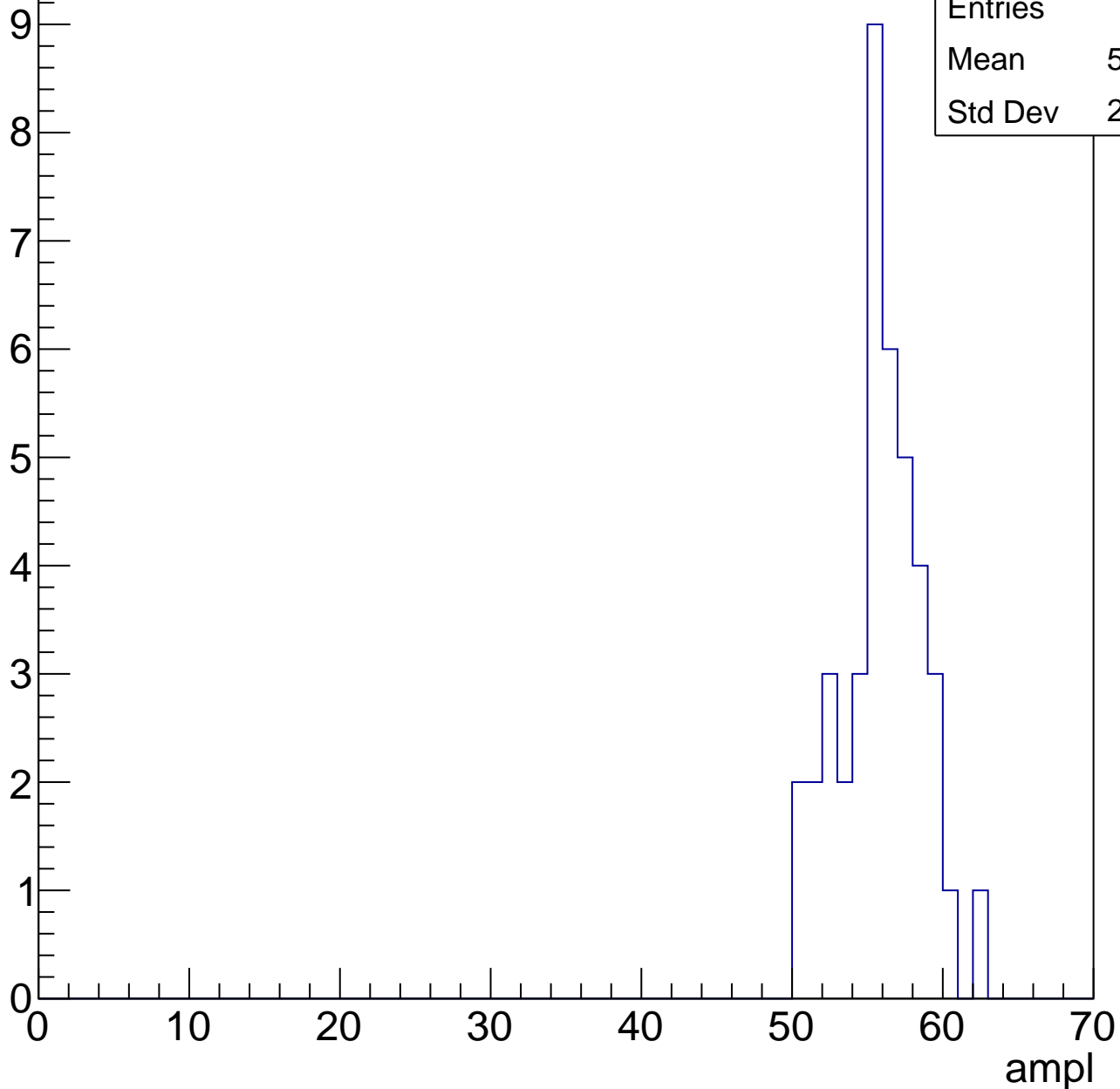
Entries	70
Mean	50.27
Std Dev	3.468



B1L100S, U10-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



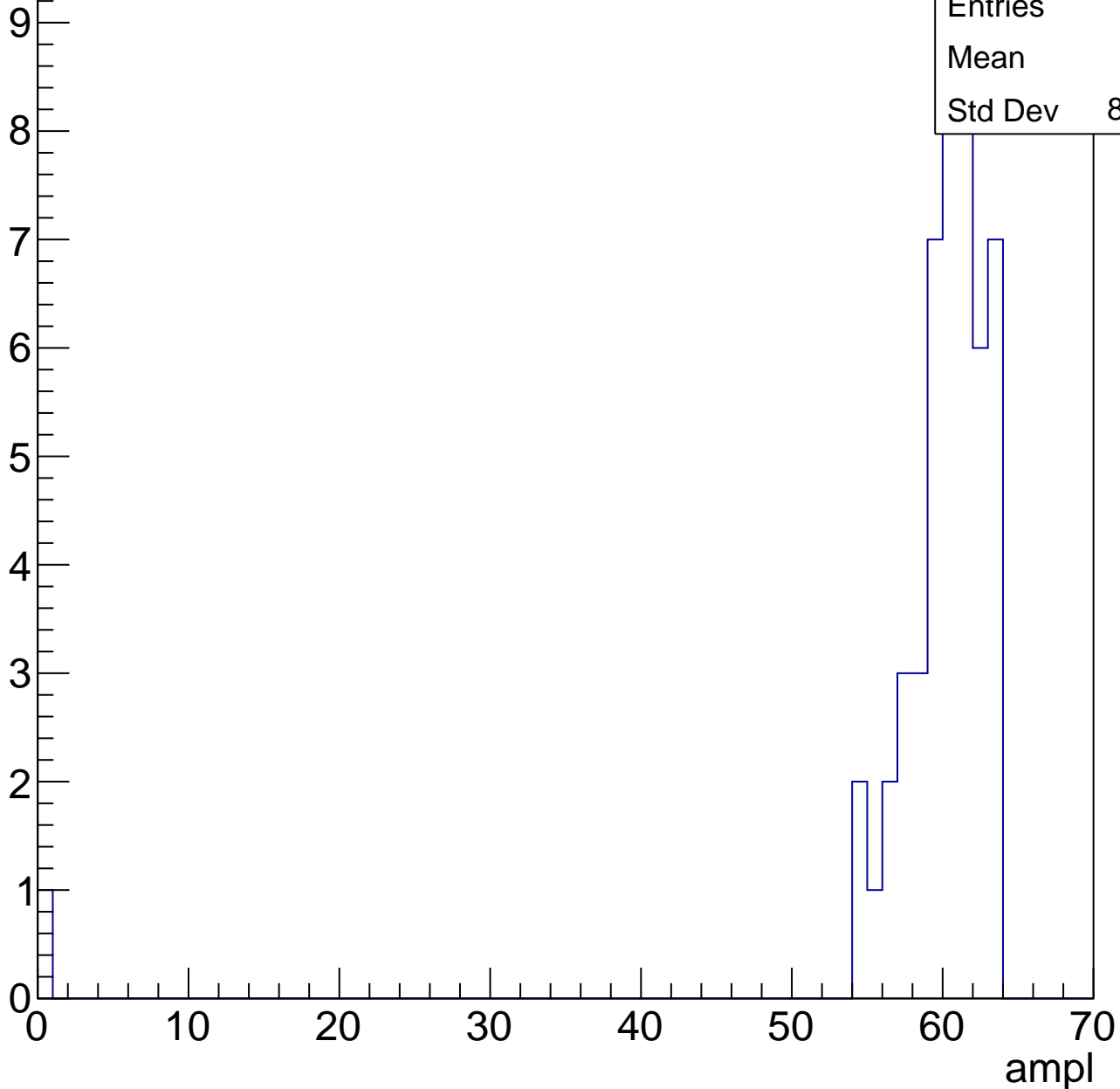
Entries	41
Mean	55.44
Std Dev	2.687

B1L100S, U10-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

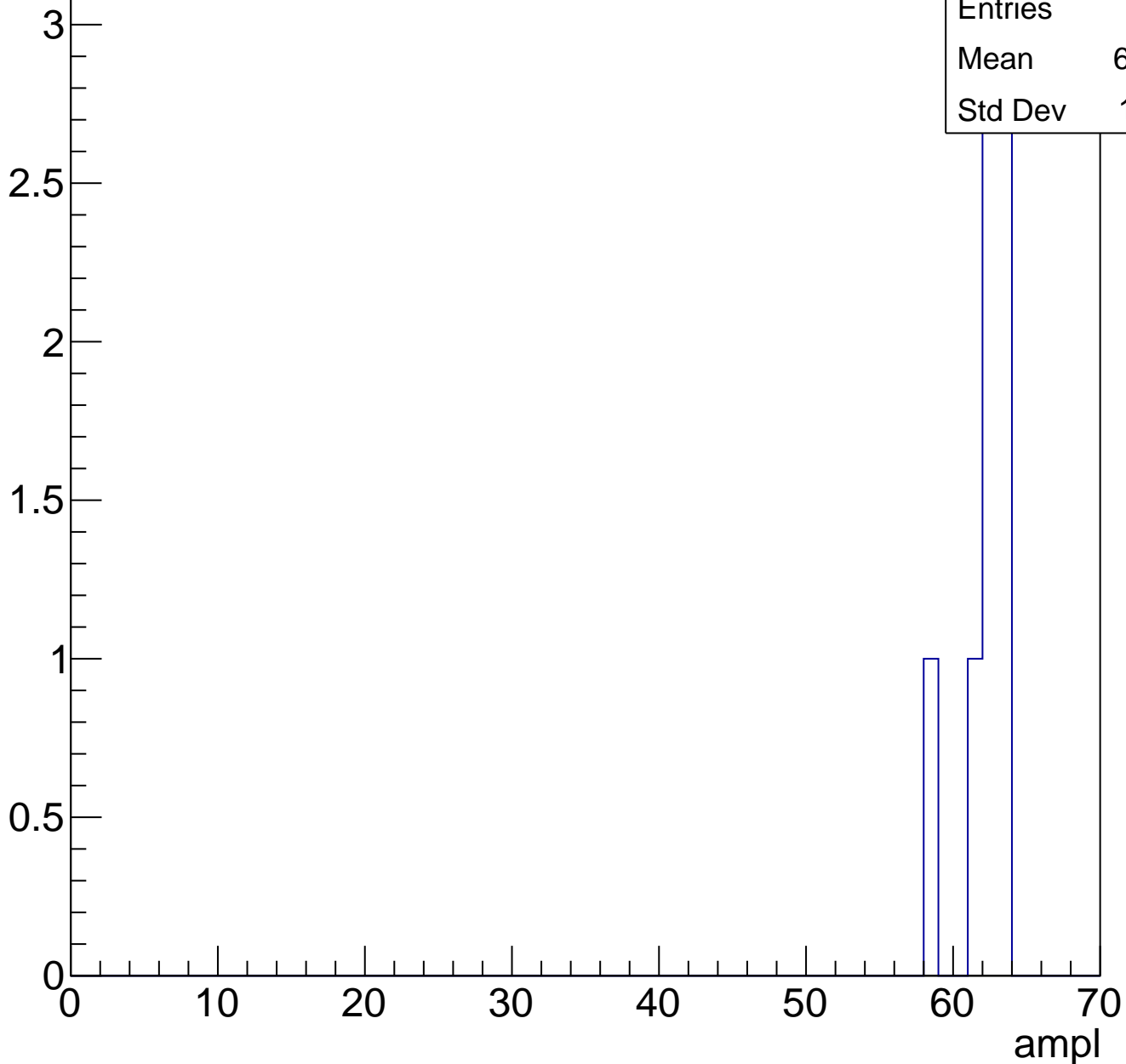
Entries	50
Mean	58.7
Std Dev	8.702



B1L100S, U10-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch108, adc7

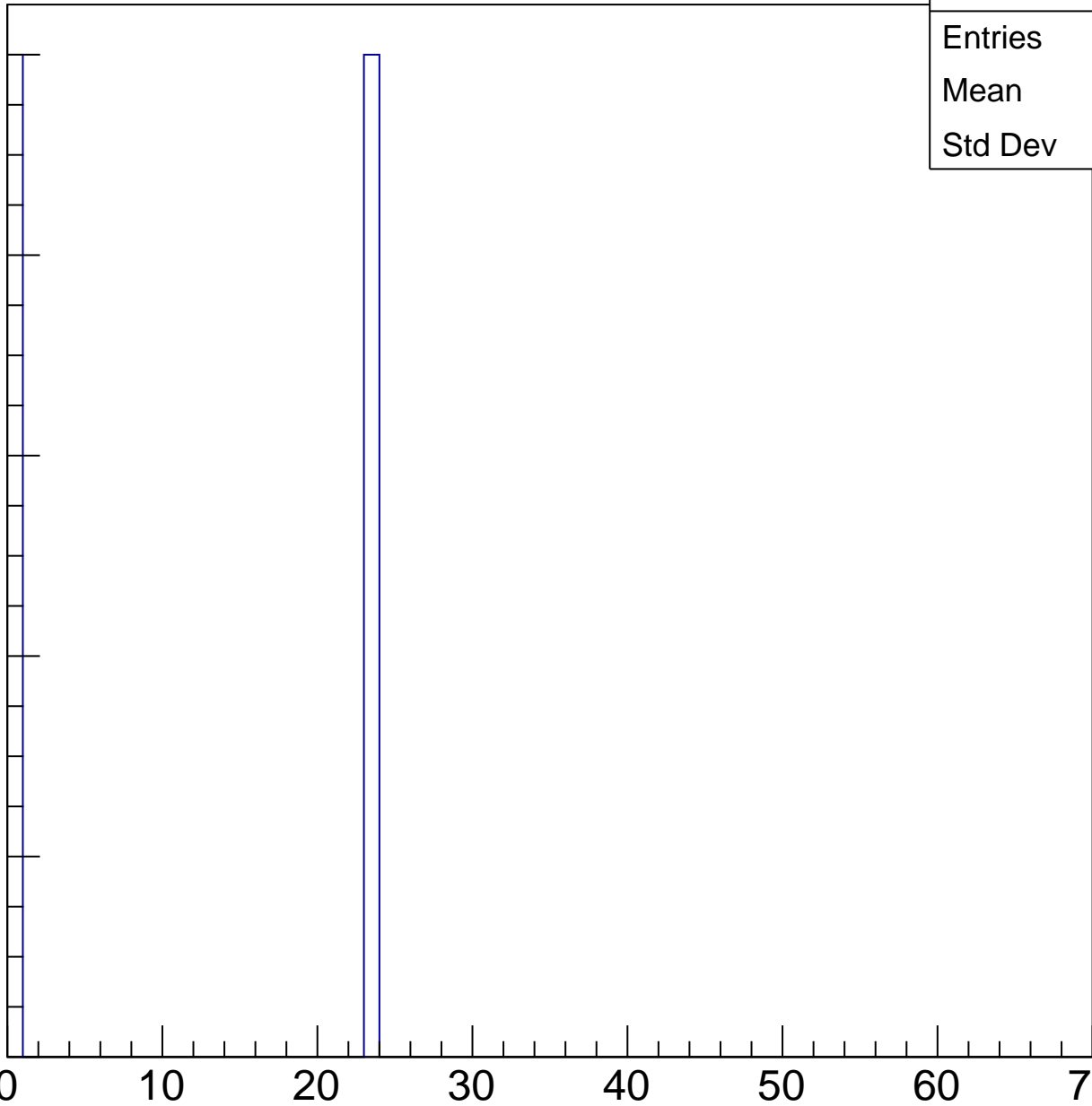
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	11.5
Std Dev	11.5

ampl



B1L100S, U10-ch109, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	30.49
Std Dev	3.235

Gaus mean : 30.8479

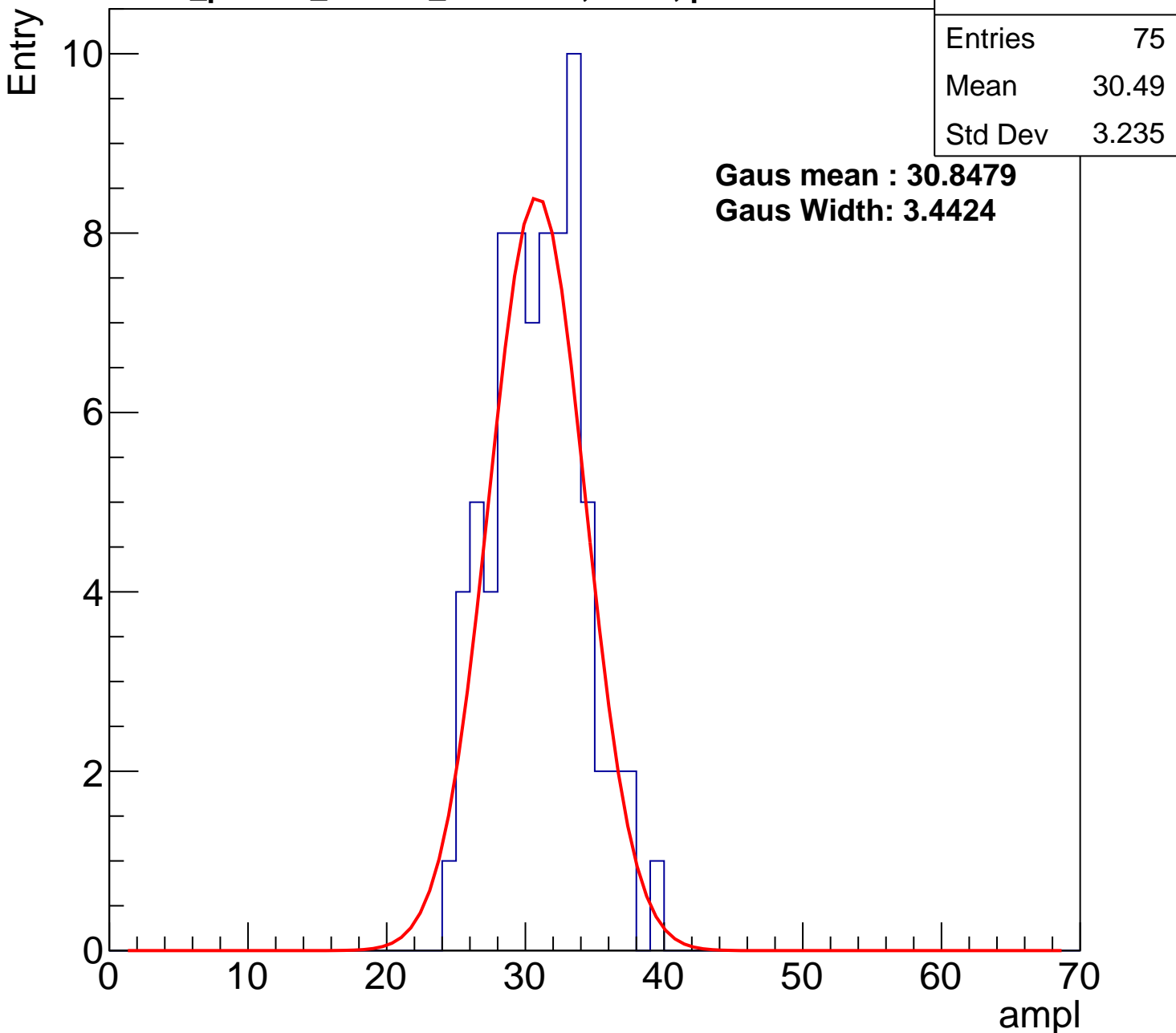
Gaus Width: 3.4424

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch109, adc1

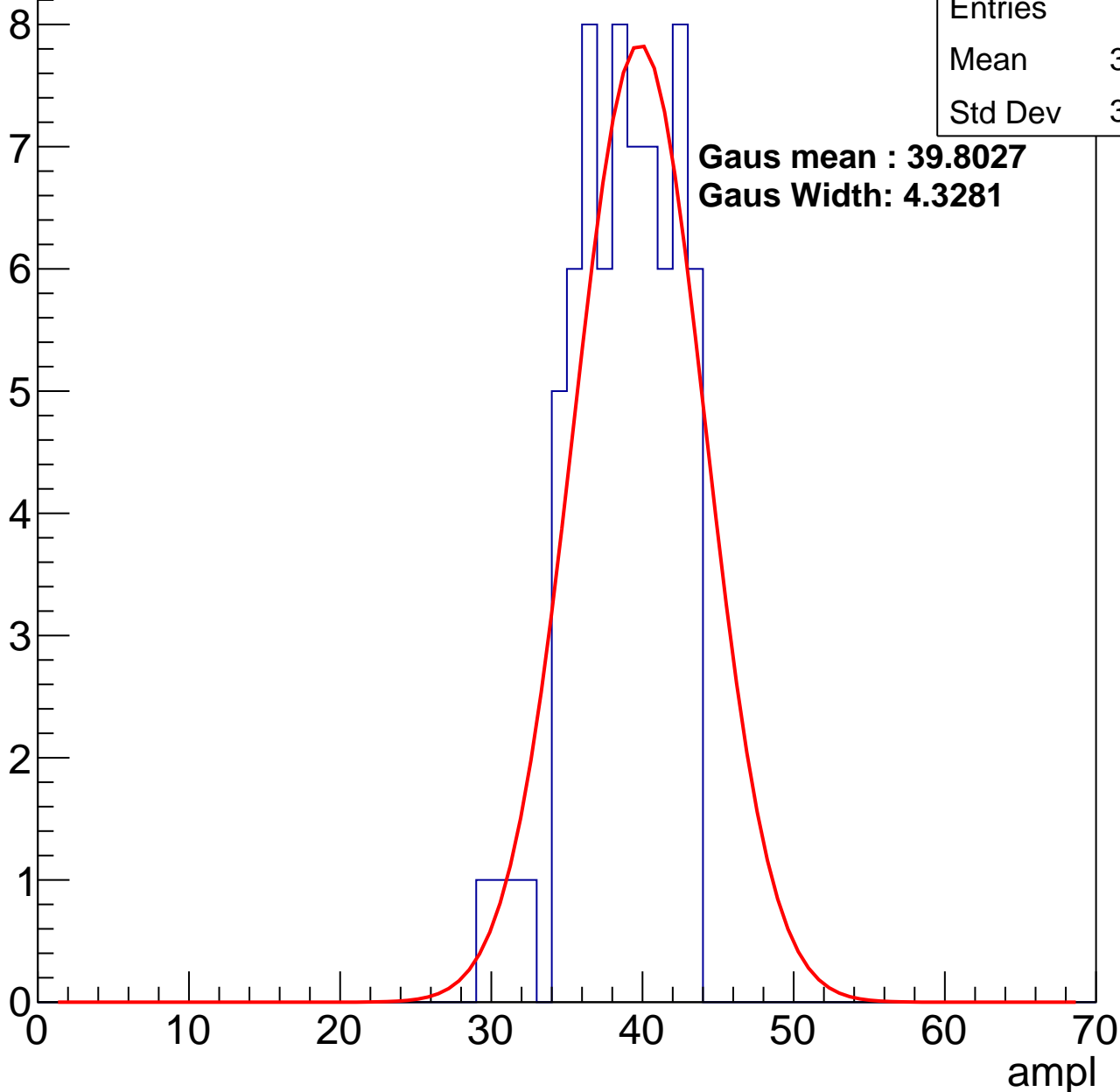
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.15
Std Dev	3.287

Gaus mean : 39.8027

Gaus Width: 4.3281



B1L100S, U10-ch109, adc2

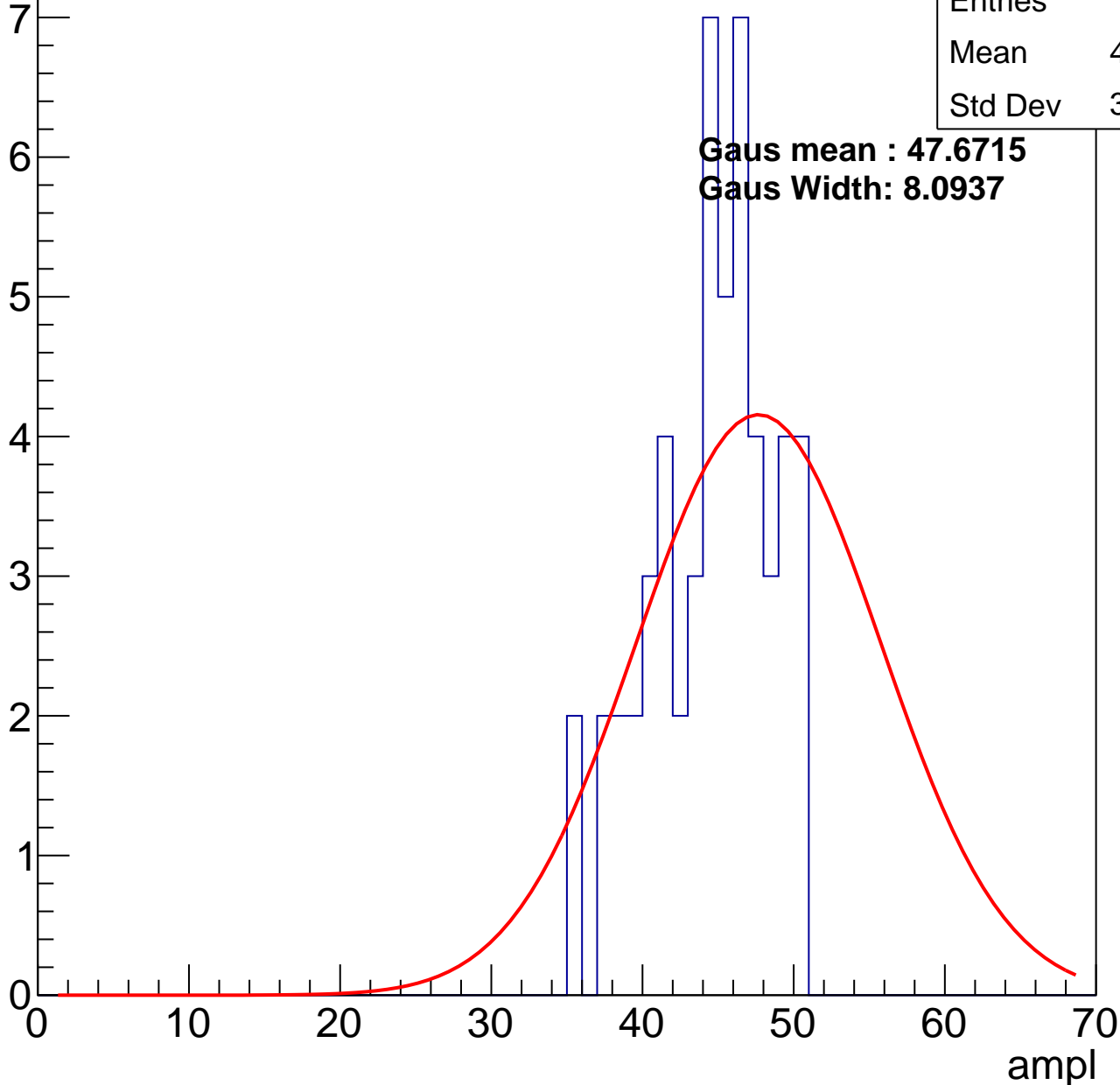
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	44.04
Std Dev	3.939

Gaus mean : 47.6715

Gaus Width: 8.0937

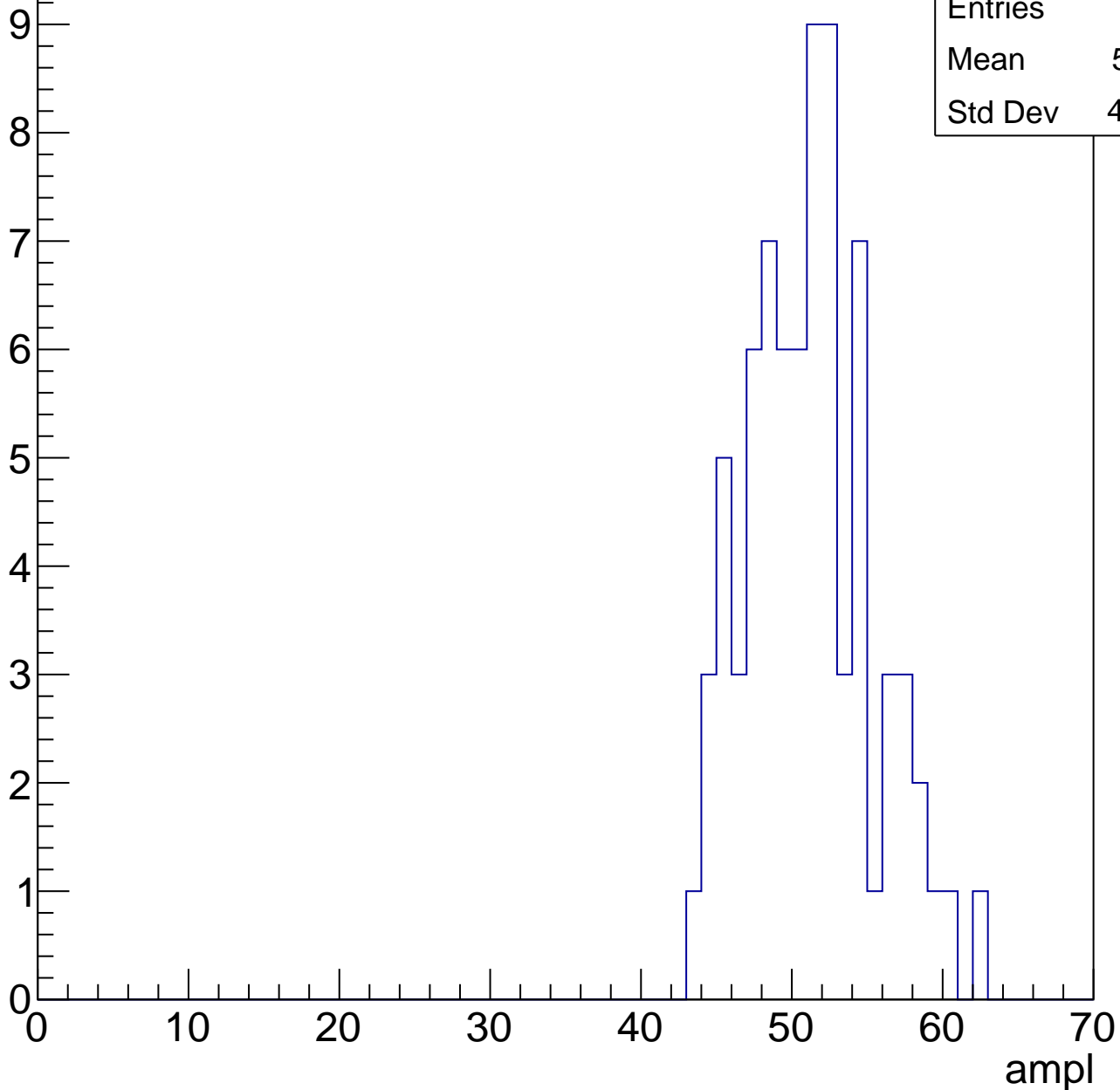


B1L100S, U10-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	50.71
Std Dev	4.102

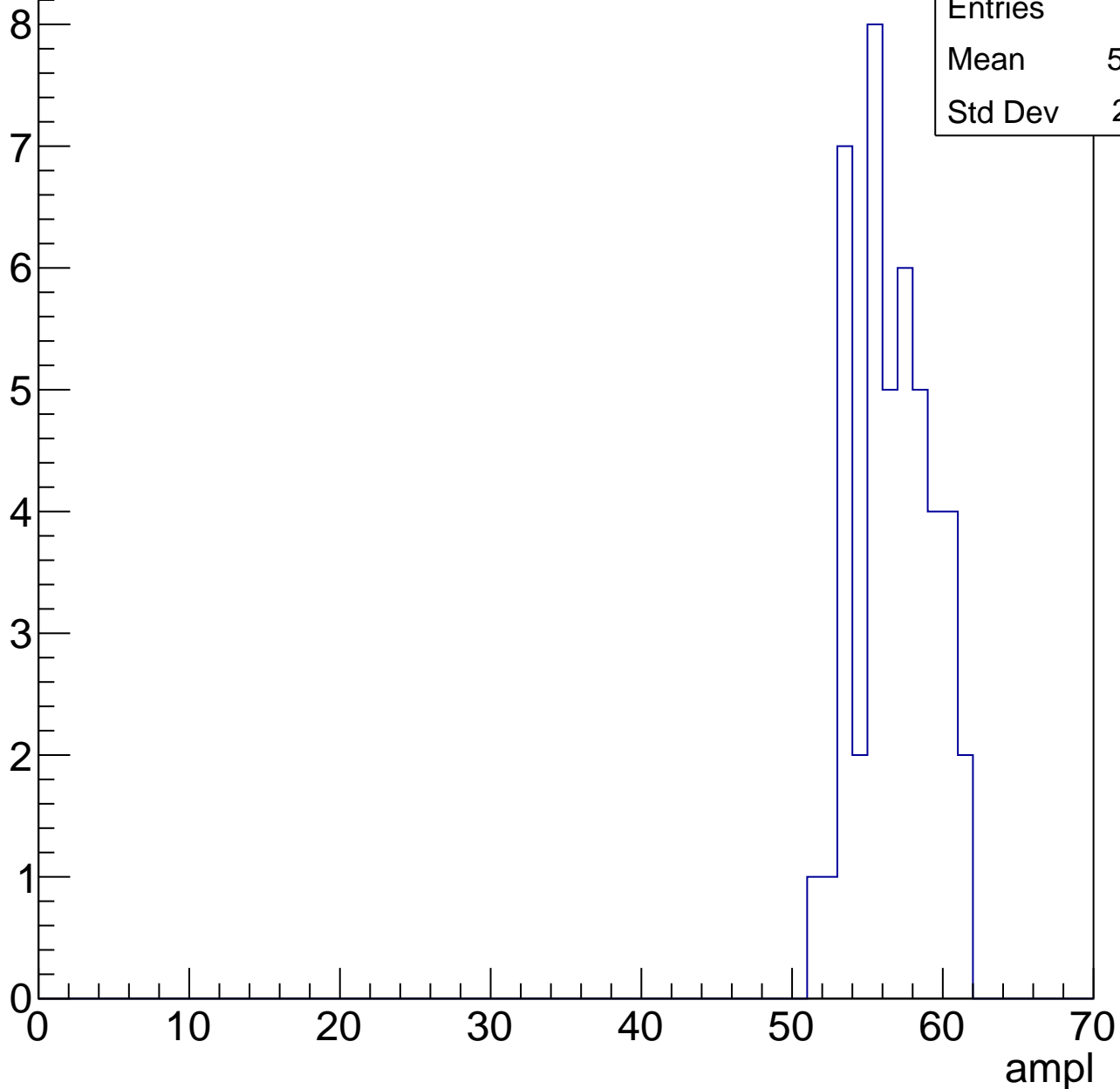


B1L100S, U10-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	56.27
Std Dev	2.551

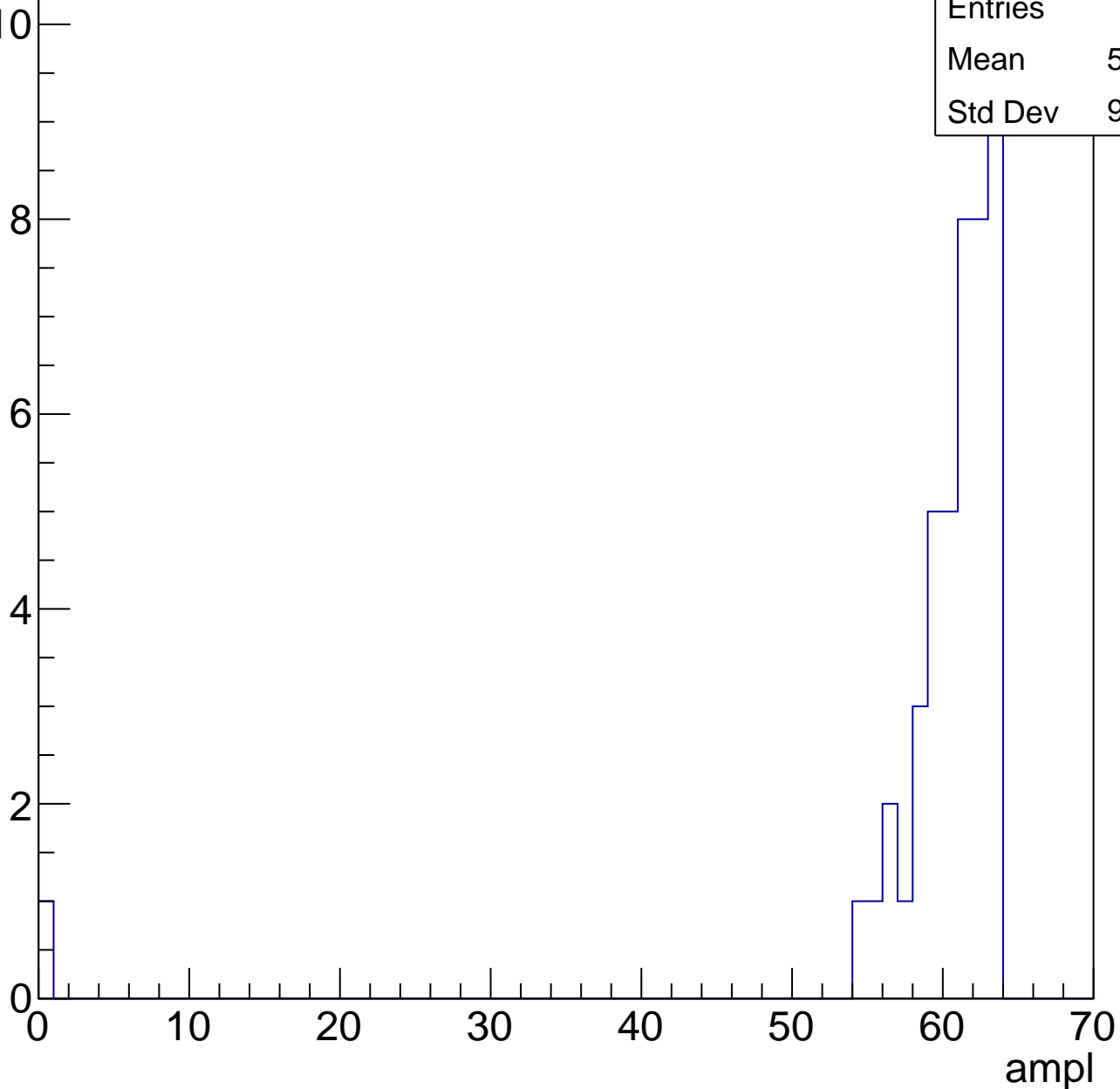


B1L100S, U10-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	59.13
Std Dev	9.208



B1L100S, U10-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U10-ch110, adc0

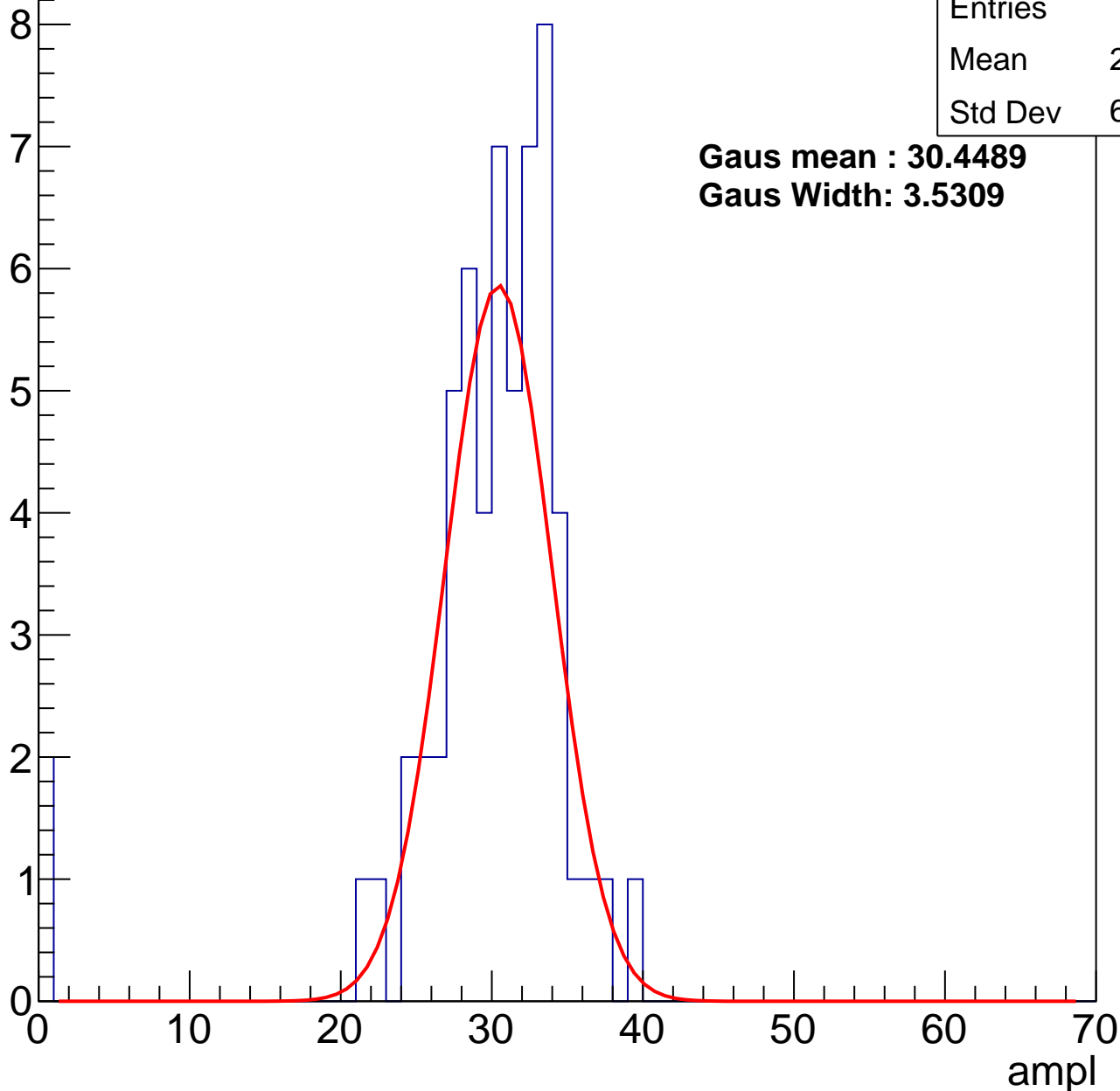
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.13
Std Dev	6.438

Gaus mean : 30.4489

Gaus Width: 3.5309



B1L100S, U10-ch110, adc1

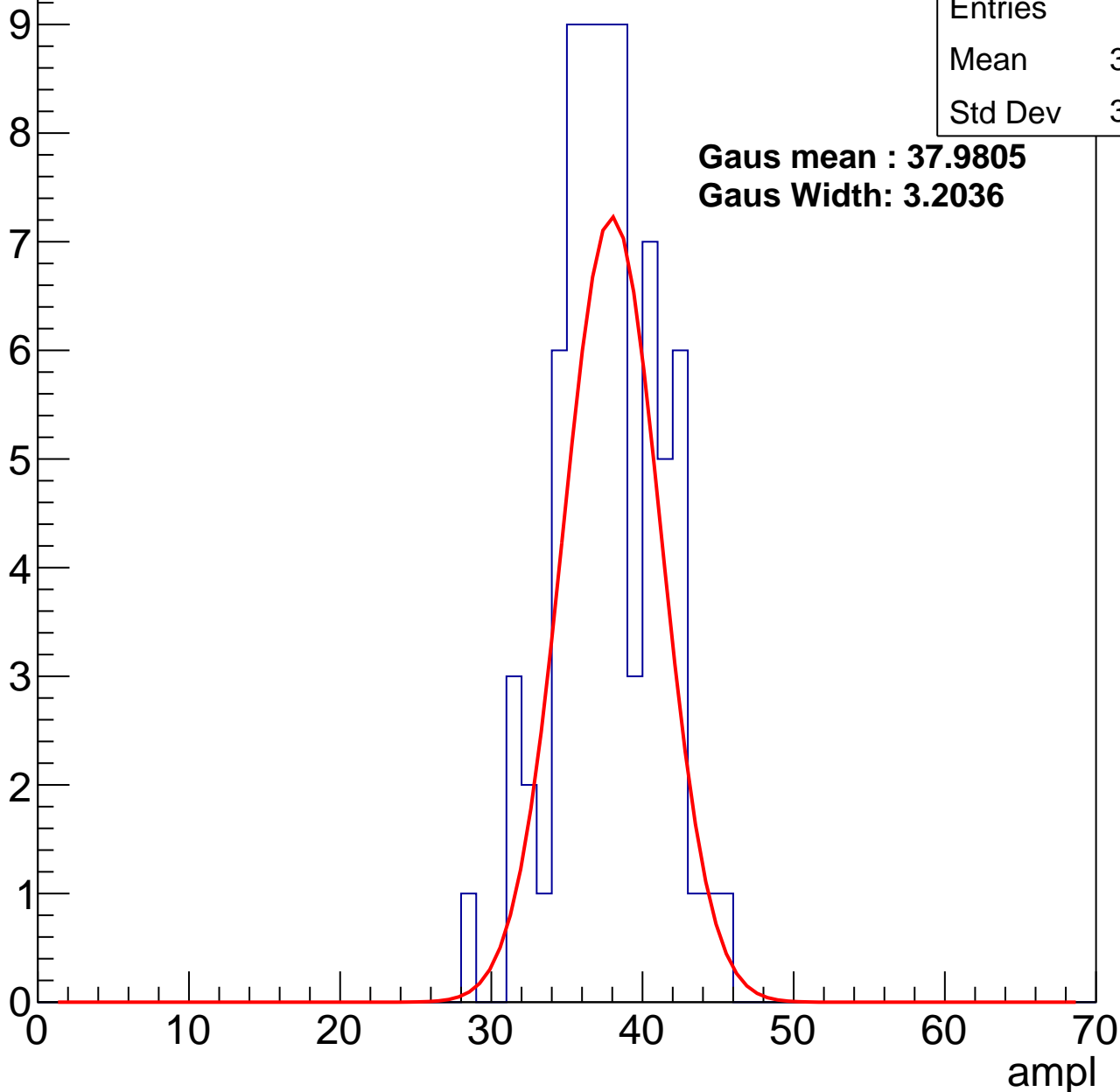
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.29
Std Dev	3.333

Gaus mean : 37.9805

Gaus Width: 3.2036



B1L100S, U10-ch110, adc2

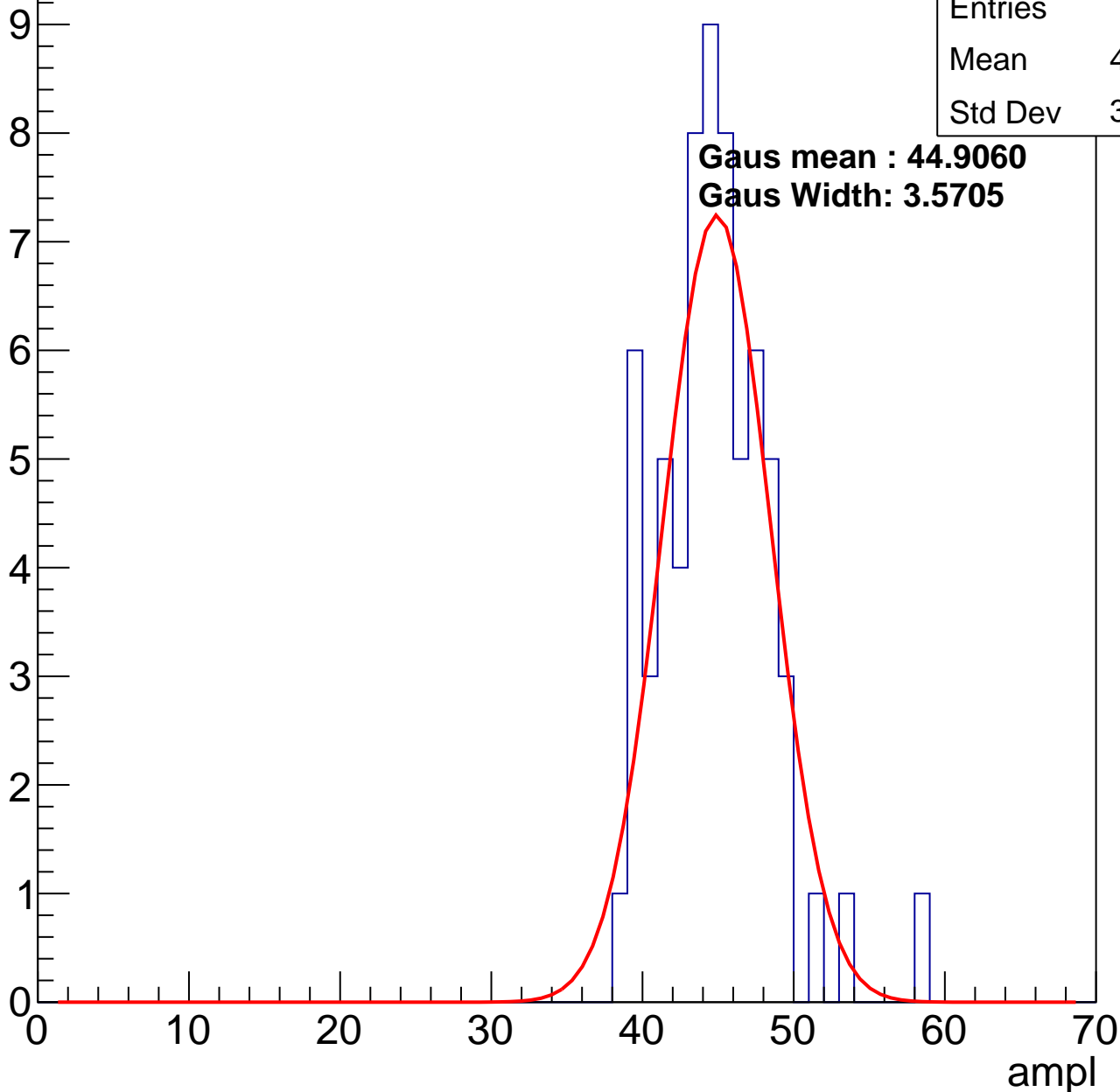
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.33
Std Dev	3.607

Gaus mean : 44.9060

Gaus Width: 3.5705

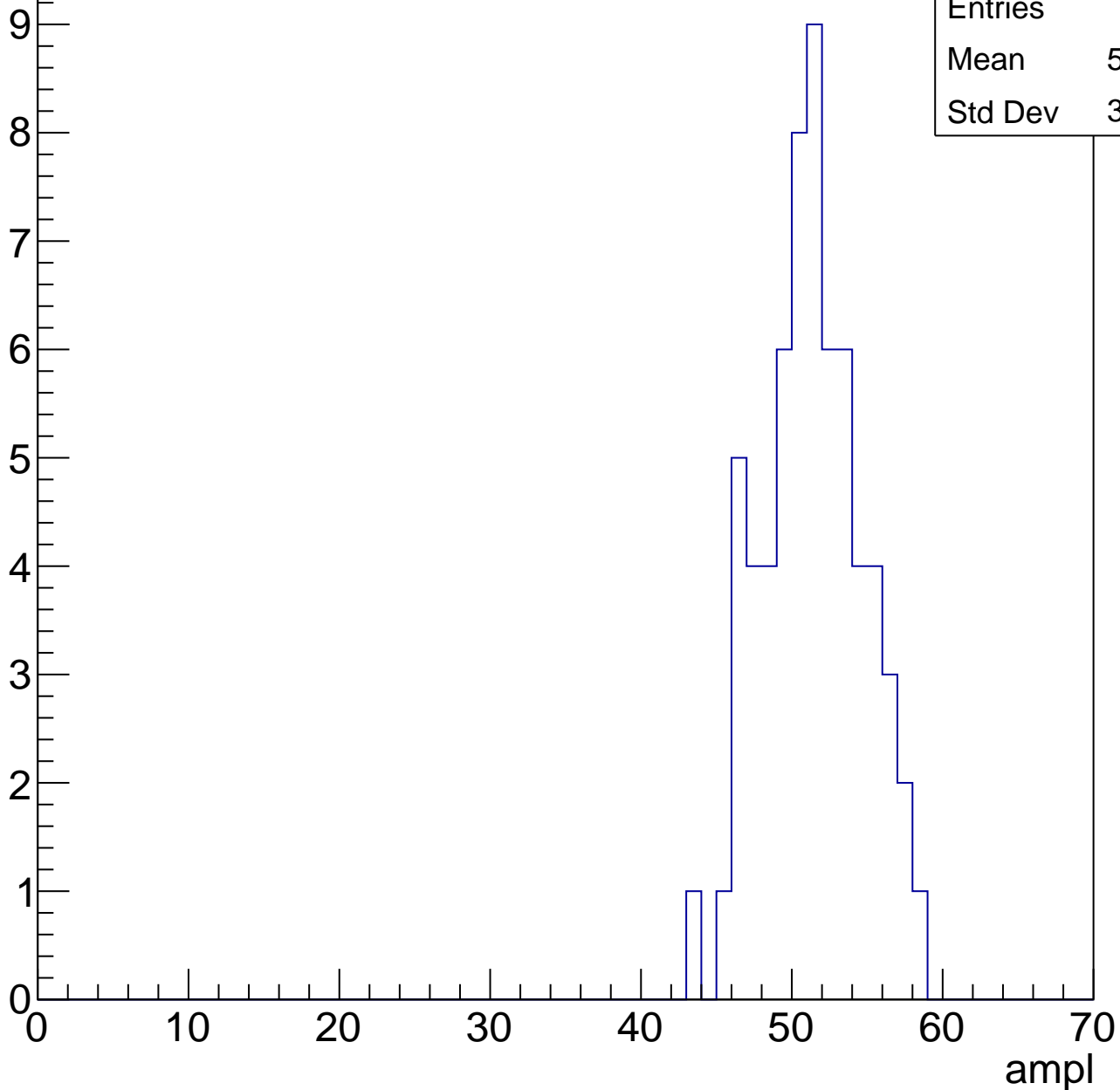


B1L100S, U10-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	50.89
Std Dev	3.265

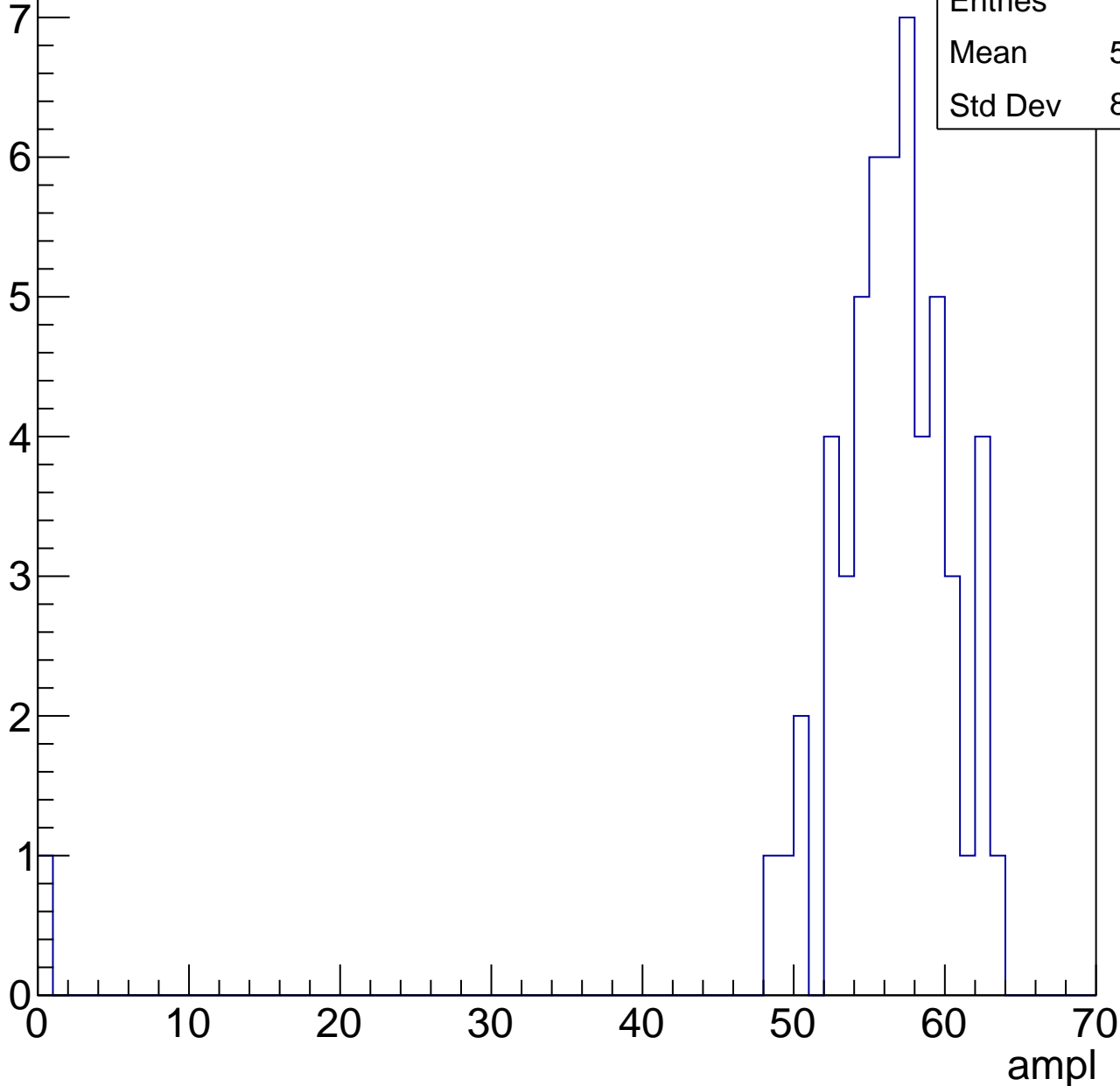


B1L100S, U10-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	55.15
Std Dev	8.314

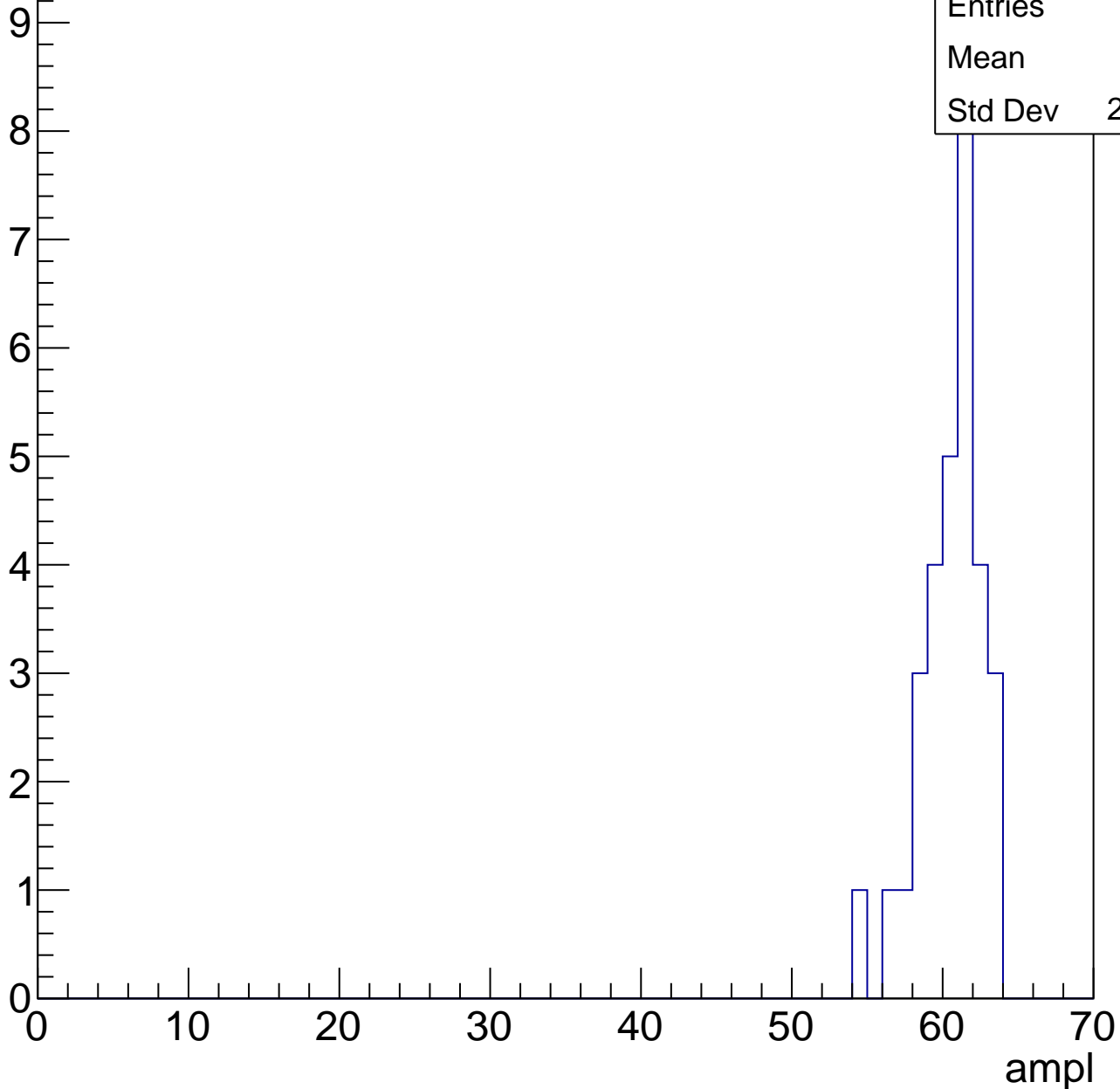


B1L100S, U10-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	60.1
Std Dev	2.038

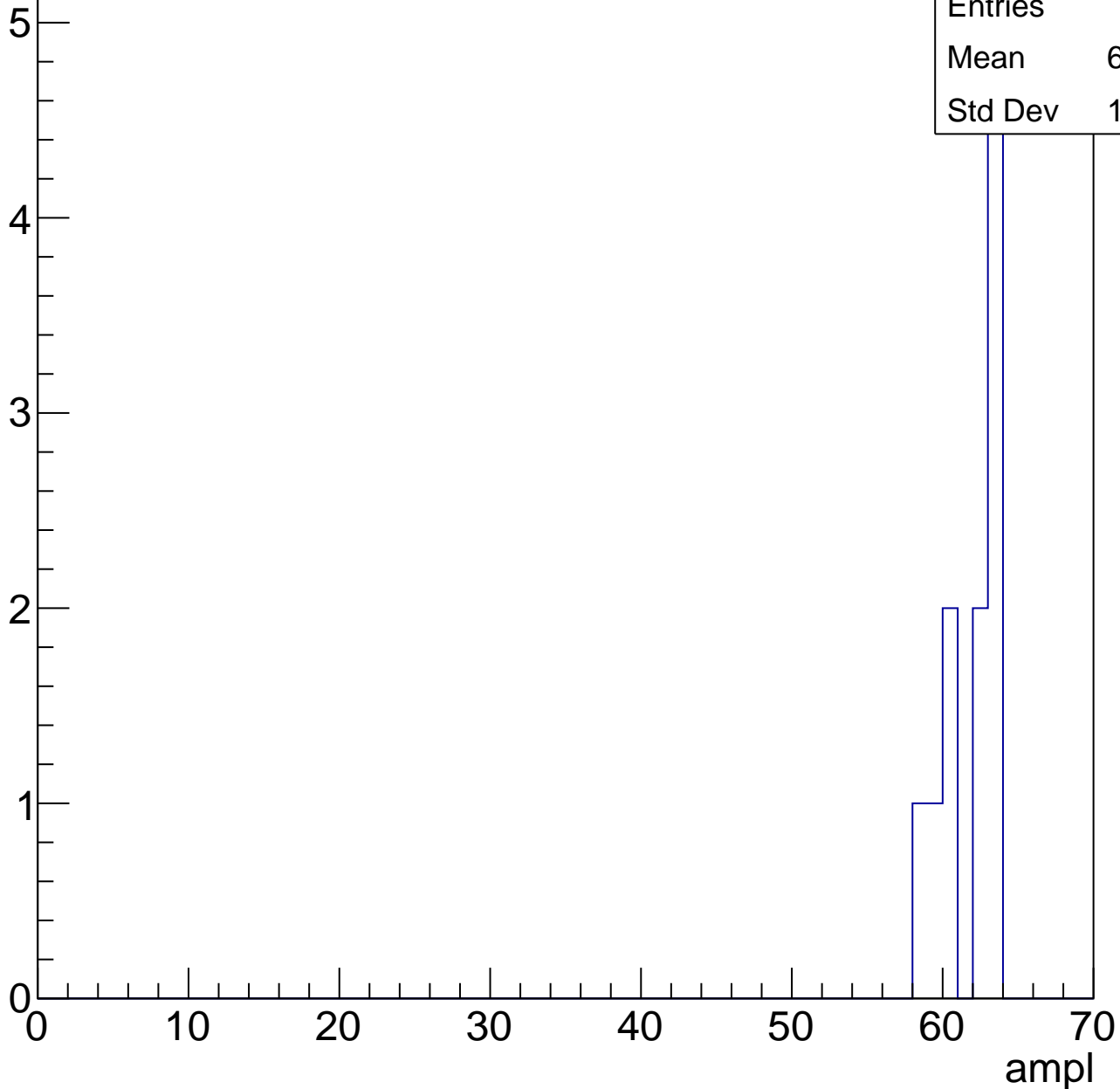


B1L100S, U10-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

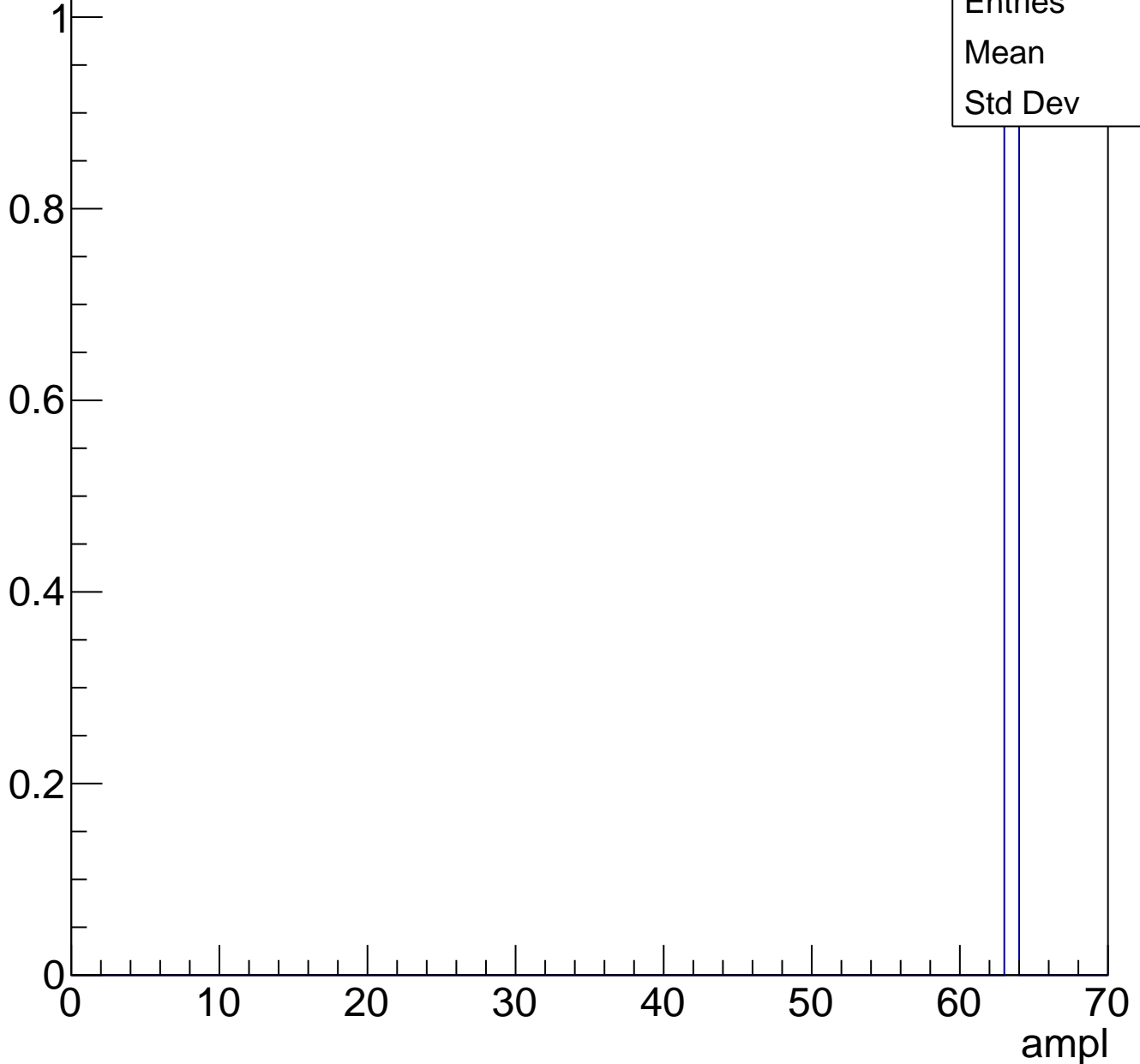
Entries	11
Mean	61.45
Std Dev	1.777



B1L100S, U10-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch111, adc0

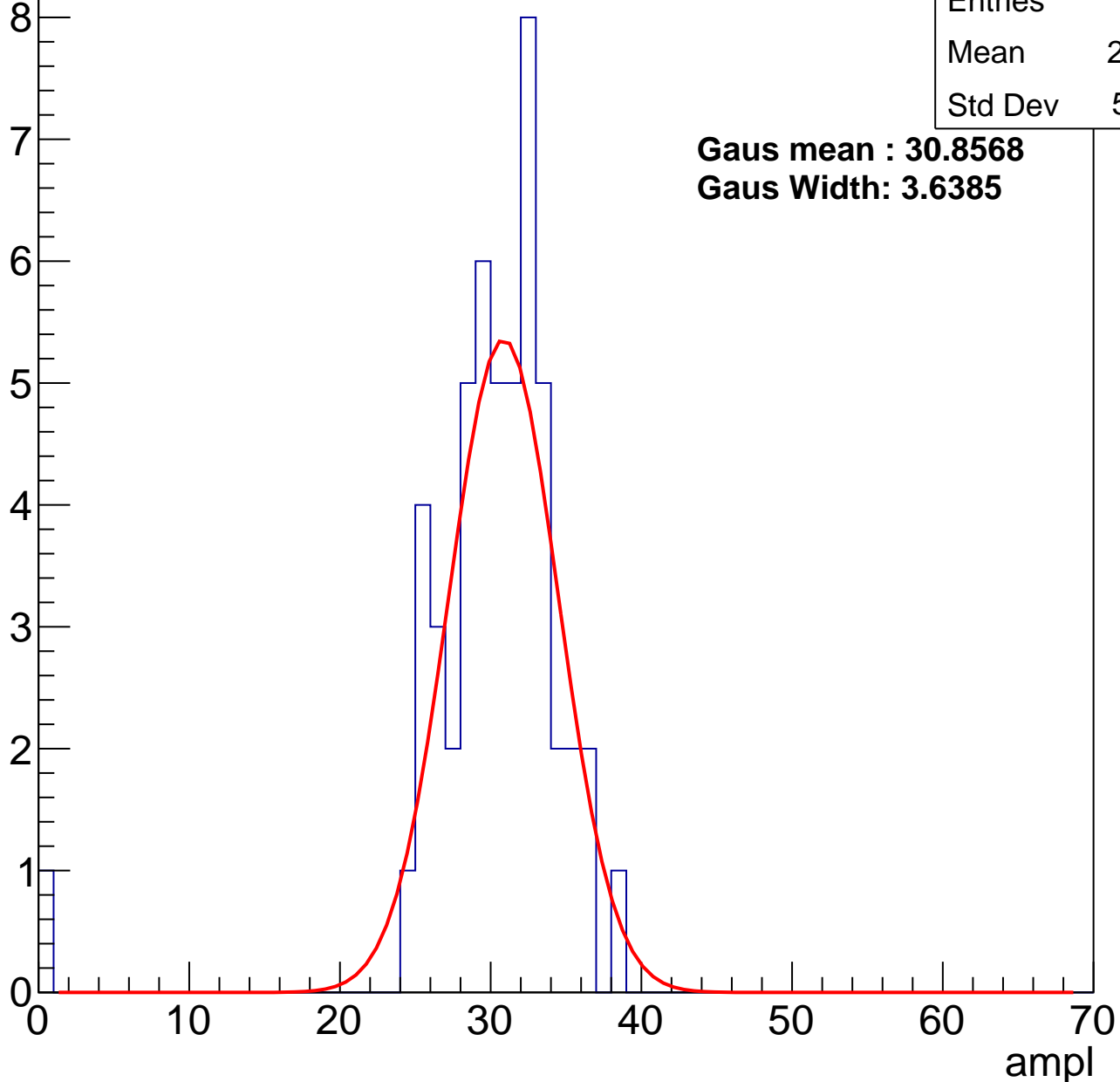
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	29.69
Std Dev	5.231

Gaus mean : 30.8568

Gaus Width: 3.6385



B1L100S, U10-ch111, adc1

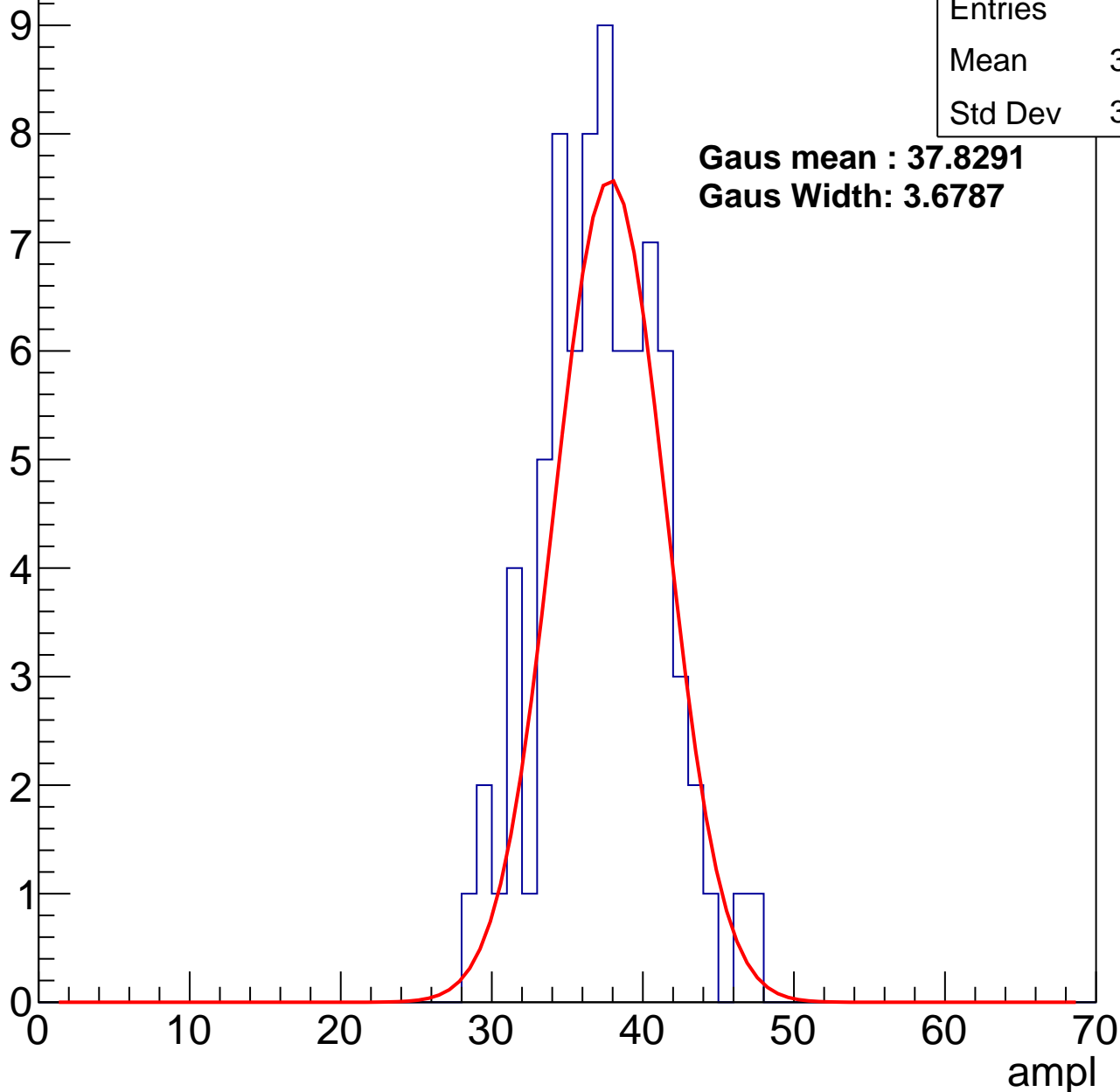
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.88
Std Dev	3.883

Gaus mean : 37.8291

Gaus Width: 3.6787



B1L100S, U10-ch111, adc2

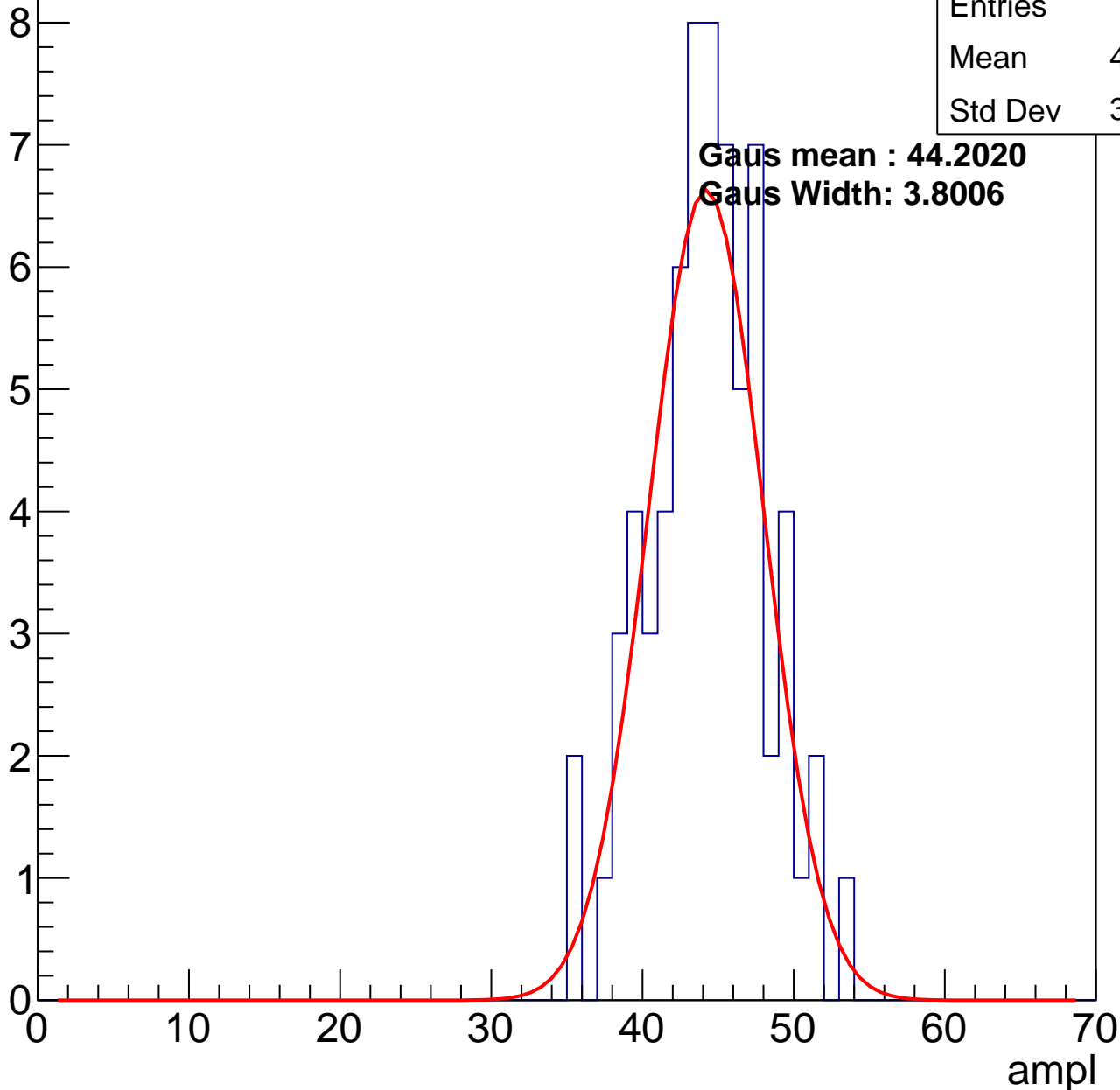
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.82
Std Dev	3.777

Gaus mean : 44.2020

Gaus Width: 3.8006

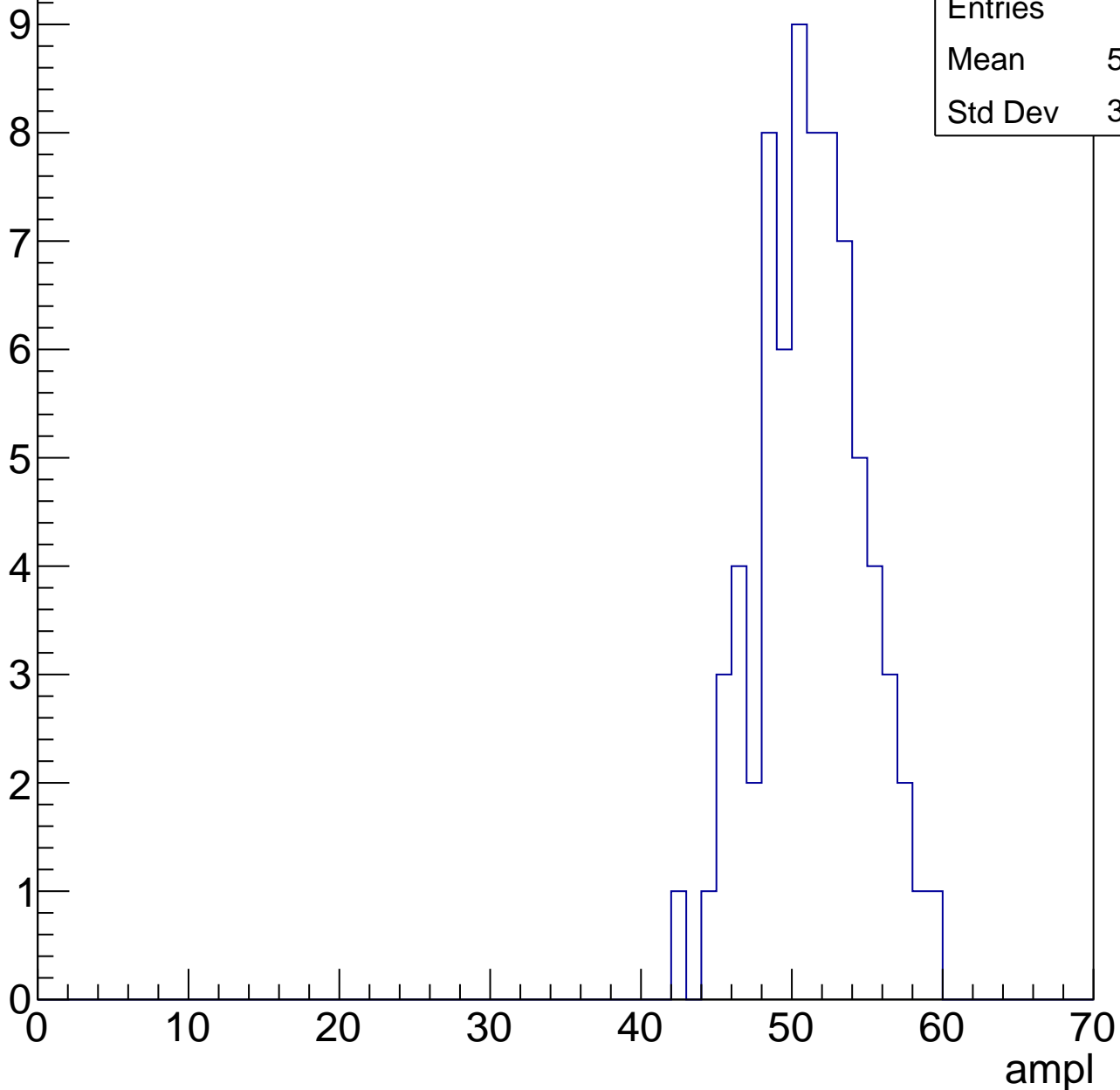


B1L100S, U10-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

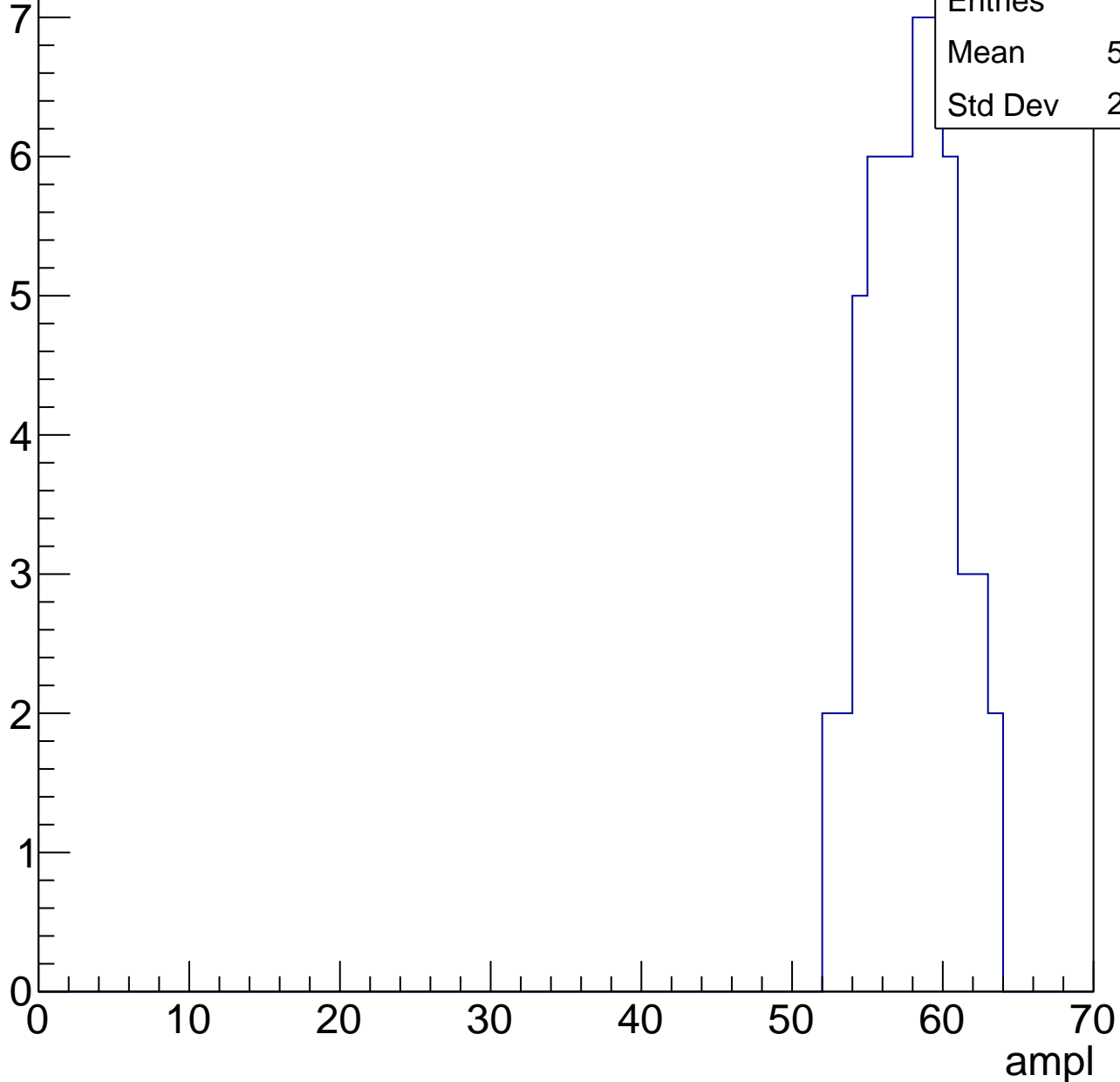
Entries	73
Mean	50.84
Std Dev	3.468



B1L100S, U10-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



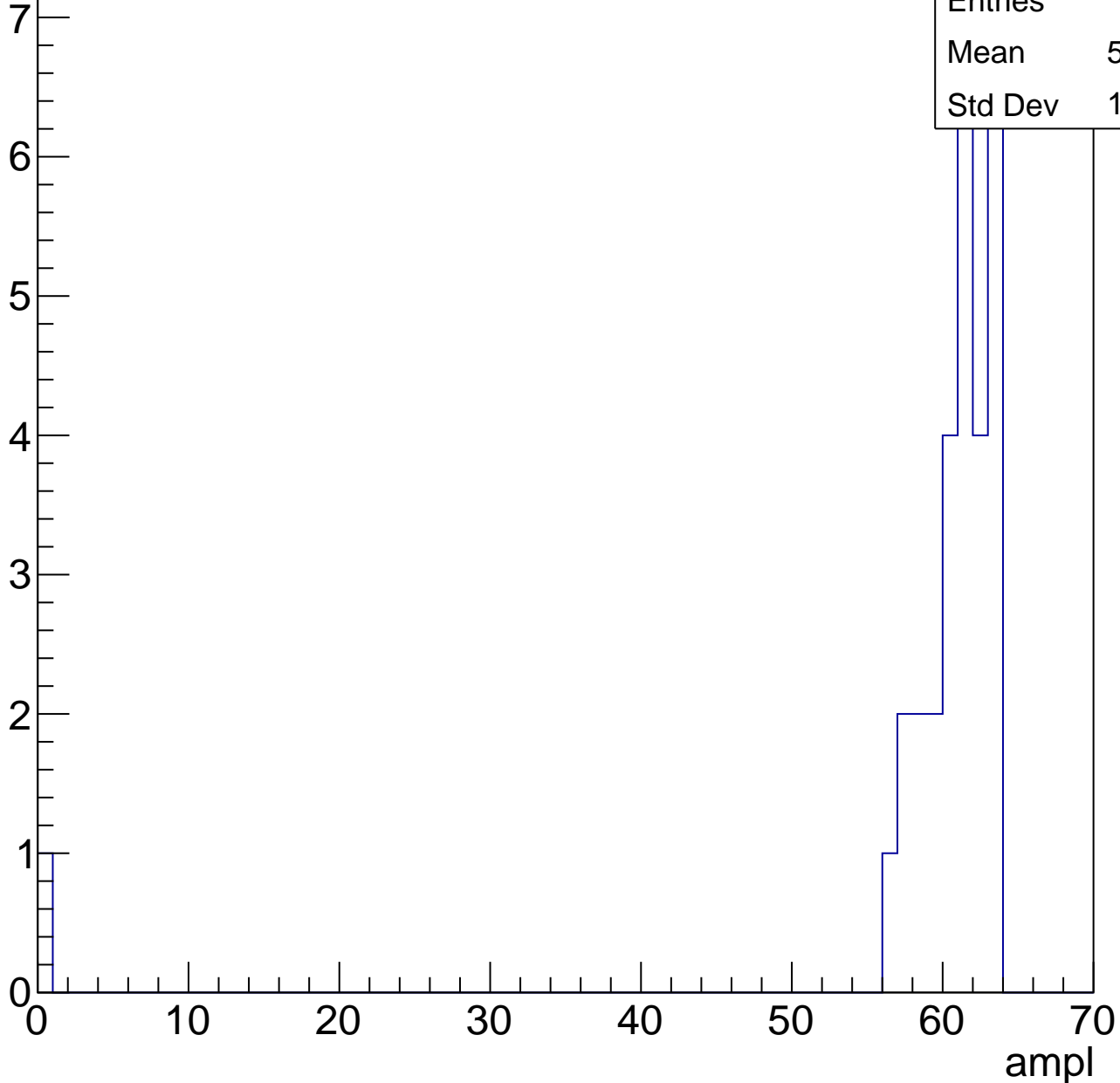
Entries	55
Mean	57.49
Std Dev	2.789

B1L100S, U10-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.67
Std Dev	11.07



B1L100S, U10-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L100S, U10-ch112, adc0

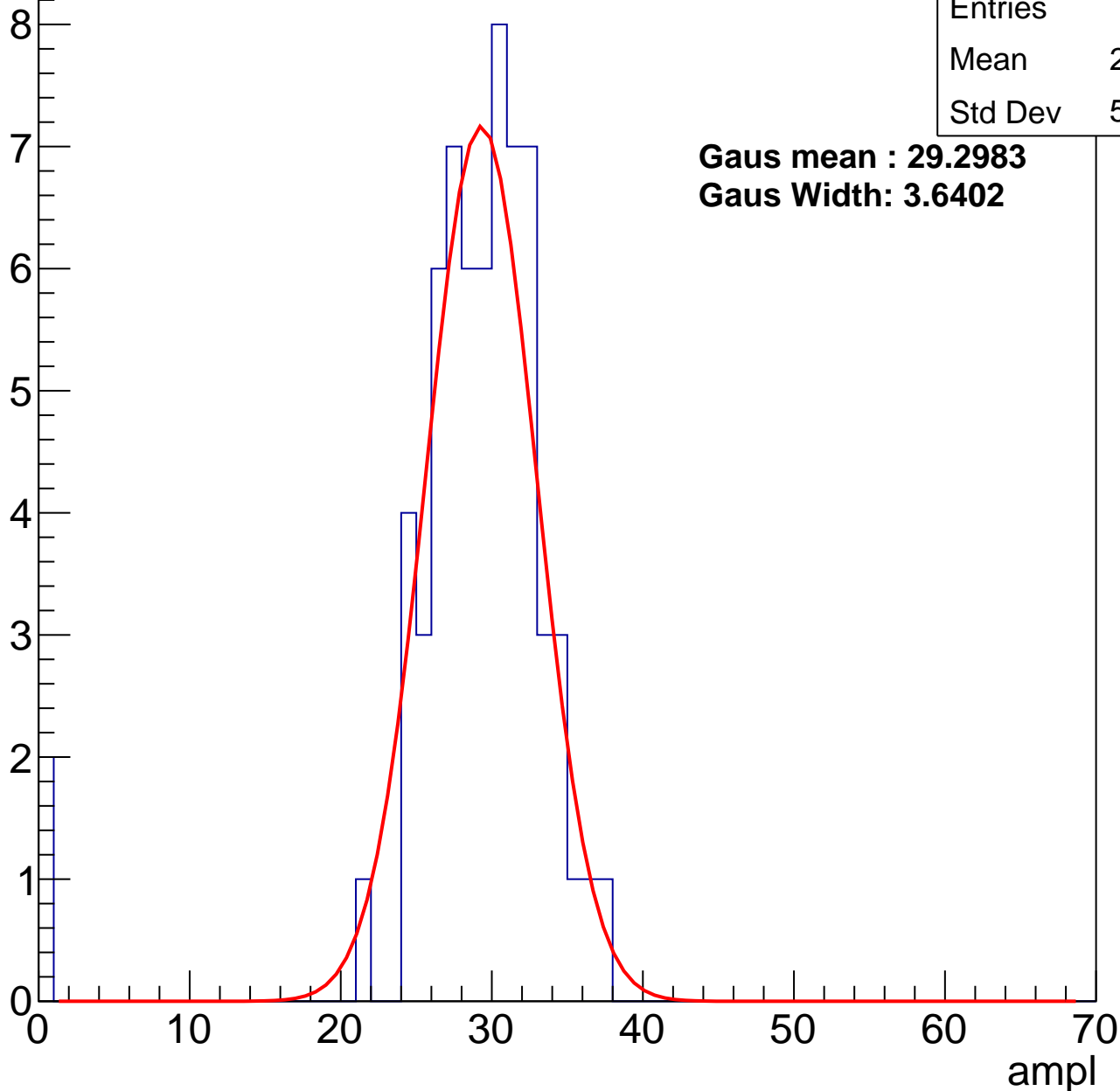
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.32
Std Dev	5.932

Gaus mean : 29.2983

Gaus Width: 3.6402



B1L100S, U10-ch112, adc1

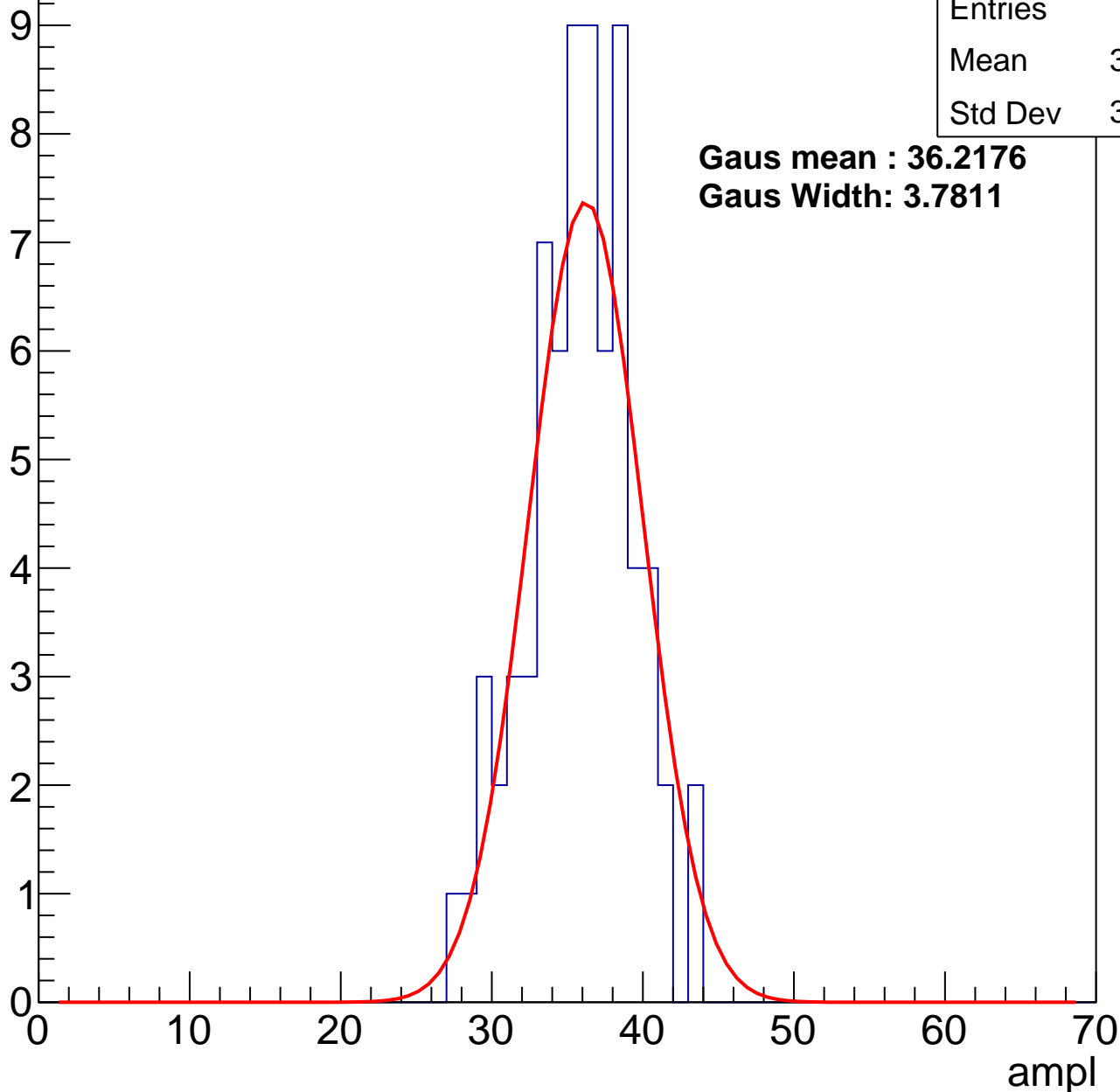
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.39
Std Dev	3.454

Gaus mean : 36.2176

Gaus Width: 3.7811



B1L100S, U10-ch112, adc2

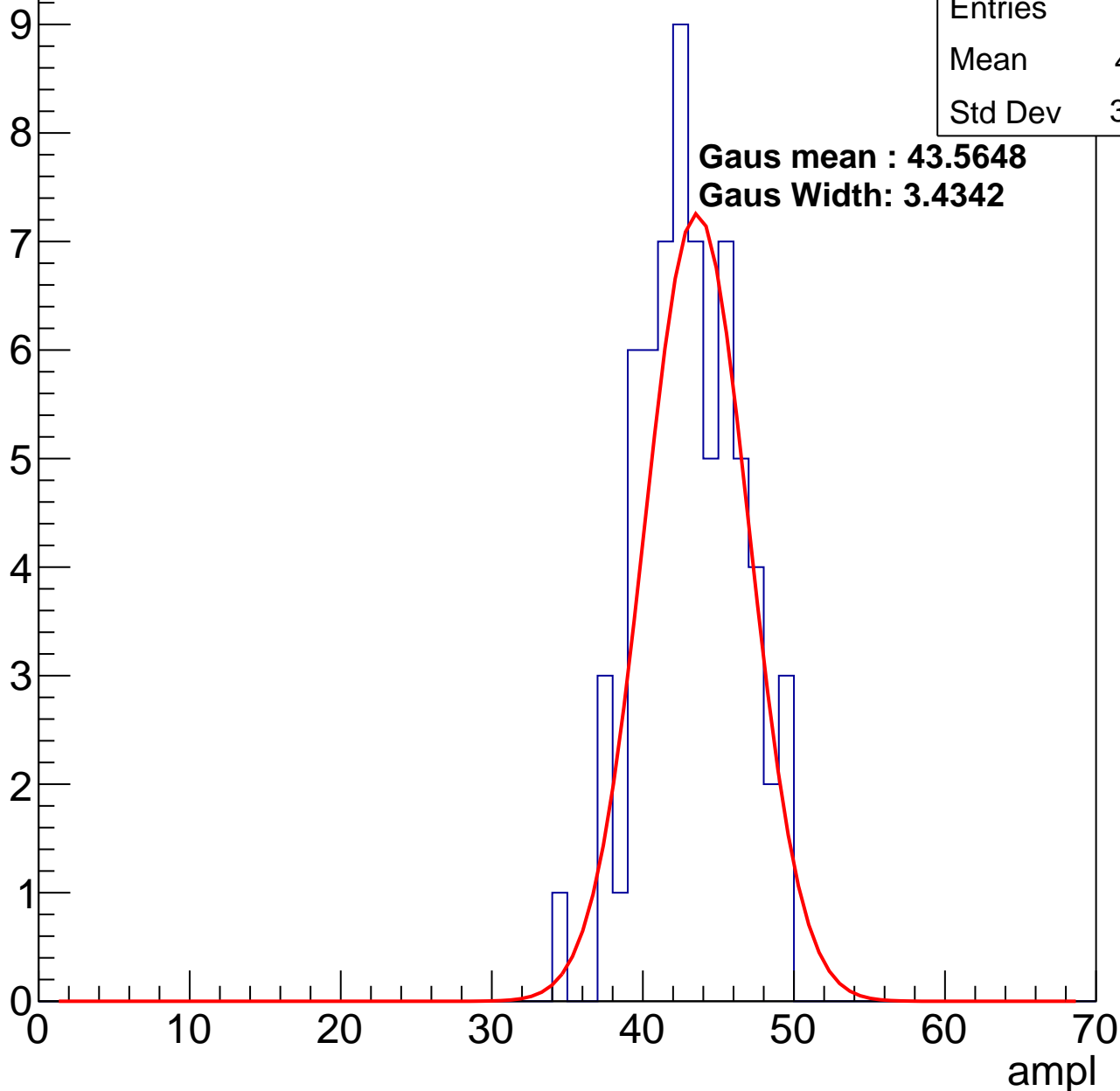
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.71
Std Dev	3.246

Gaus mean : 43.5648

Gaus Width: 3.4342

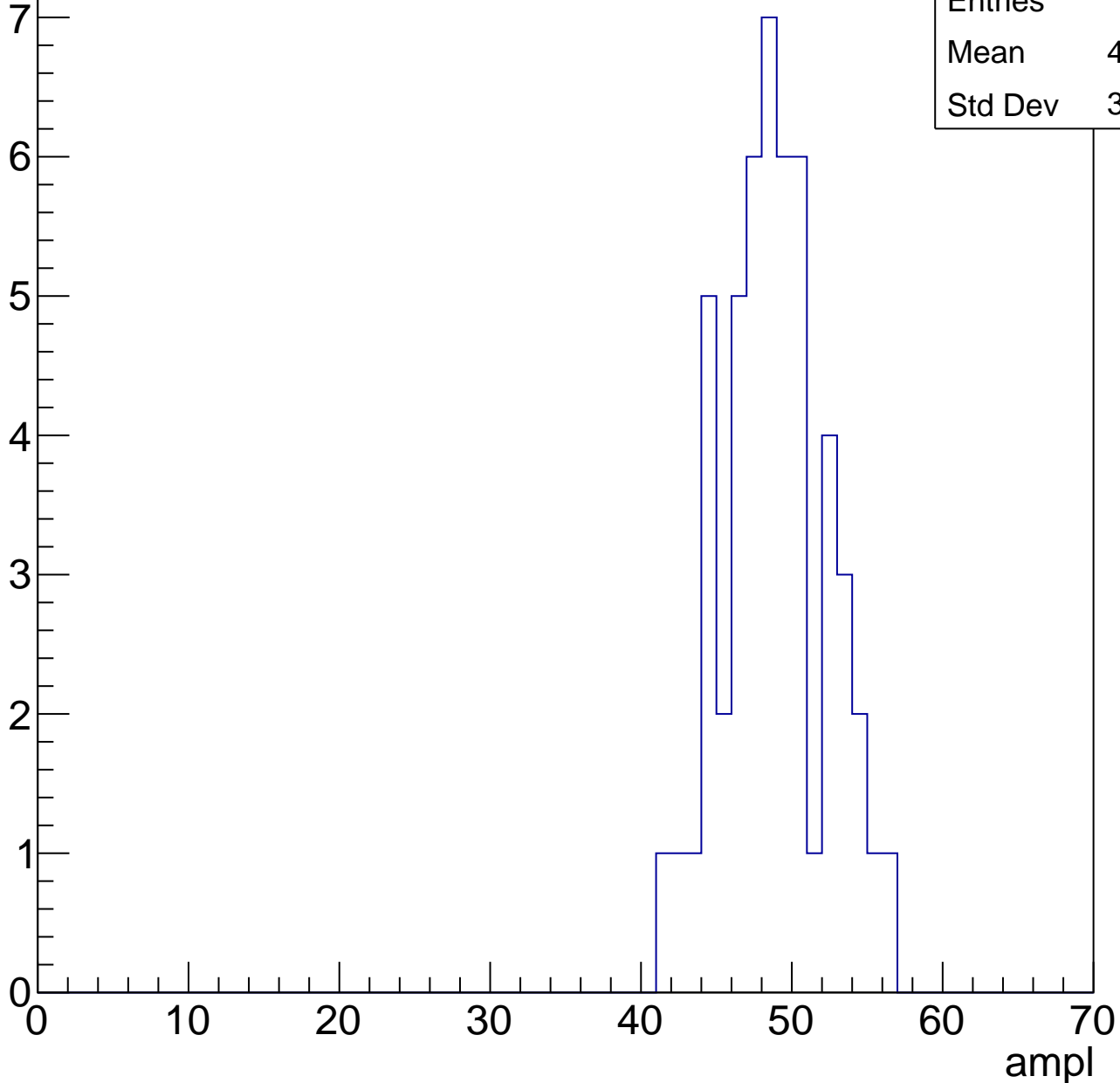


B1L100S, U10-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	48.37
Std Dev	3.369

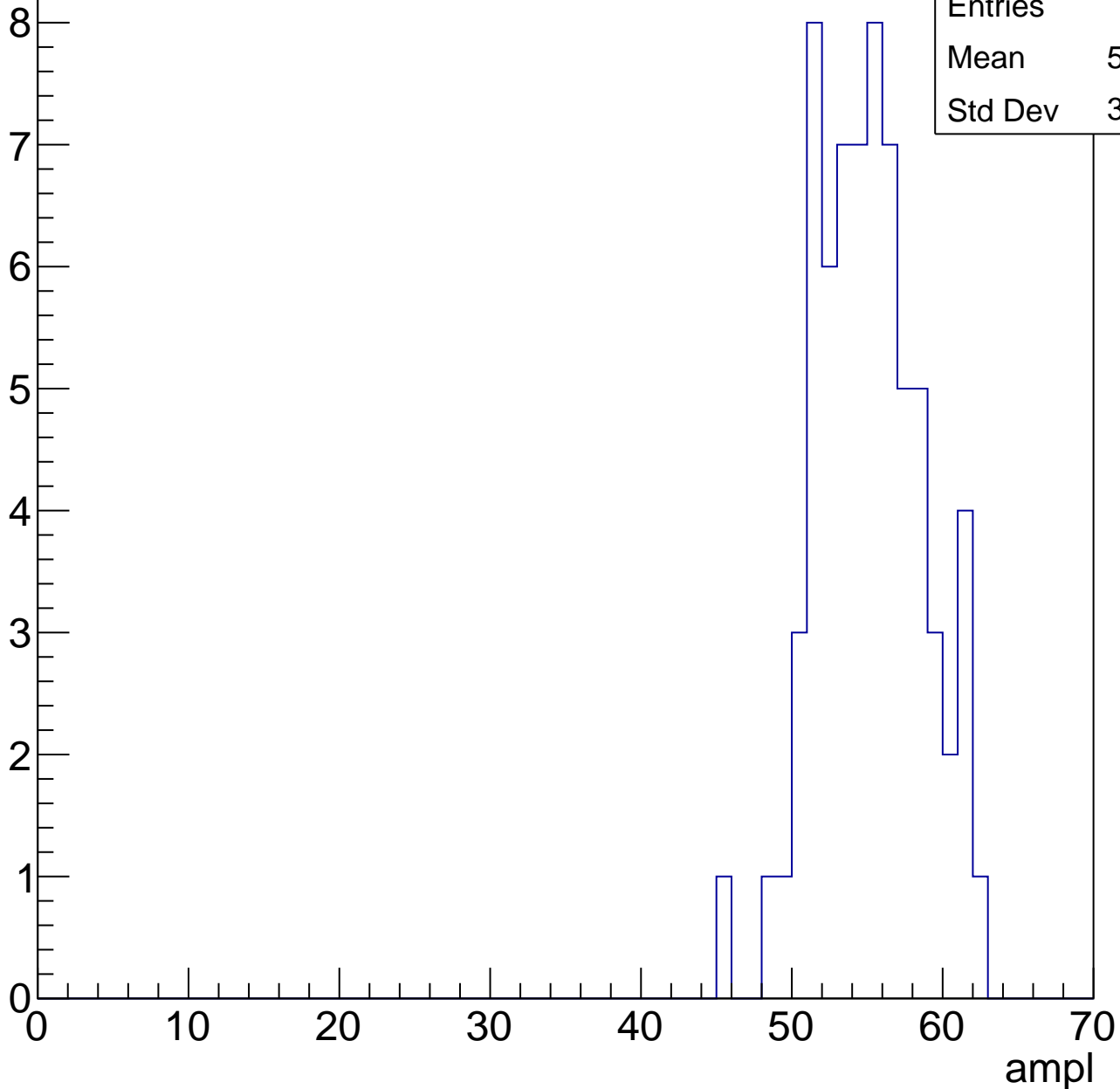


B1L100S, U10-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	54.65
Std Dev	3.472

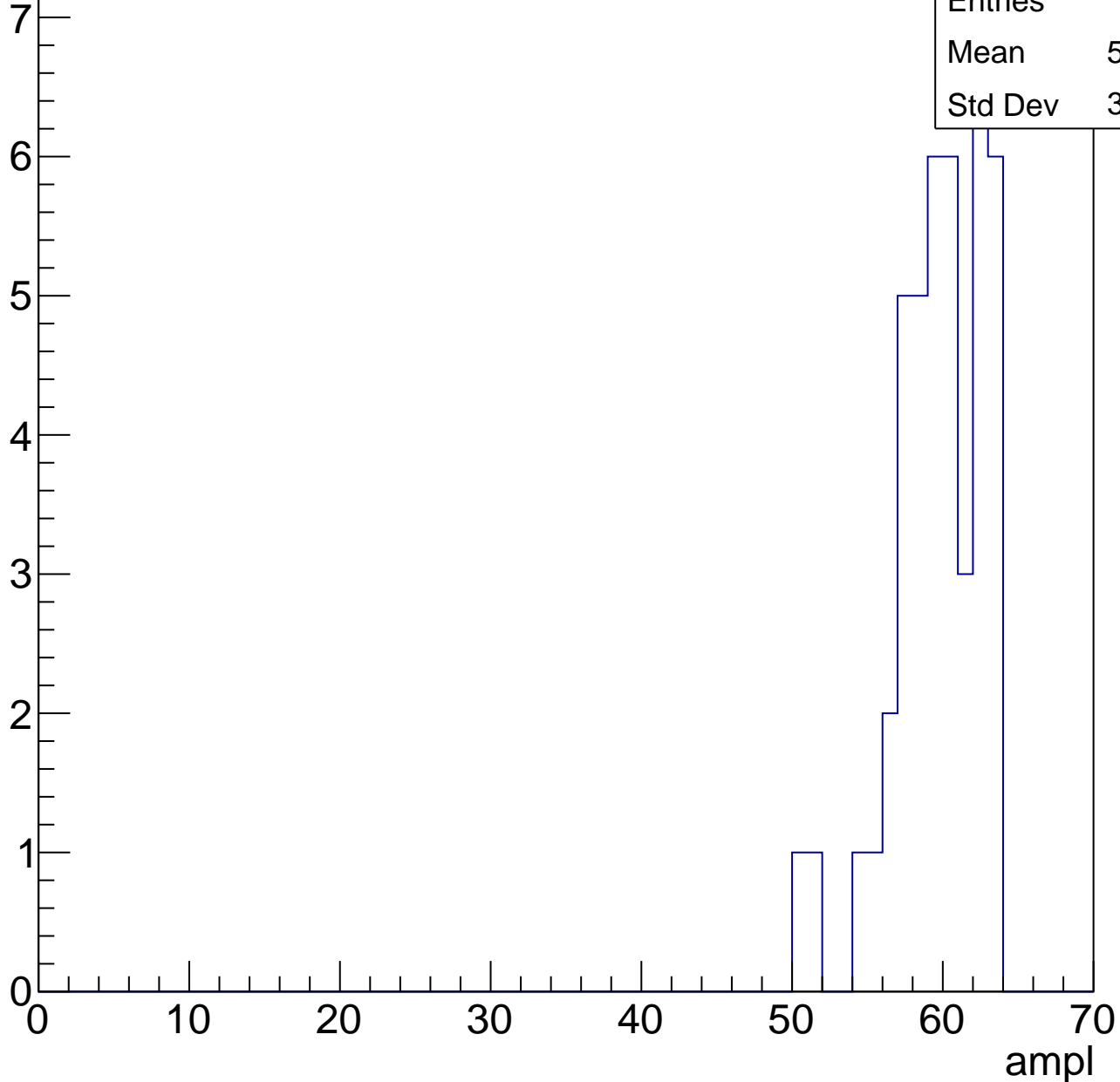


B1L100S, U10-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

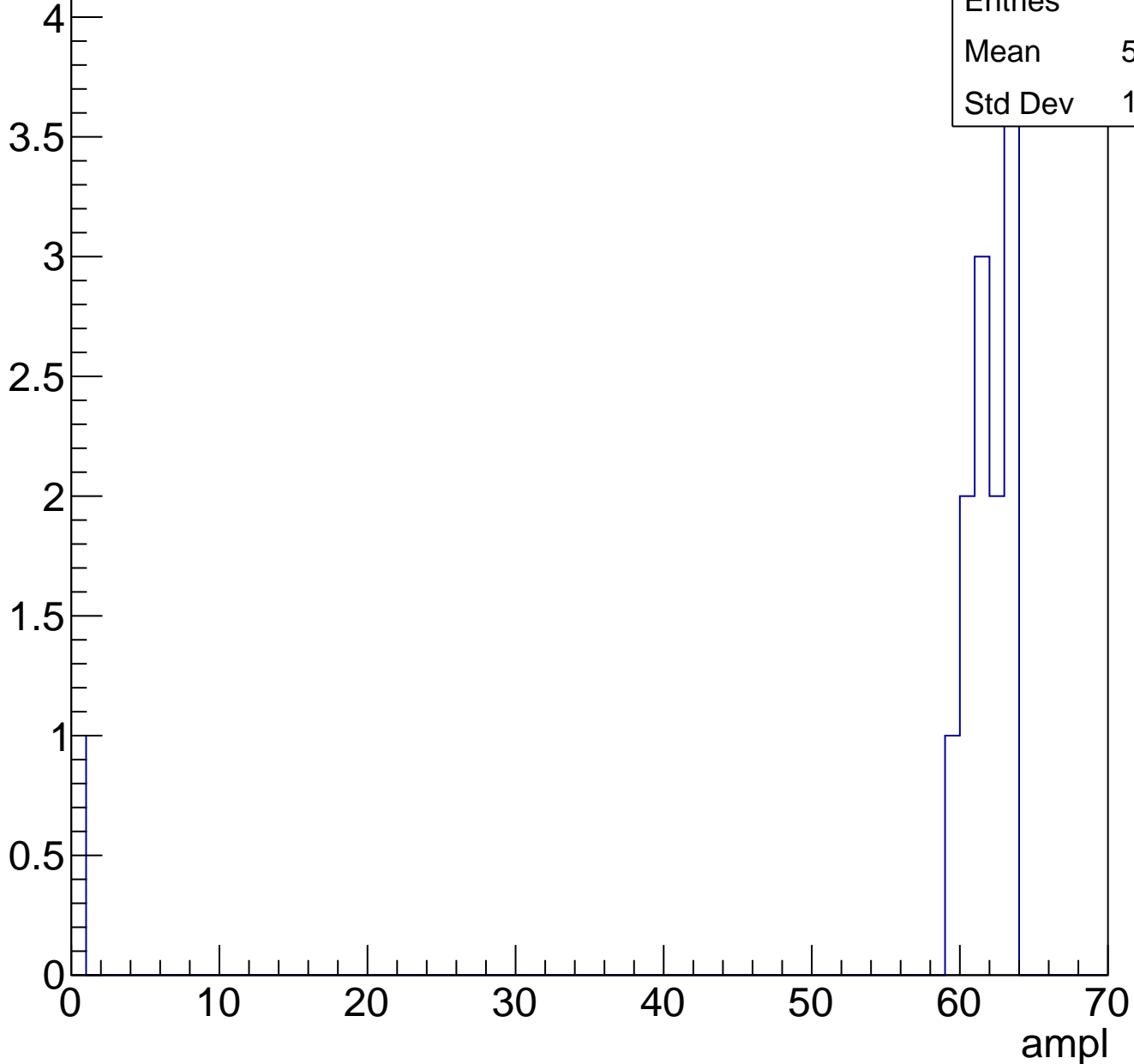
Entries	44
Mean	59.23
Std Dev	3.029



B1L100S, U10-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	13
Mean	56.77
Std Dev	16.44

B1L100S, U10-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch113, adc0

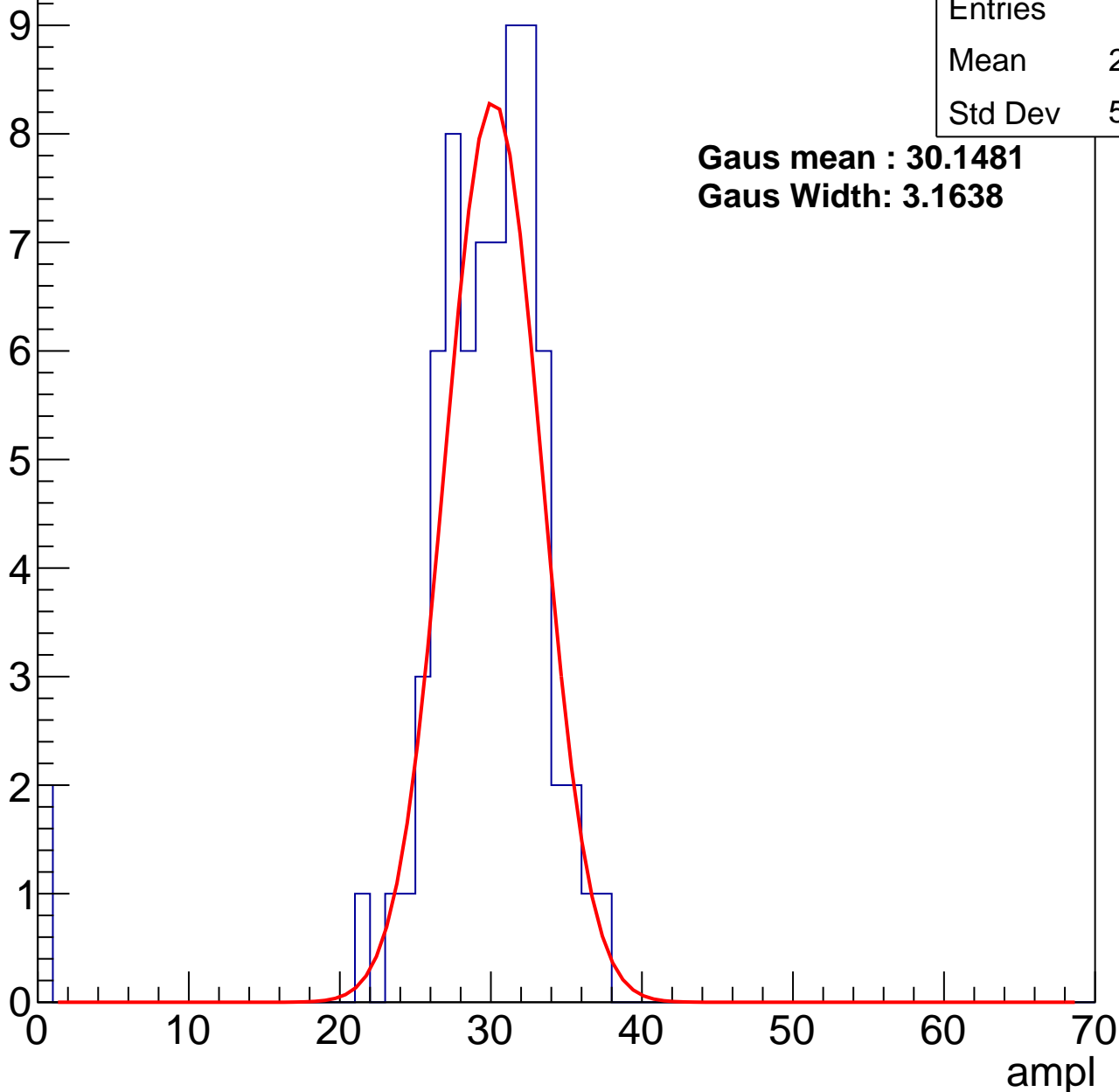
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.78
Std Dev	5.772

Gaus mean : 30.1481

Gaus Width: 3.1638



B1L100S, U10-ch113, adc1

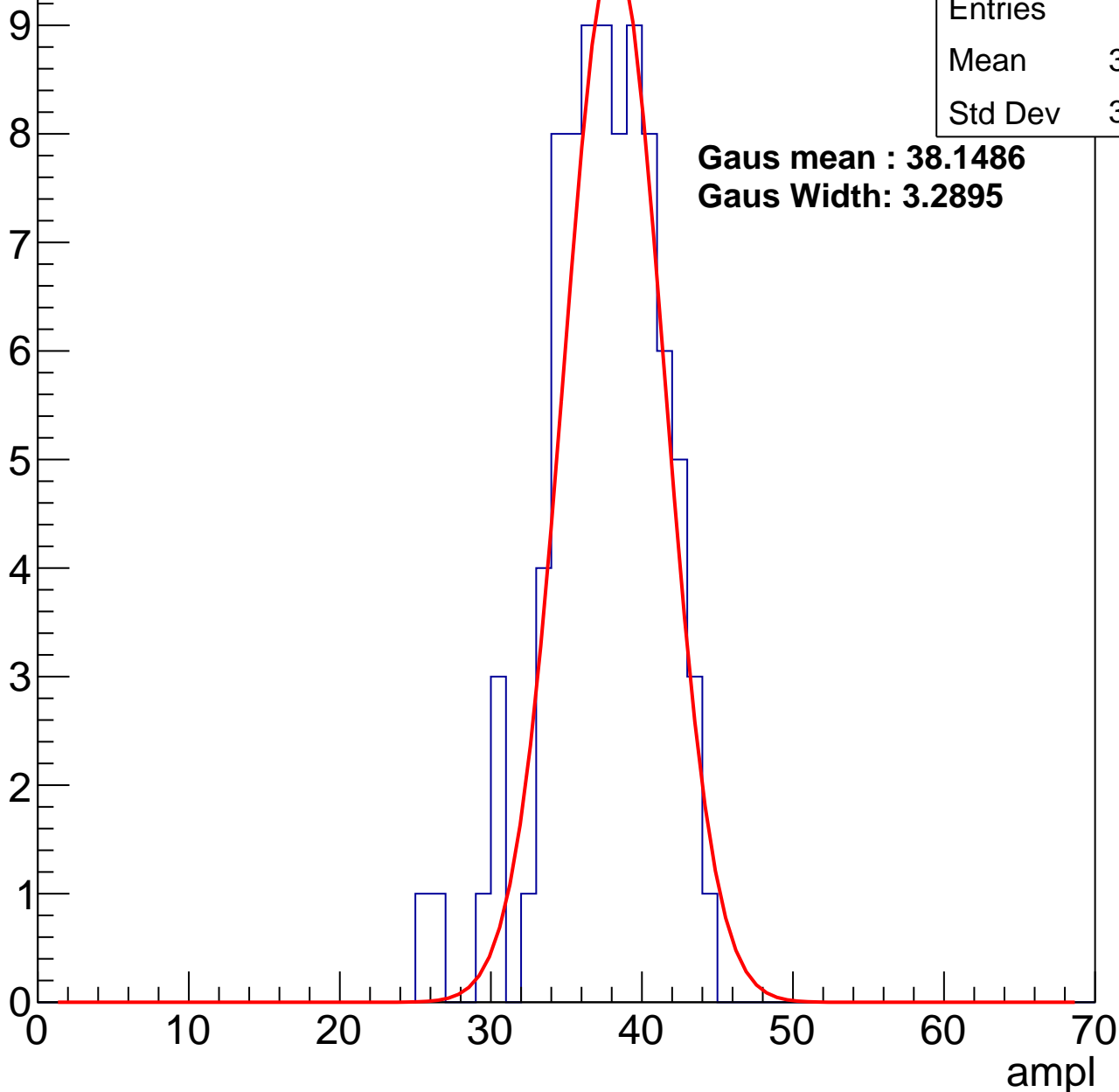
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	37.02
Std Dev	3.713

Gaus mean : 38.1486

Gaus Width: 3.2895



B1L100S, U10-ch113, adc2

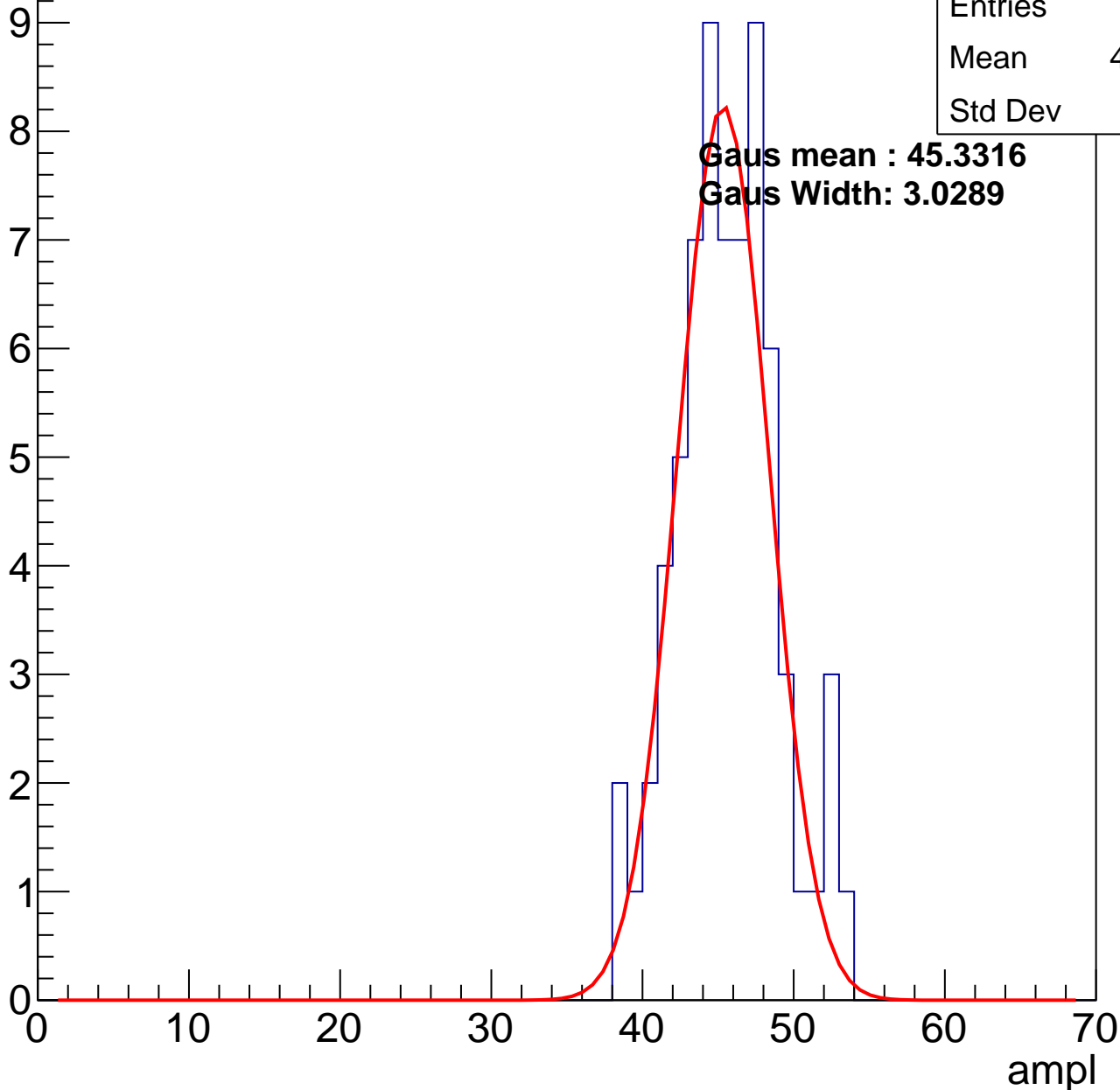
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	45.16
Std Dev	3.31

Gaus mean : 45.3316

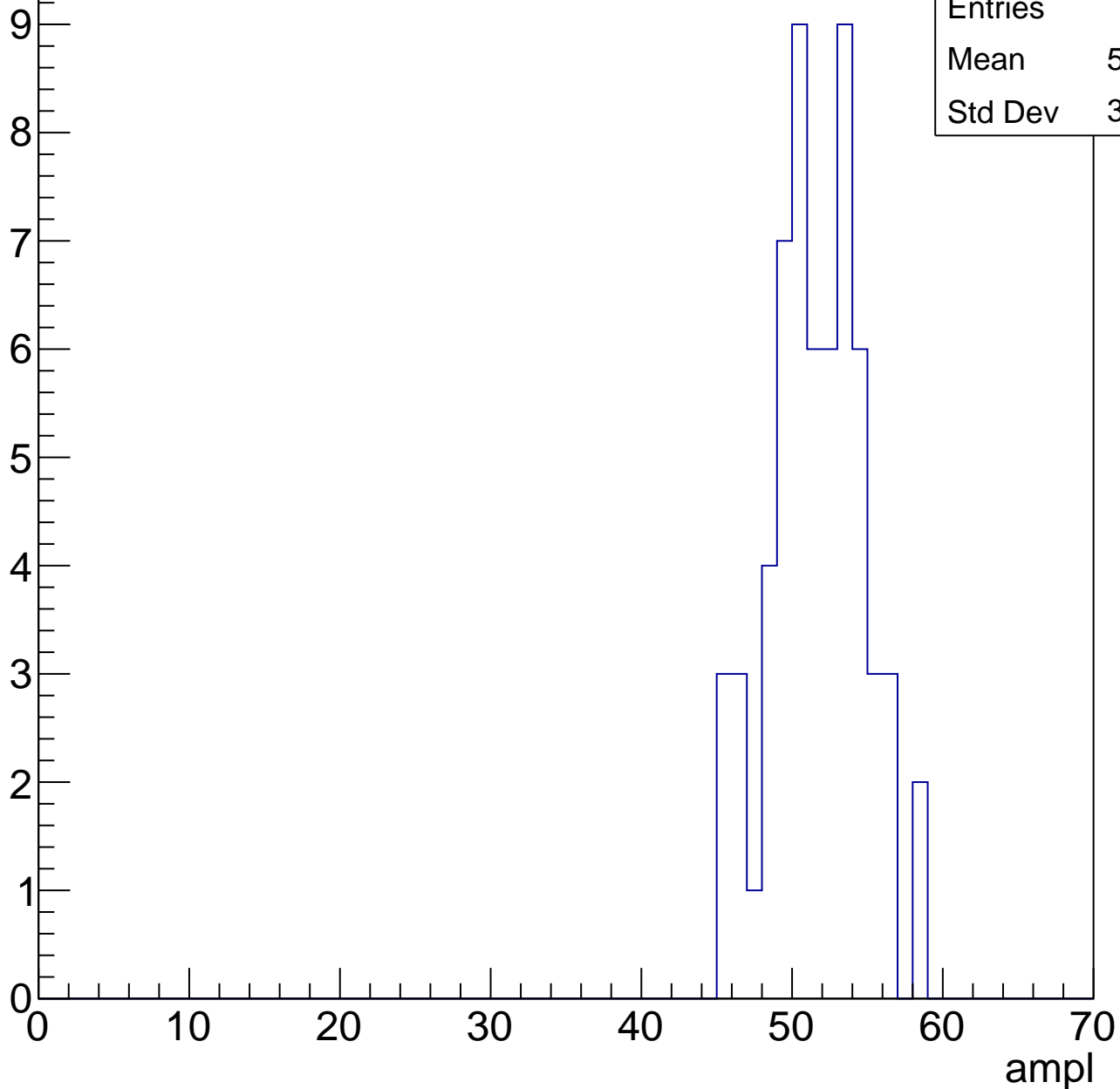
Gaus Width: 3.0289



B1L100S, U10-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

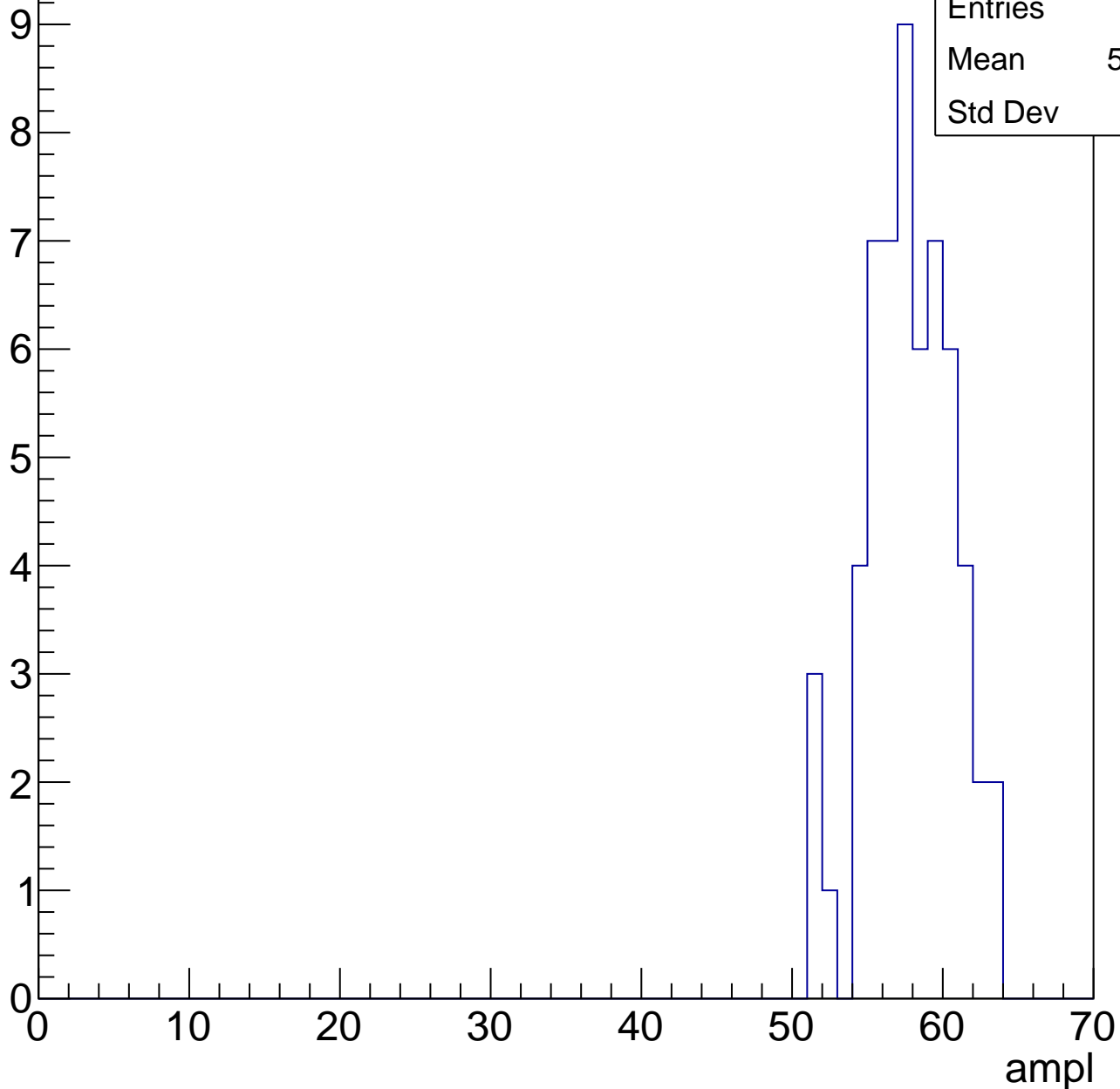
Entry



B1L100S, U10-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



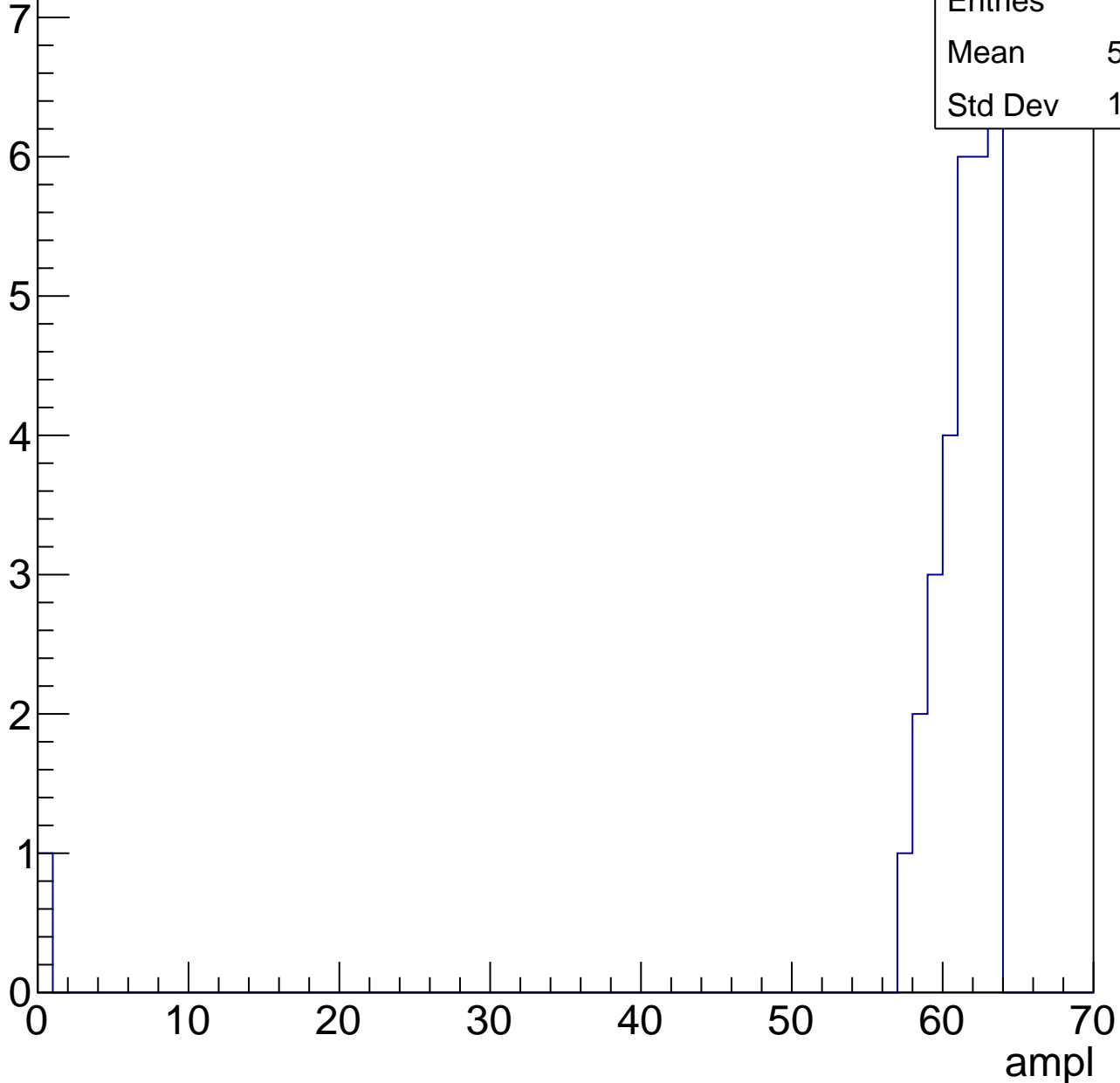
Entries	58
Mean	57.34
Std Dev	2.85

B1L100S, U10-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.97
Std Dev	11.08



B1L100S, U10-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L100S, U10-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch114, adc0

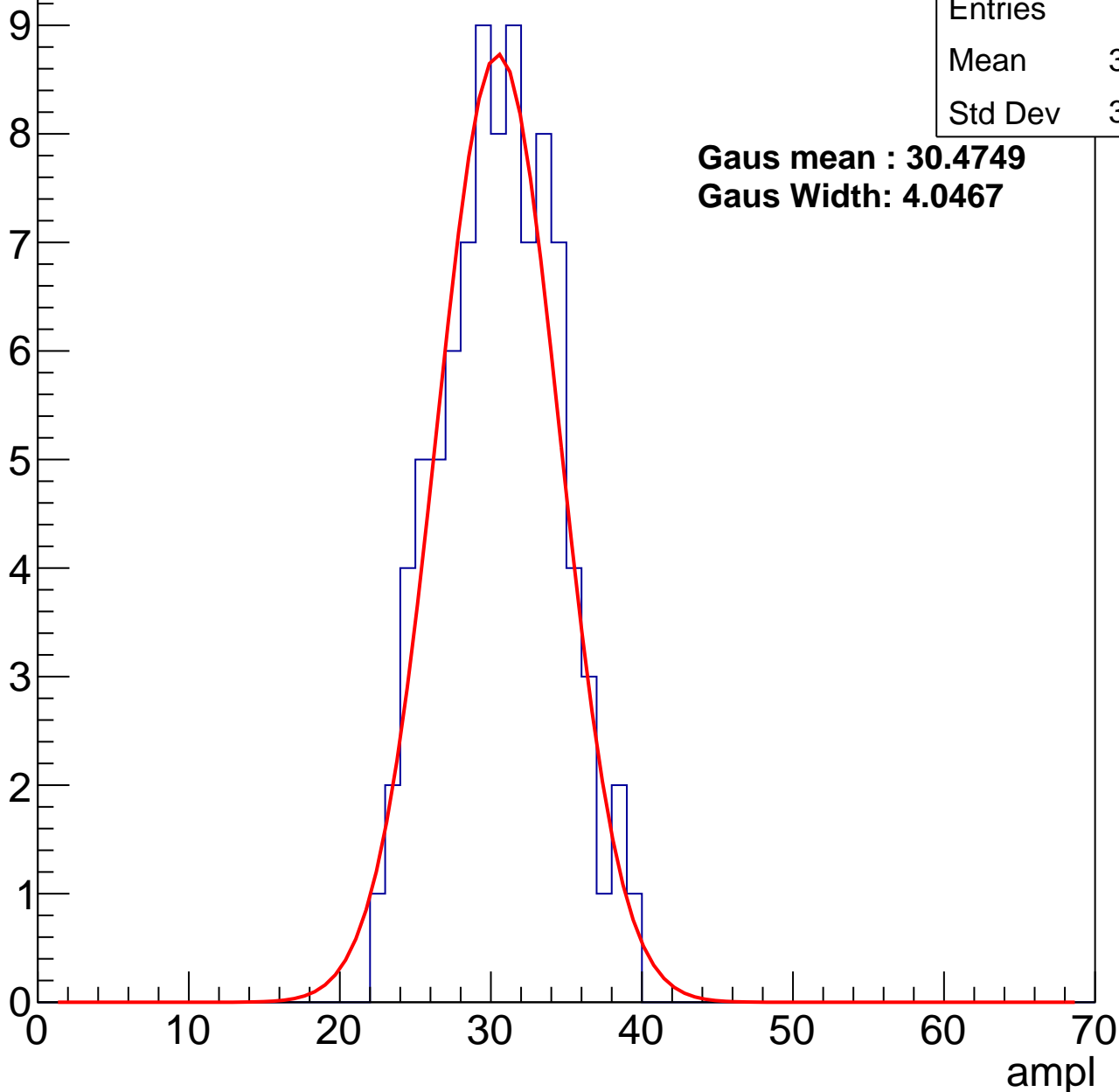
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	30.15
Std Dev	3.803

Gaus mean : 30.4749

Gaus Width: 4.0467



B1L100S, U10-ch114, adc1

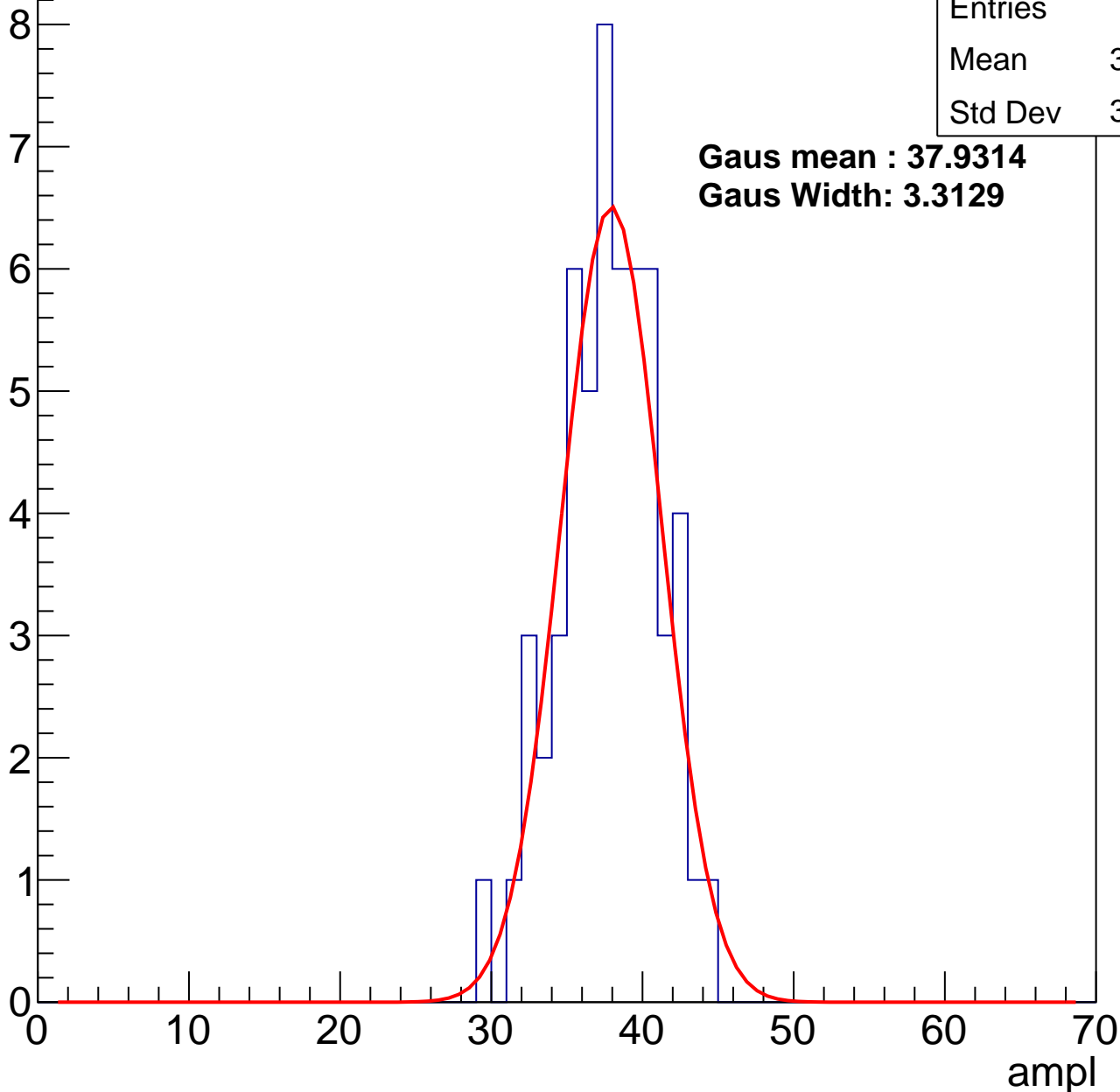
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.32
Std Dev	3.202

Gaus mean : 37.9314

Gaus Width: 3.3129



B1L100S, U10-ch114, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	89
Mean	44.62
Std Dev	3.74

Gaus mean : 45.4134

Gaus Width: 4.2344

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

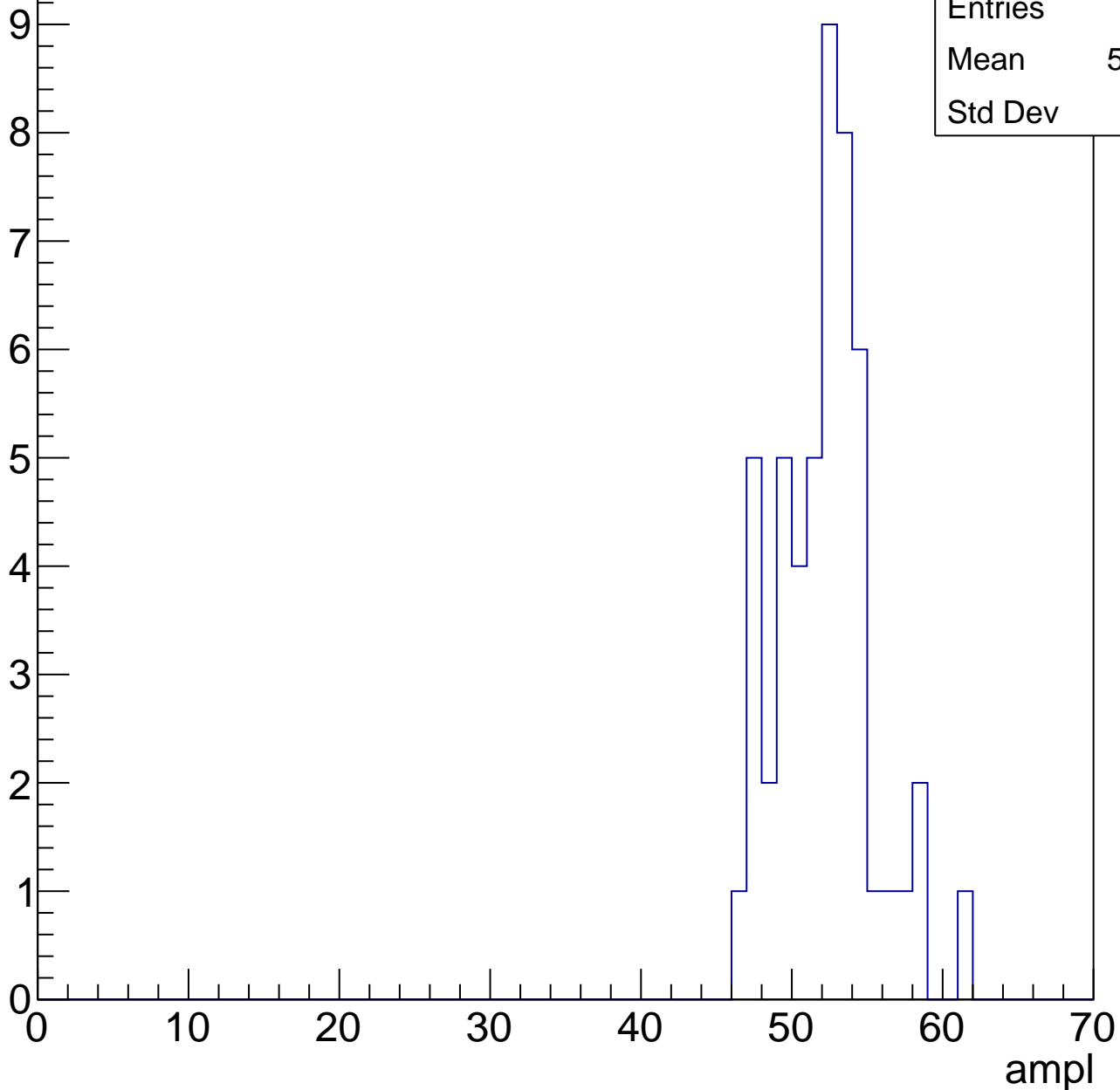


B1L100S, U10-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

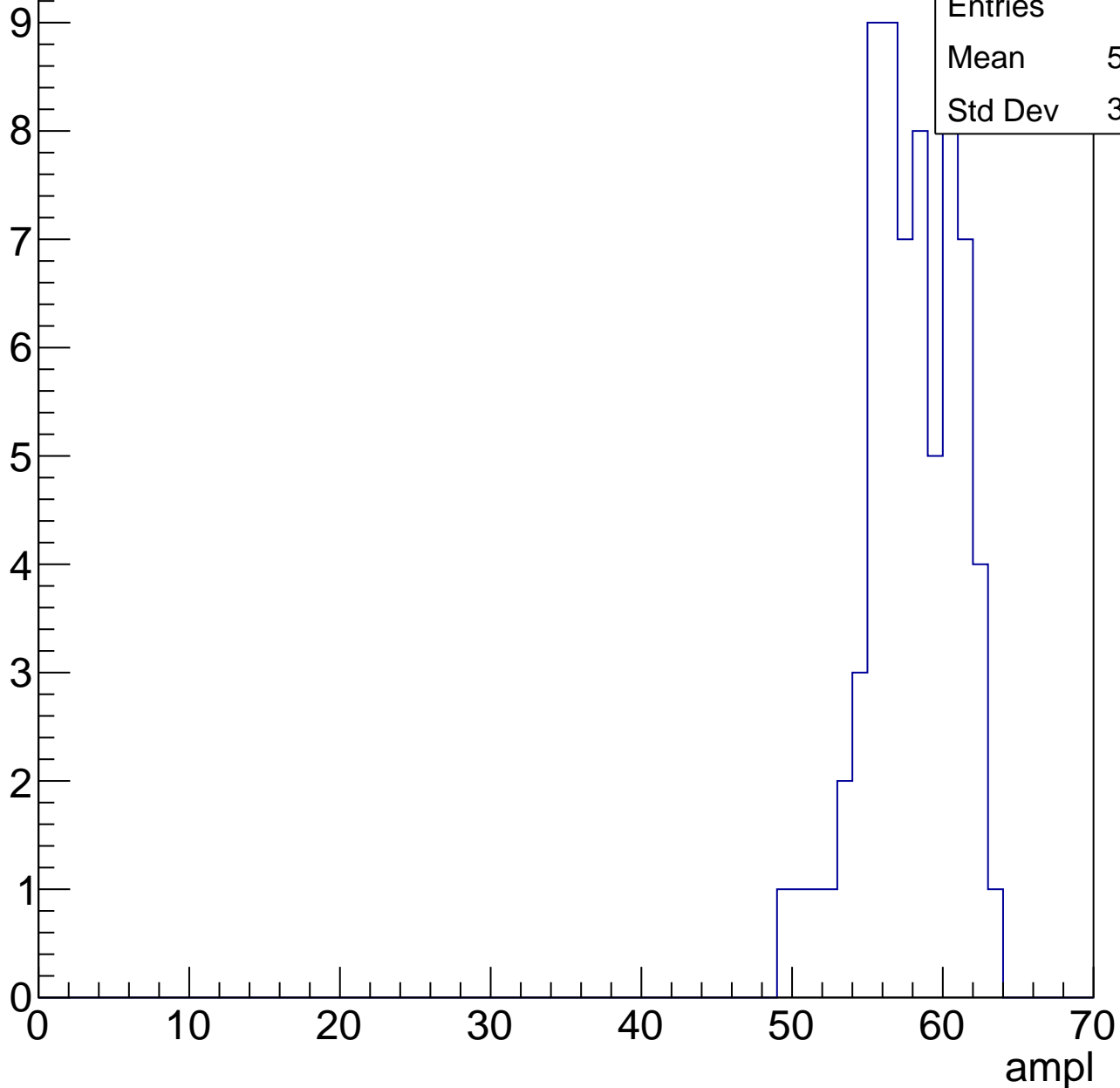
Entries	51
Mean	51.73
Std Dev	3.1



B1L100S, U10-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

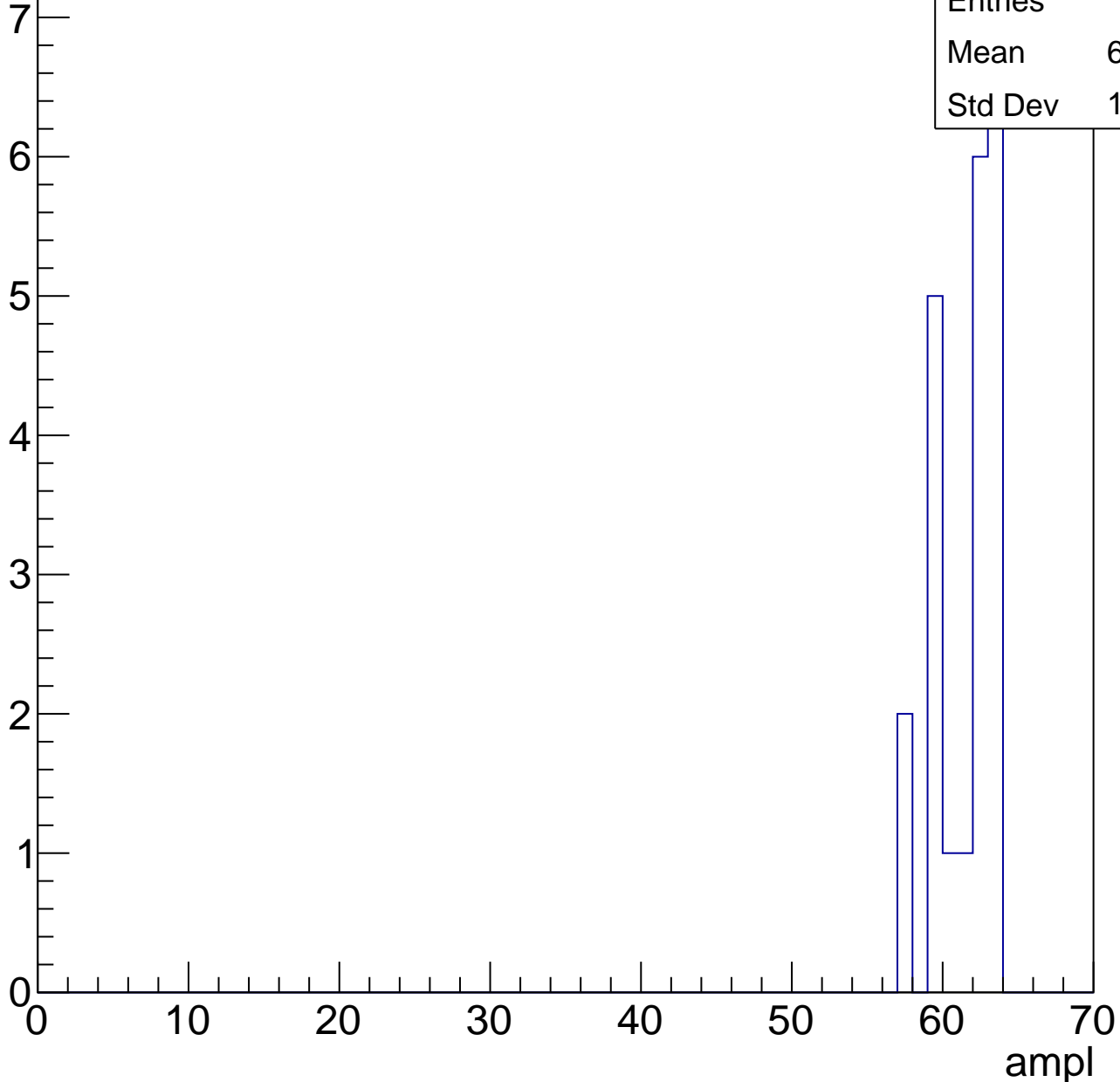


B1L100S, U10-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	61.05
Std Dev	1.988



B1L100S, U10-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

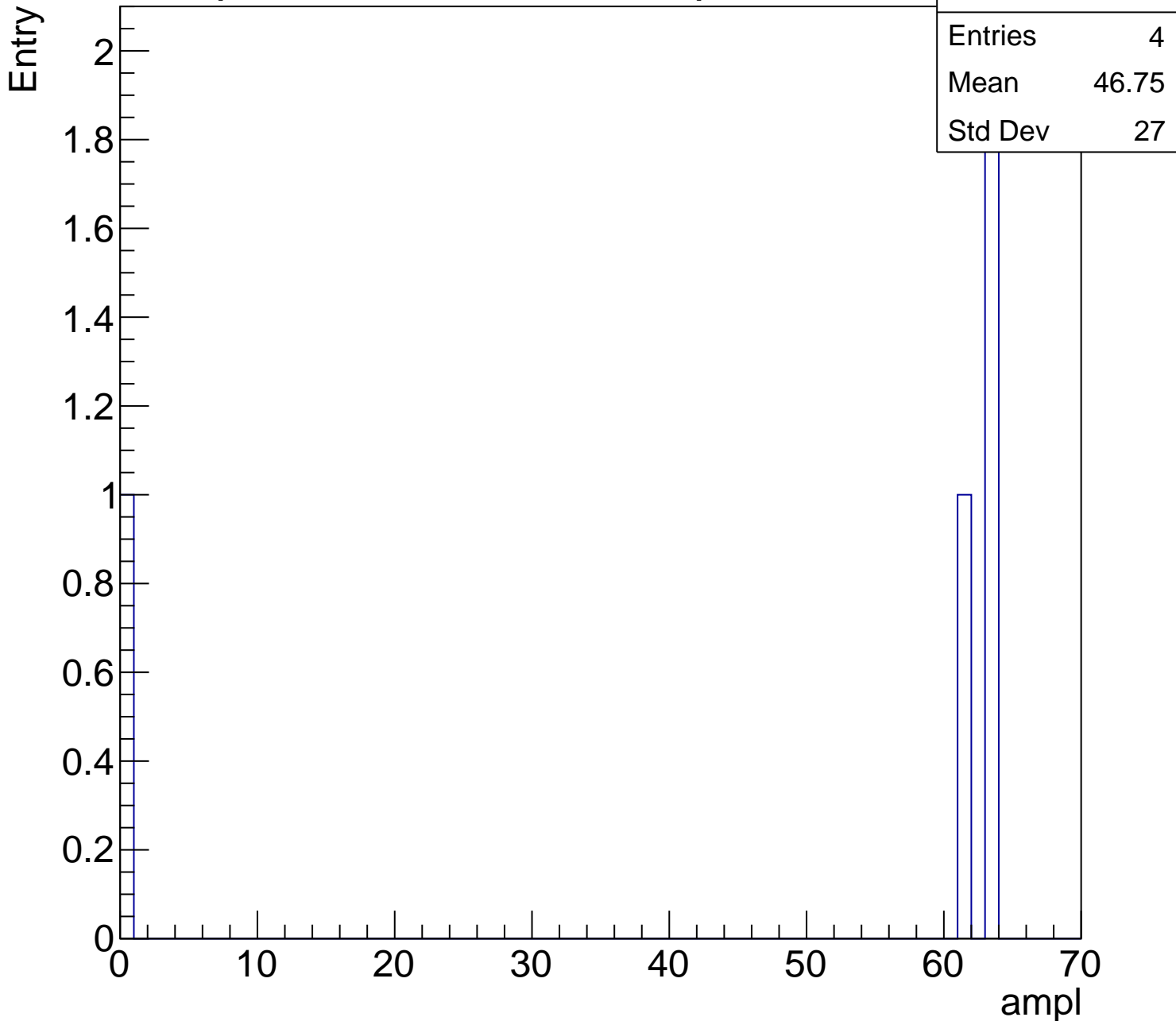
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch115, adc0

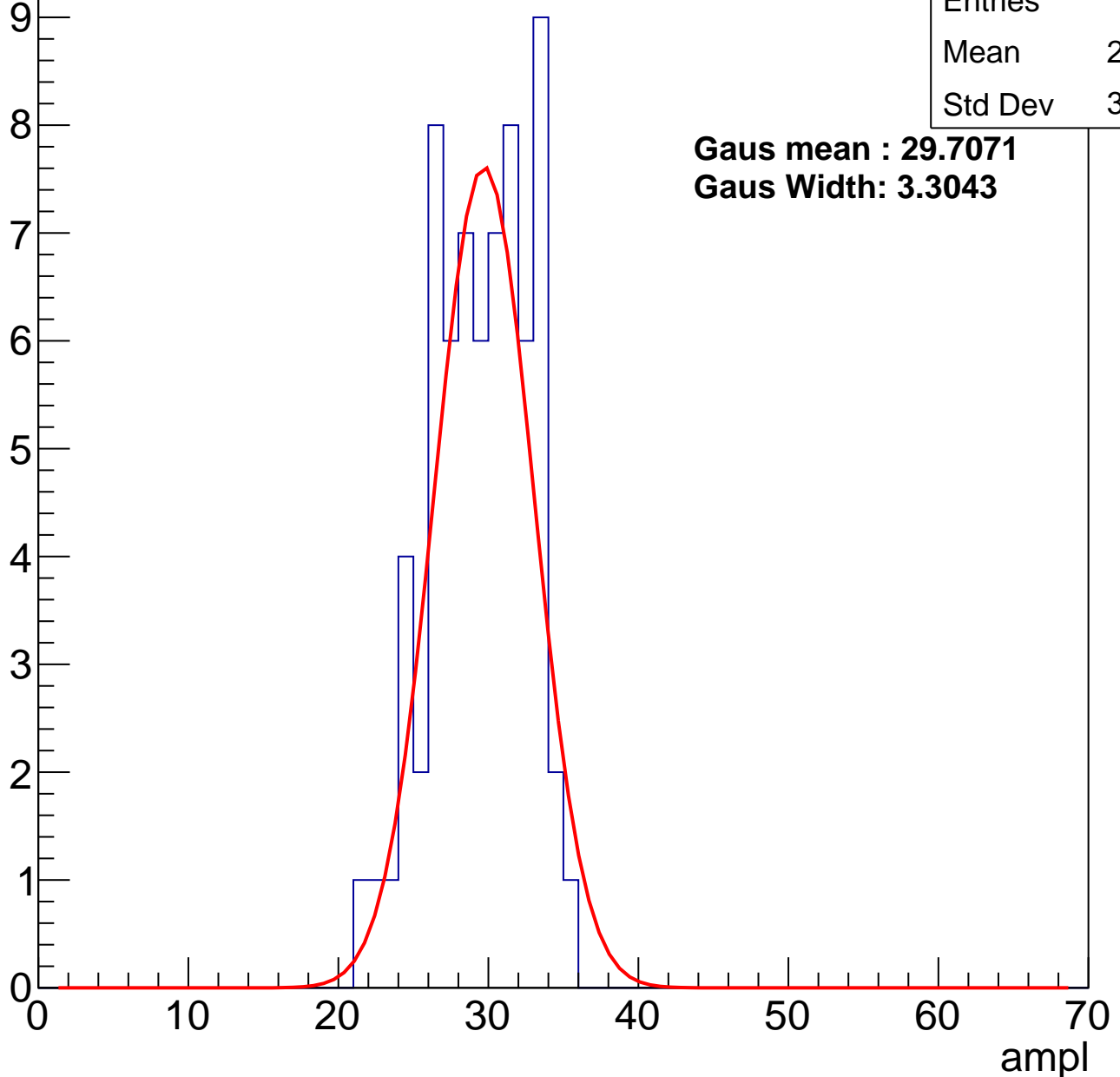
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.01
Std Dev	3.201

Gaus mean : 29.7071

Gaus Width: 3.3043



B1L100S, U10-ch115, adc1

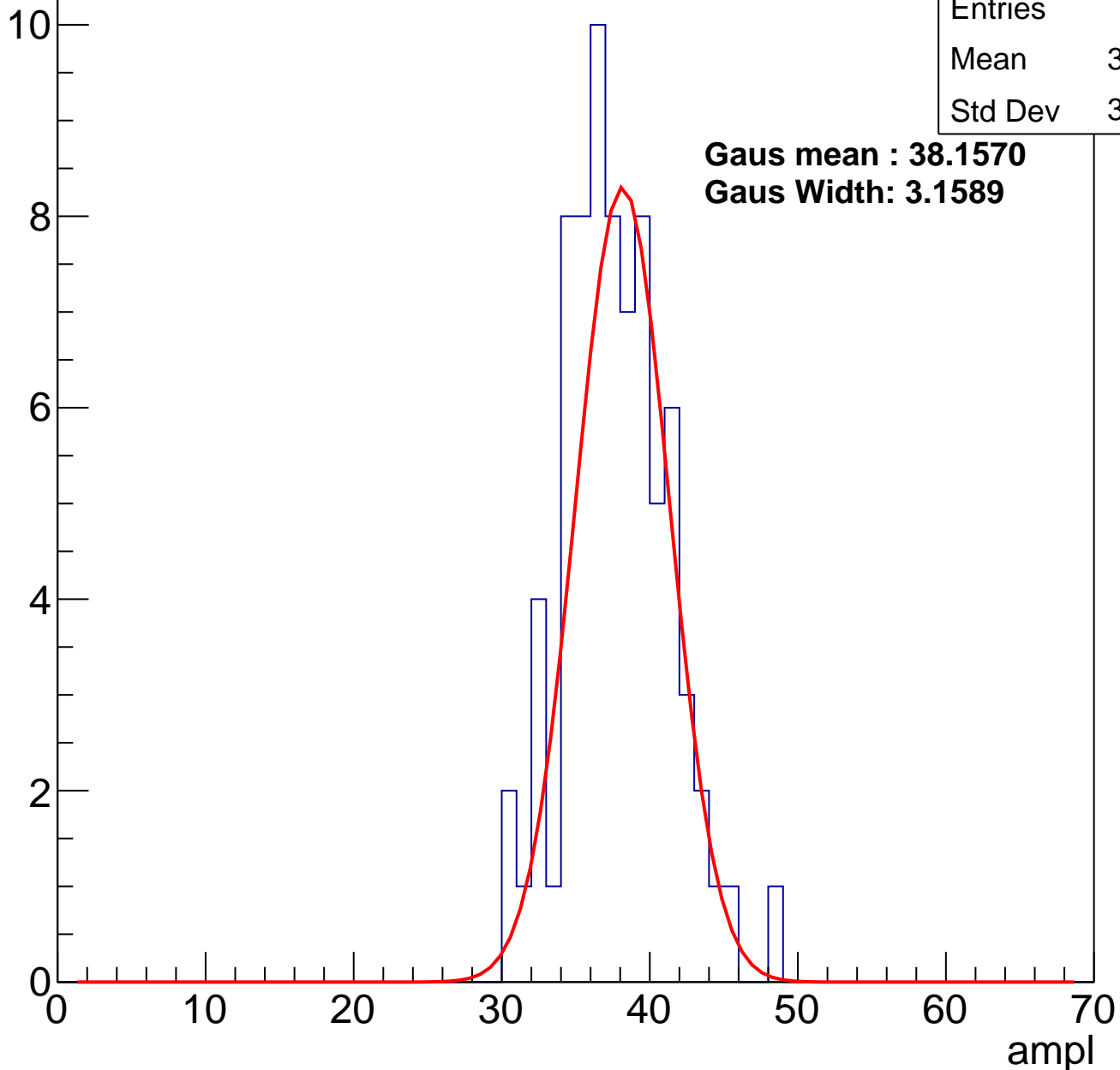
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	37.28
Std Dev	3.478

Gaus mean : 38.1570

Gaus Width: 3.1589

Entry



B1L100S, U10-ch115, adc2

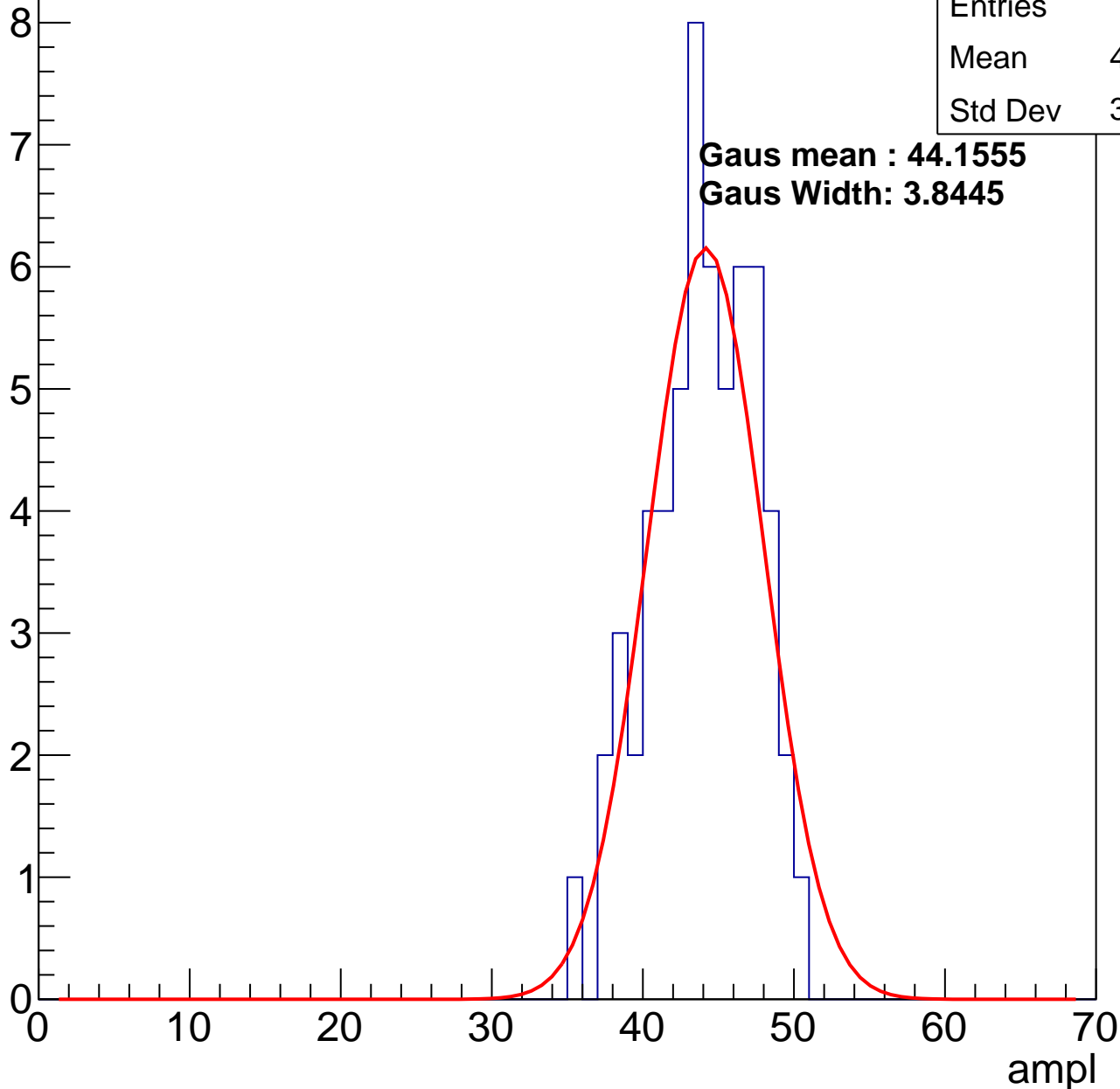
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.49
Std Dev	3.402

Gaus mean : 44.1555

Gaus Width: 3.8445

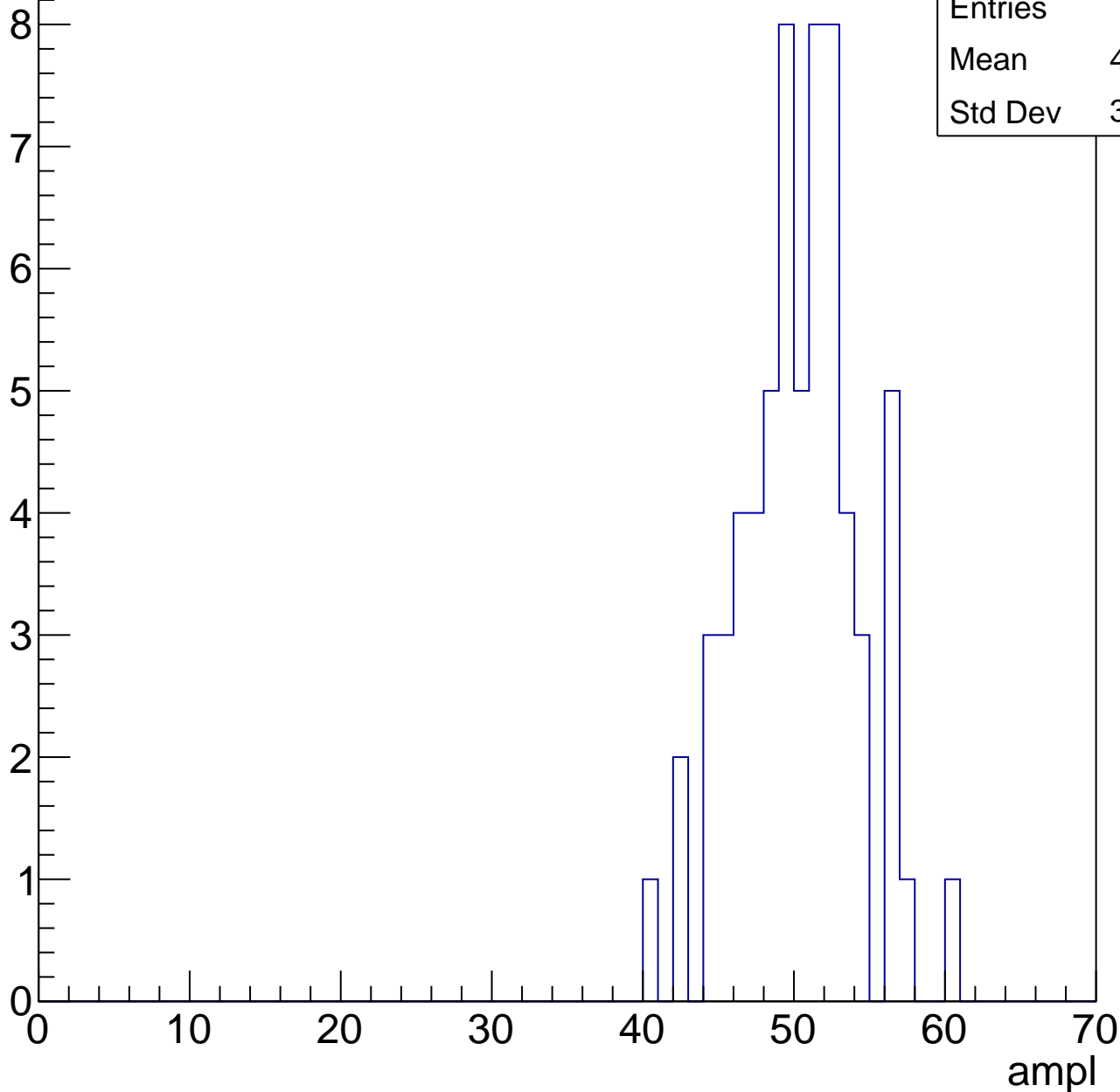


B1L100S, U10-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.85
Std Dev	3.904

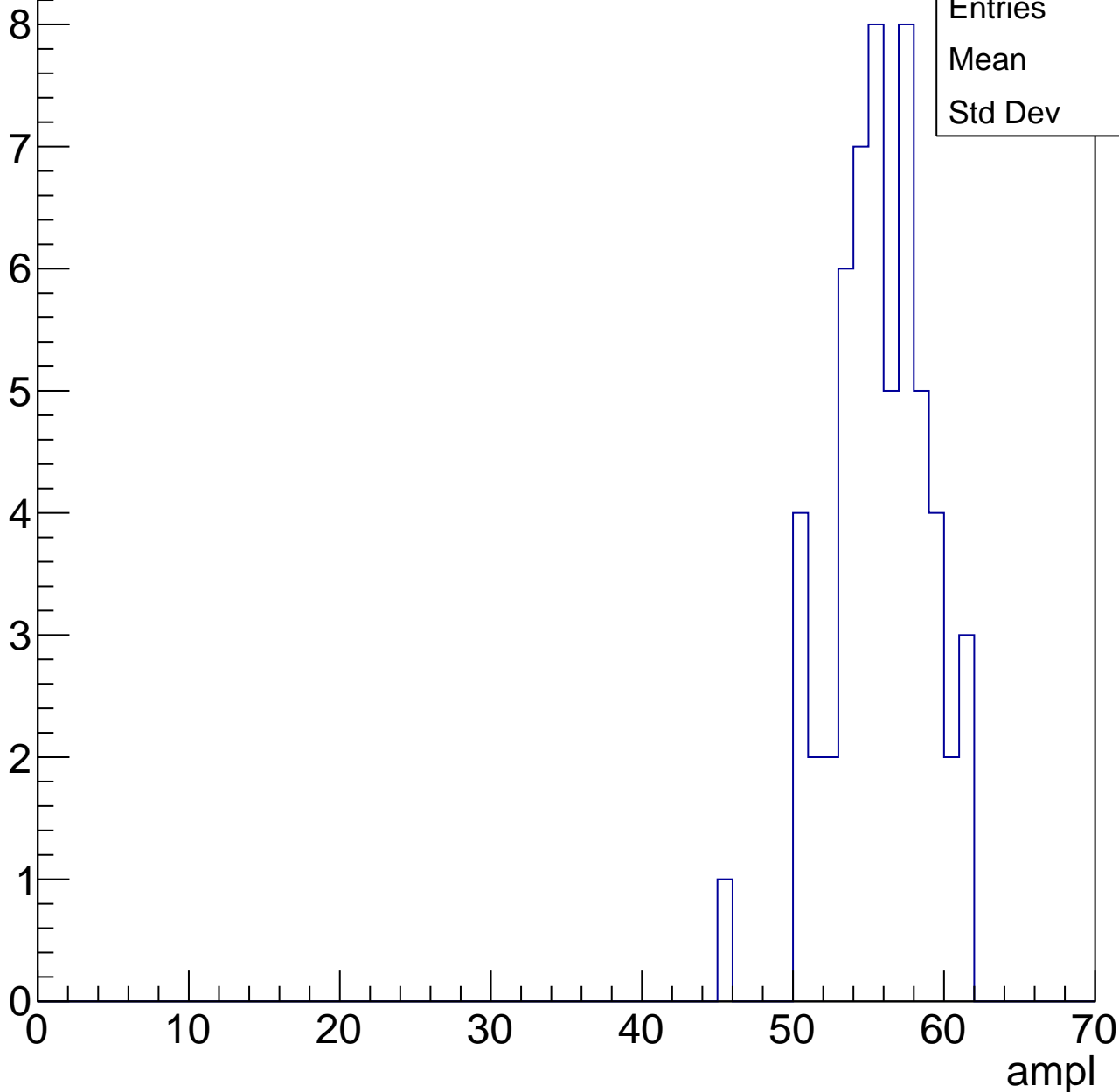


B1L100S, U10-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.3
Std Dev	3.19

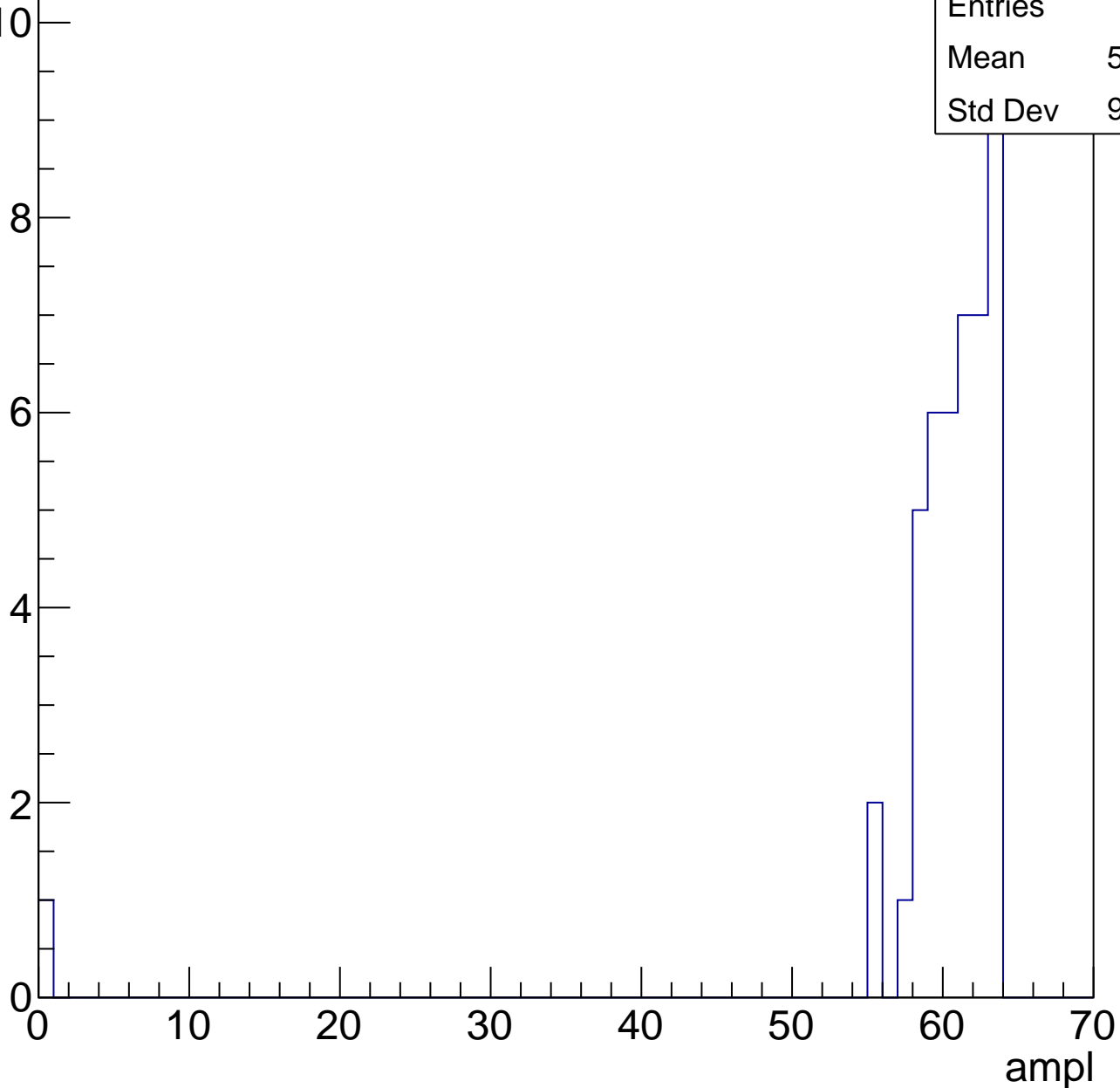


B1L100S, U10-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

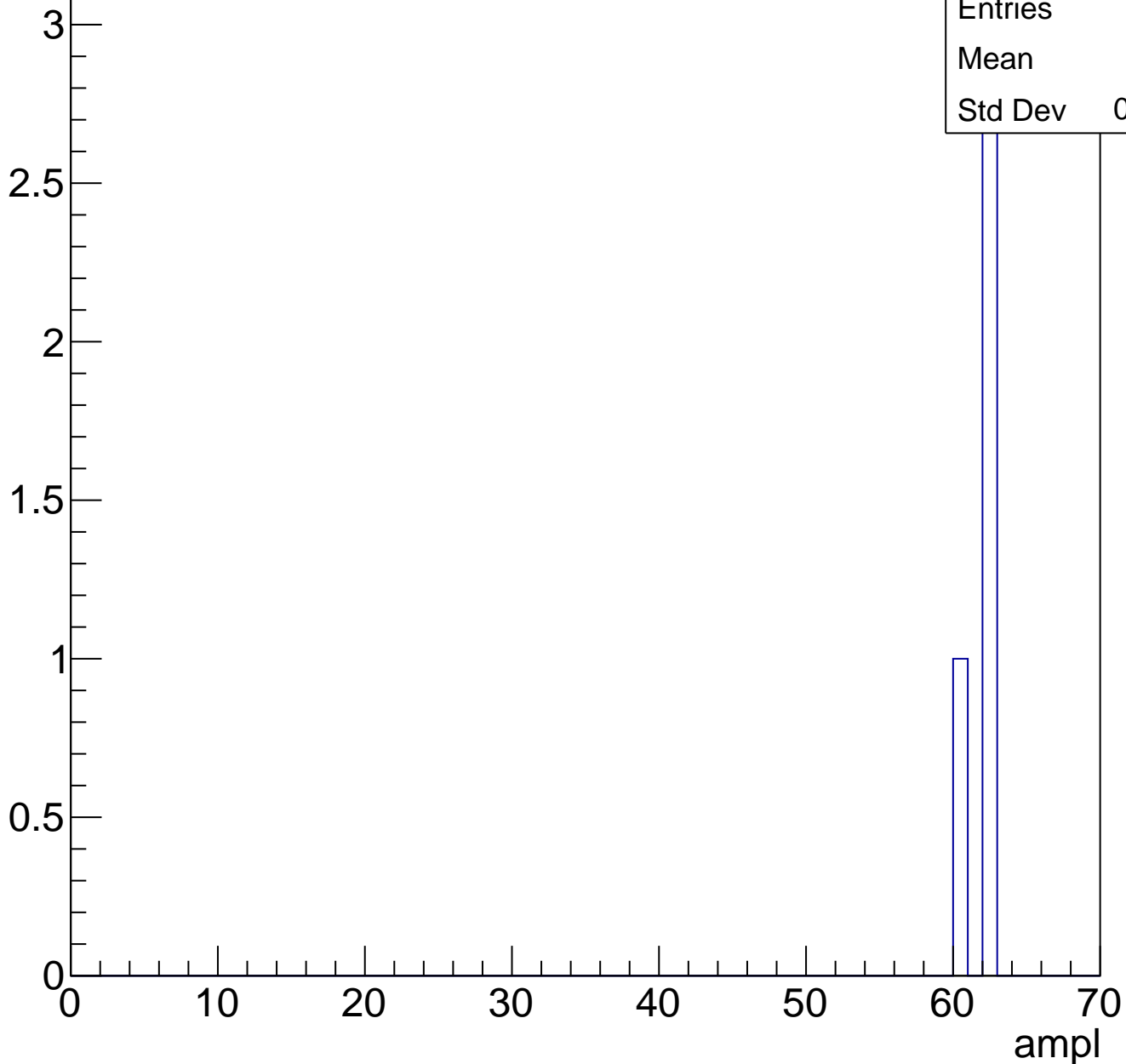
Entries	45
Mean	59.16
Std Dev	9.163



B1L100S, U10-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch116, adc0

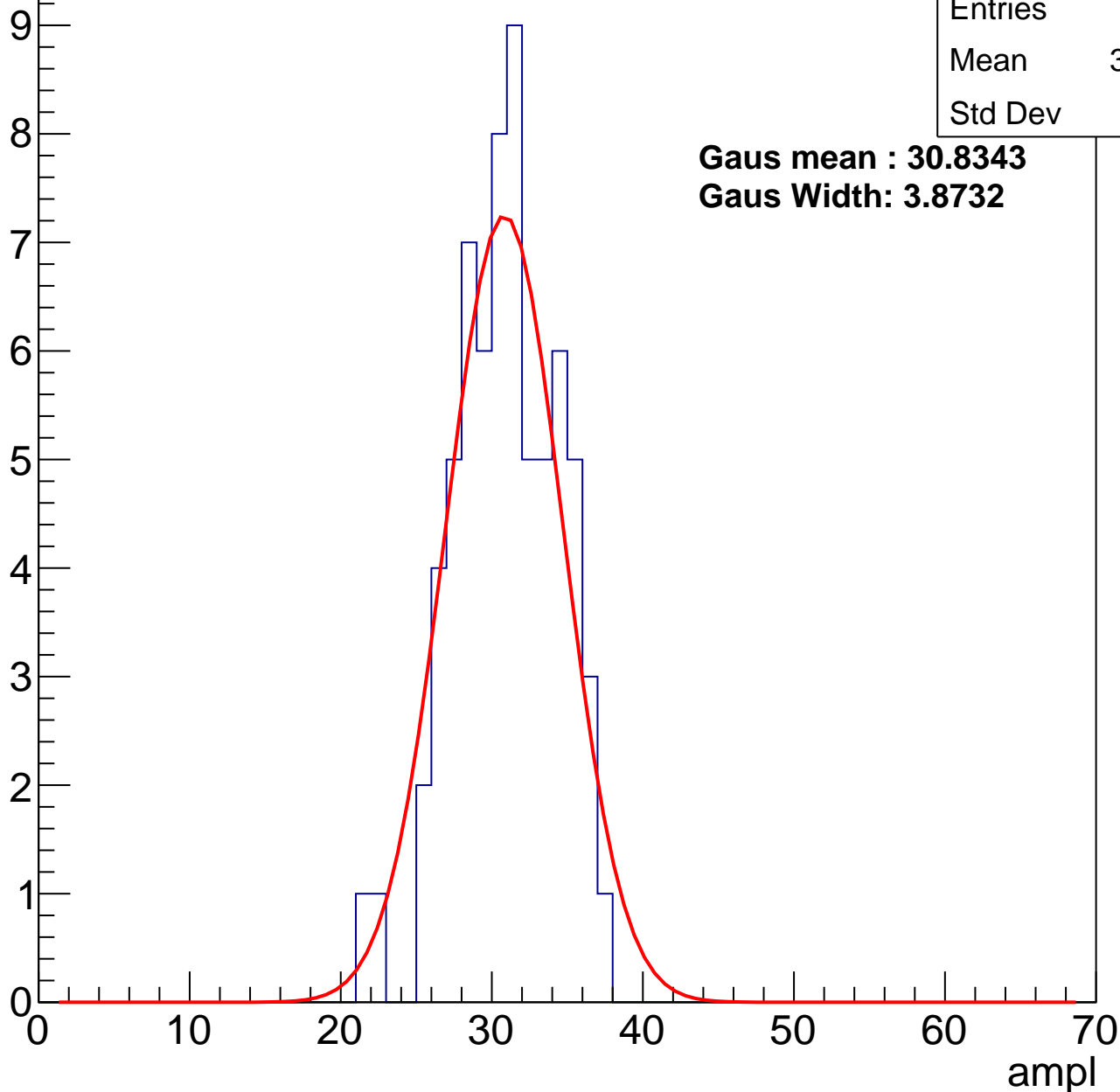
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.44
Std Dev	3.38

Gaus mean : 30.8343

Gaus Width: 3.8732



B1L100S, U10-ch116, adc1

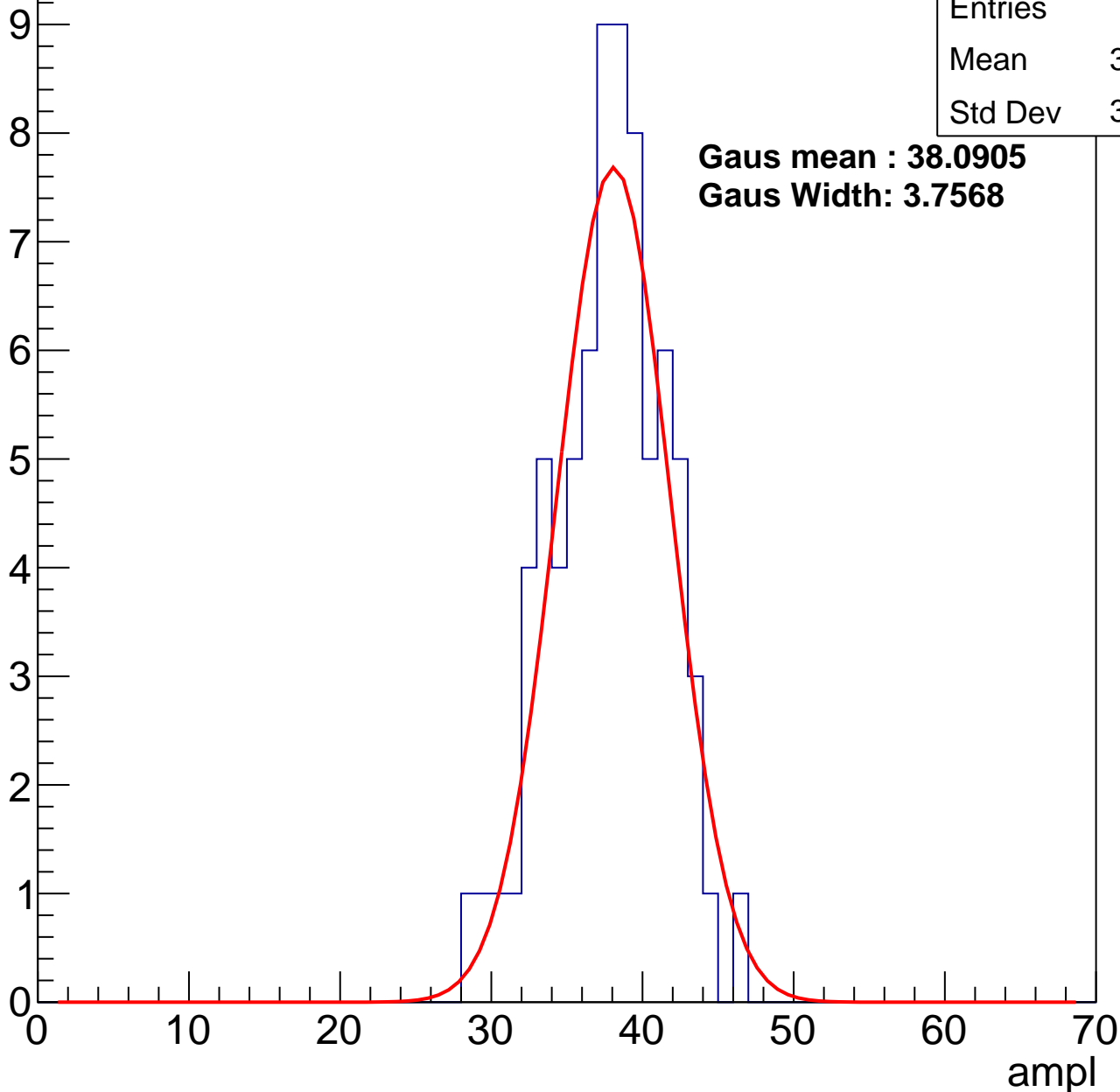
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.33
Std Dev	3.667

Gaus mean : 38.0905

Gaus Width: 3.7568



B1L100S, U10-ch116, adc2

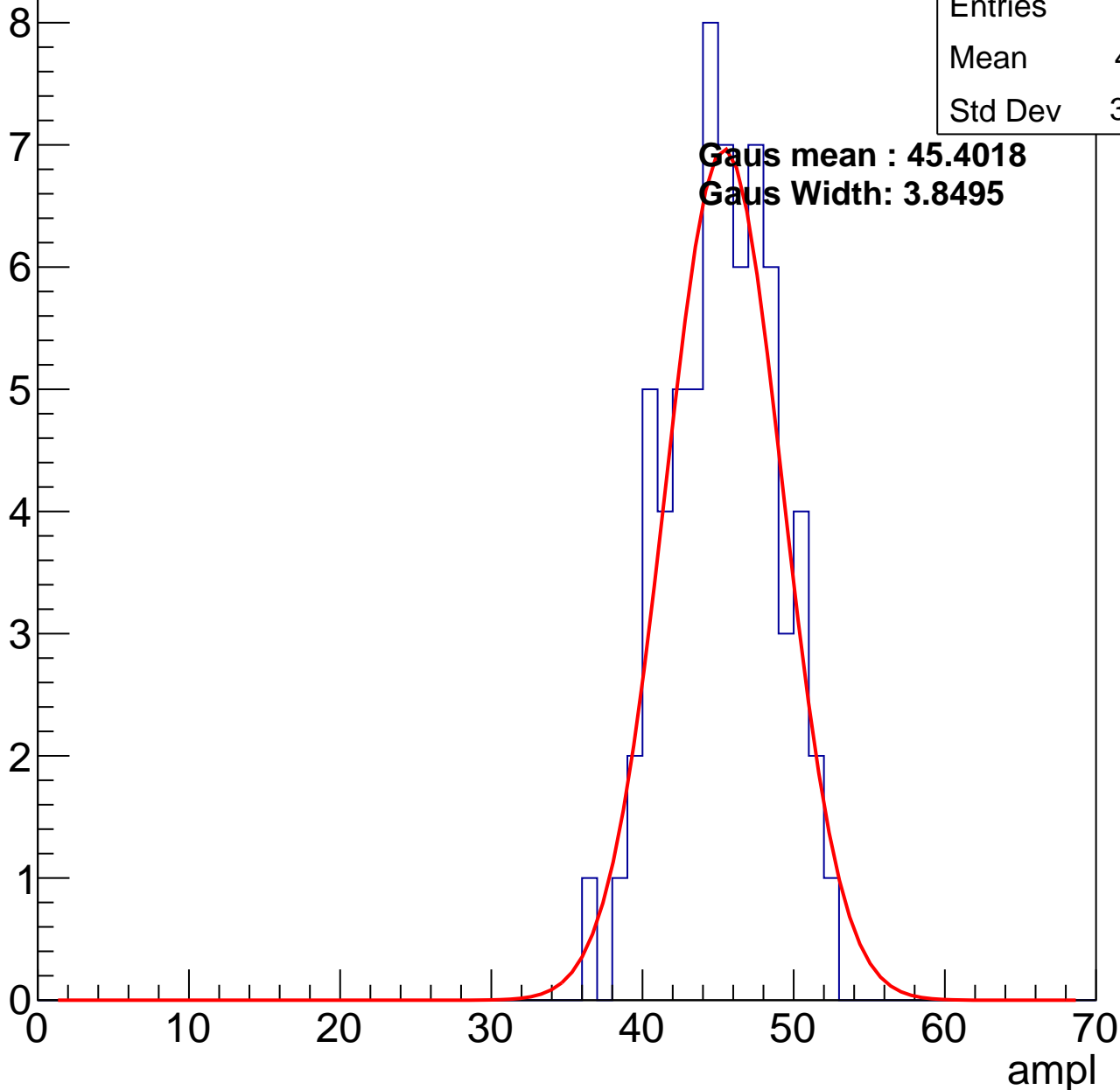
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.81
Std Dev	3.495

Gaus mean : 45.4018

Gaus Width: 3.8495

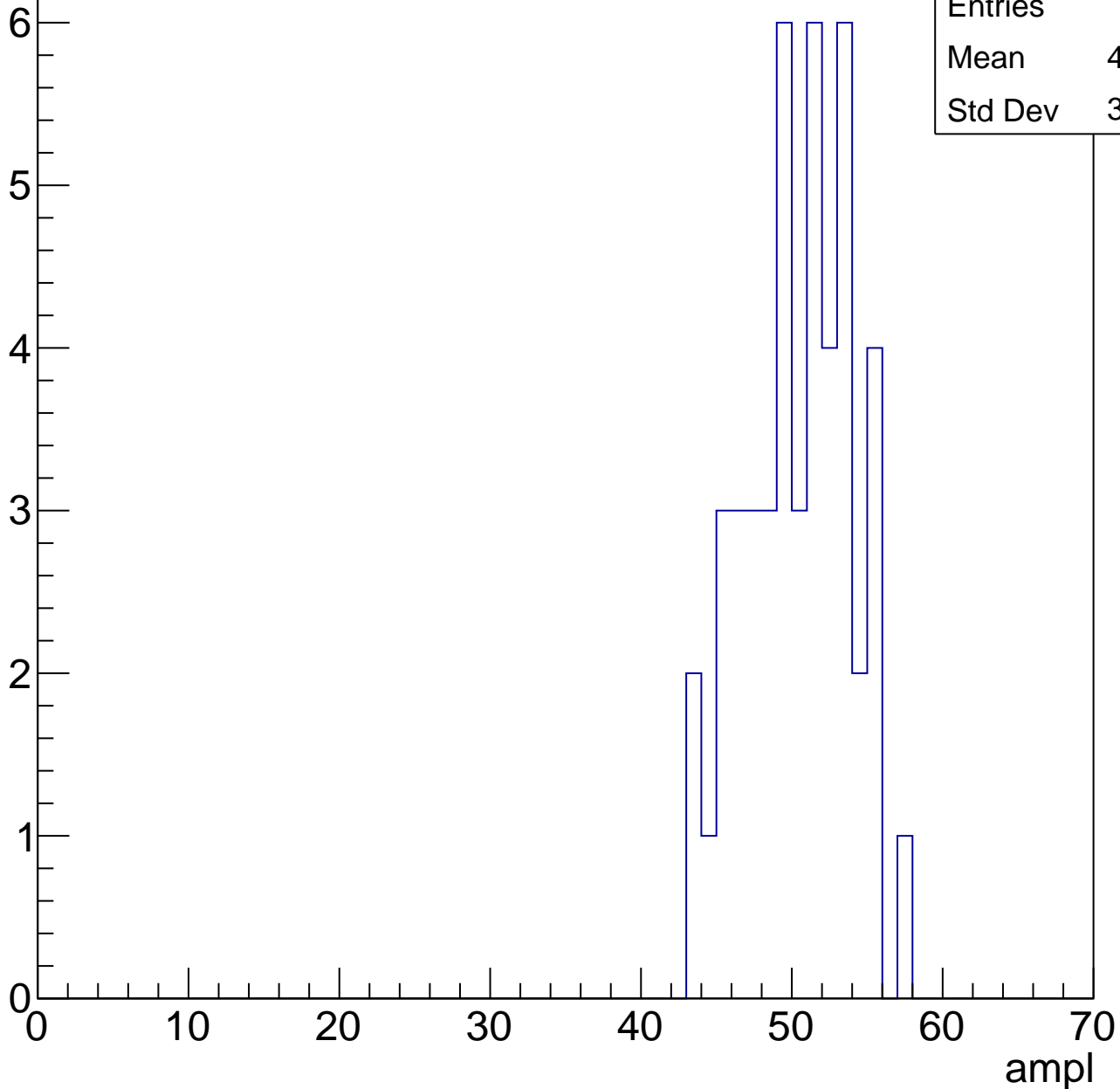


B1L100S, U10-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

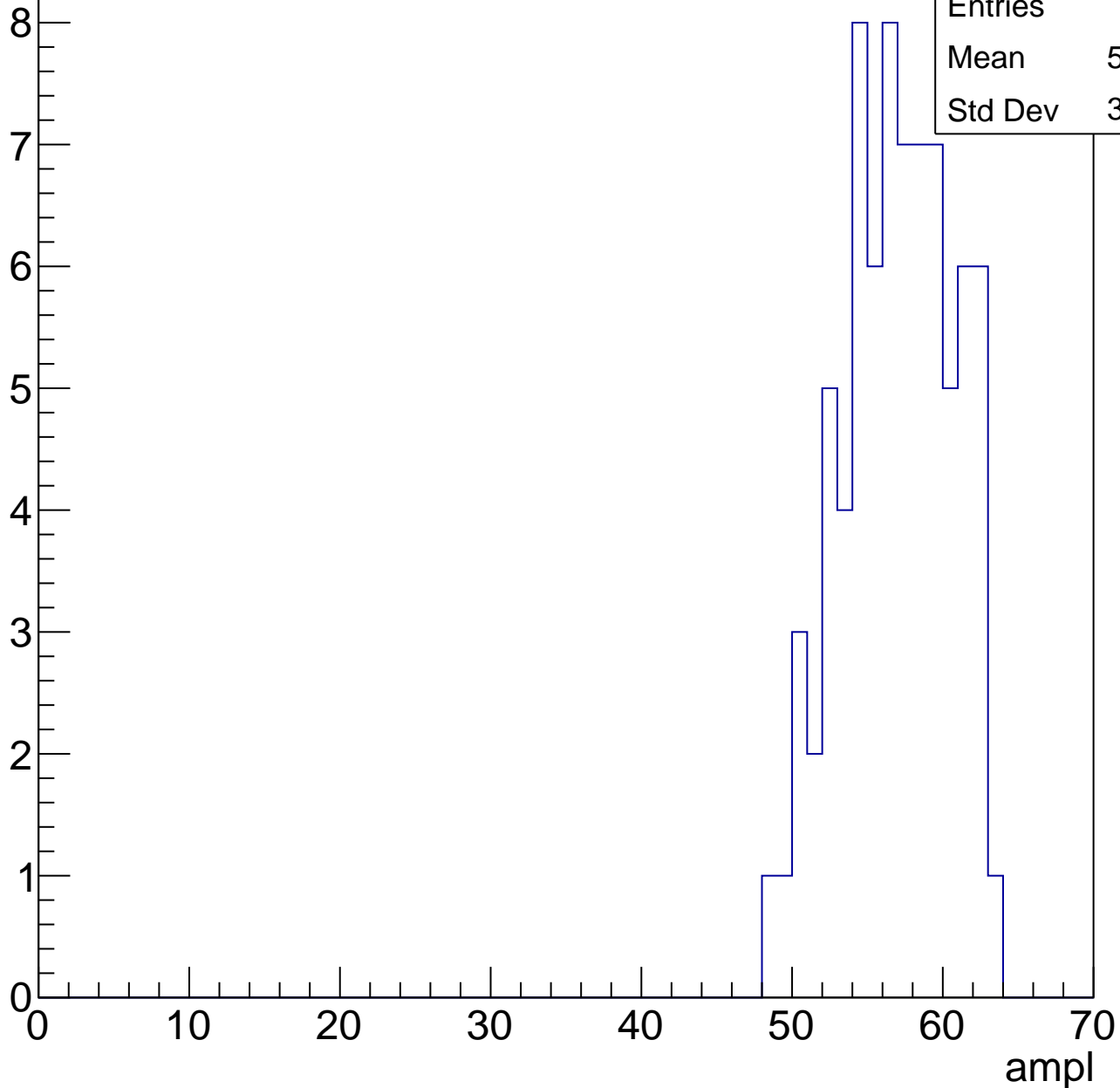
Entries	47
Mean	49.98
Std Dev	3.449



B1L100S, U10-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

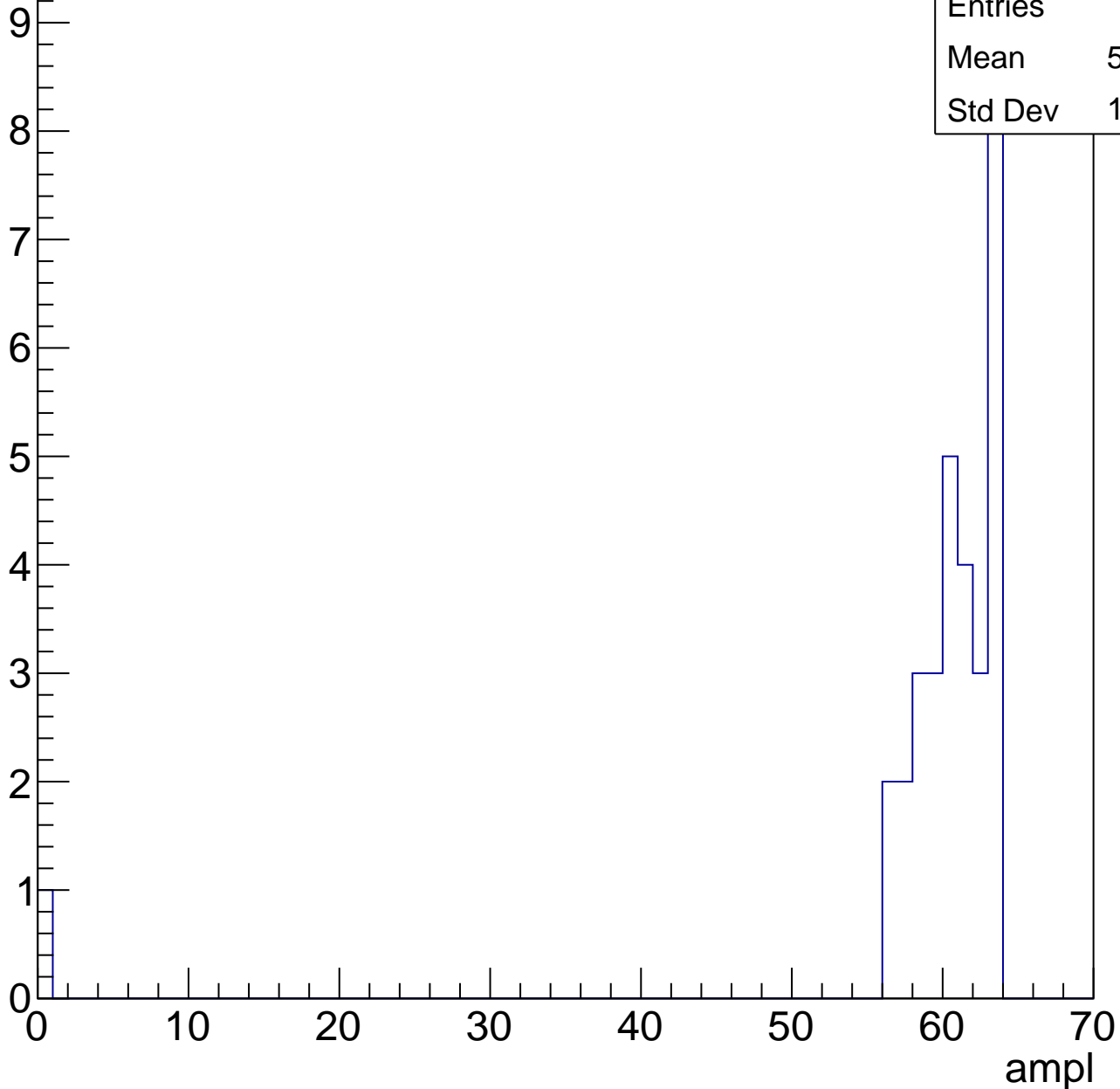


B1L100S, U10-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.56
Std Dev	10.75



B1L100S, U10-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U10-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch117, adc0

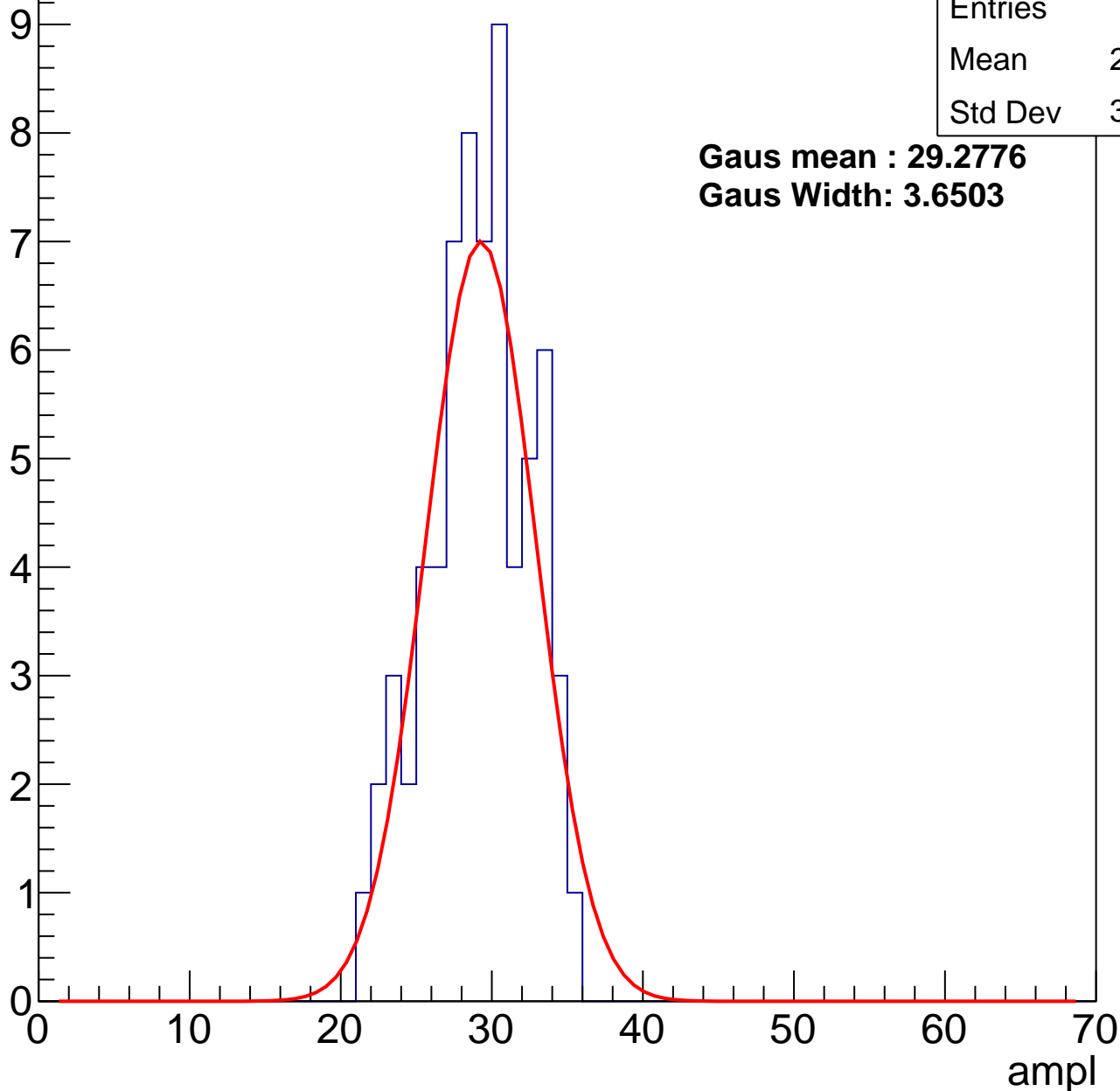
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.65
Std Dev	3.314

Gaus mean : 29.2776

Gaus Width: 3.6503



B1L100S, U10-ch117, adc1

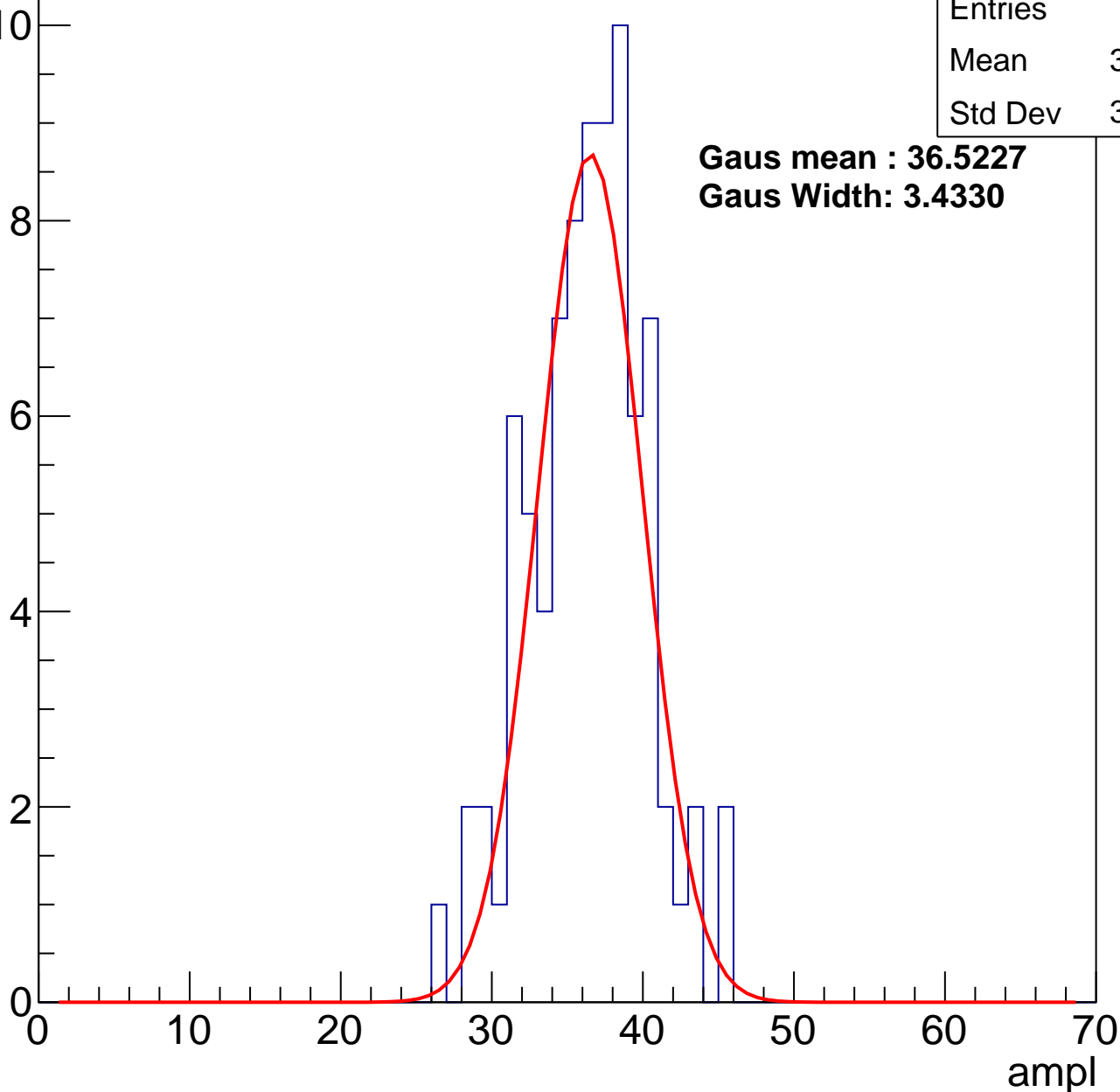
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	35.92
Std Dev	3.818

Gaus mean : 36.5227

Gaus Width: 3.4330



B1L100S, U10-ch117, adc2

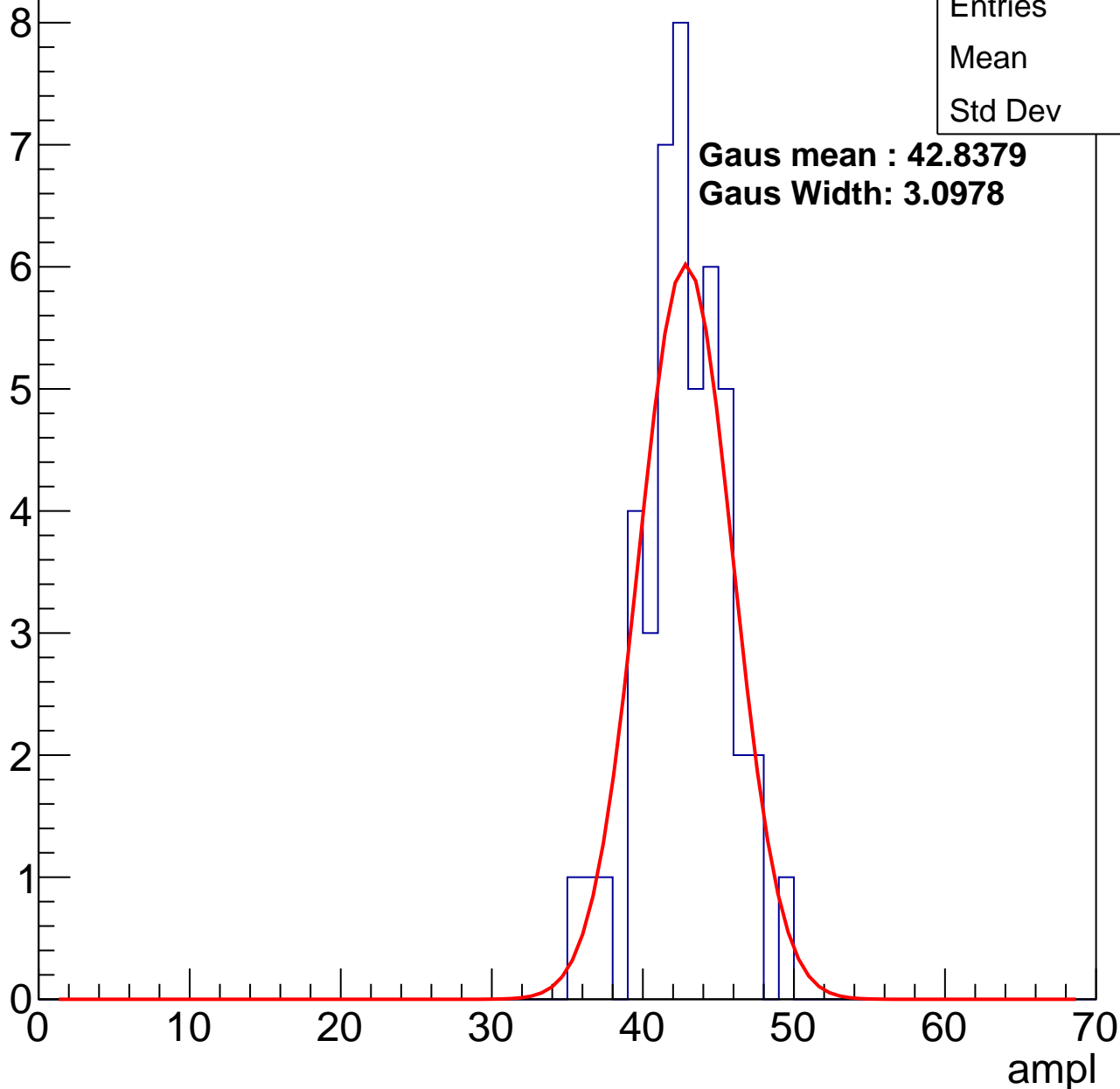
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	42.3
Std Dev	2.82

Gaus mean : 42.8379

Gaus Width: 3.0978

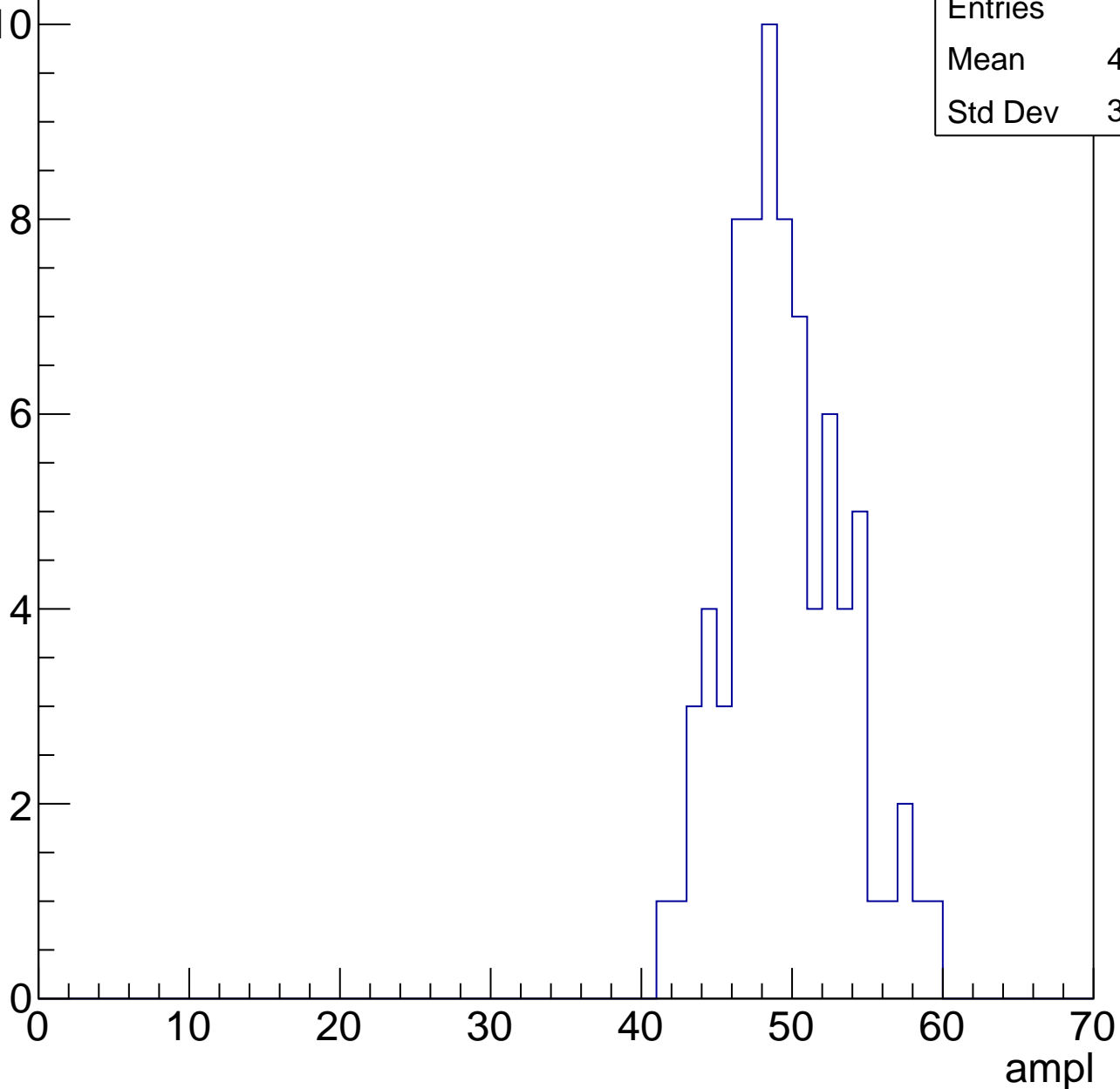


B1L100S, U10-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	49.09
Std Dev	3.827

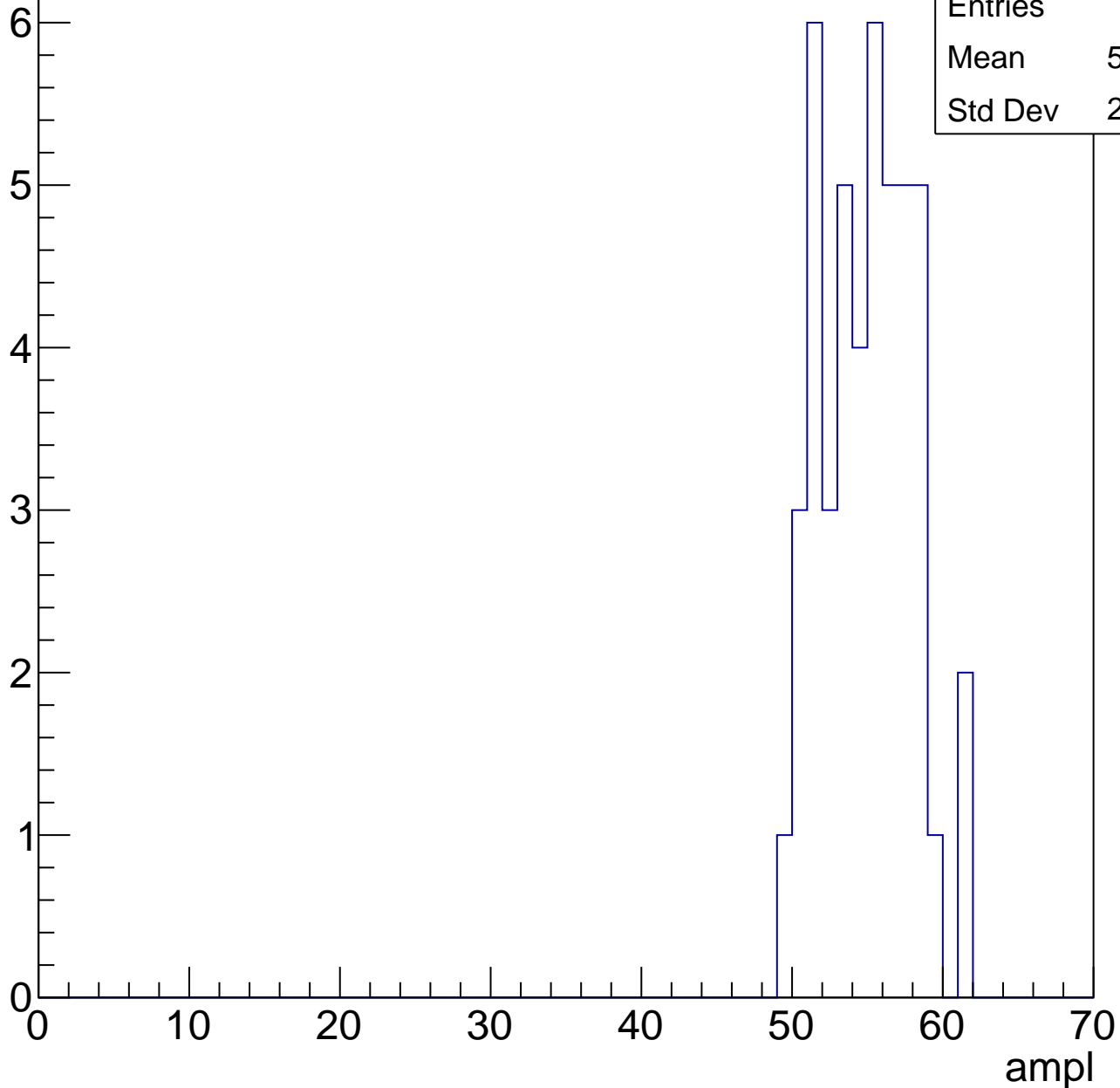


B1L100S, U10-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	54.52
Std Dev	2.969

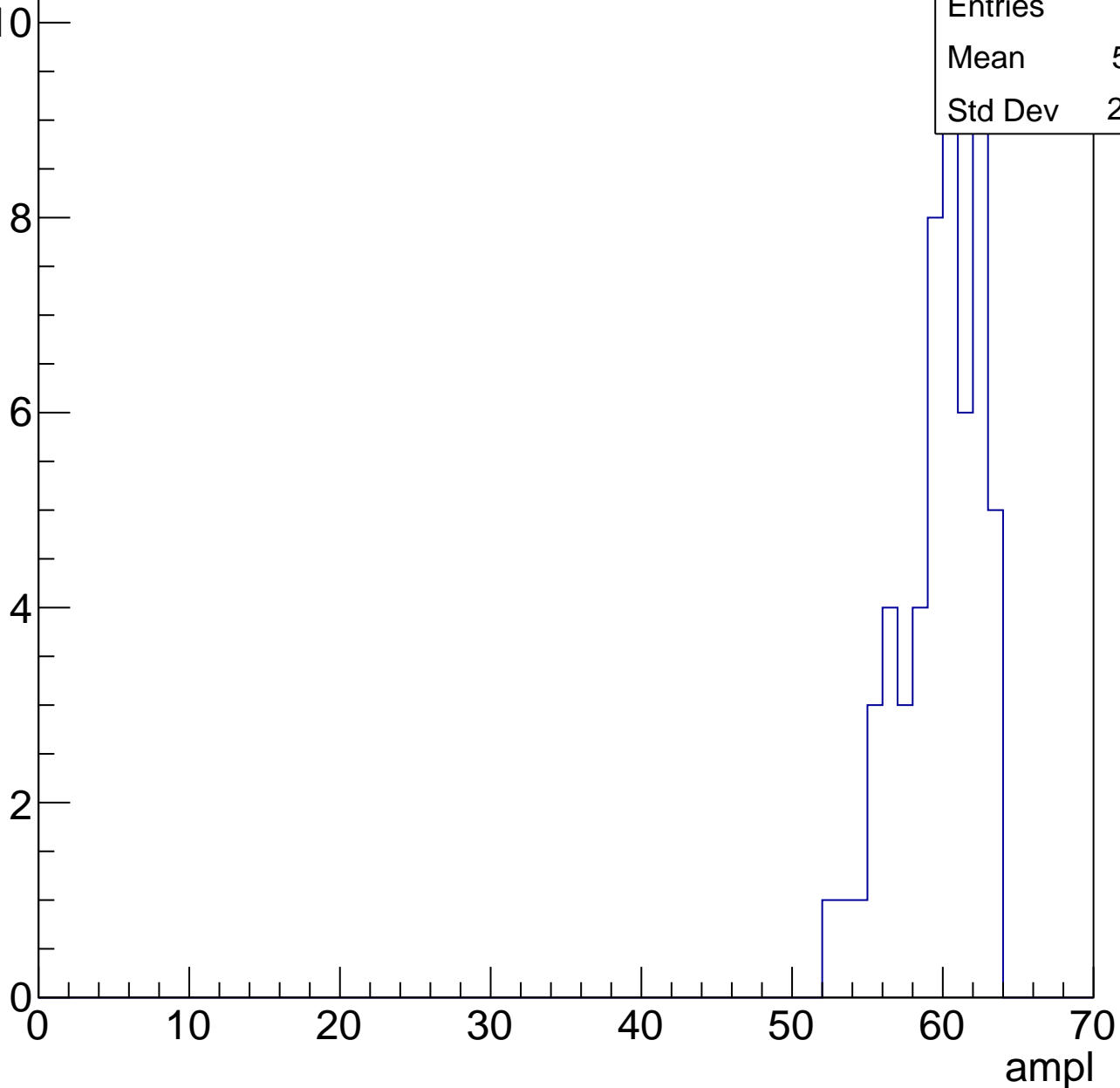


B1L100S, U10-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	59.31
Std Dev	2.696

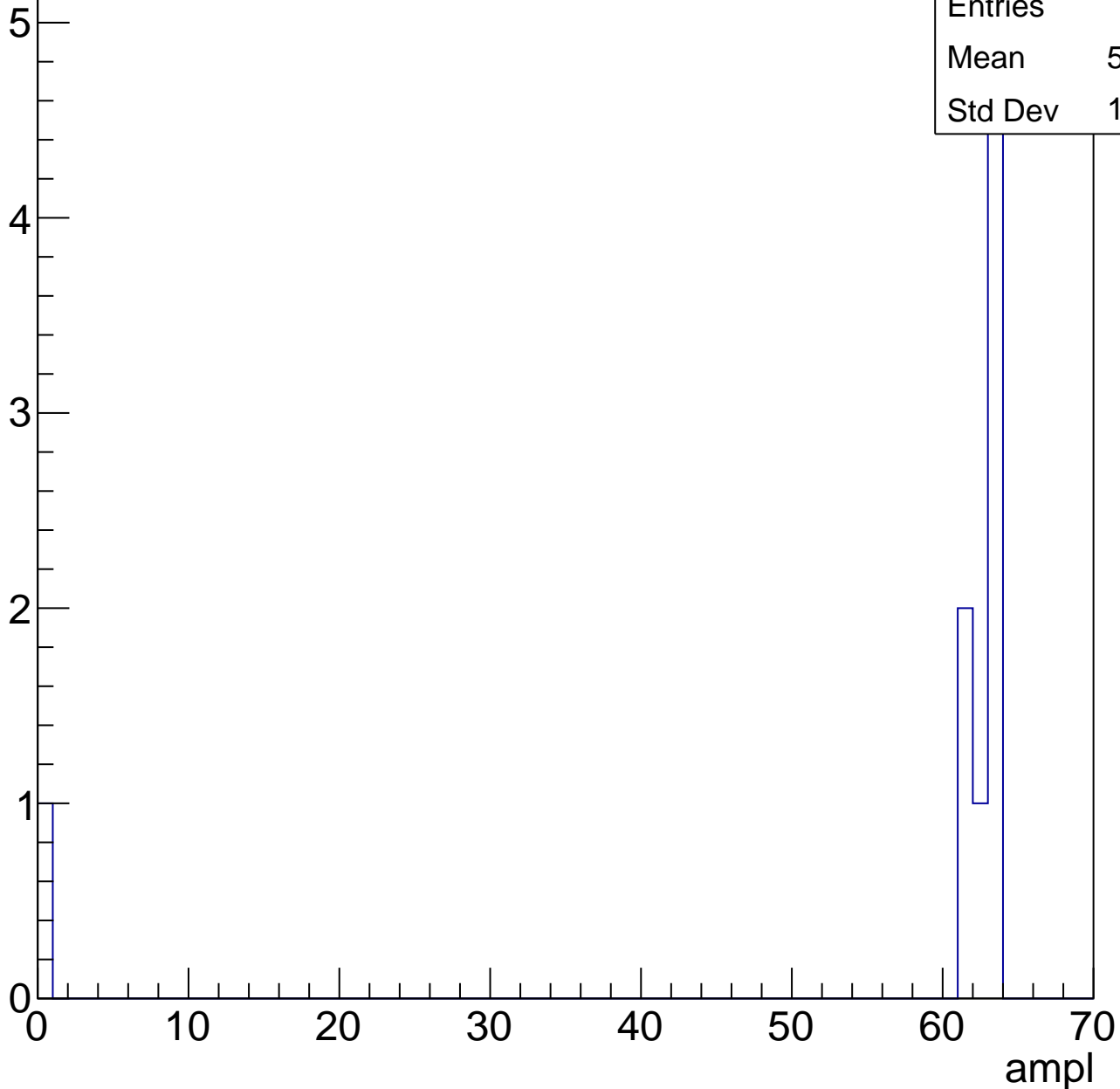


B1L100S, U10-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	55.44
Std Dev	19.62



B1L100S, U10-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch118, adc0

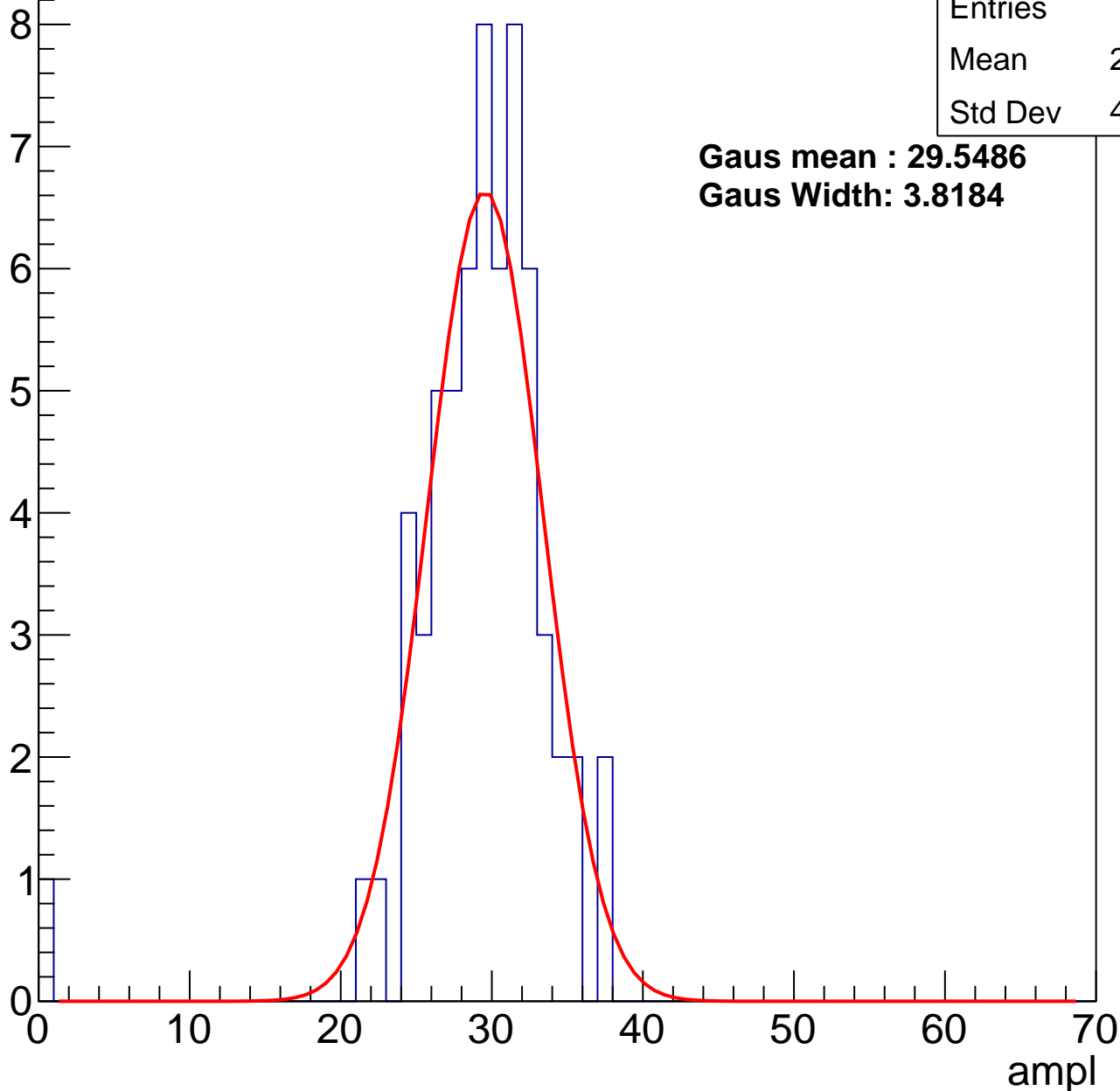
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.73
Std Dev	4.974

Gaus mean : 29.5486

Gaus Width: 3.8184



B1L100S, U10-ch118, adc1

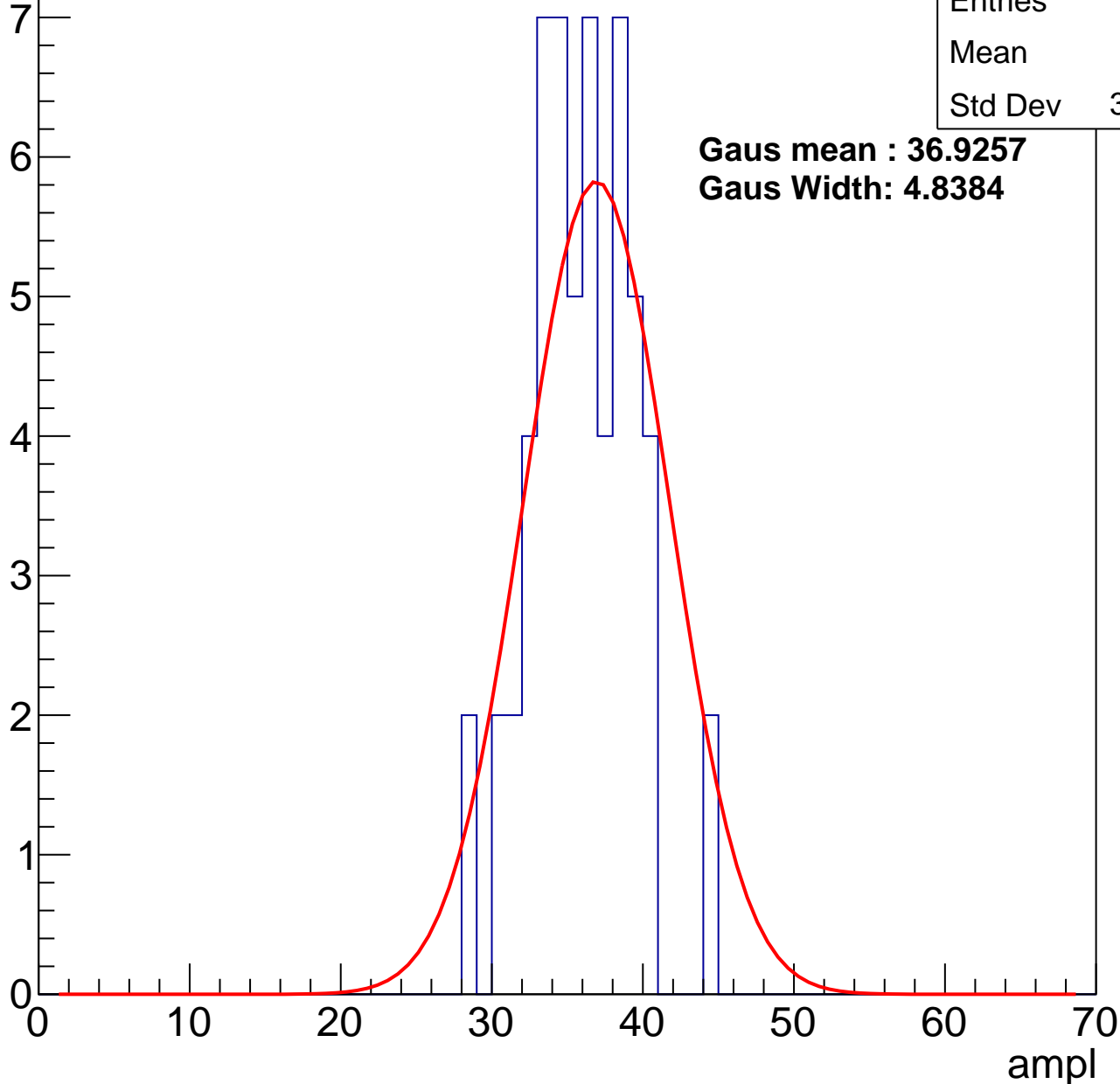
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.5
Std Dev	3.385

Gaus mean : 36.9257

Gaus Width: 4.8384



B1L100S, U10-ch118, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	42.31
Std Dev	3.745

Gaus mean : 42.6444

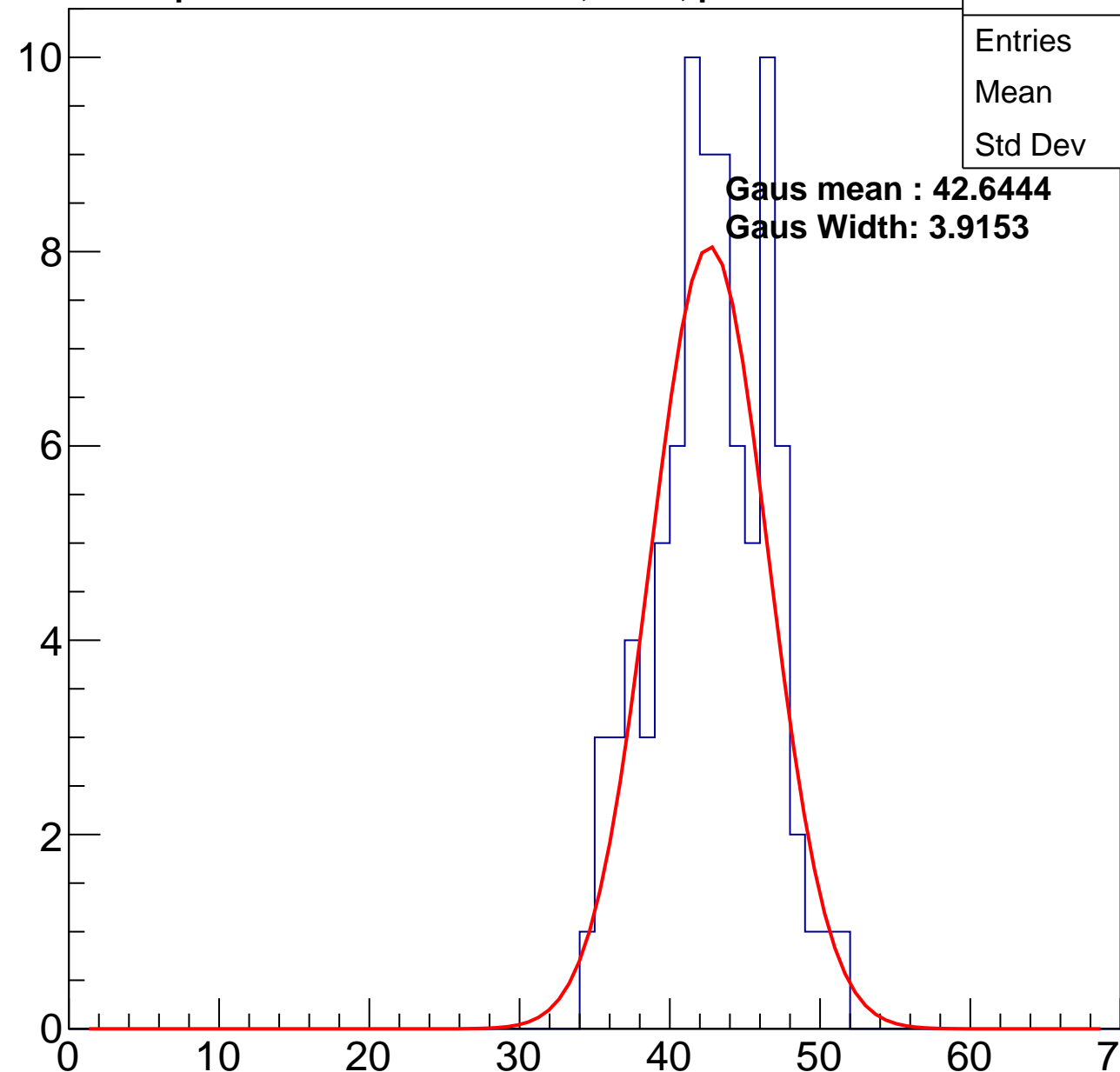
Gaus Width: 3.9153

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

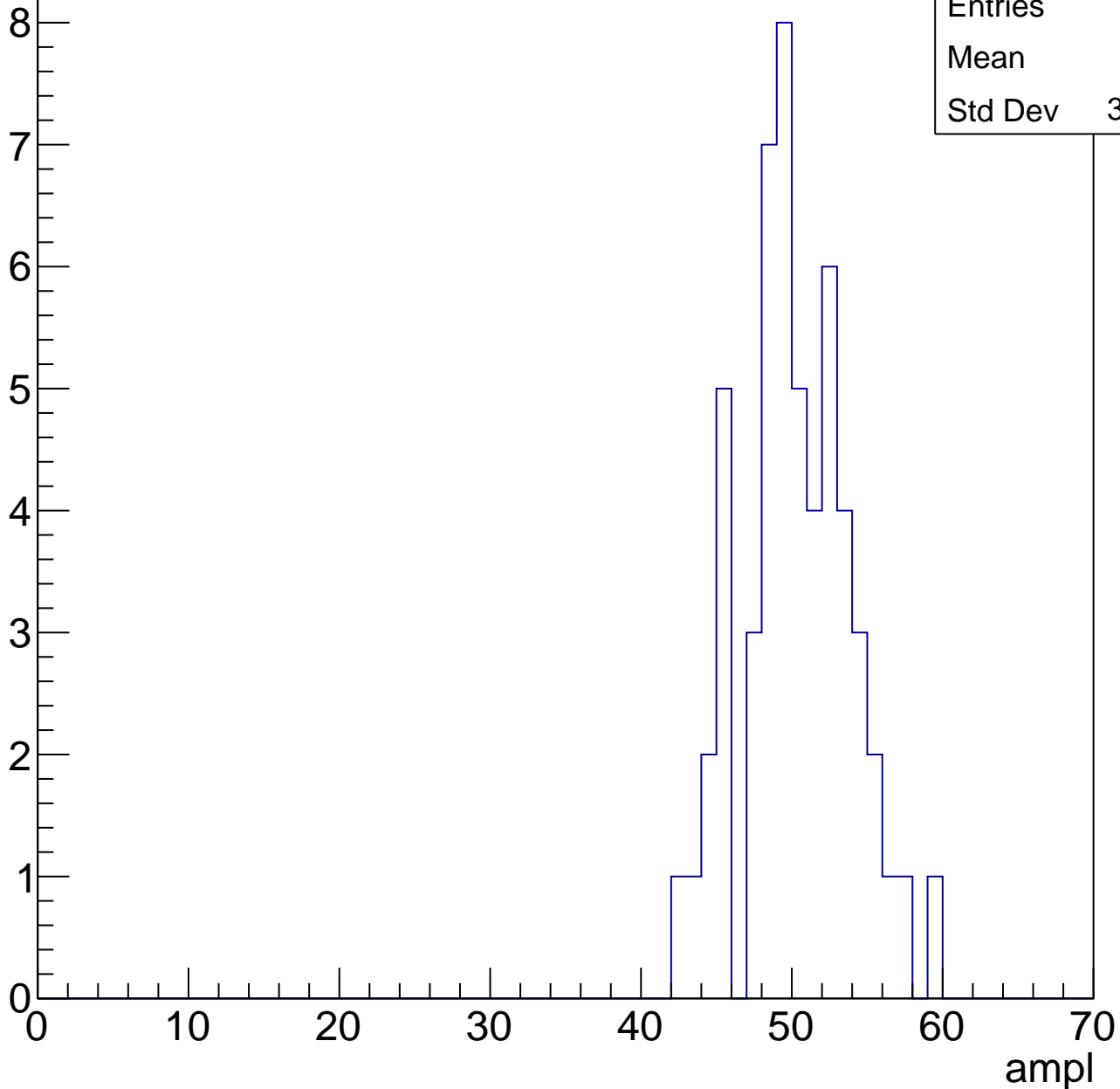


B1L100S, U10-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	49.8
Std Dev	3.597

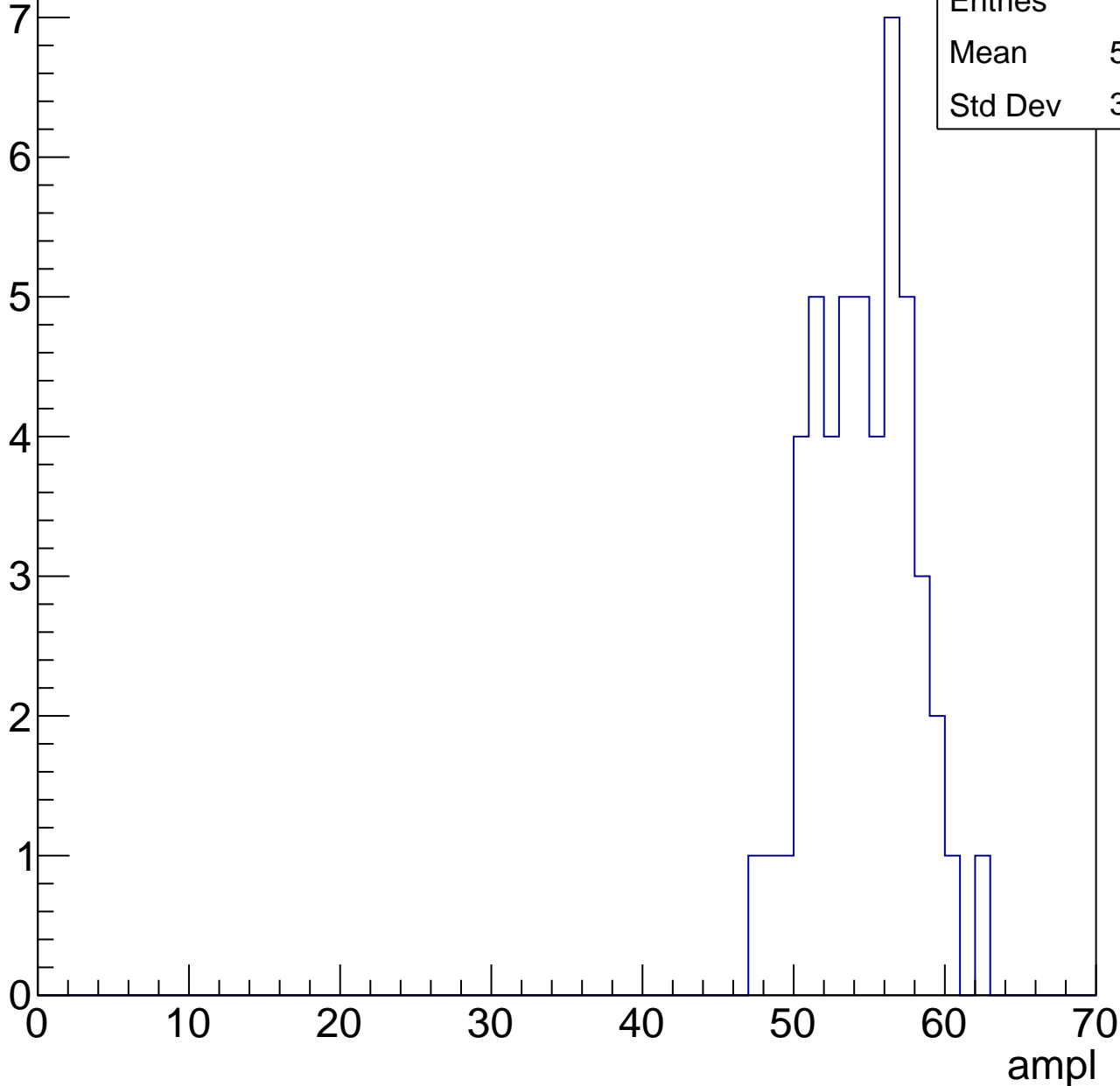


B1L100S, U10-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

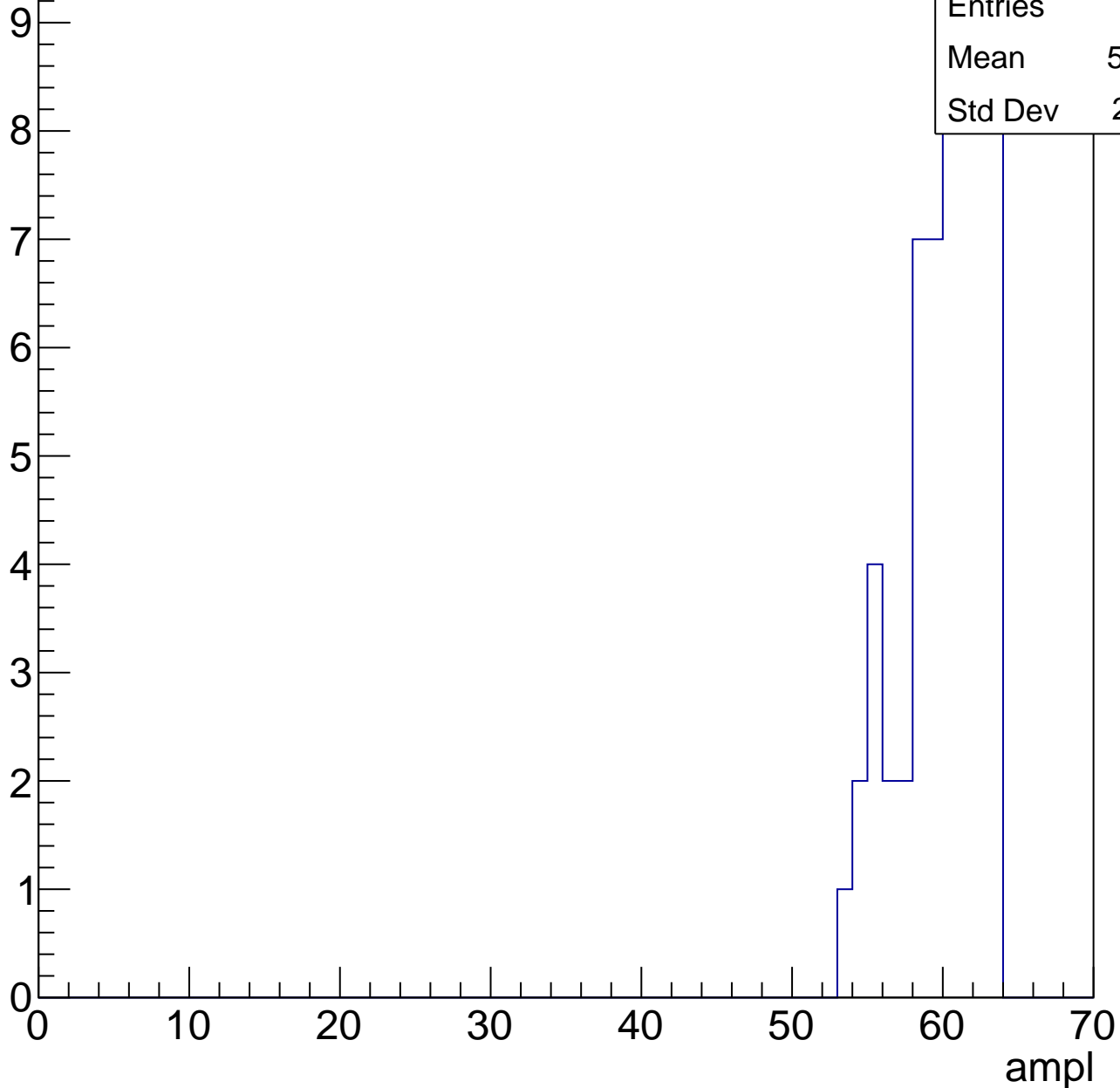
Entries	49
Mean	54.14
Std Dev	3.239



B1L100S, U10-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

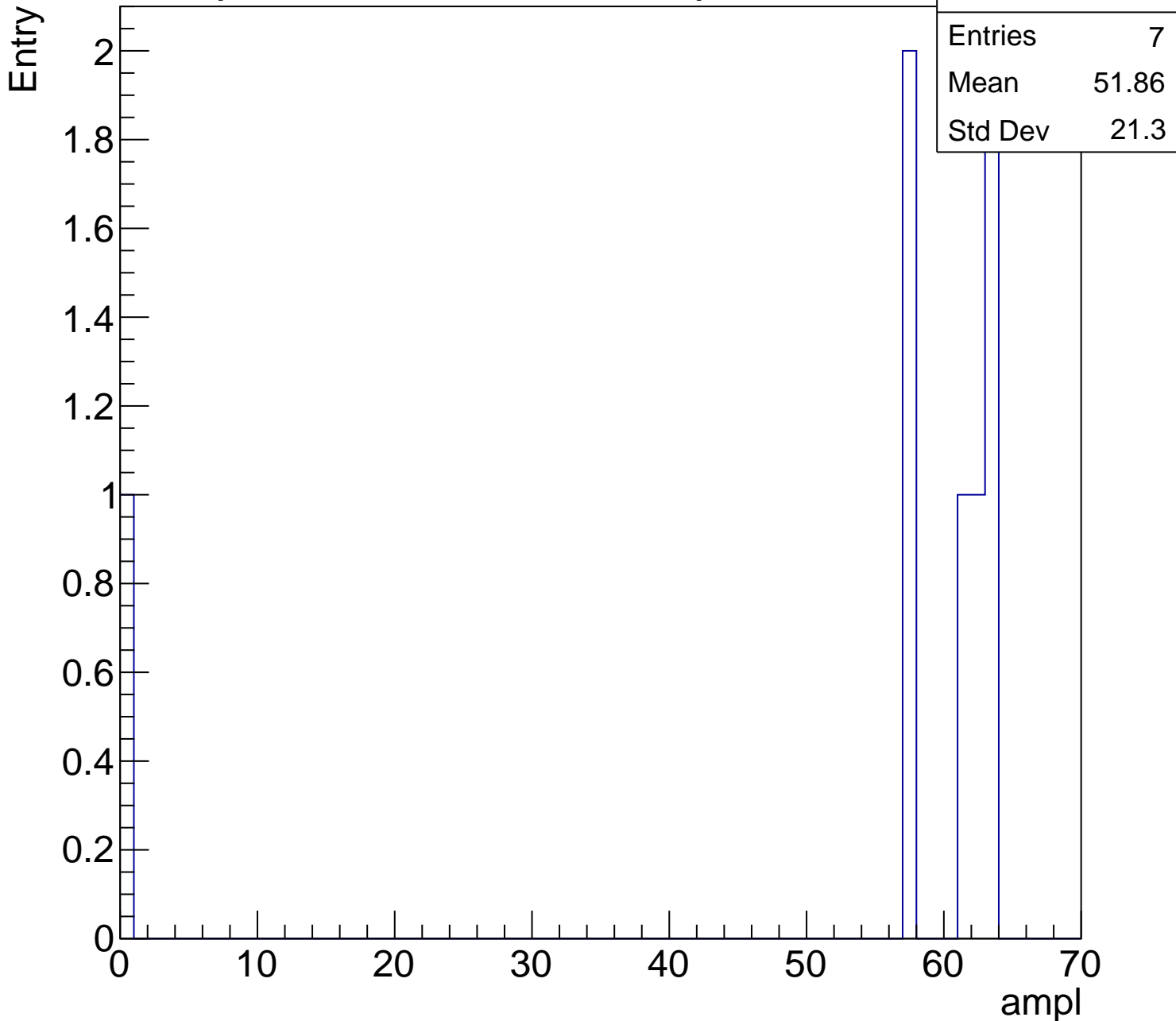
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	51.86
Std Dev	21.3

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch119, adc0

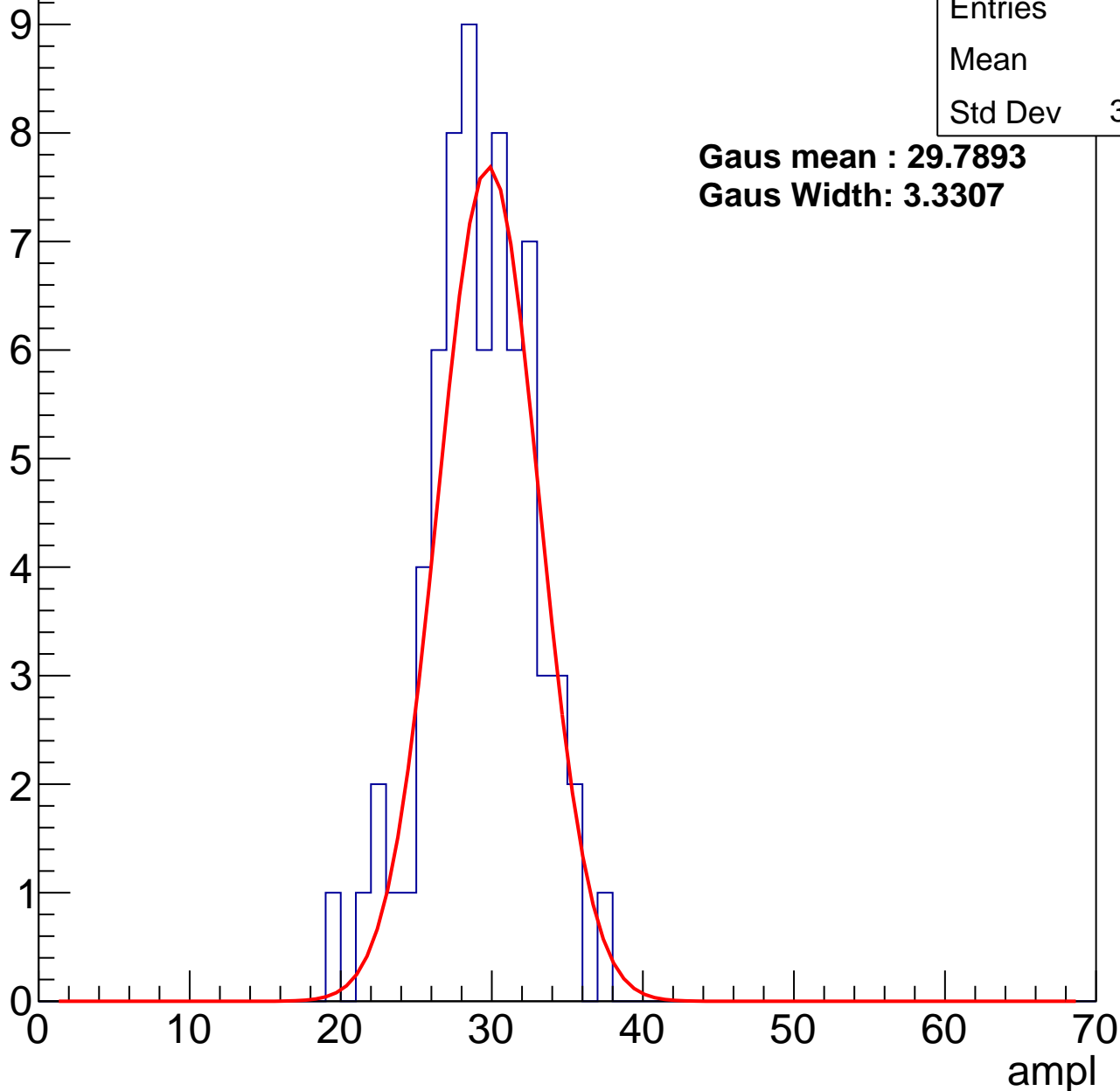
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.8
Std Dev	3.475

Gaus mean : 29.7893

Gaus Width: 3.3307



B1L100S, U10-ch119, adc1

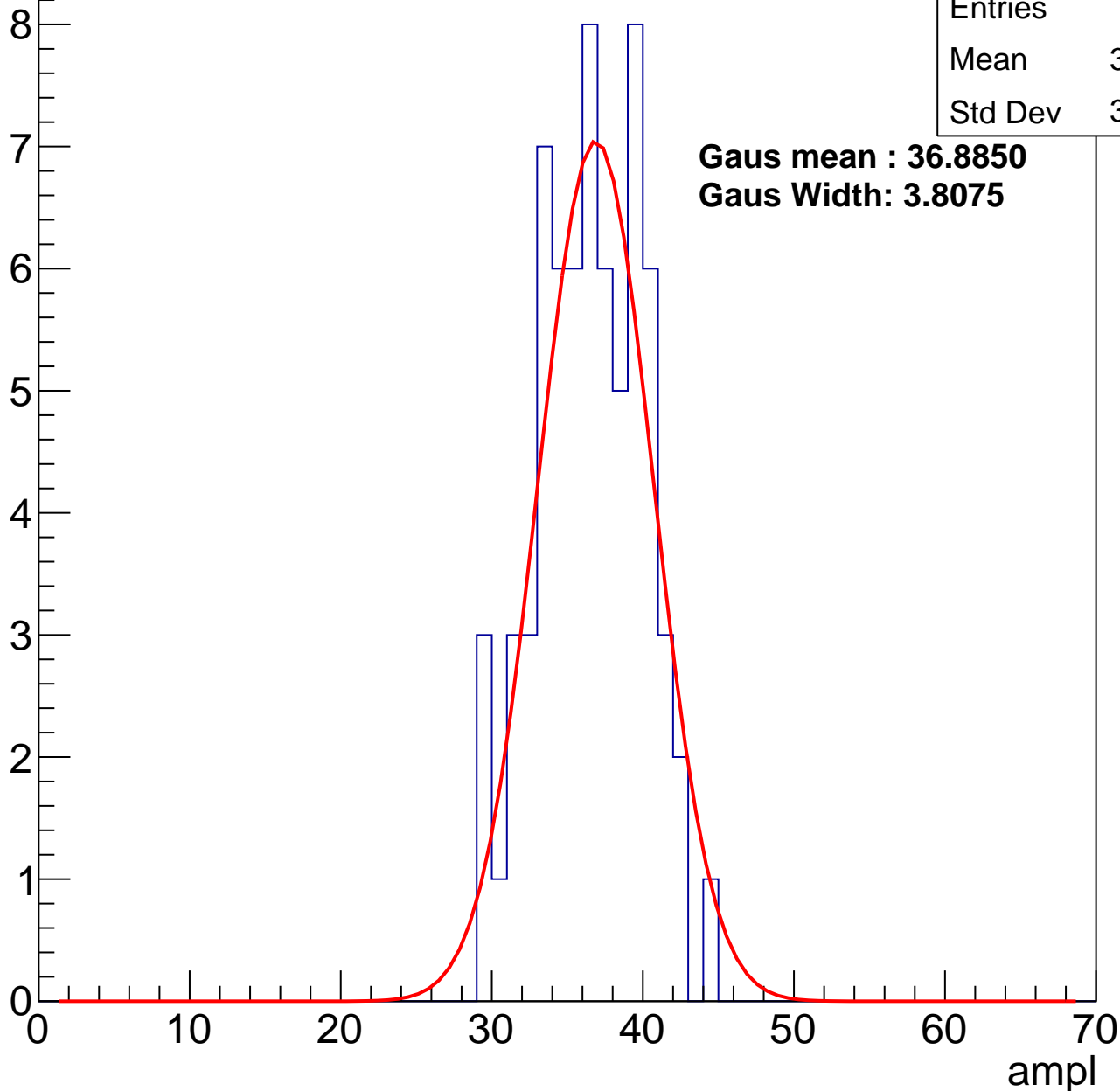
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.09
Std Dev	3.437

Gaus mean : 36.8850

Gaus Width: 3.8075



B1L100S, U10-ch119, adc2

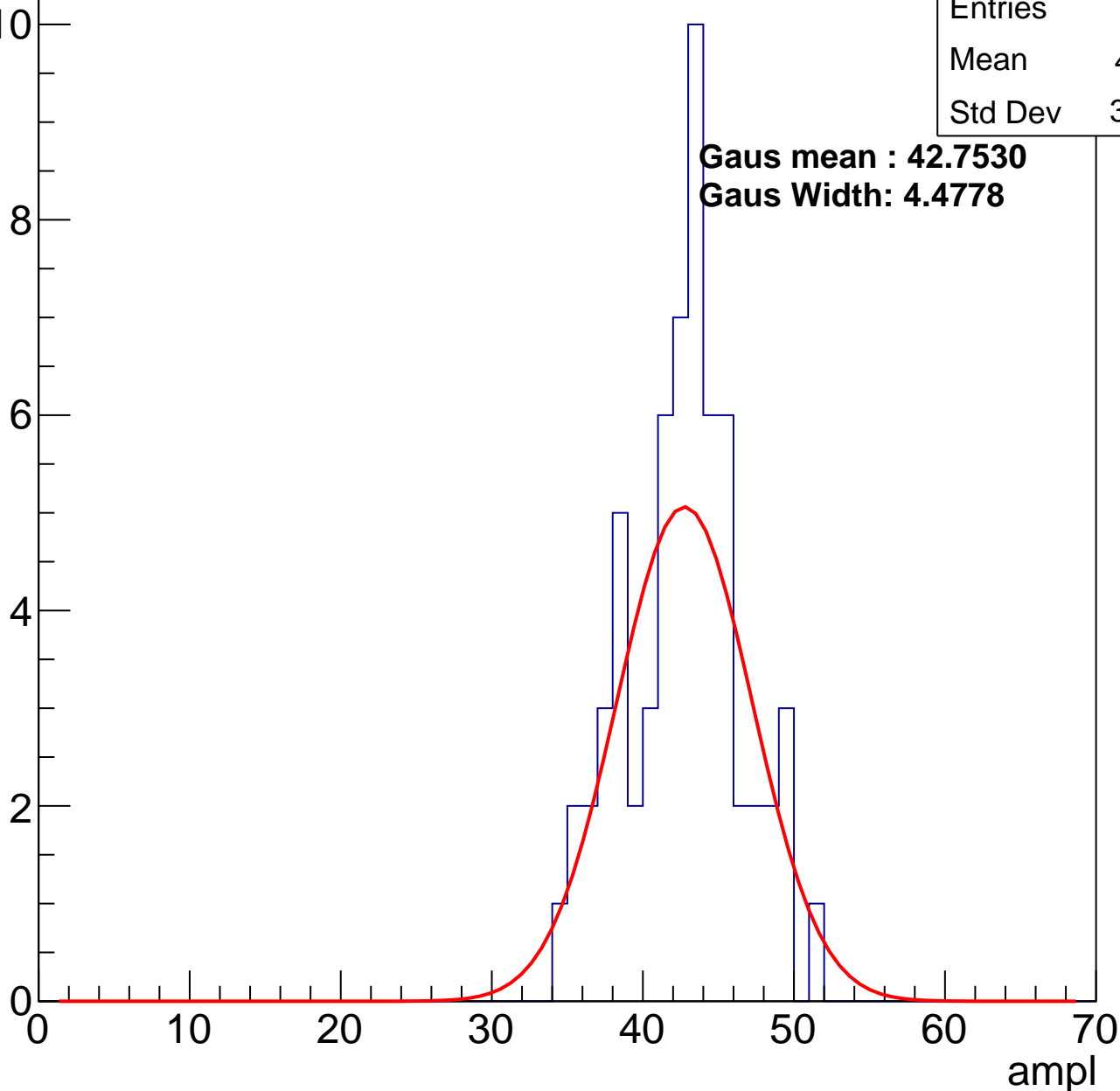
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.21
Std Dev	3.763

Gaus mean : 42.7530

Gaus Width: 4.4778

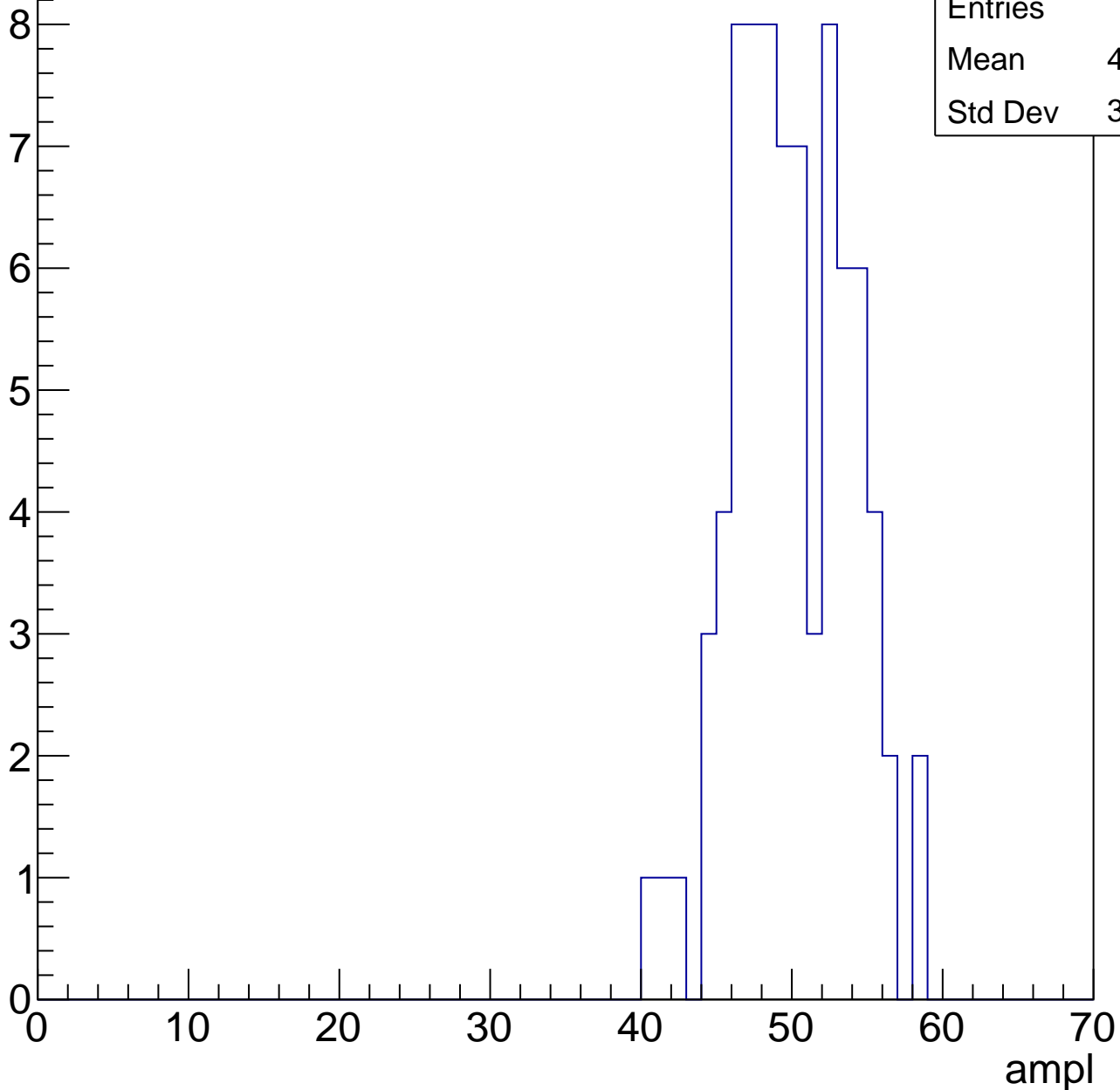


B1L100S, U10-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	49.56
Std Dev	3.834

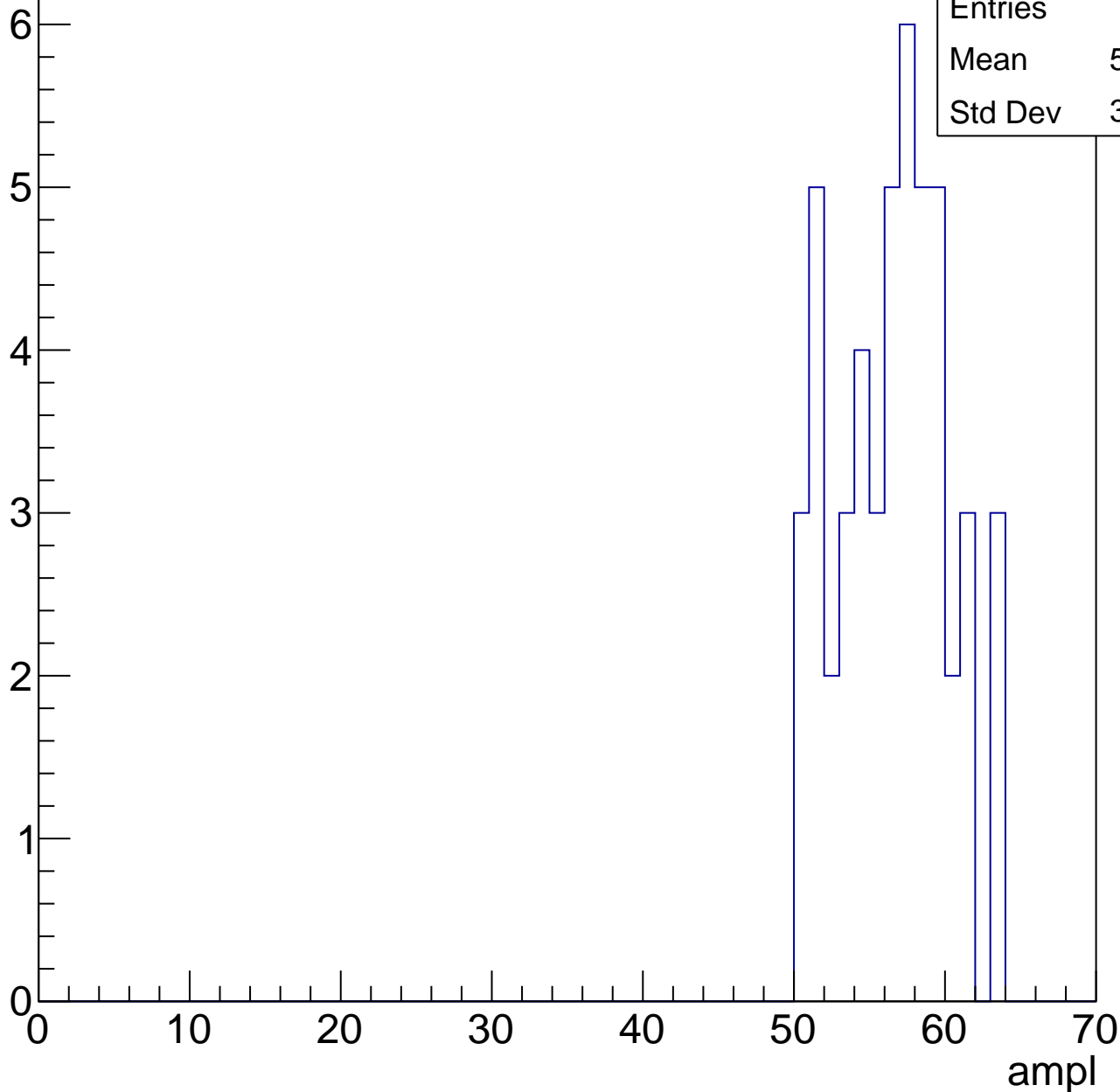


B1L100S, U10-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	56.08
Std Dev	3.602

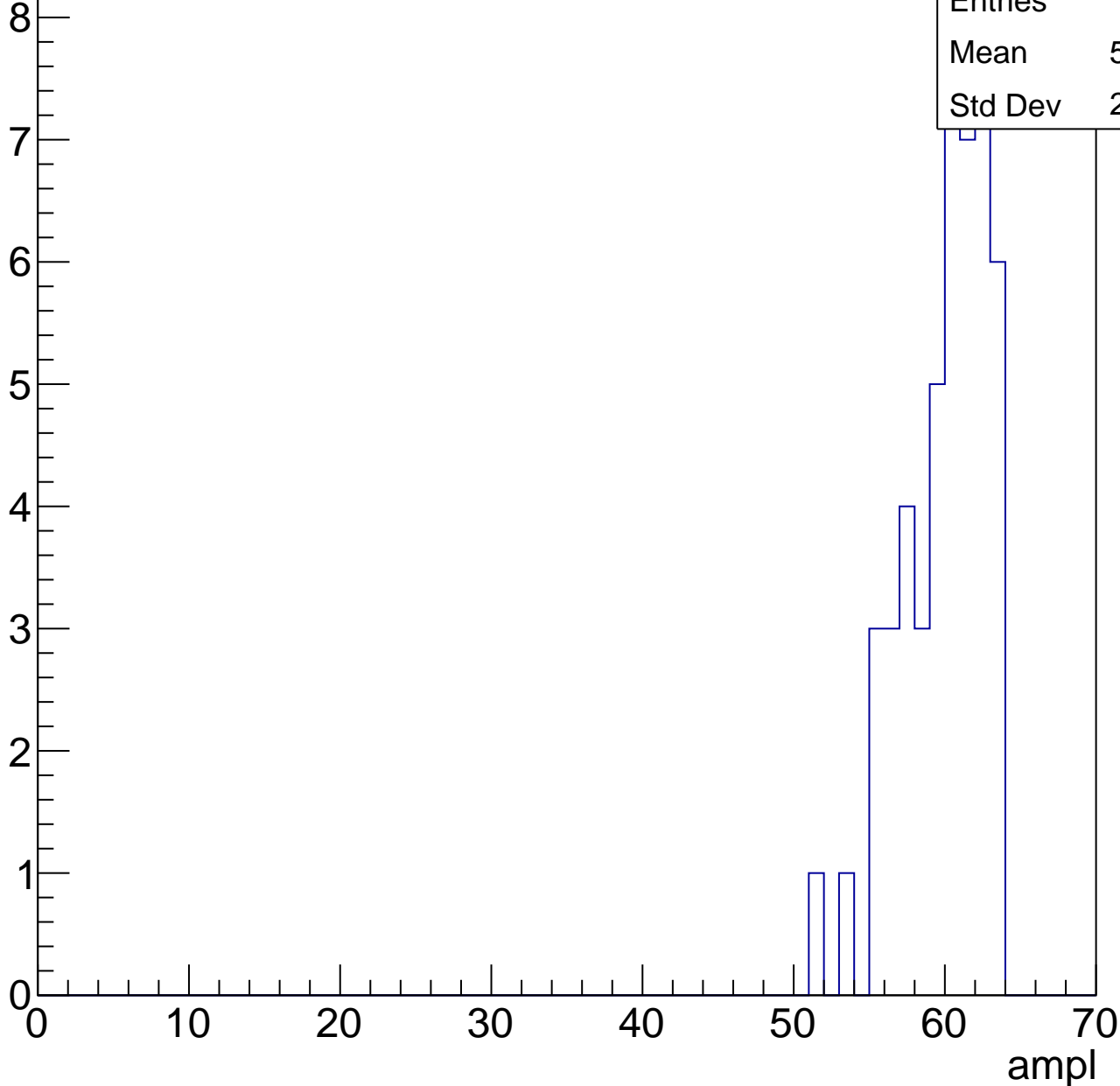


B1L100S, U10-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	59.49
Std Dev	2.808



B1L100S, U10-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	26.99

ampl

B1L100S, U10-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch120, adc0

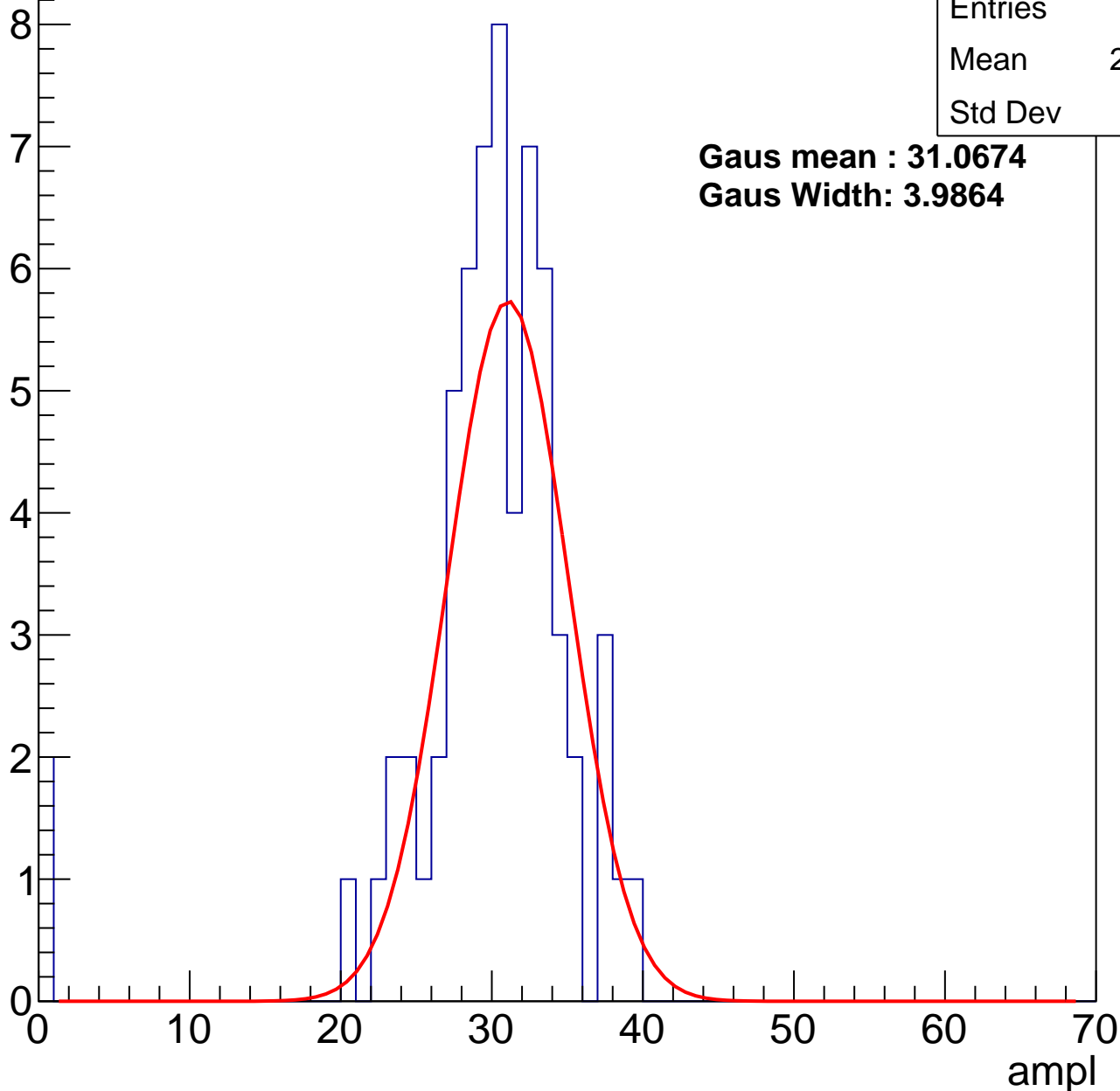
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.14
Std Dev	6.49

Gaus mean : 31.0674

Gaus Width: 3.9864



B1L100S, U10-ch120, adc1

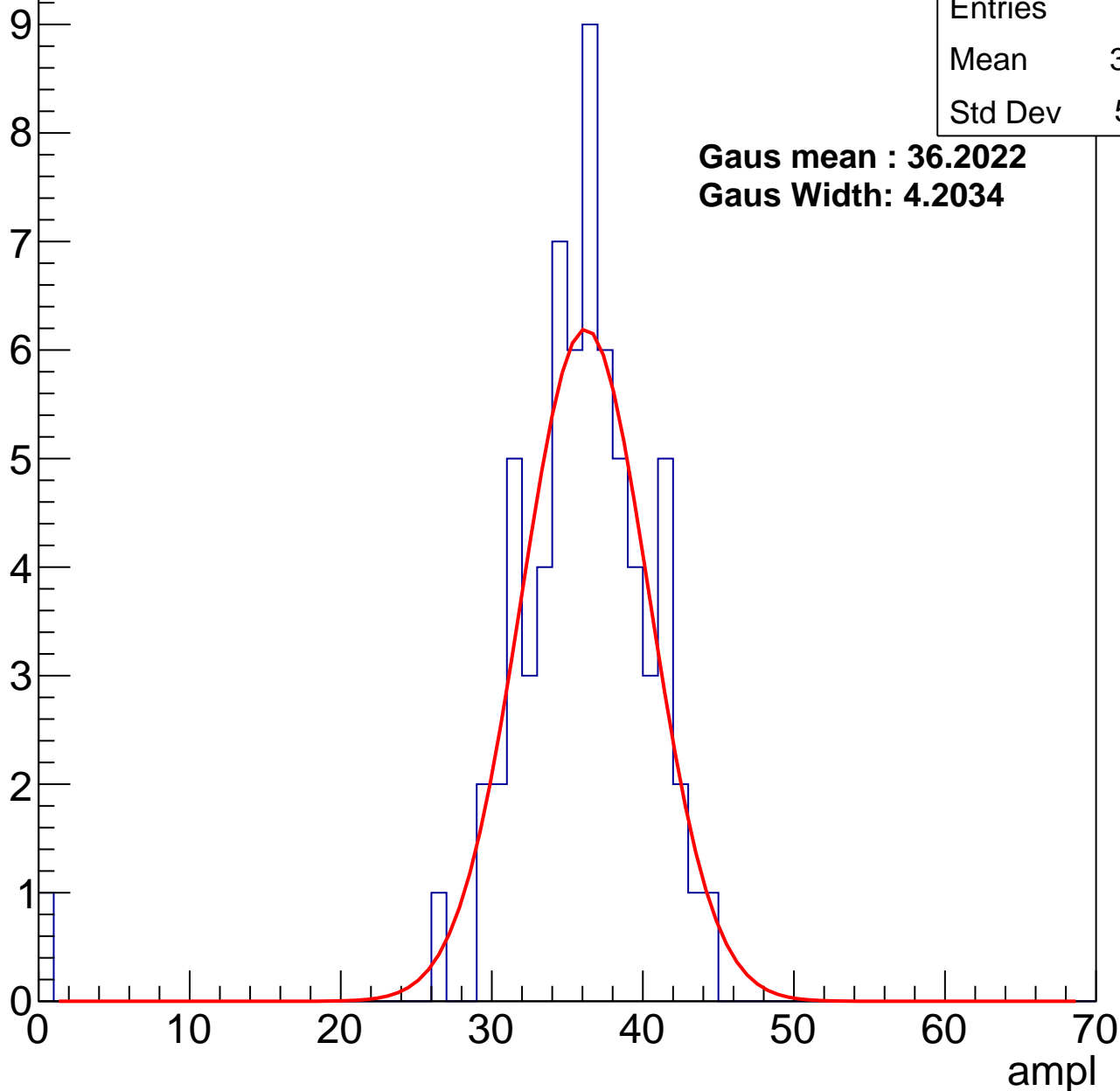
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.27
Std Dev	5.721

Gaus mean : 36.2022

Gaus Width: 4.2034



B1L100S, U10-ch120, adc2

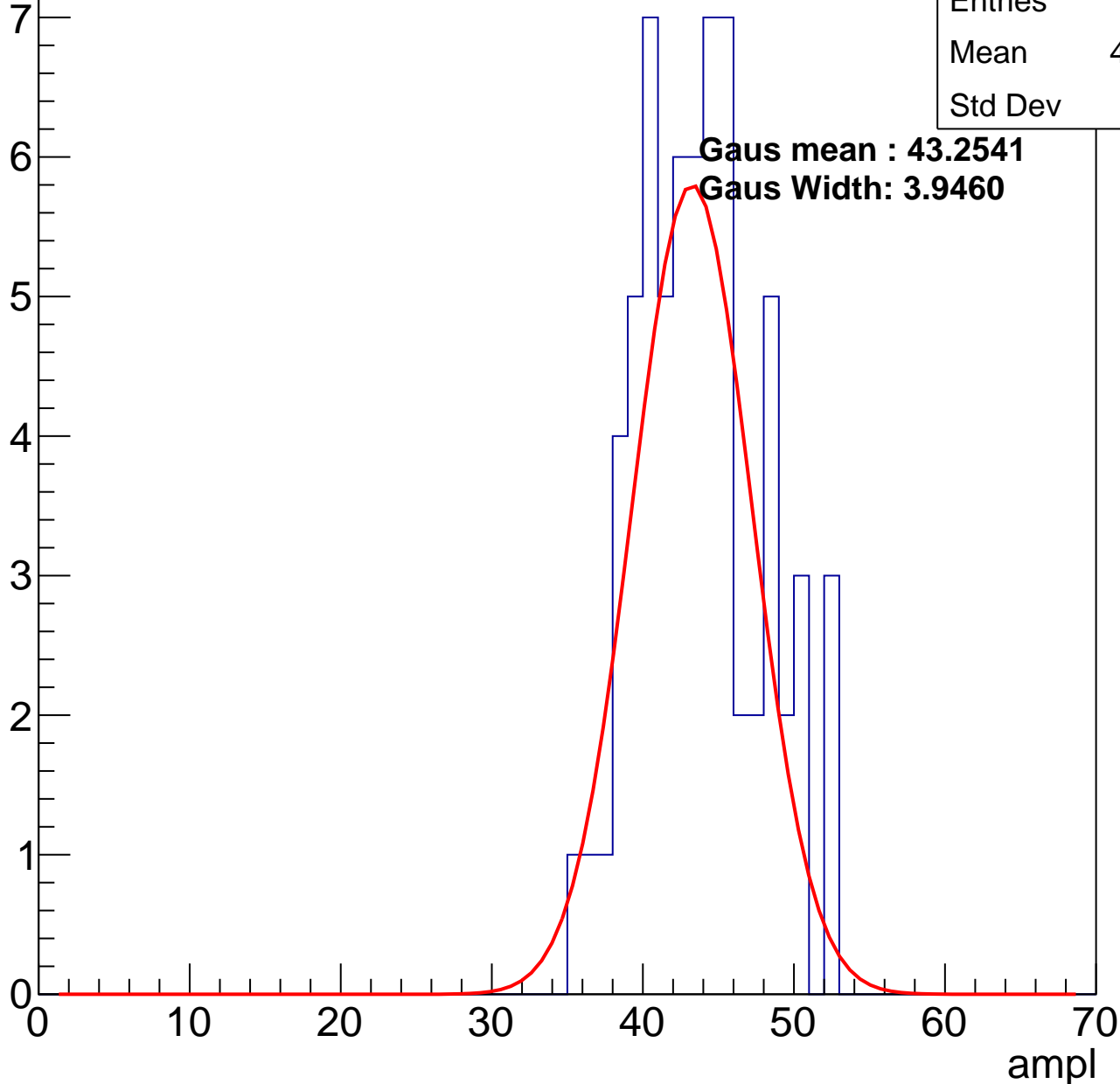
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.33
Std Dev	4.02

Gaus mean : 43.2541

Gaus Width: 3.9460

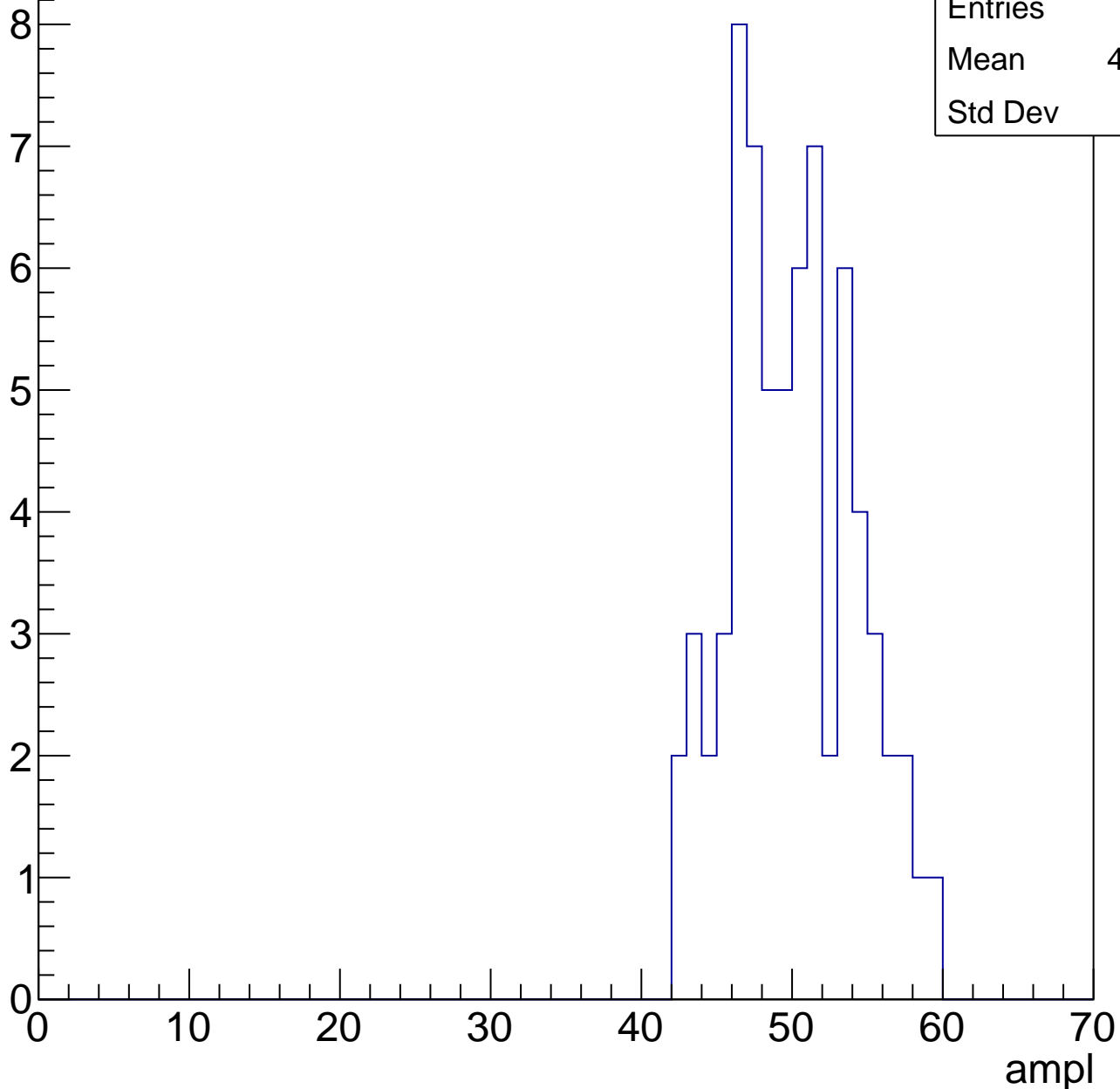


B1L100S, U10-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	49.58
Std Dev	4.08

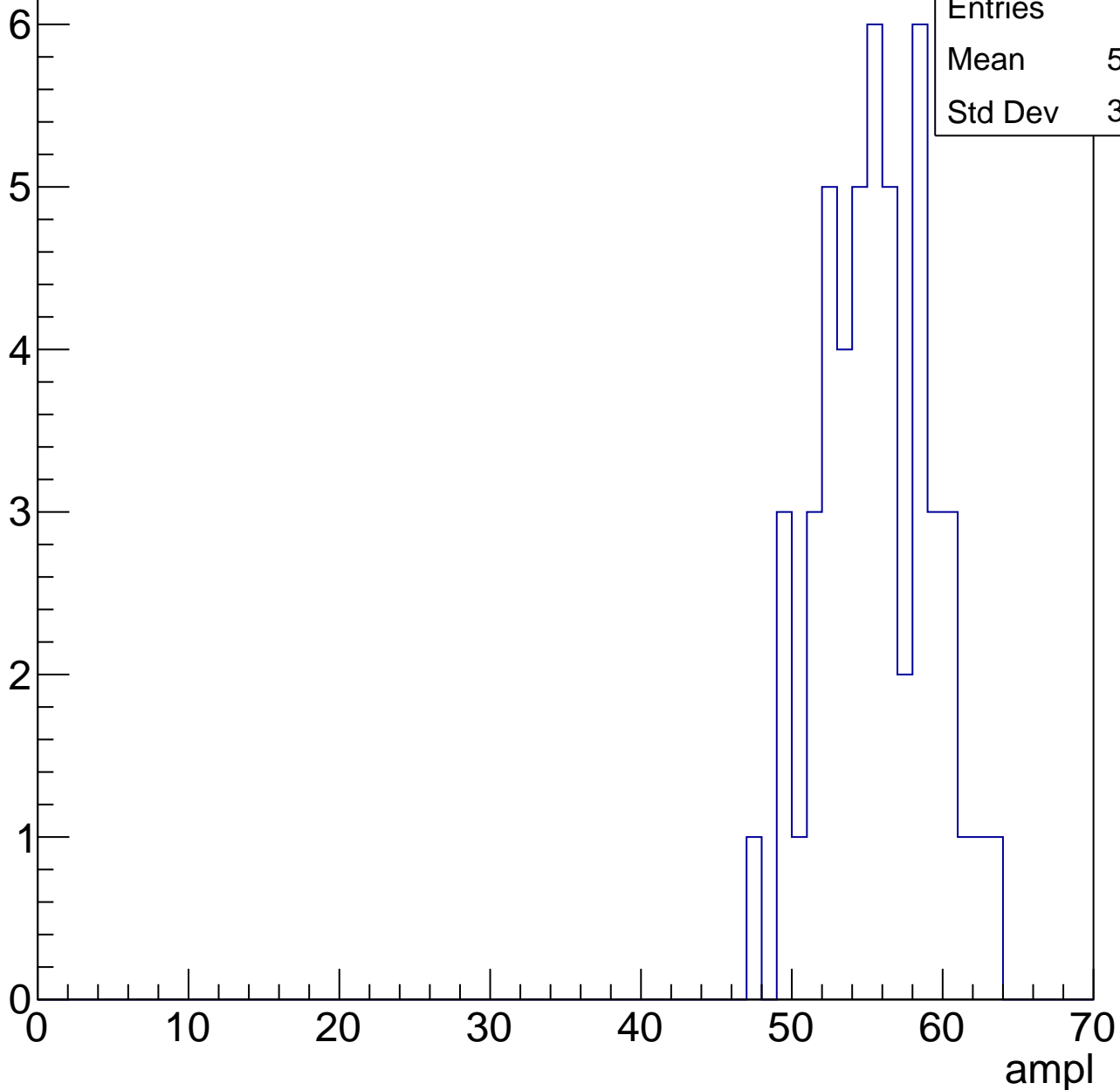


B1L100S, U10-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	55.08
Std Dev	3.605

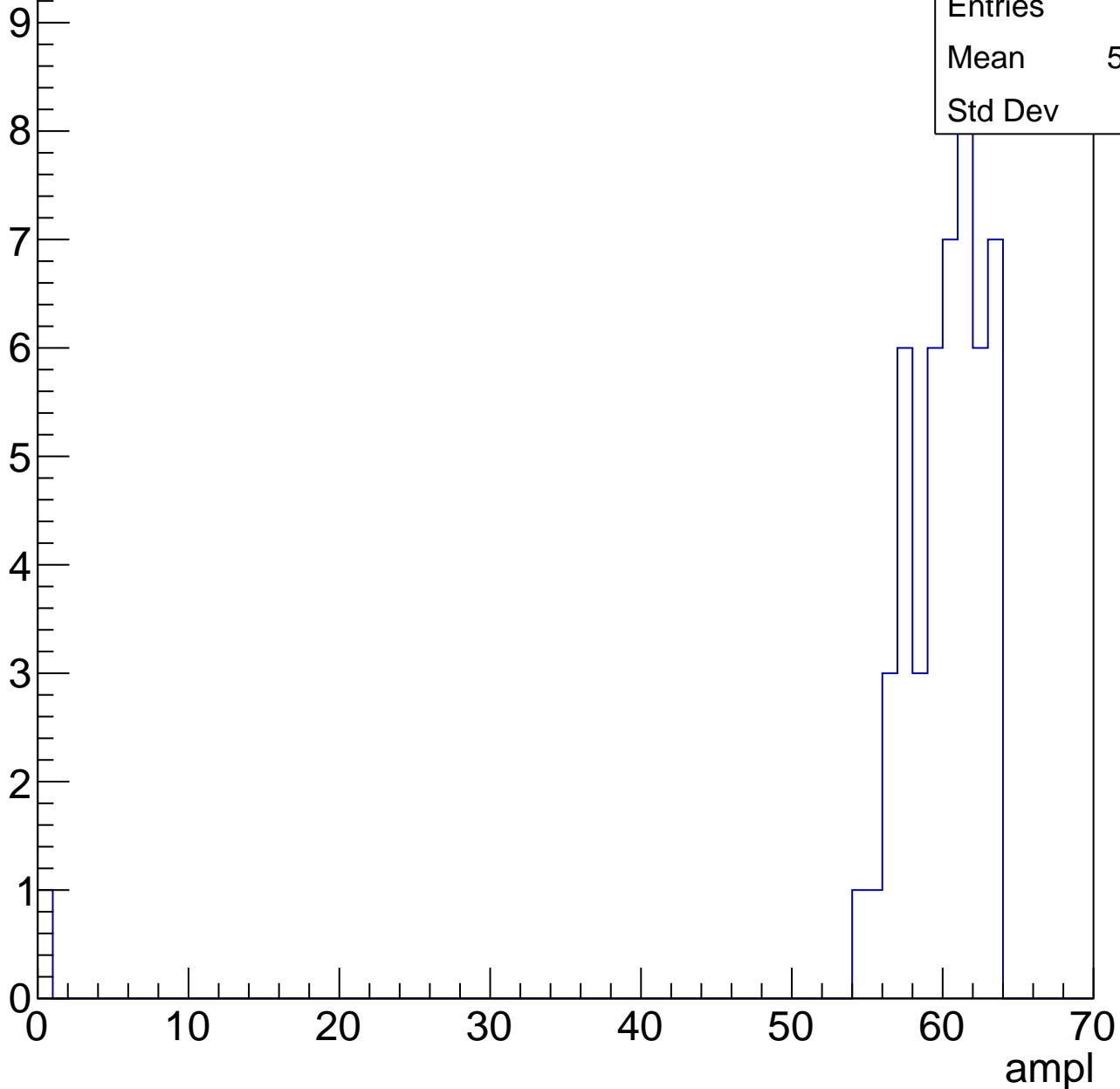


B1L100S, U10-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

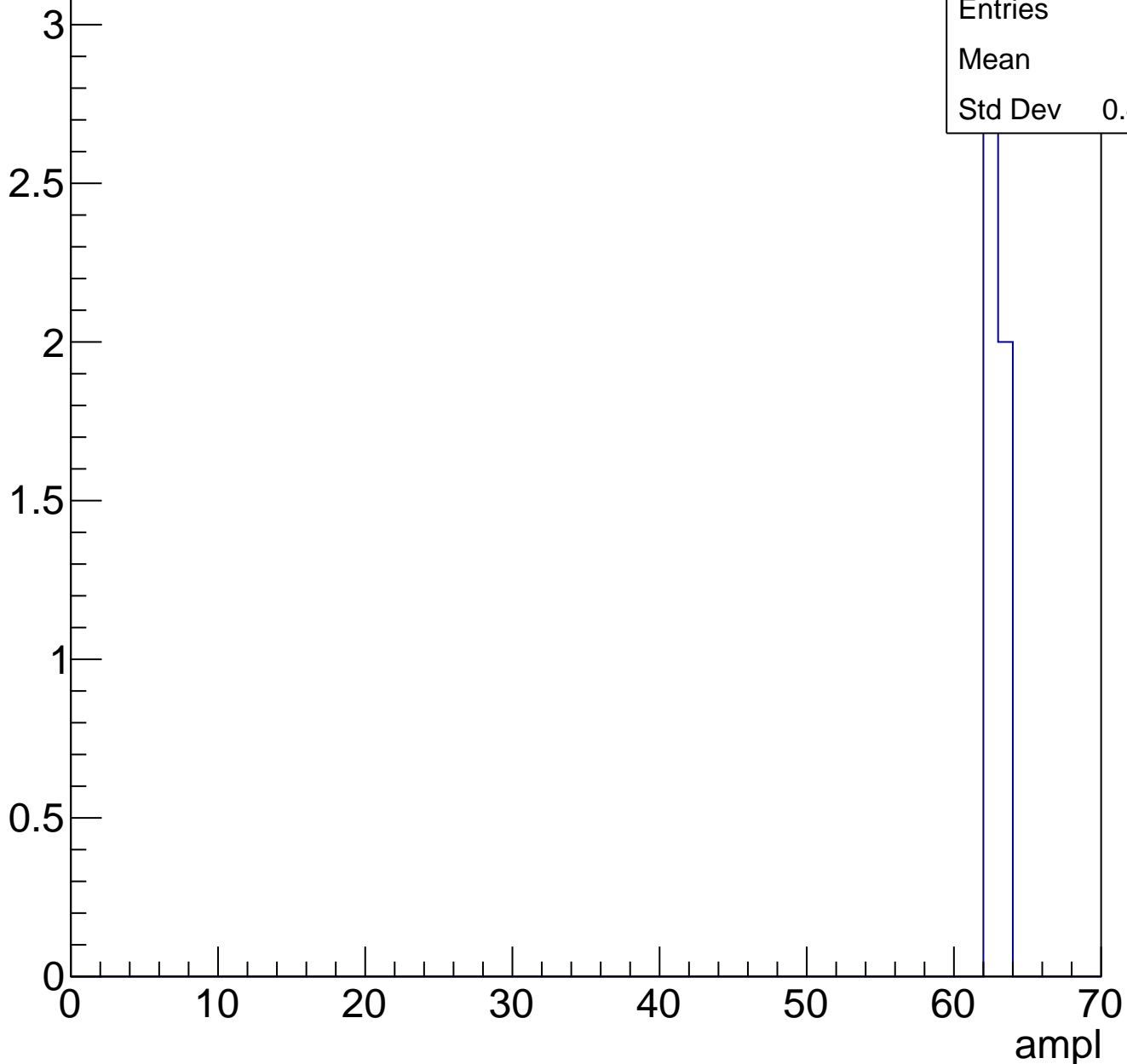
Entries	50
Mean	58.58
Std Dev	8.69



B1L100S, U10-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch121, adc0

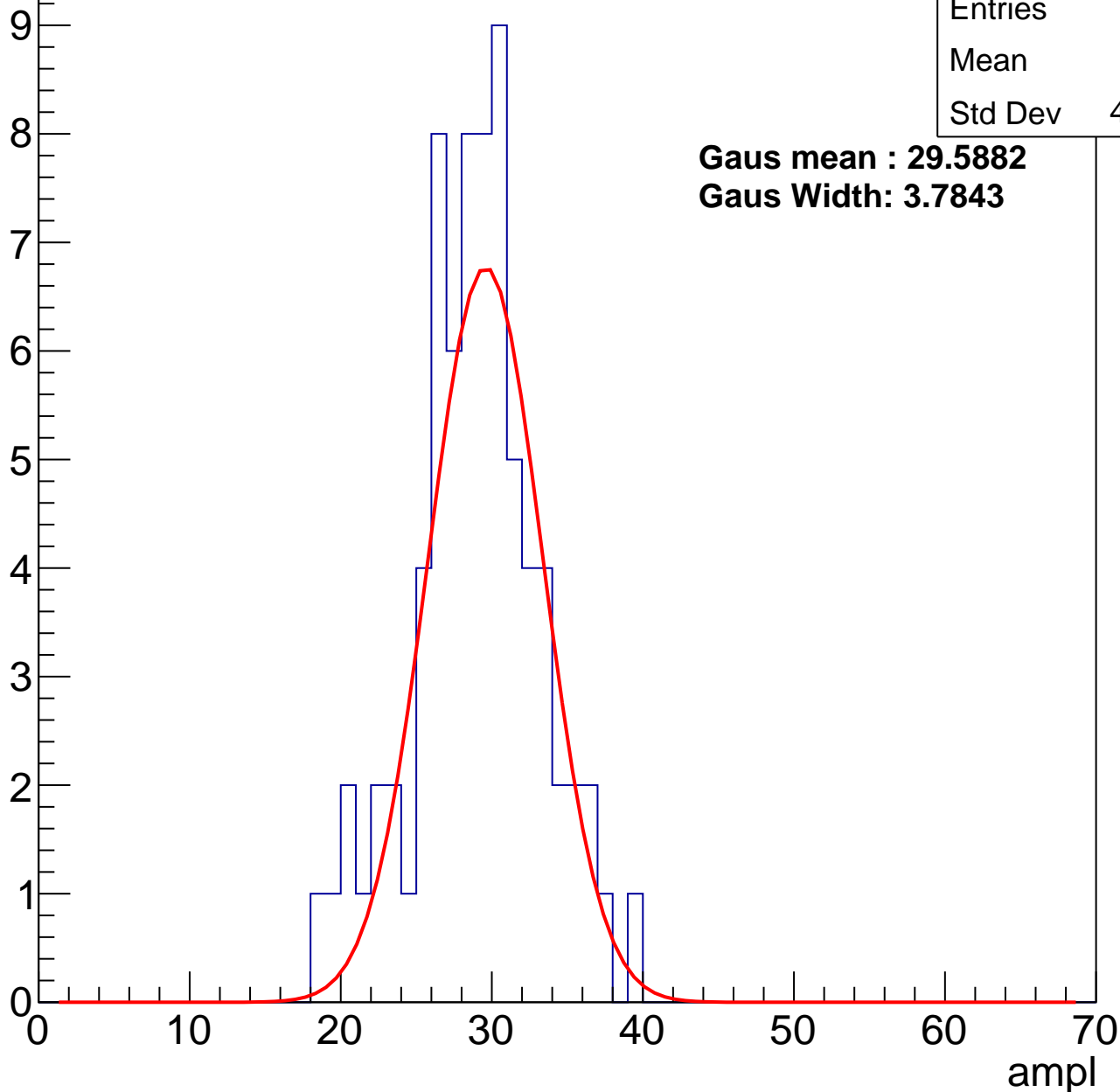
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.5
Std Dev	4.199

Gaus mean : 29.5882

Gaus Width: 3.7843



B1L100S, U10-ch121, adc1

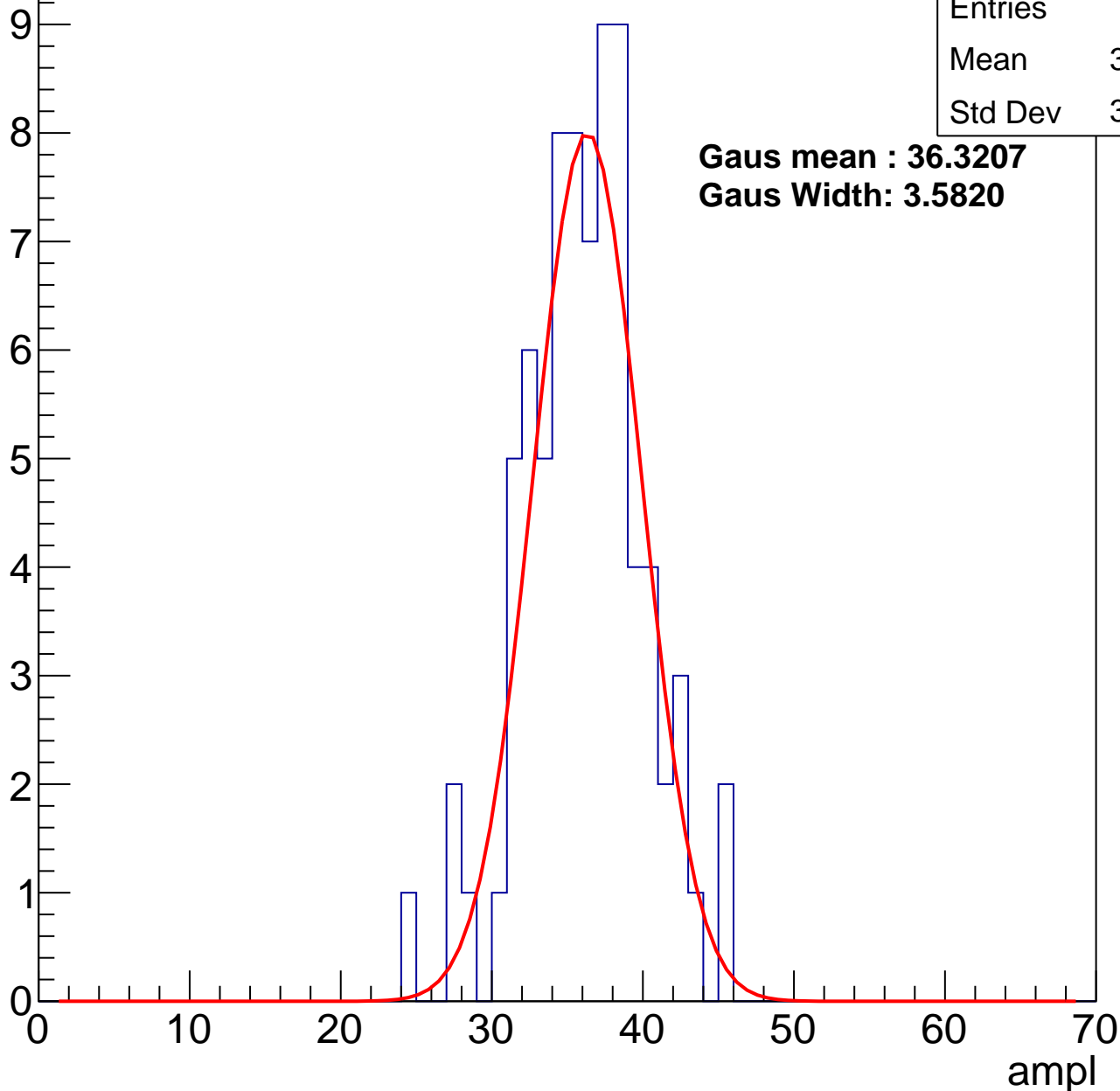
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	35.69
Std Dev	3.953

Gaus mean : 36.3207

Gaus Width: 3.5820



B1L100S, U10-ch121, adc2

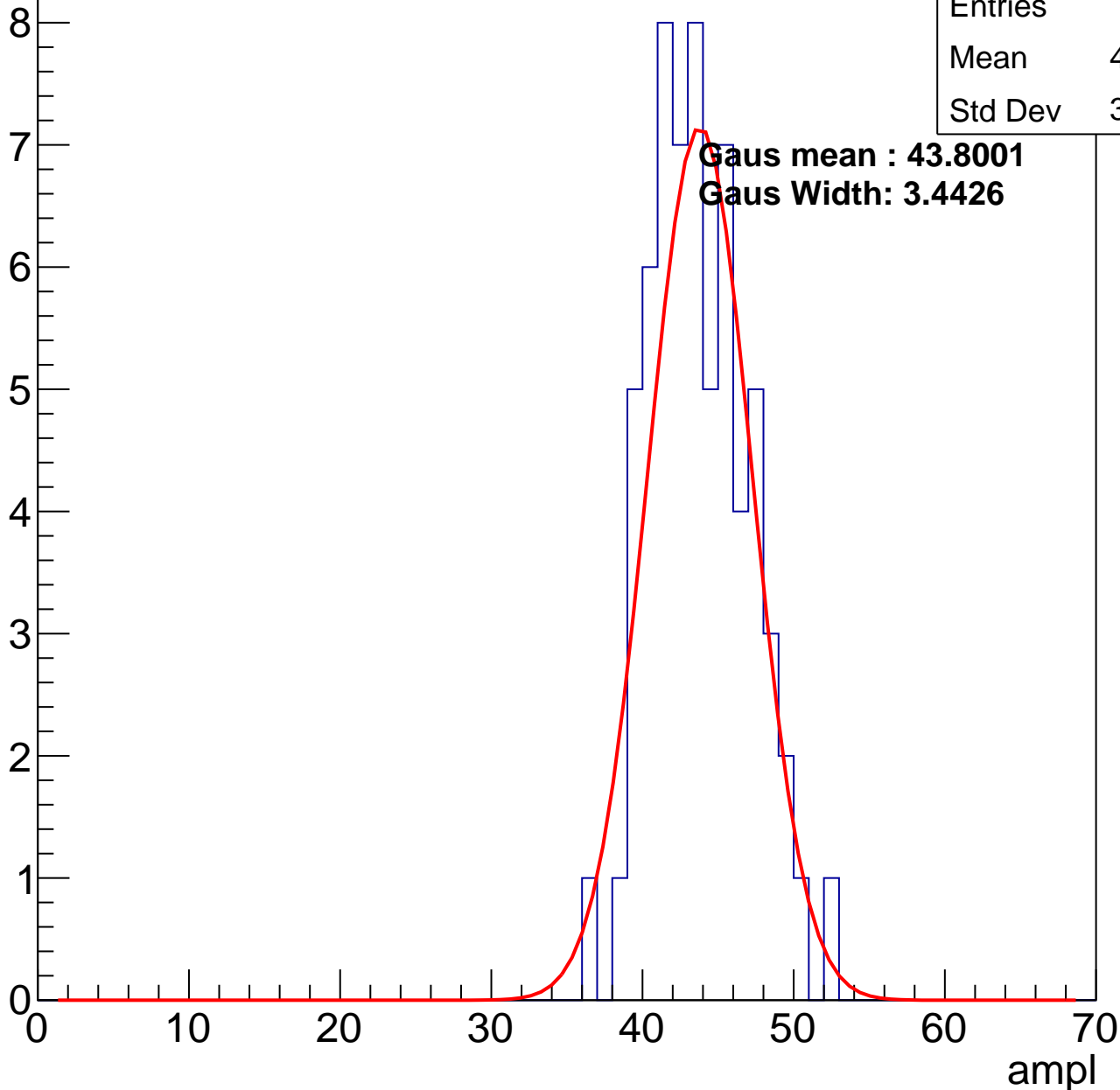
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.33
Std Dev	3.226

Gaus mean : 43.8001

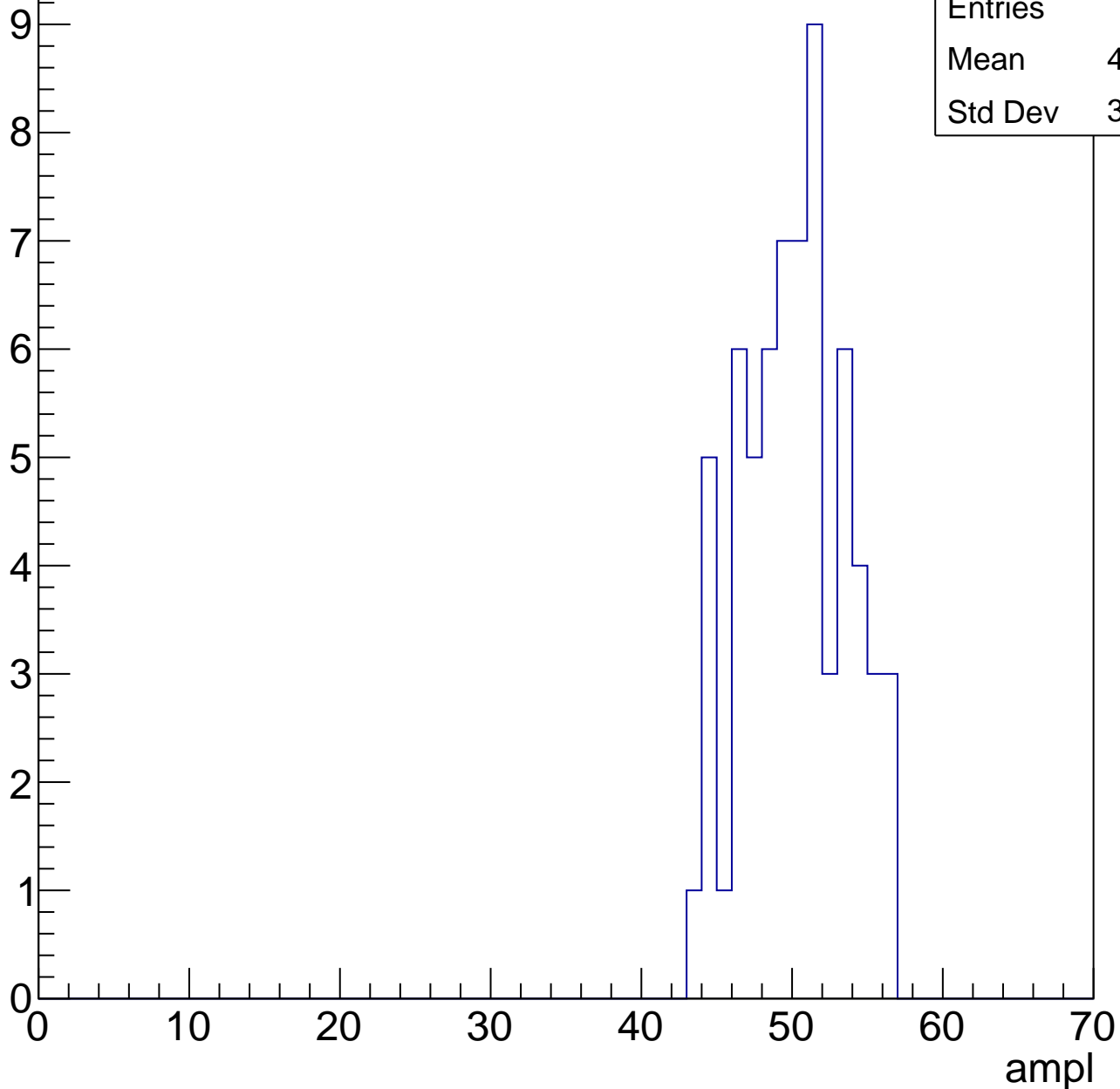
Gaus Width: 3.4426



B1L100S, U10-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

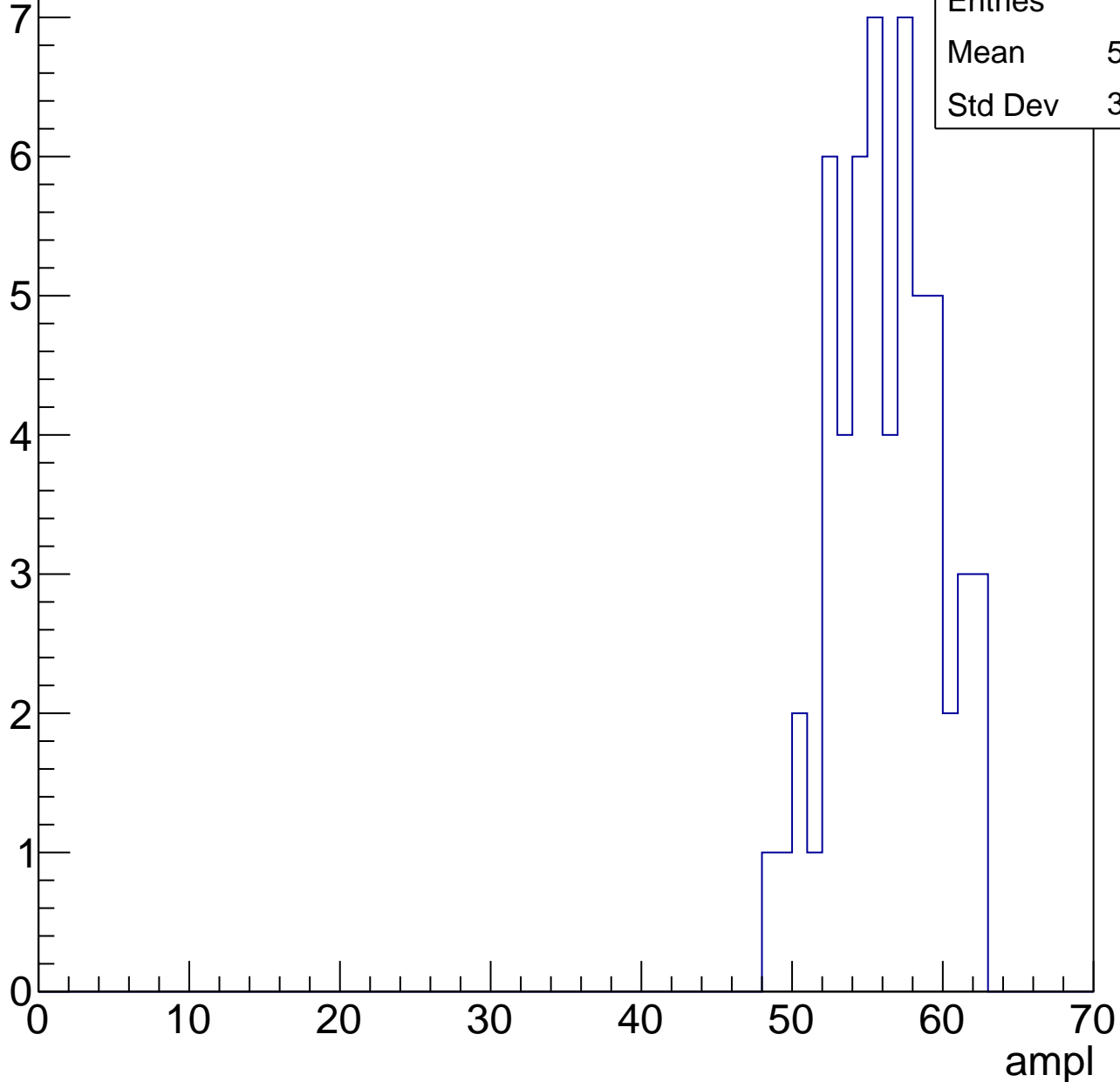
Entry



B1L100S, U10-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

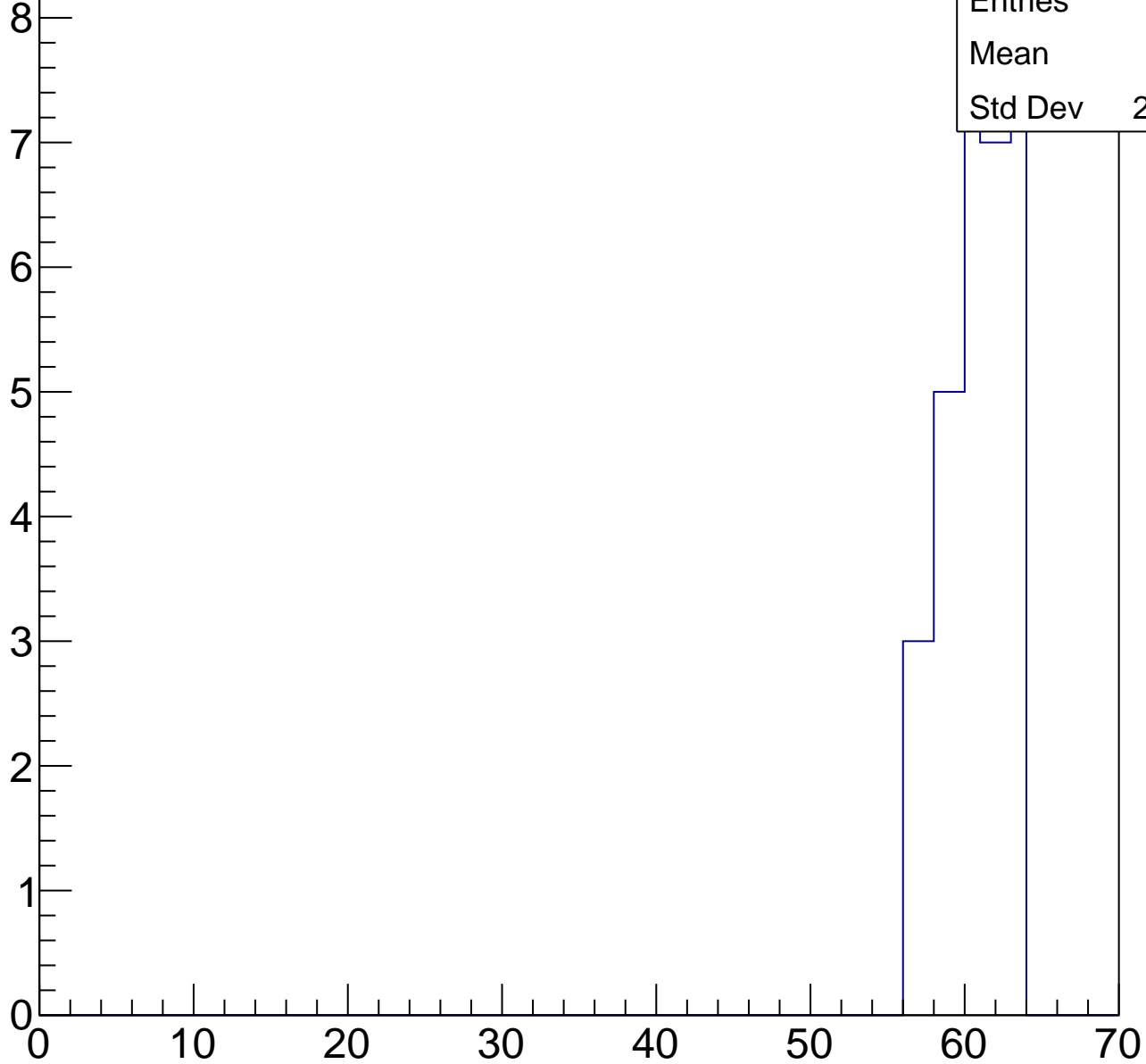


Entries	57
Mean	55.75
Std Dev	3.394

B1L100S, U10-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	46
Mean	60.2
Std Dev	2.112

B1L100S, U10-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch122, adc0

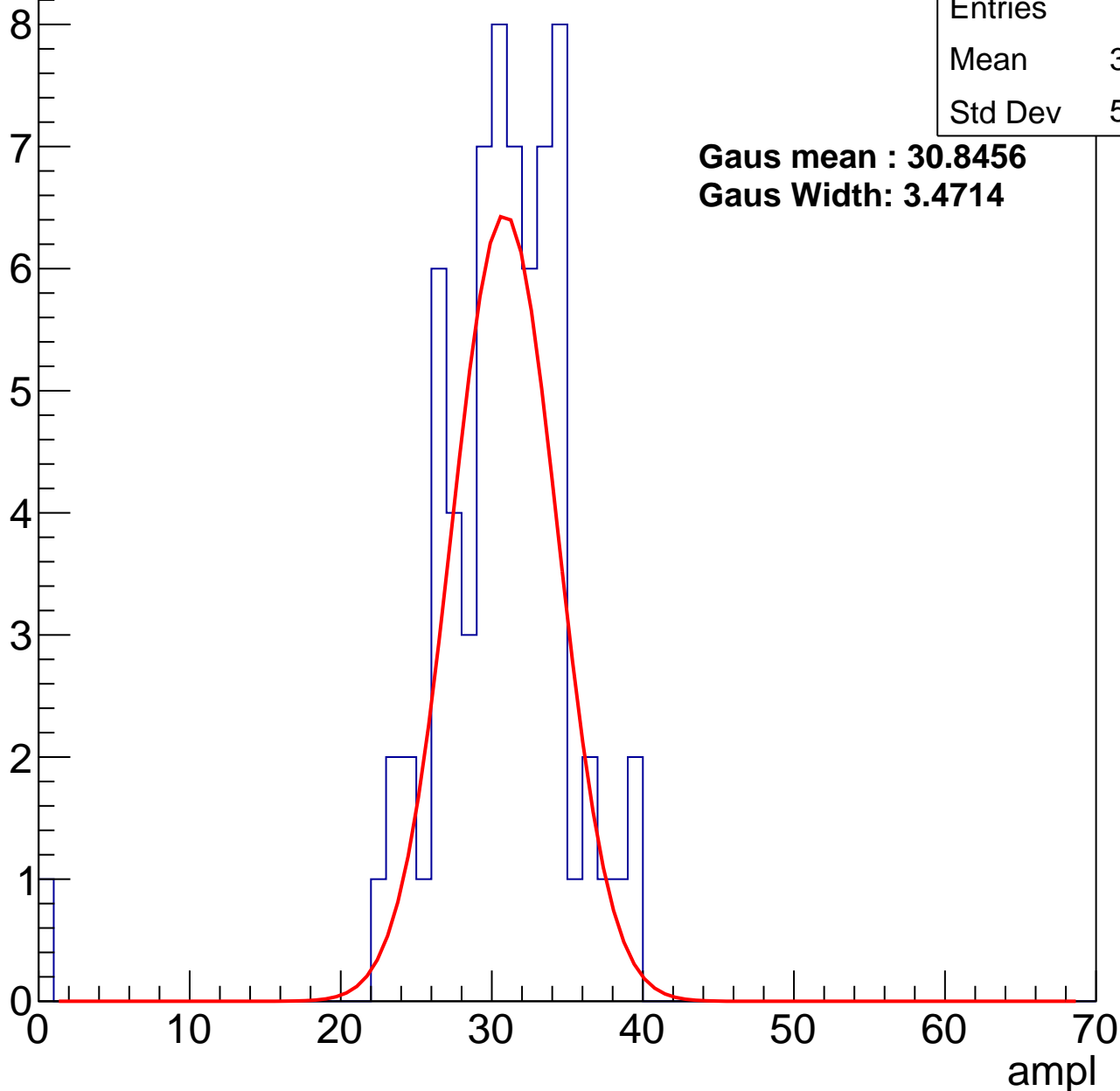
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.06
Std Dev	5.215

Gaus mean : 30.8456

Gaus Width: 3.4714



B1L100S, U10-ch122, adc1

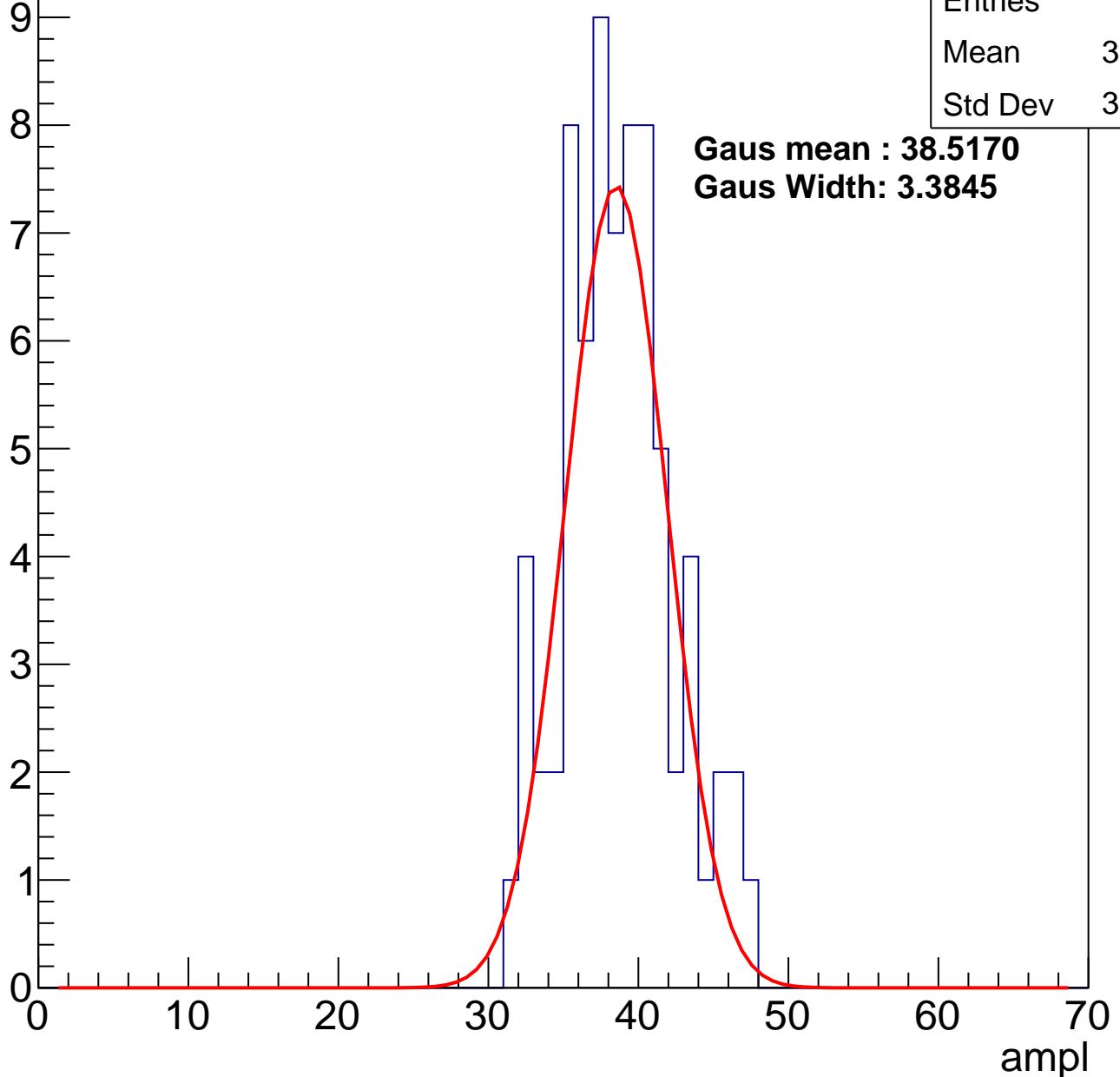
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.25
Std Dev	3.605

Gaus mean : 38.5170

Gaus Width: 3.3845



B1L100S, U10-ch122, adc2

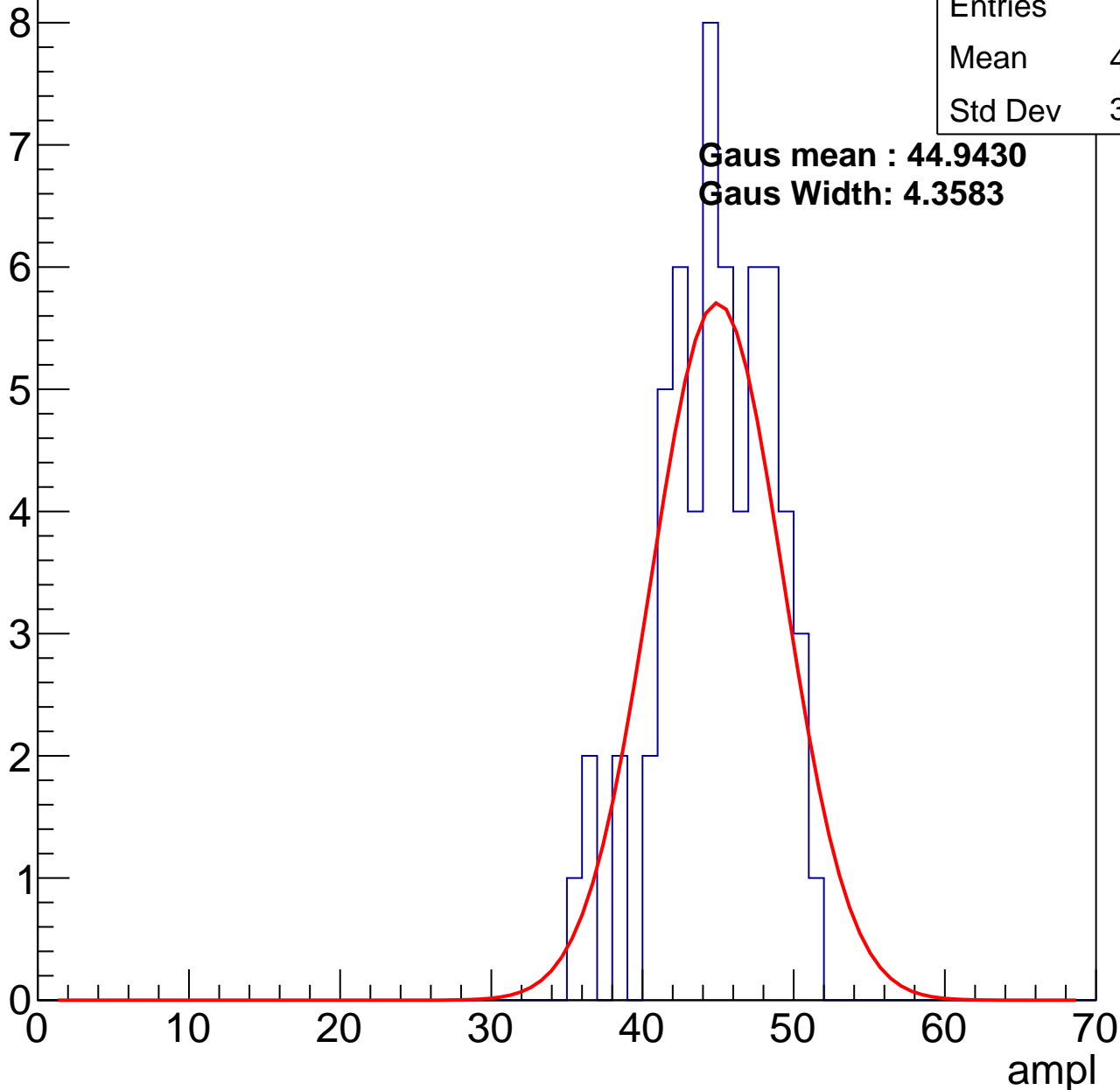
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.42
Std Dev	3.662

Gaus mean : 44.9430

Gaus Width: 4.3583

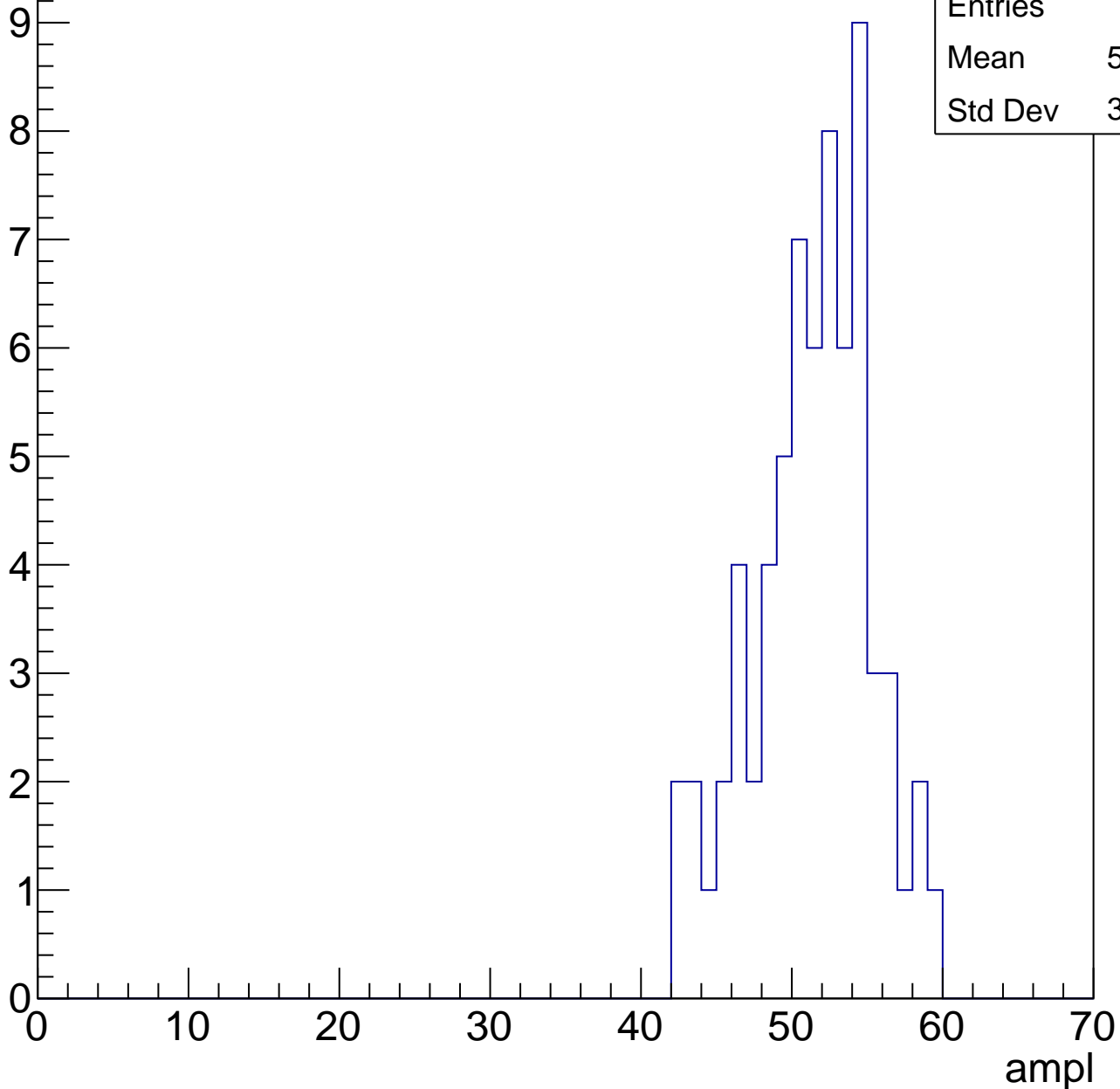


B1L100S, U10-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.88
Std Dev	3.909

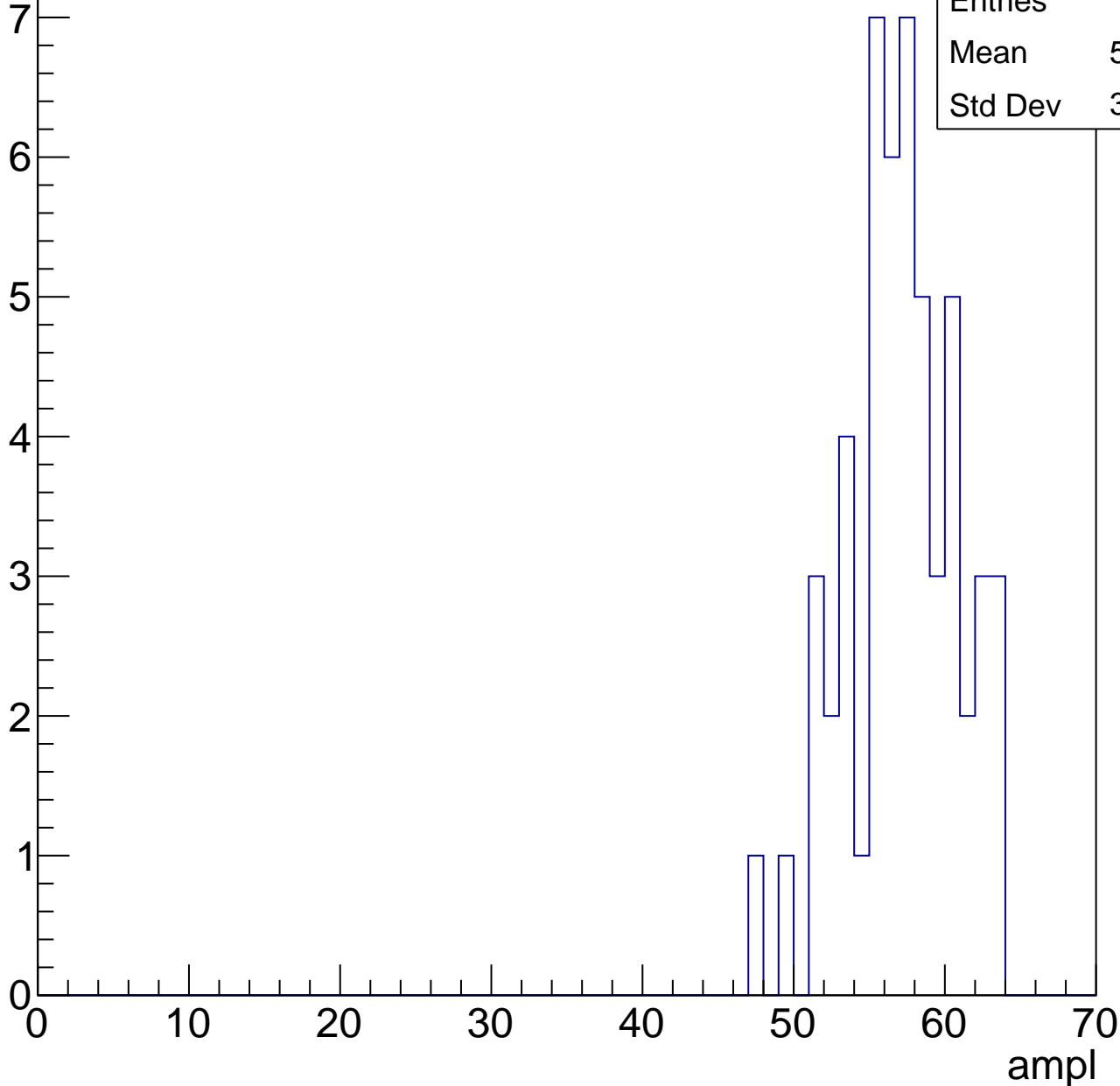


B1L100S, U10-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	56.66
Std Dev	3.634

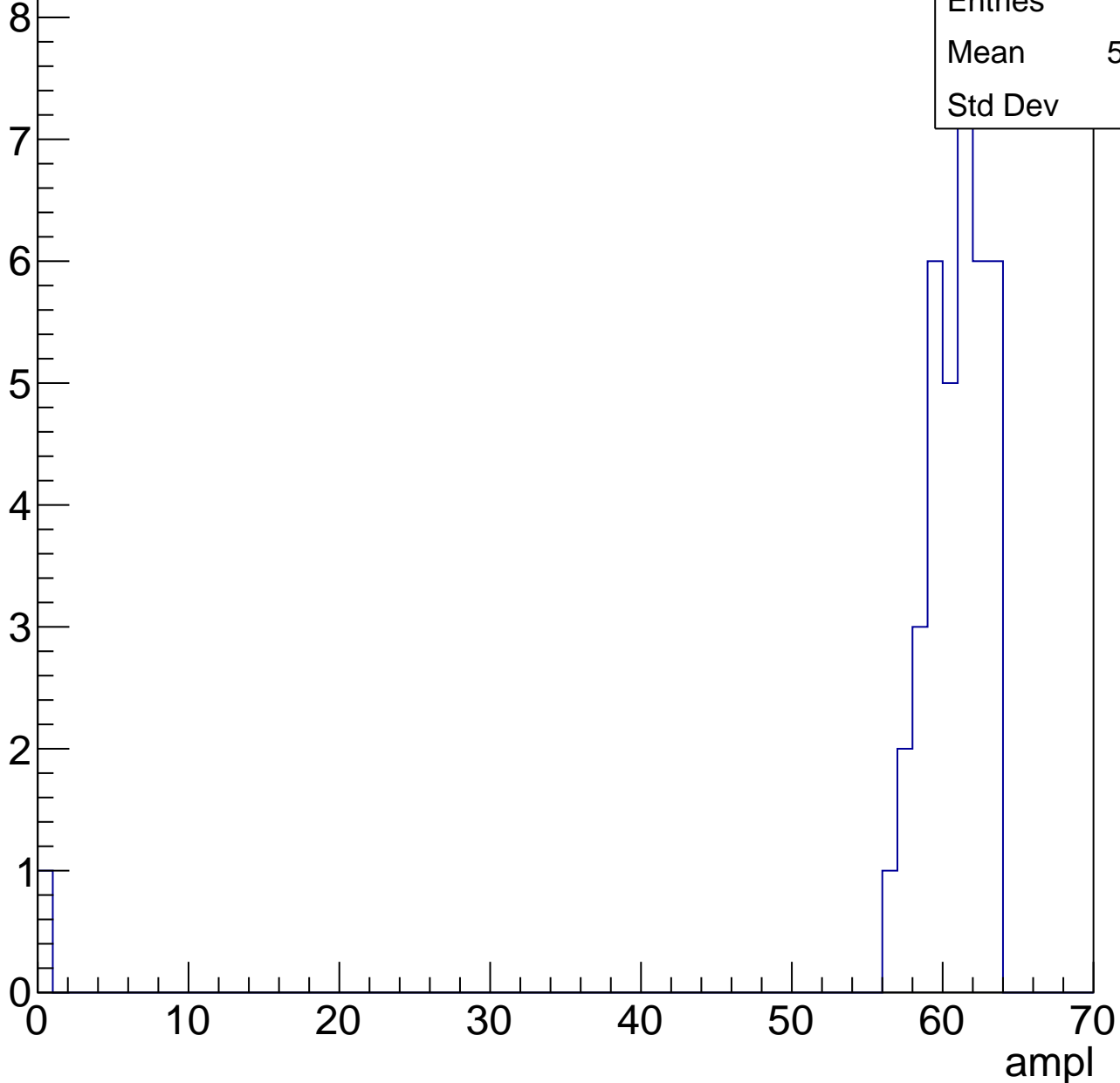


B1L100S, U10-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.84
Std Dev	9.85



B1L100S, U10-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U10-ch123, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	29.31
Std Dev	4.872

Gaus mean : 30.2577

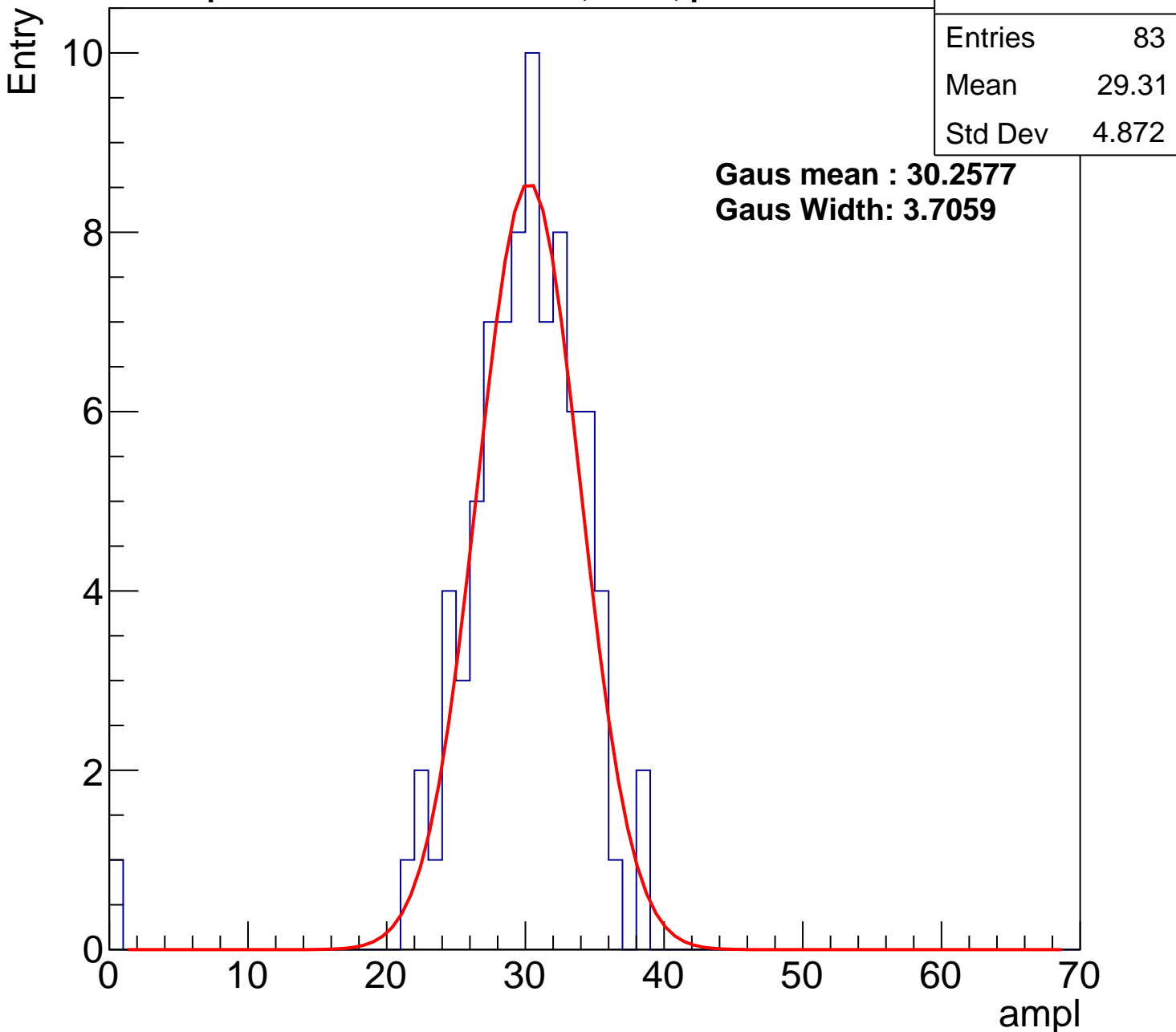
Gaus Width: 3.7059

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch123, adc1

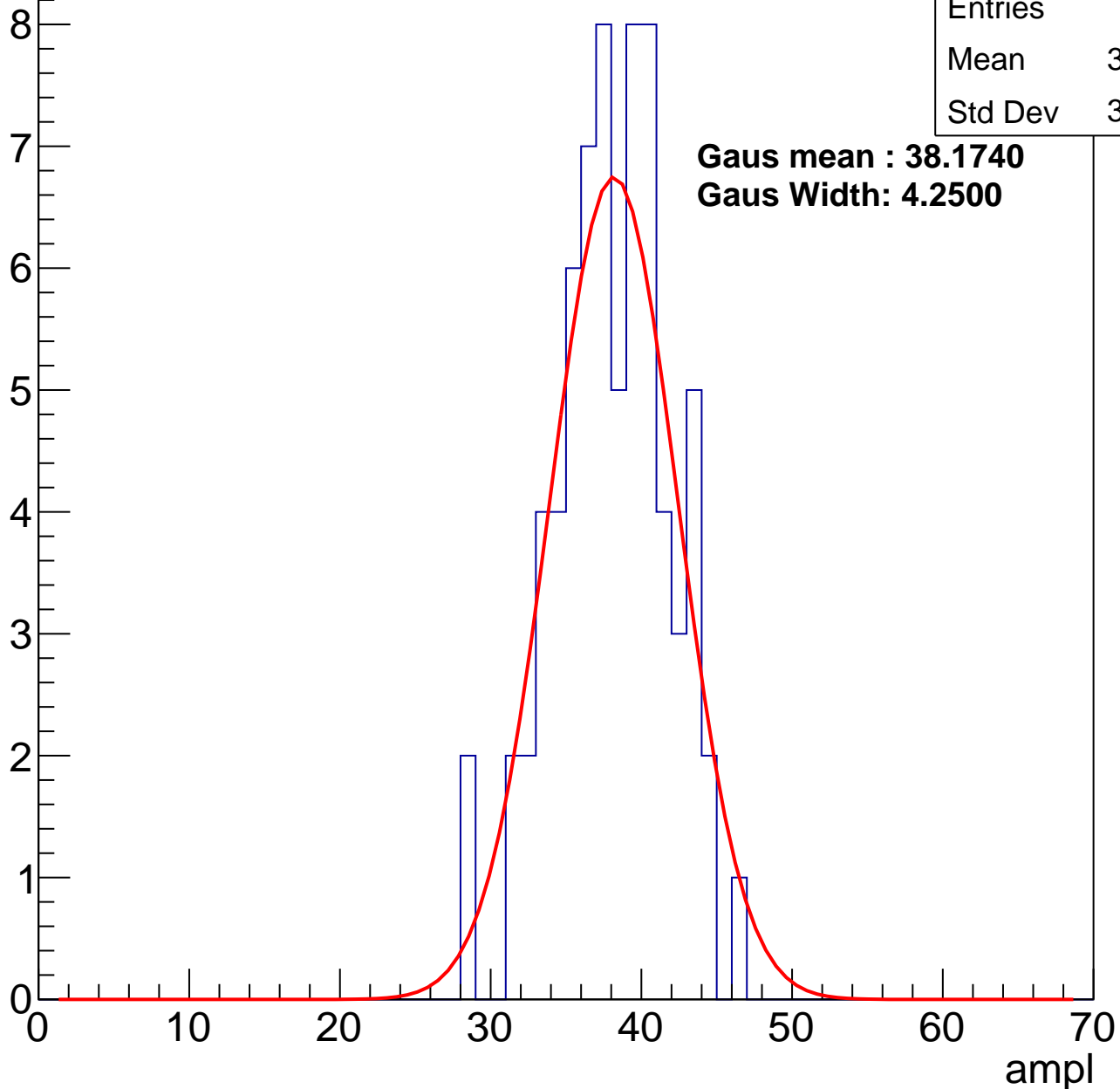
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.59
Std Dev	3.748

Gaus mean : 38.1740

Gaus Width: 4.2500



B1L100S, U10-ch123, adc2

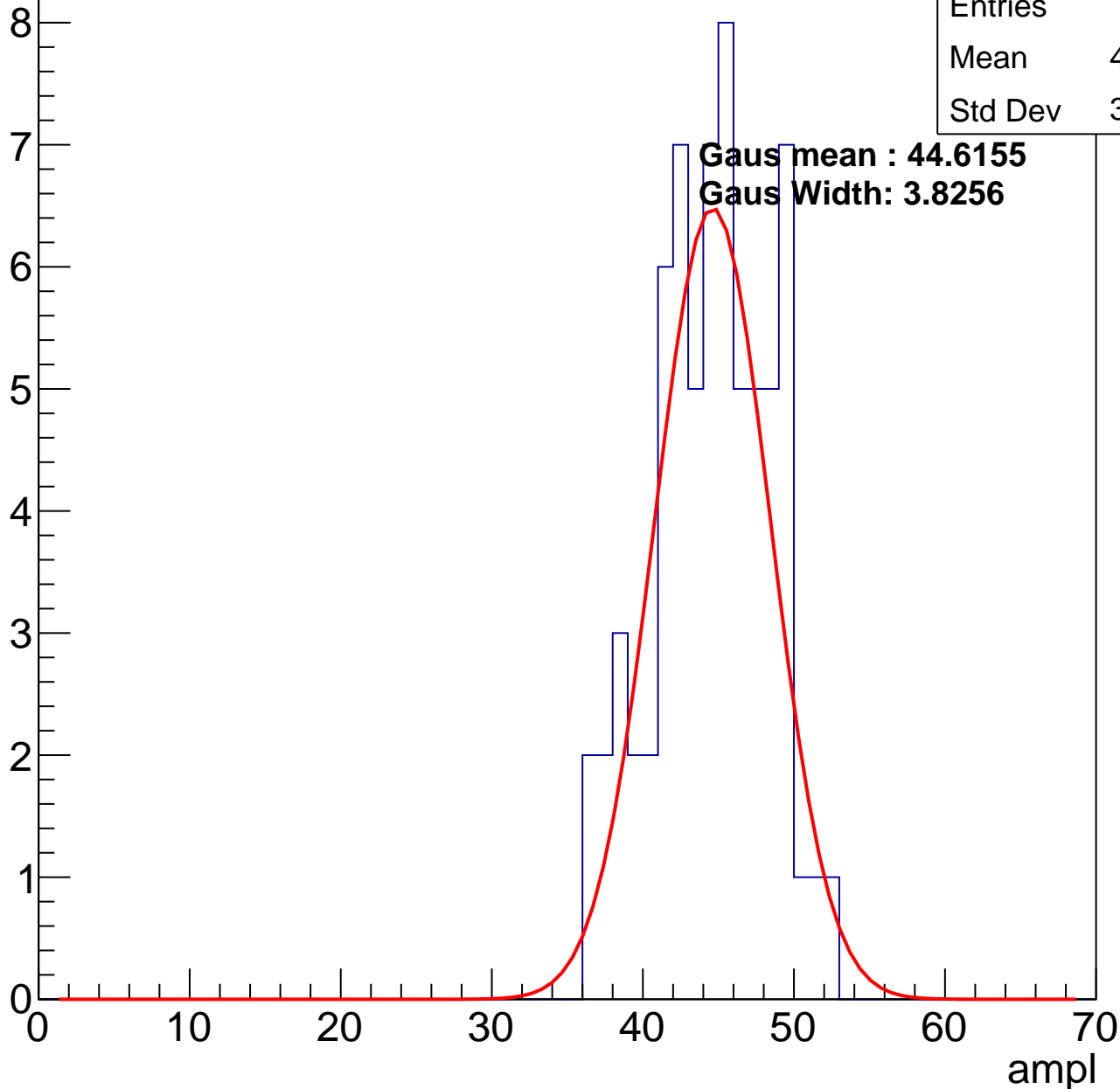
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.09
Std Dev	3.768

Gaus mean : 44.6155

Gaus Width: 3.8256

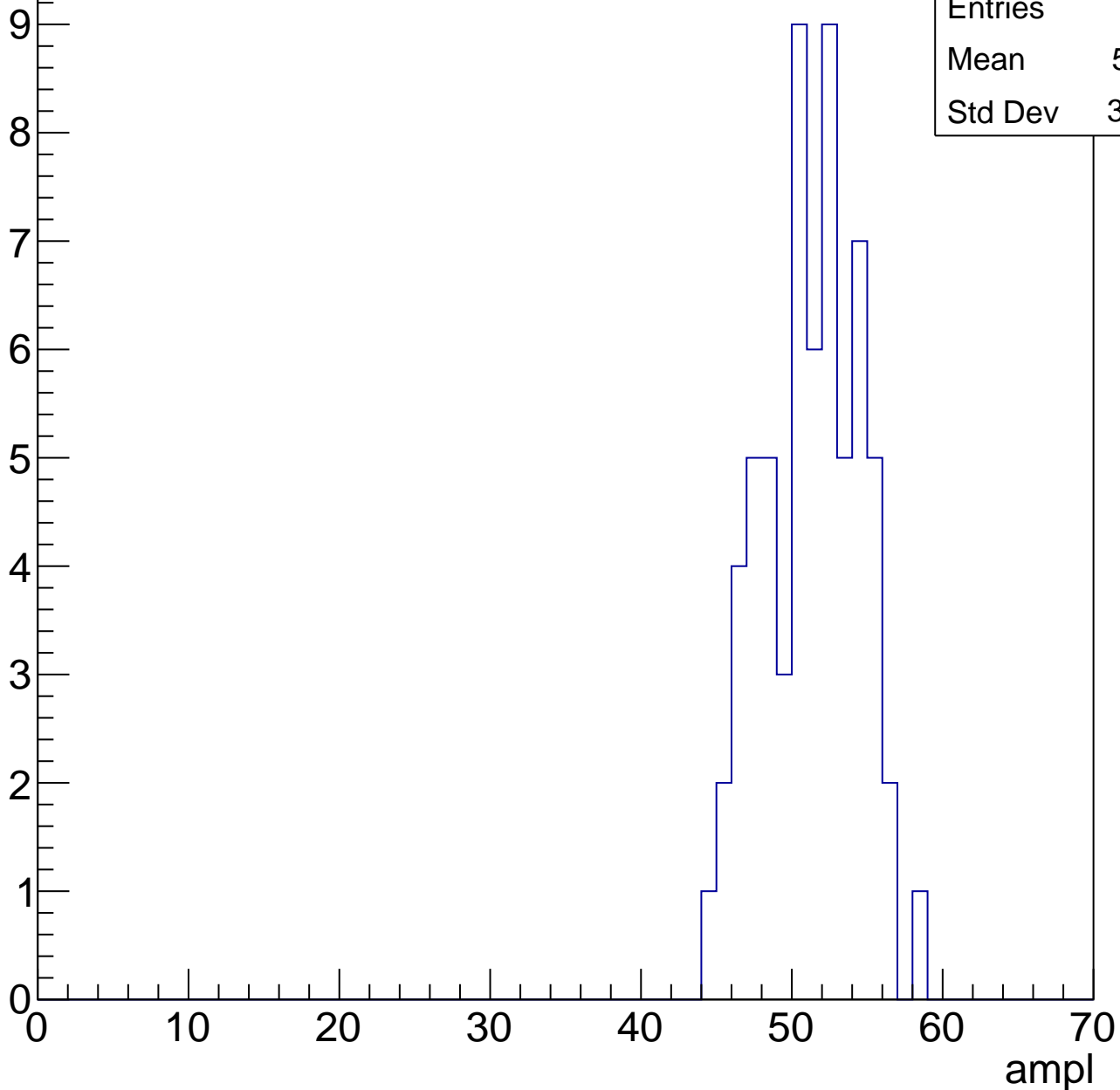


B1L100S, U10-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	50.81
Std Dev	3.152

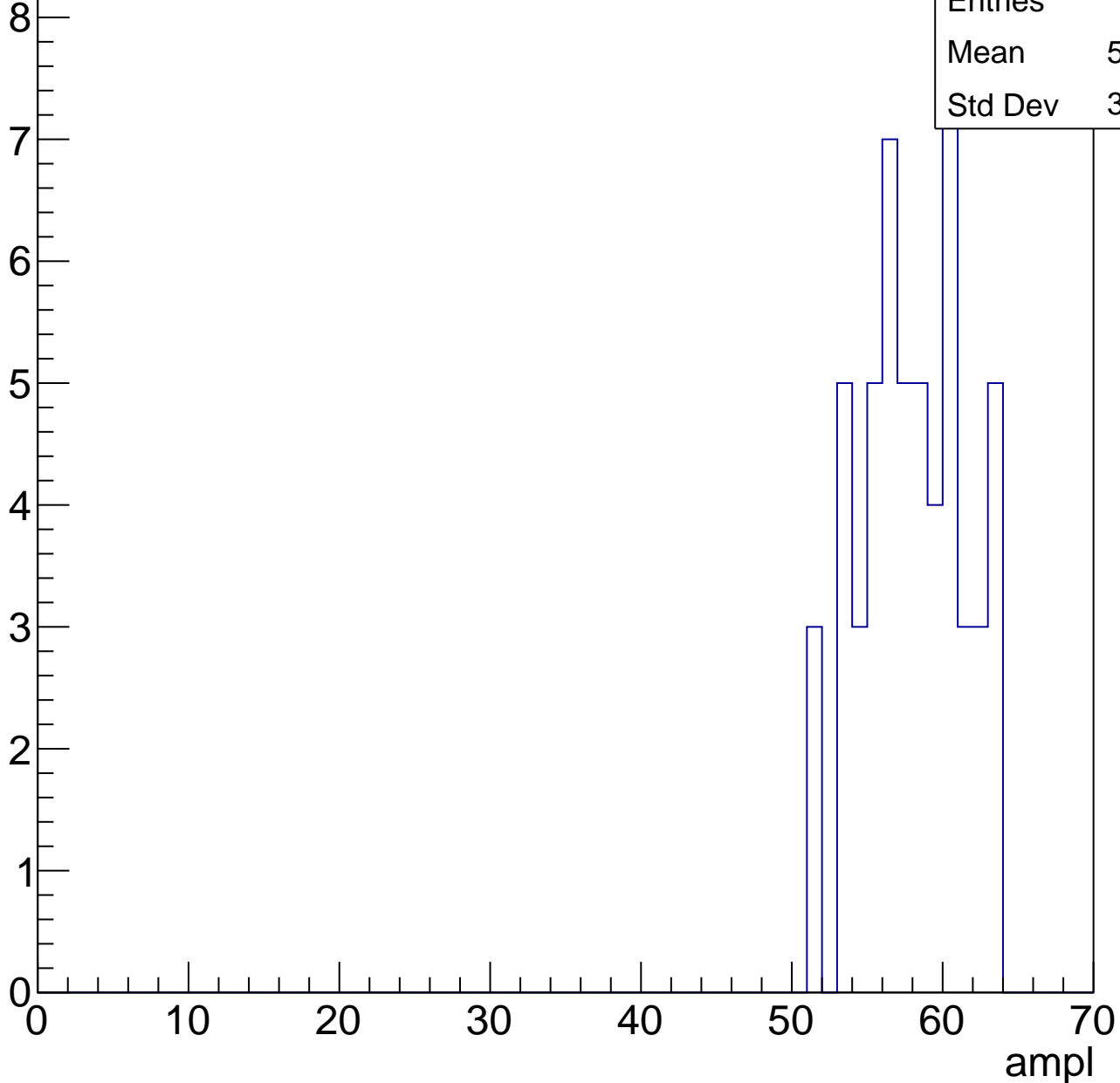


B1L100S, U10-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.54
Std Dev	3.333

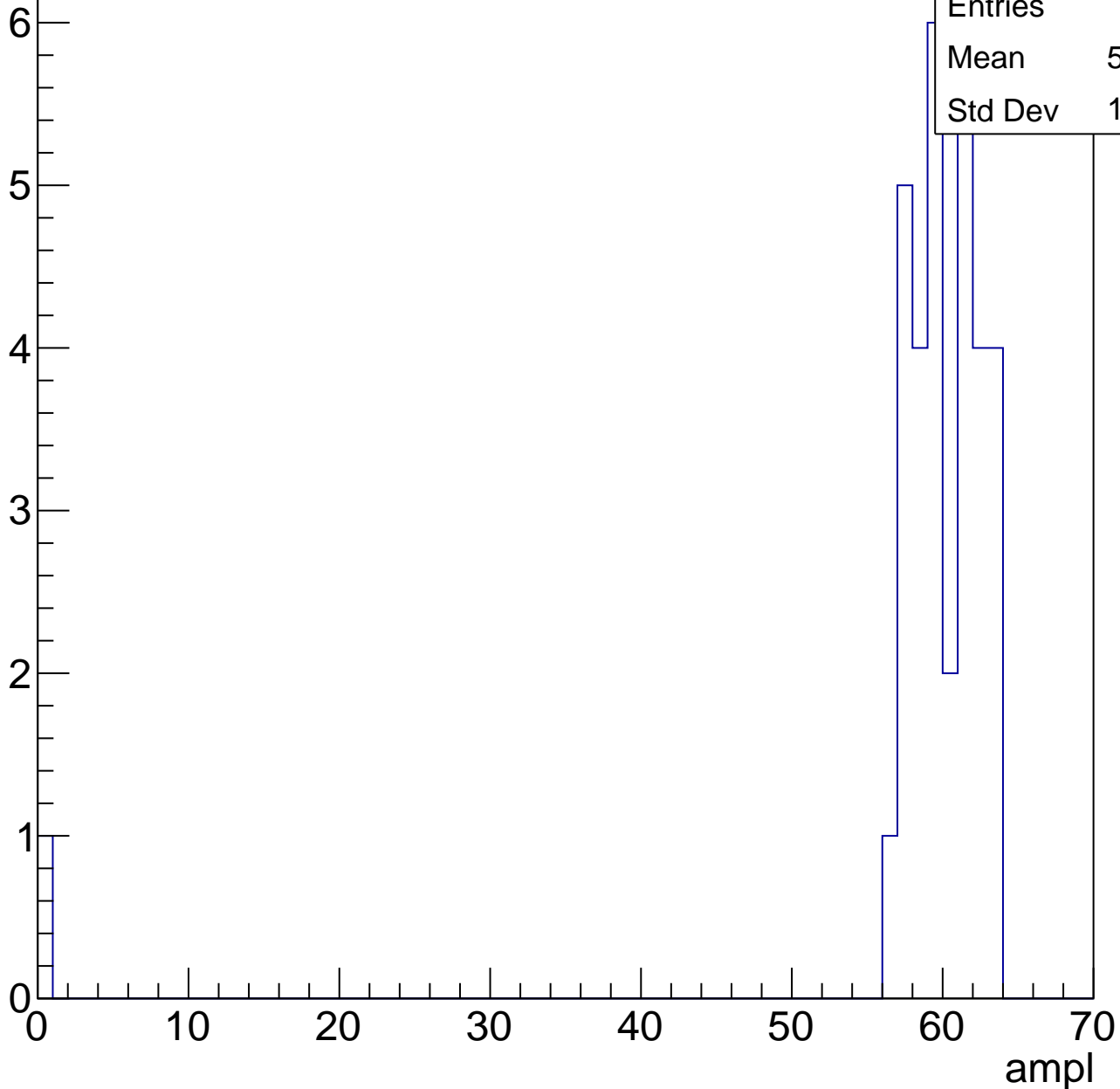


B1L100S, U10-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	57.97
Std Dev	10.45



B1L100S, U10-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch124, adc0

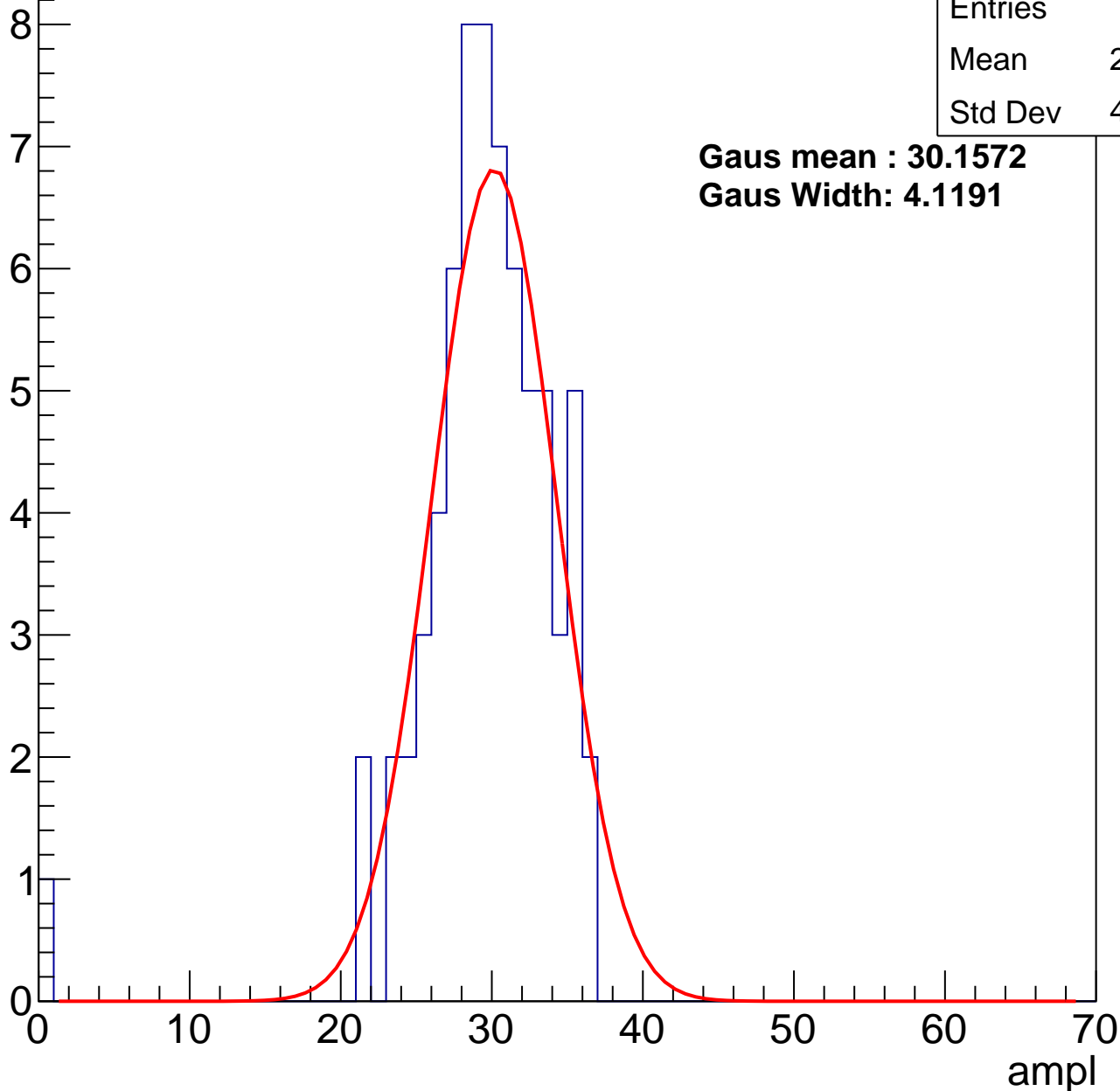
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.03
Std Dev	4.987

Gaus mean : 30.1572

Gaus Width: 4.1191



B1L100S, U10-ch124, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	36.64
Std Dev	3.805

Gaus mean : 37.5298

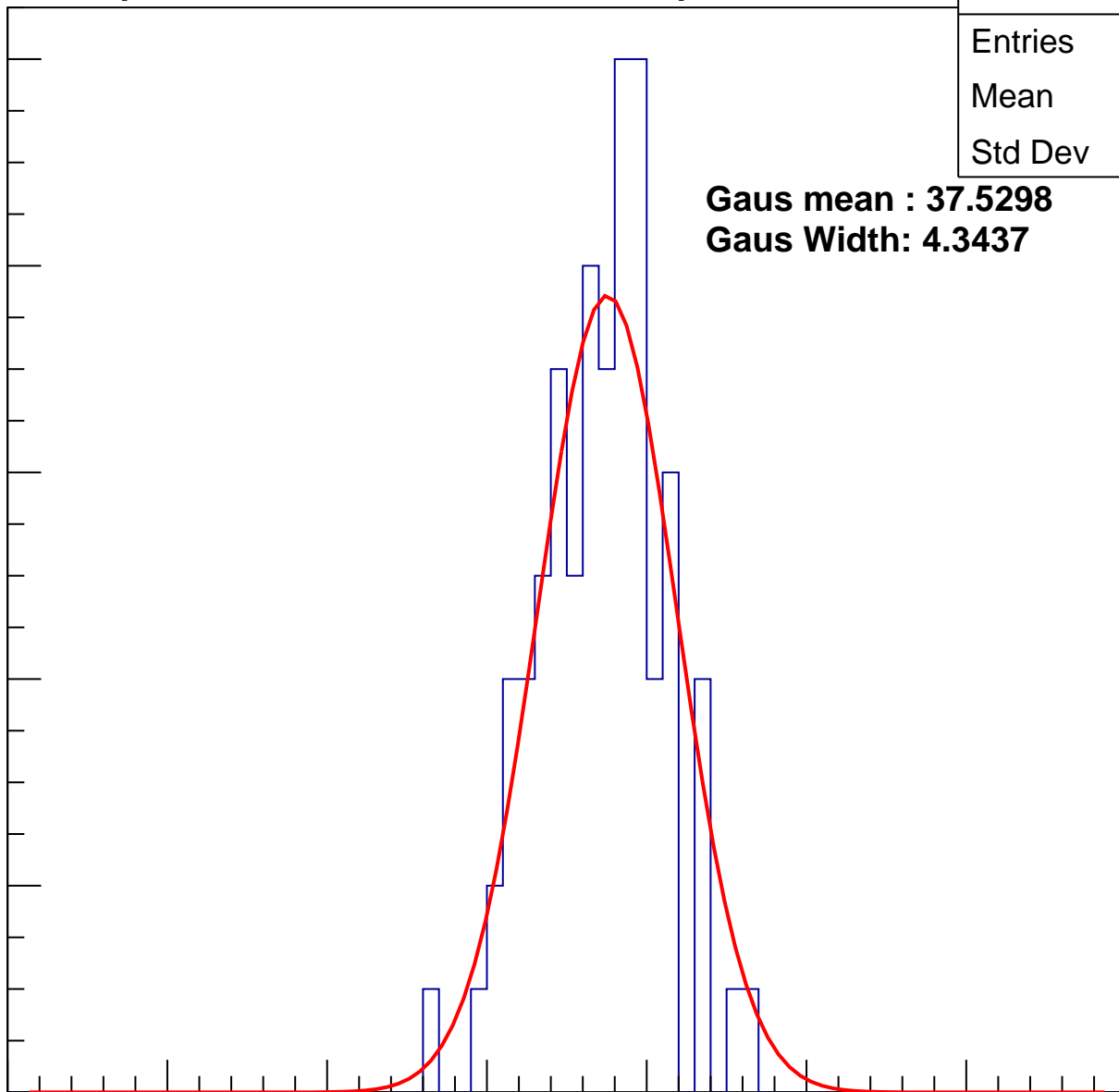
Gaus Width: 4.3437

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U10-ch124, adc2

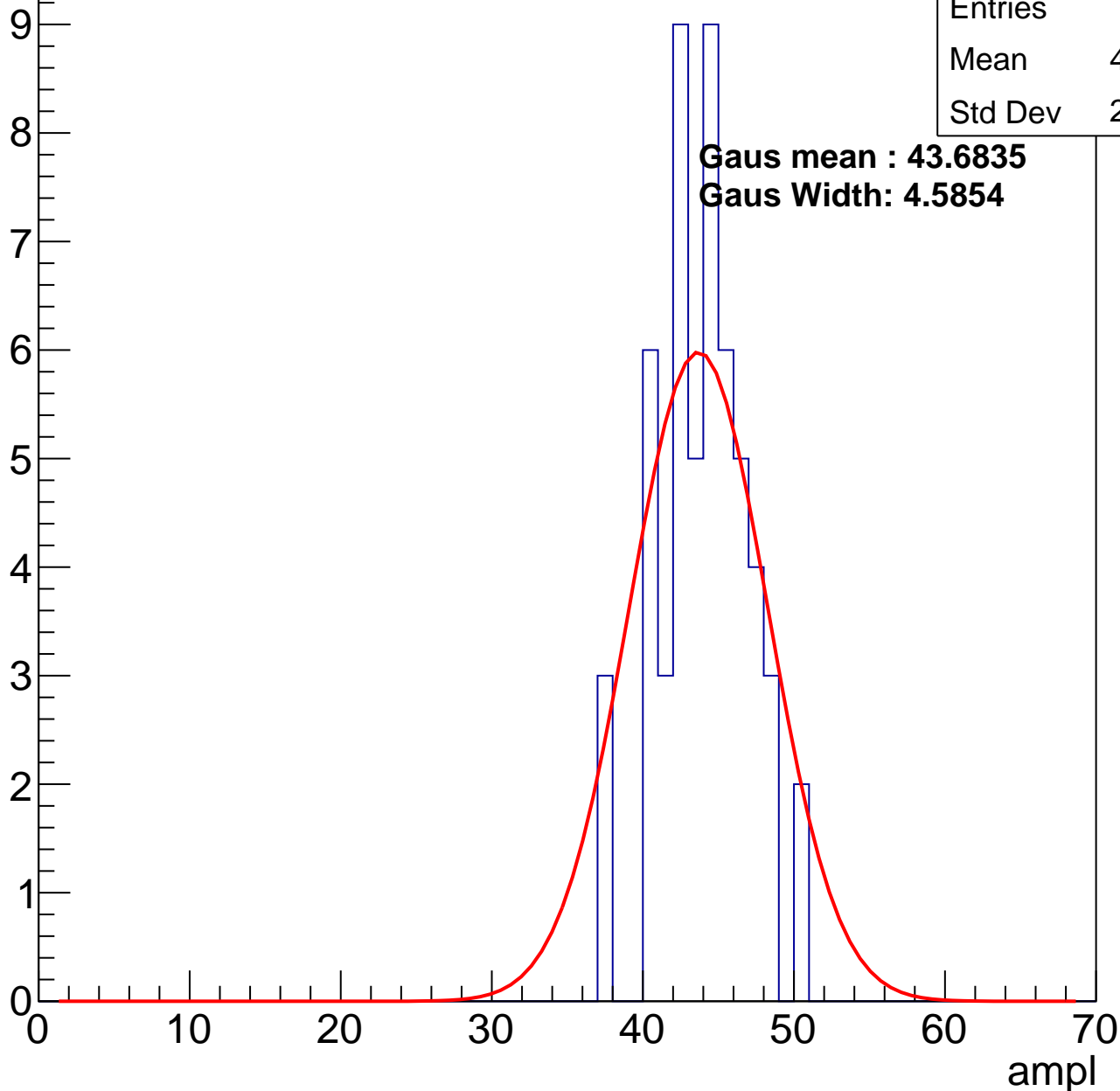
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.55
Std Dev	2.959

Gaus mean : 43.6835

Gaus Width: 4.5854

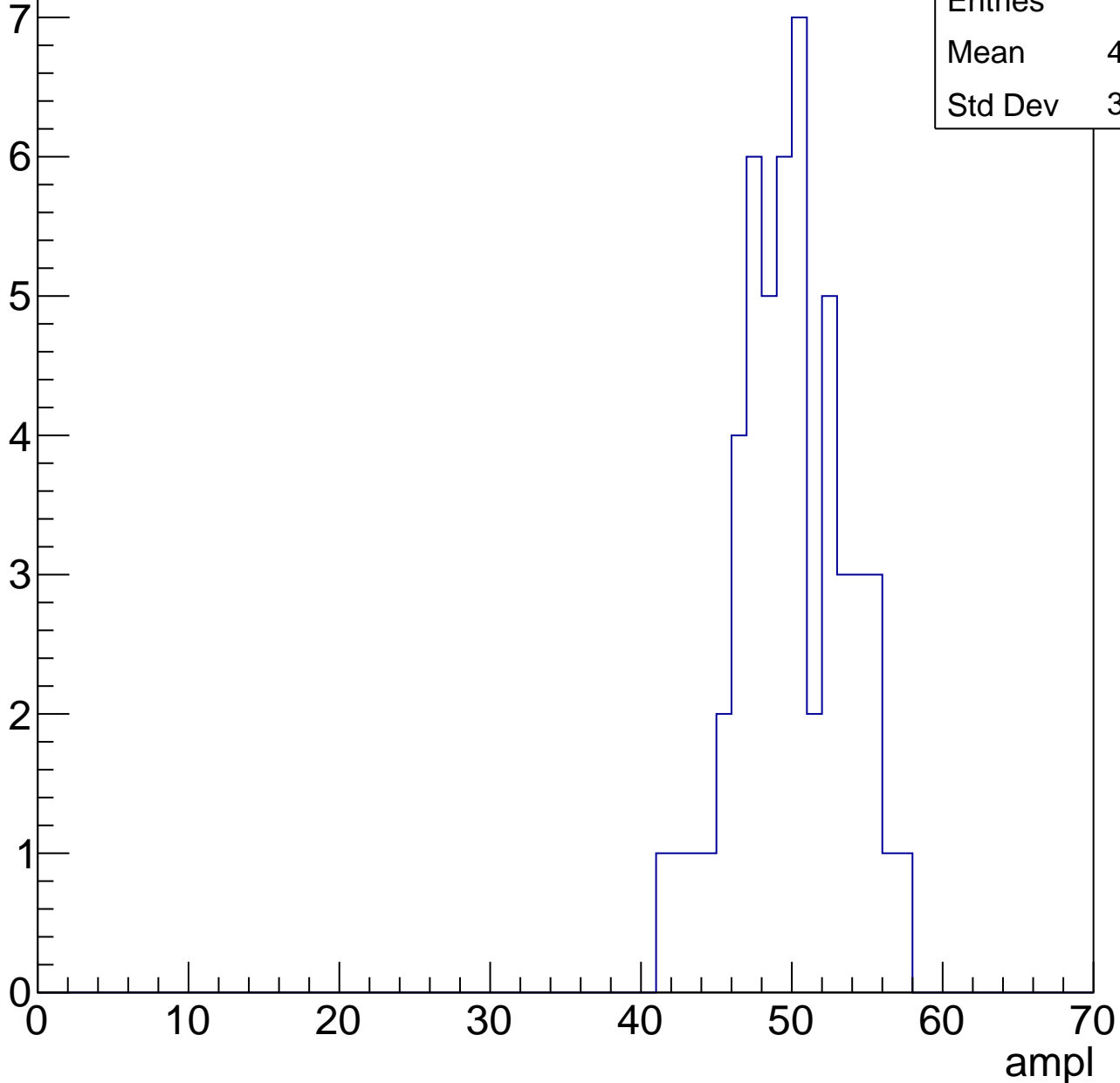


B1L100S, U10-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	49.44
Std Dev	3.576

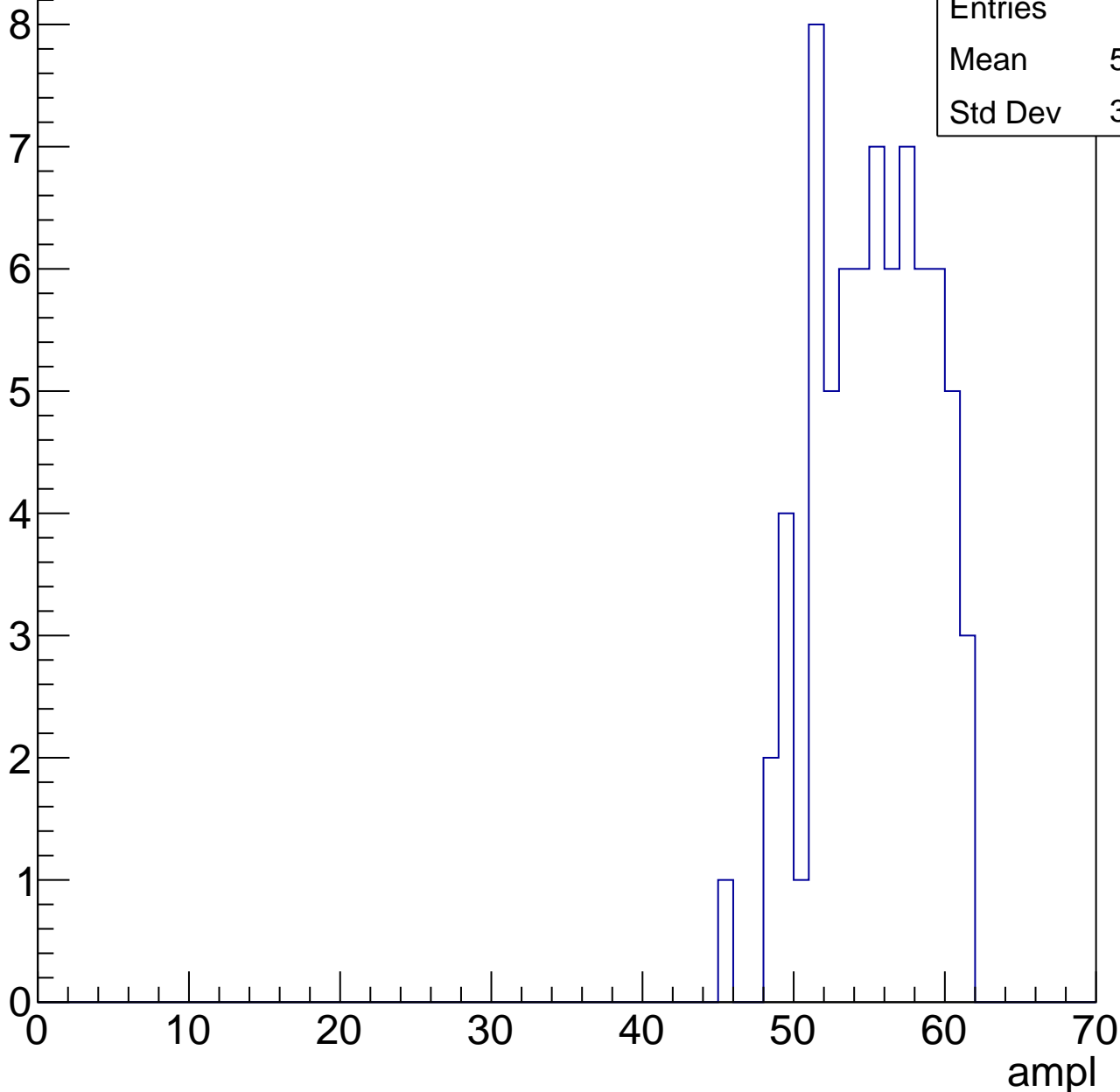


B1L100S, U10-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	54.82
Std Dev	3.673

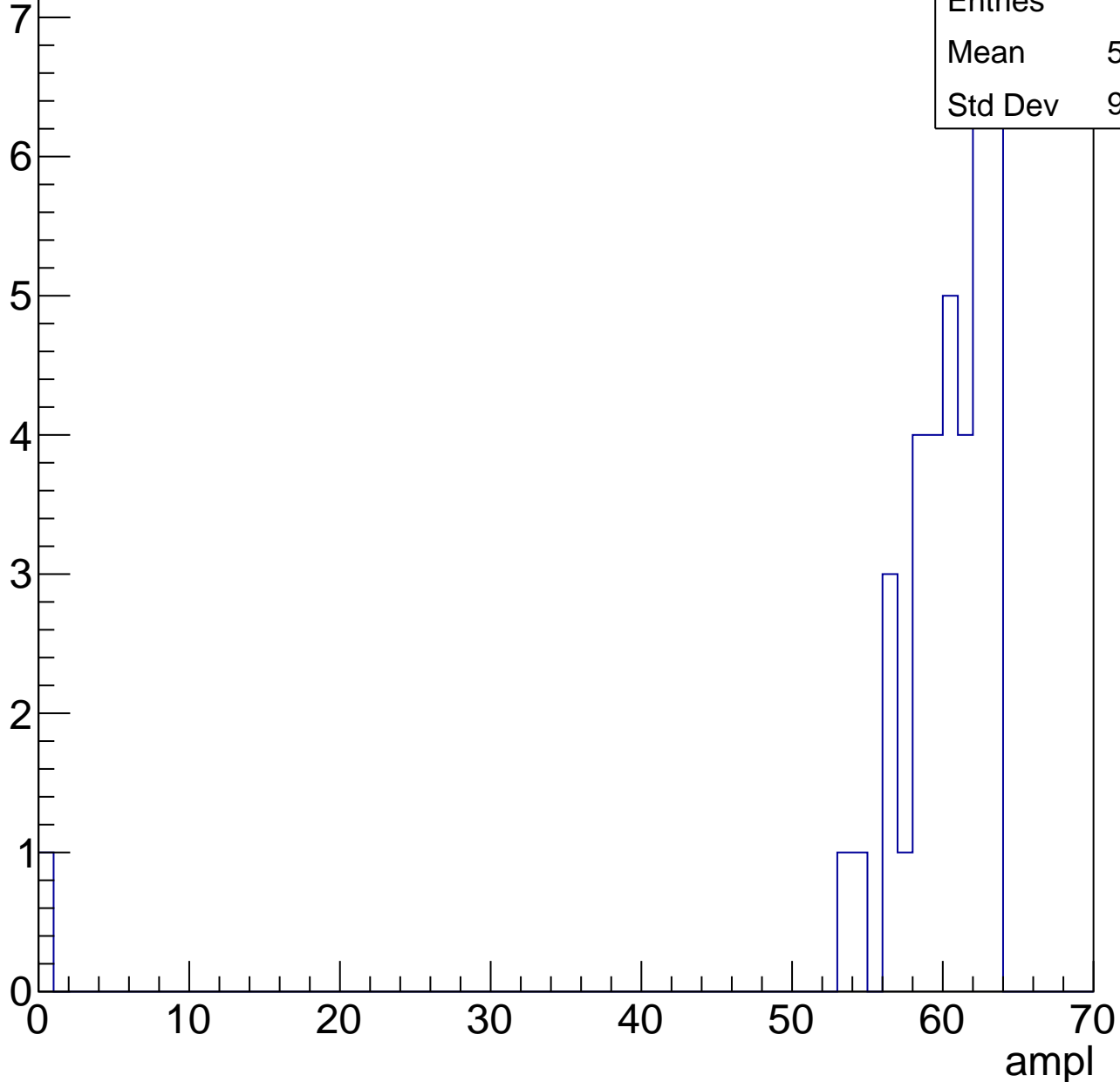


B1L100S, U10-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

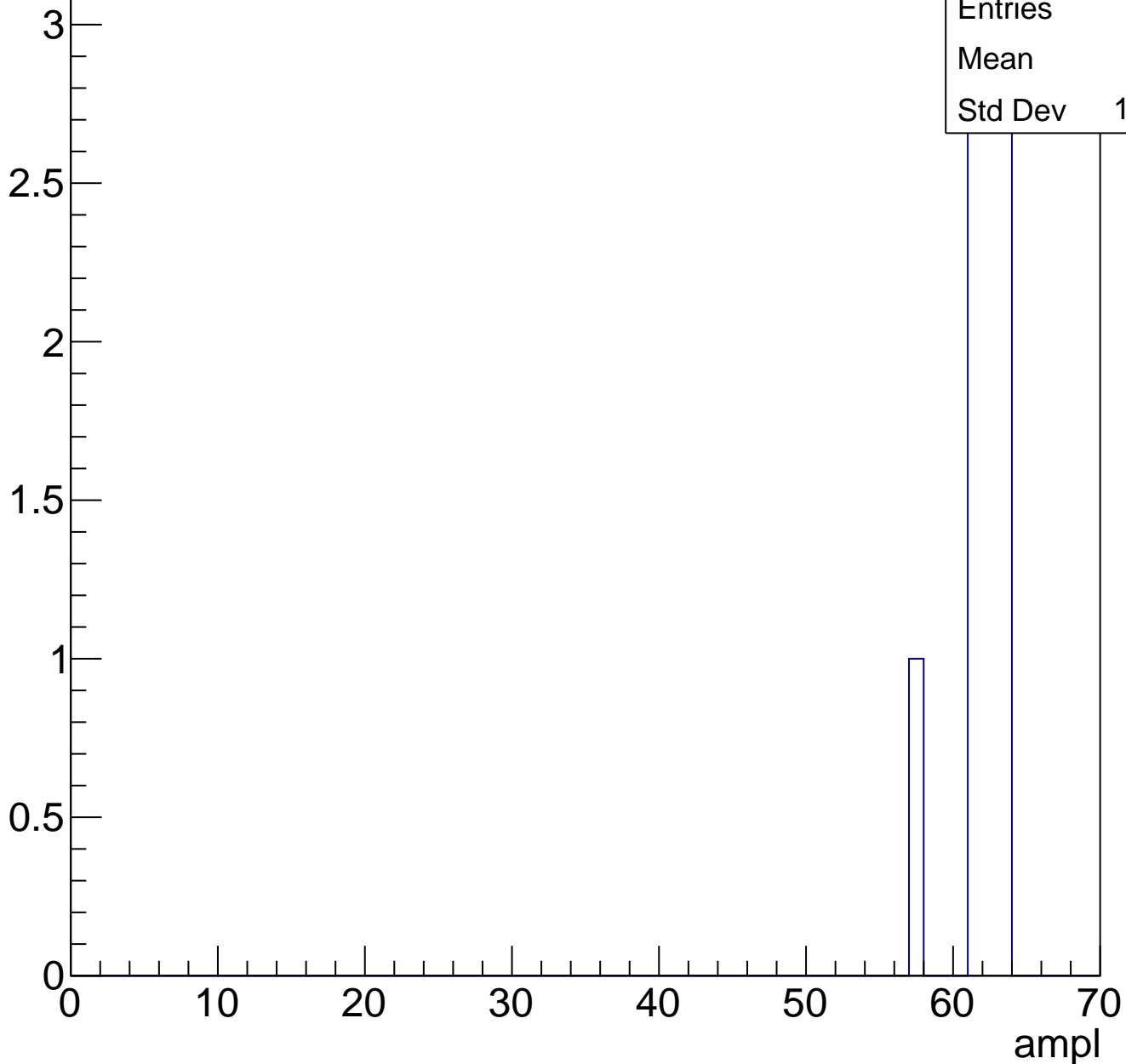
Entries	38
Mean	58.39
Std Dev	9.946



B1L100S, U10-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U10-ch125, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	29.24
Std Dev	3.39

Gaus mean : 29.4347

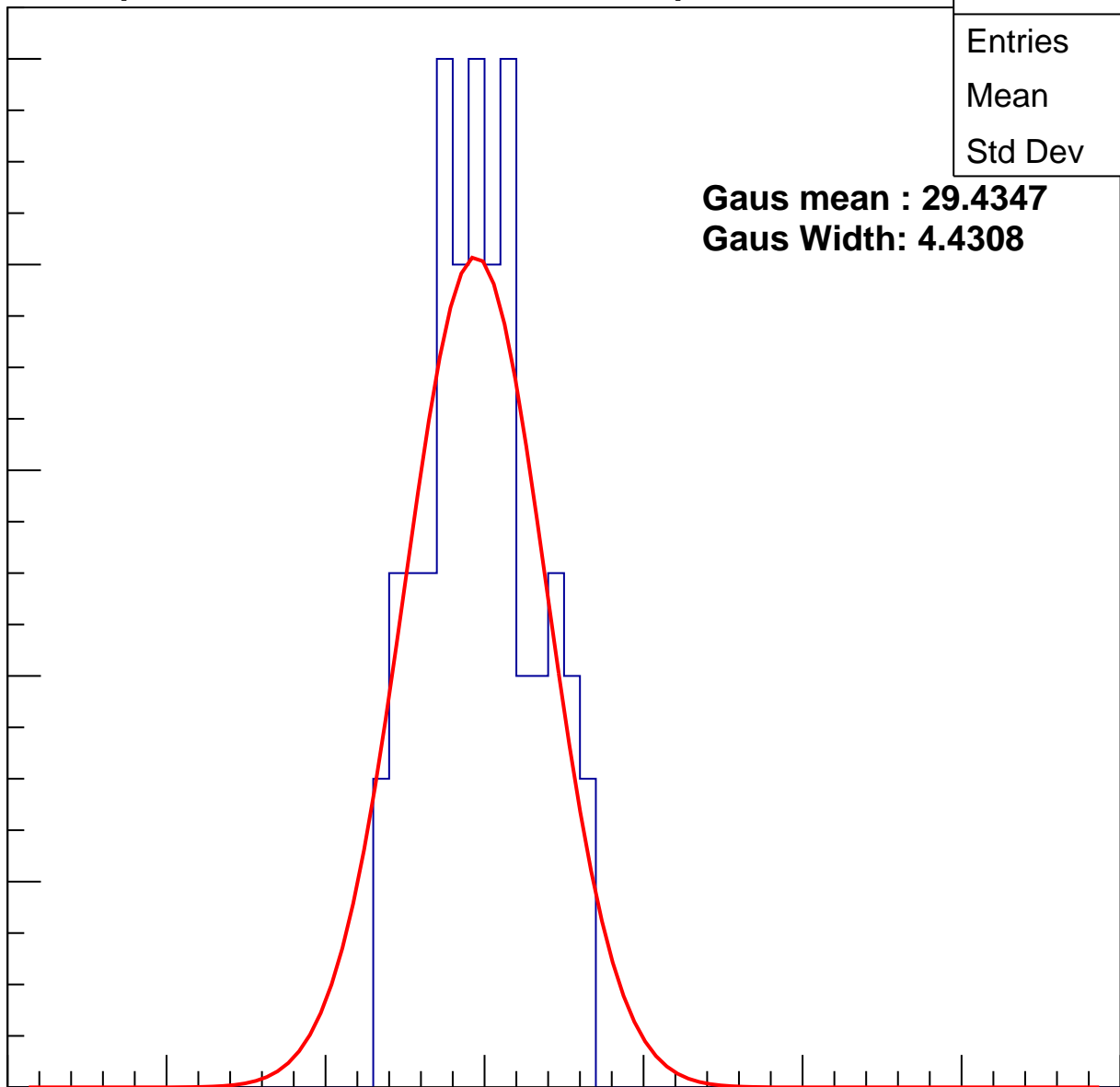
Gaus Width: 4.4308

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch125, adc1

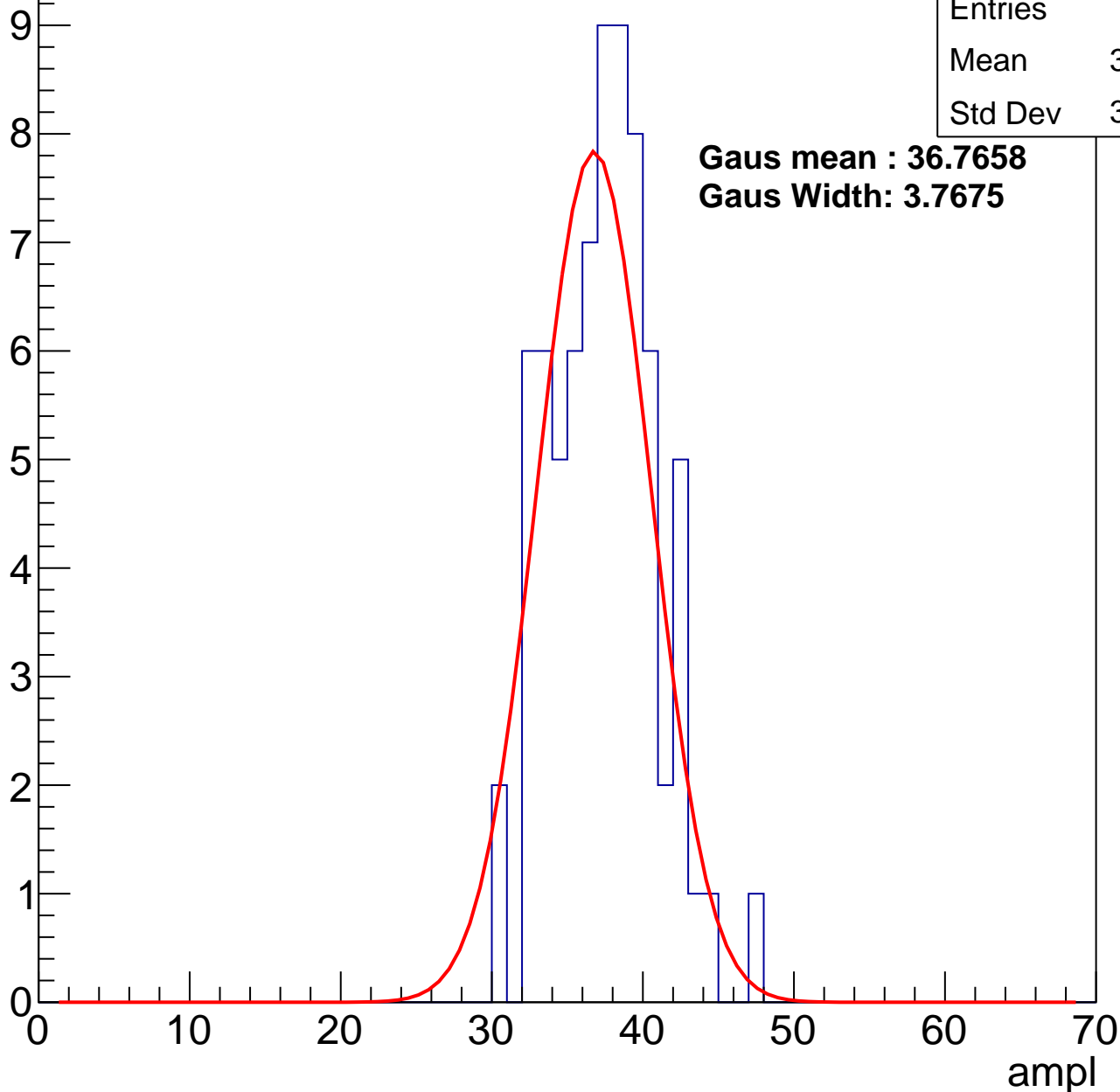
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.96
Std Dev	3.403

Gaus mean : 36.7658

Gaus Width: 3.7675



B1L100S, U10-ch125, adc2

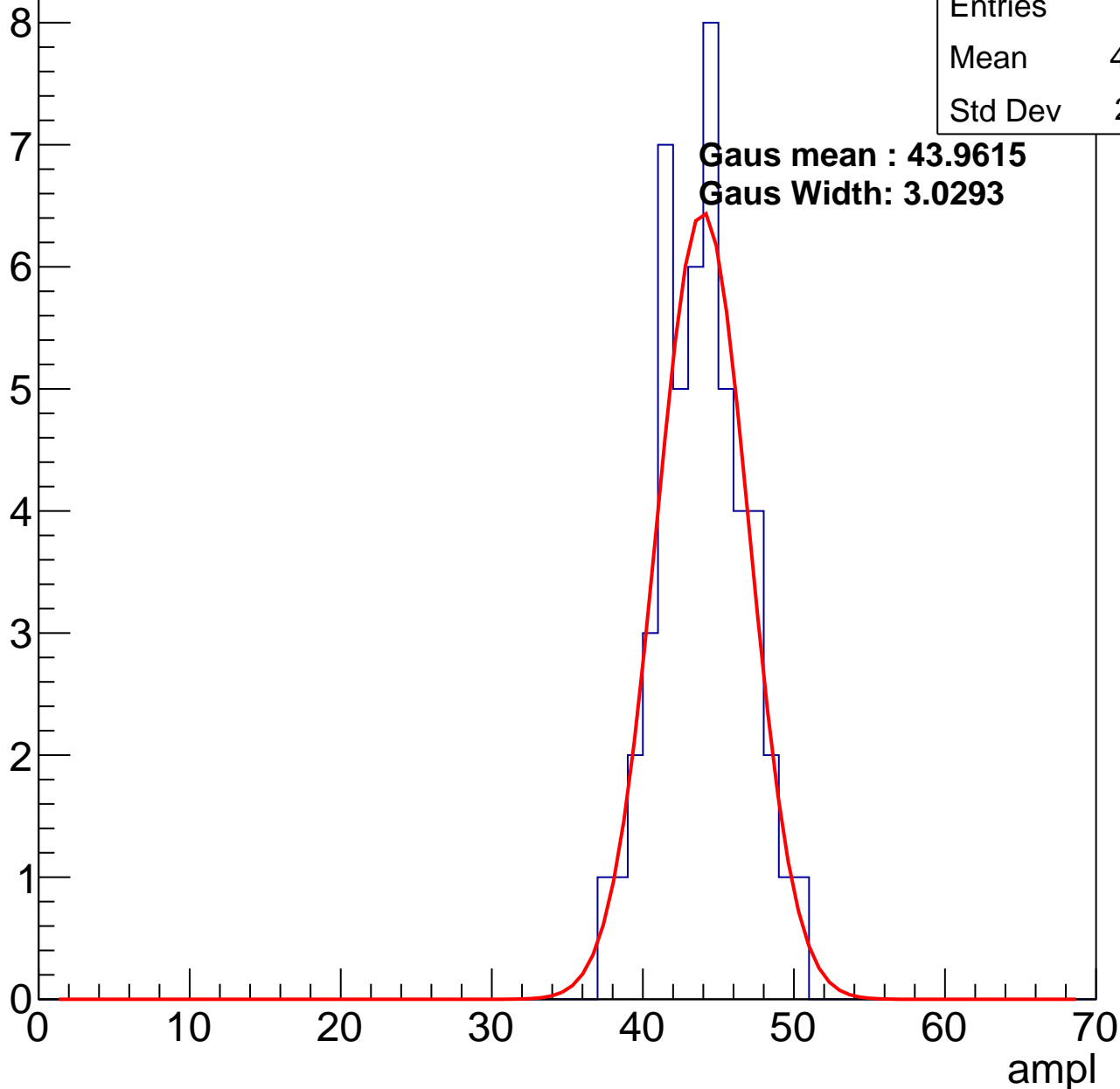
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	43.44
Std Dev	2.851

Gaus mean : 43.9615

Gaus Width: 3.0293

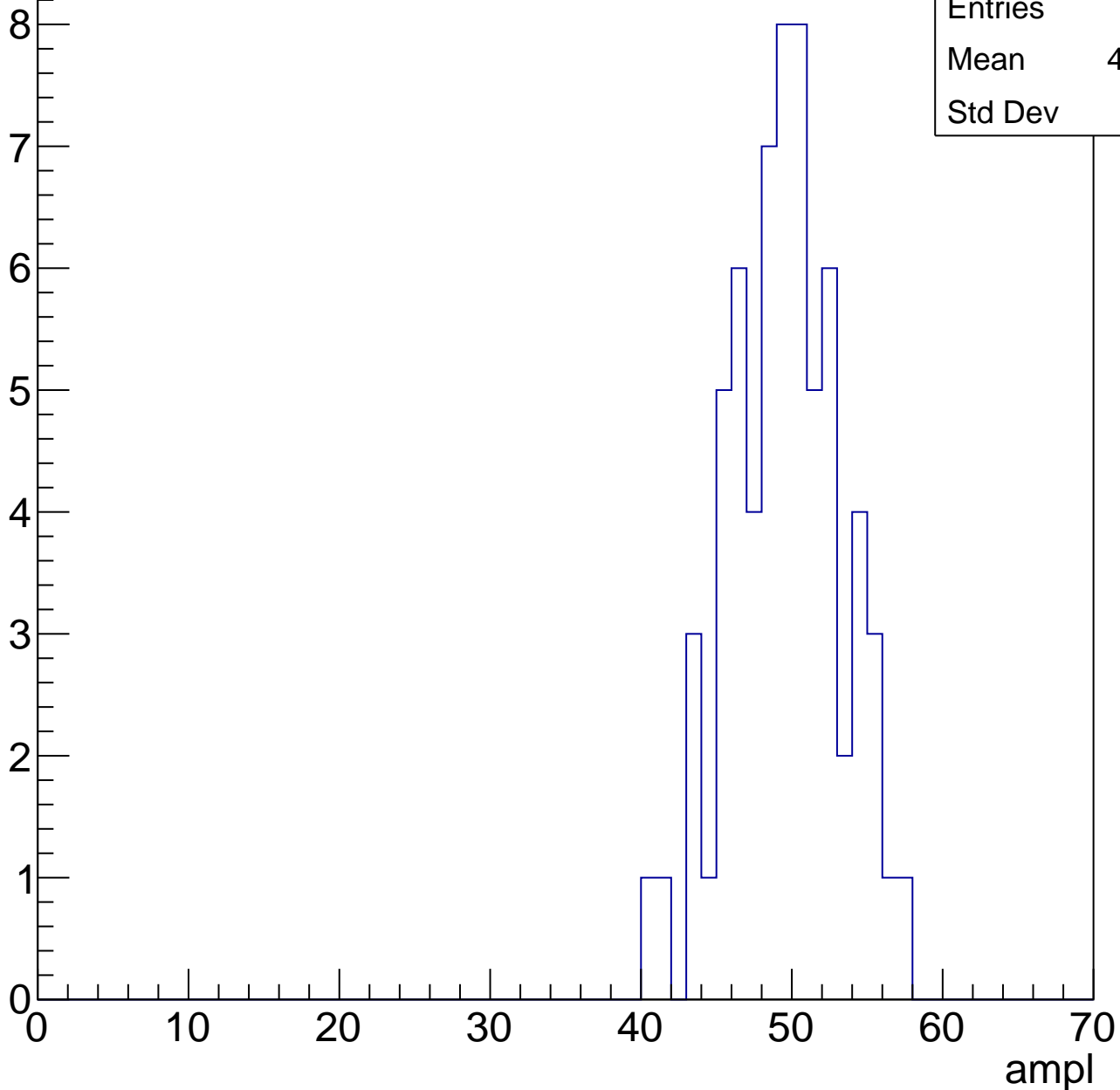


B1L100S, U10-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

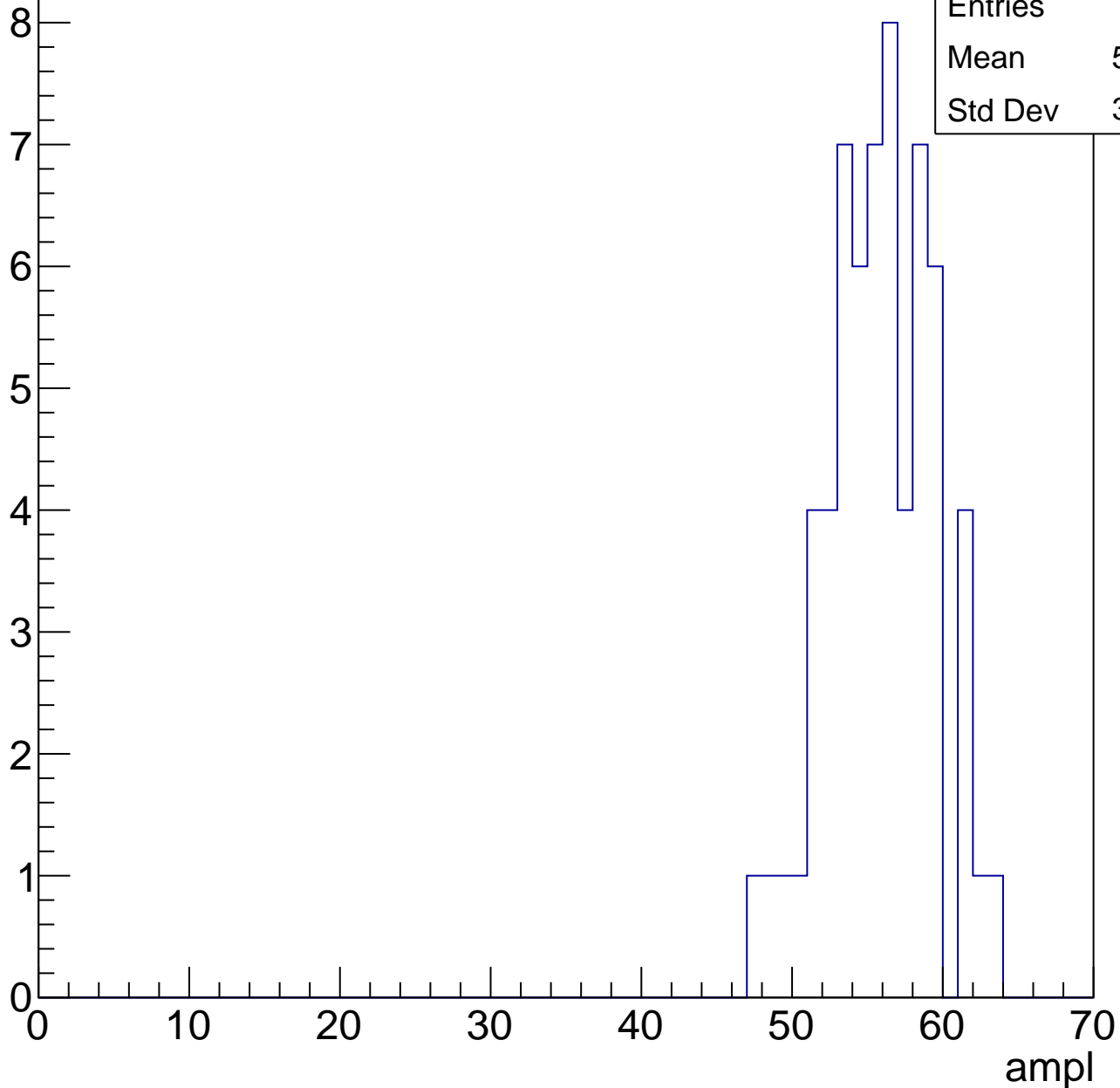
Entries	66
Mean	49.06
Std Dev	3.63



B1L100S, U10-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

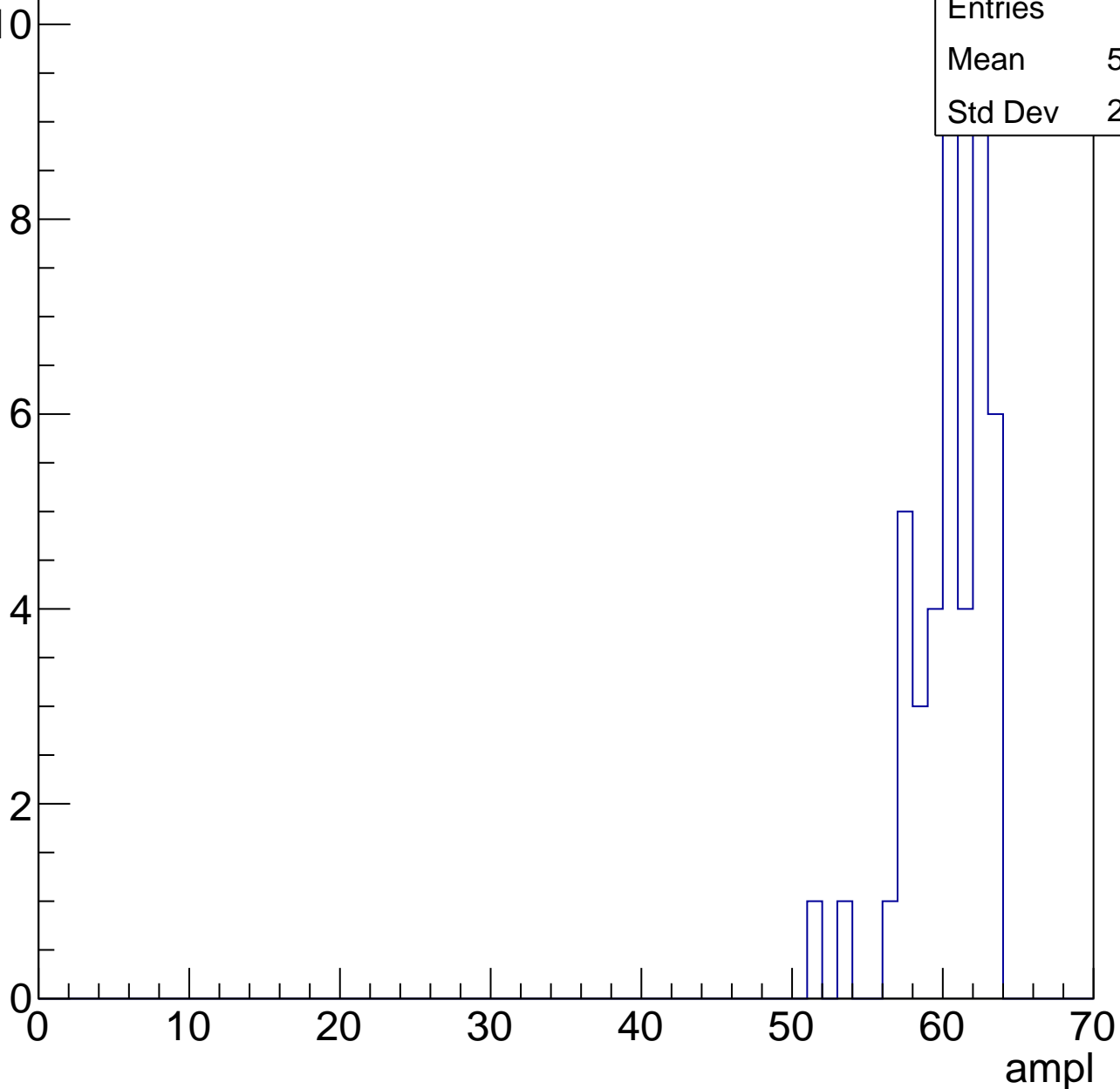


B1L100S, U10-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

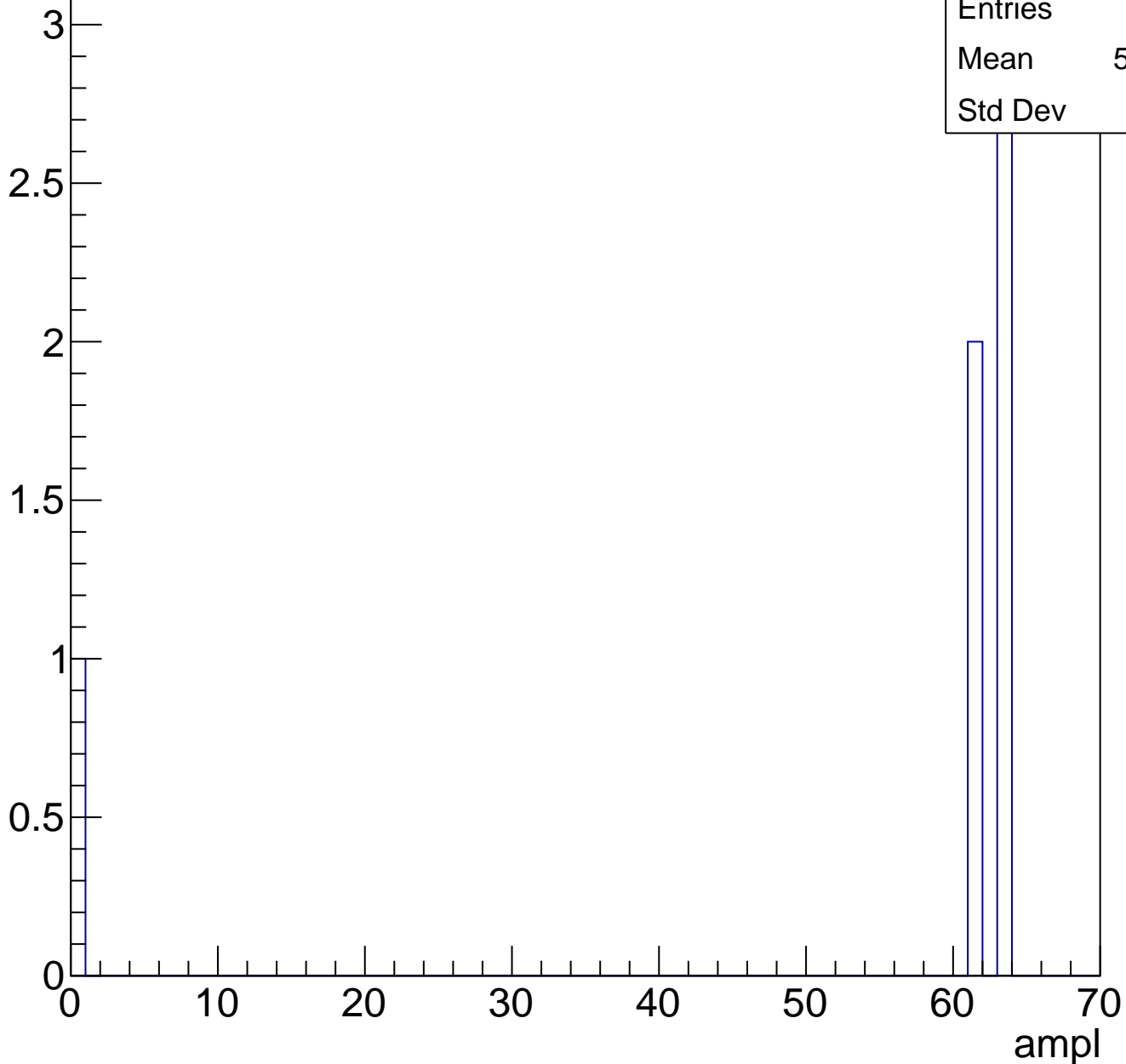
Entries	44
Mean	59.89
Std Dev	2.613



B1L100S, U10-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U10-ch126, adc0

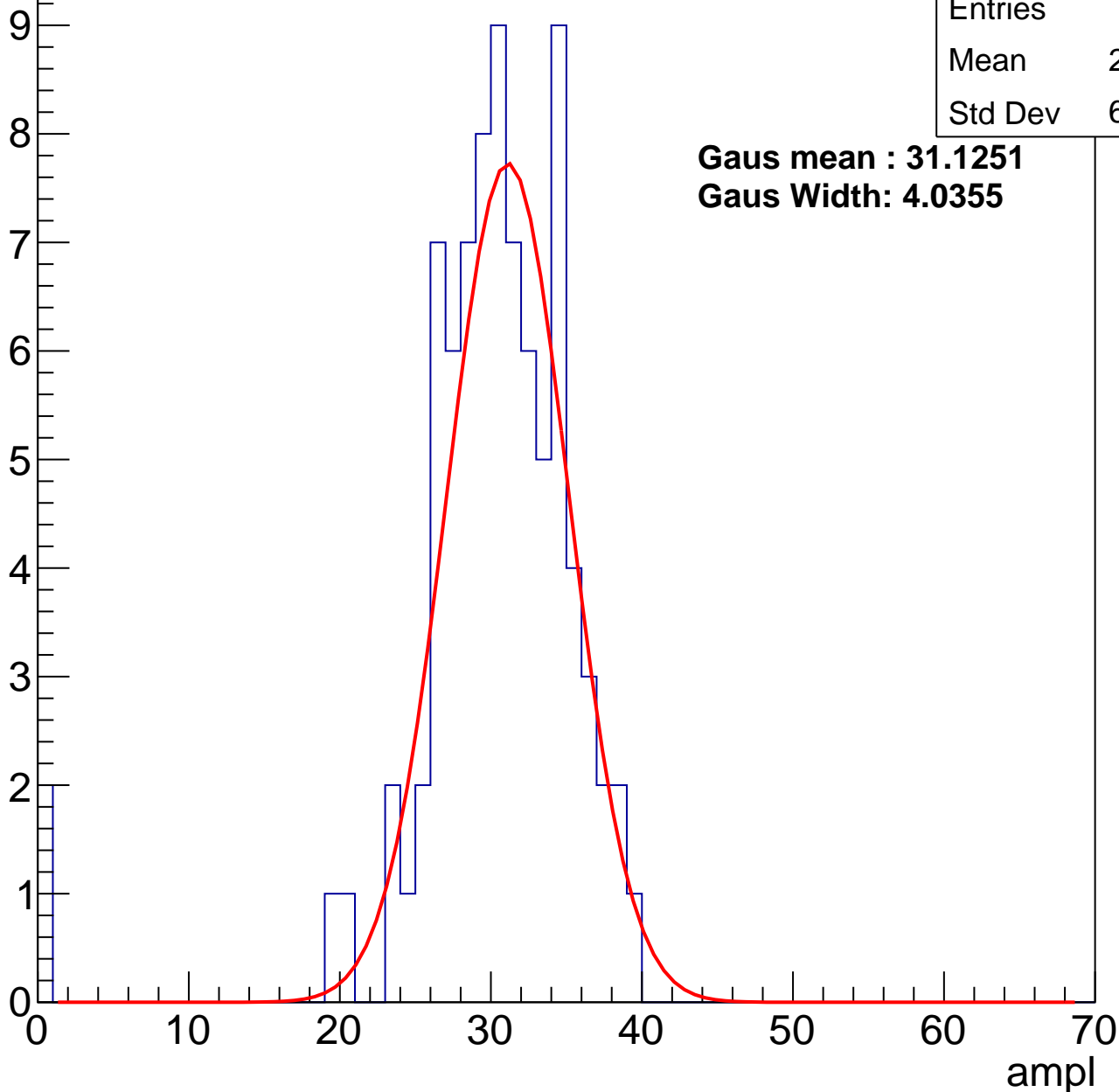
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	29.62
Std Dev	6.068

Gaus mean : 31.1251

Gaus Width: 4.0355



B1L100S, U10-ch126, adc1

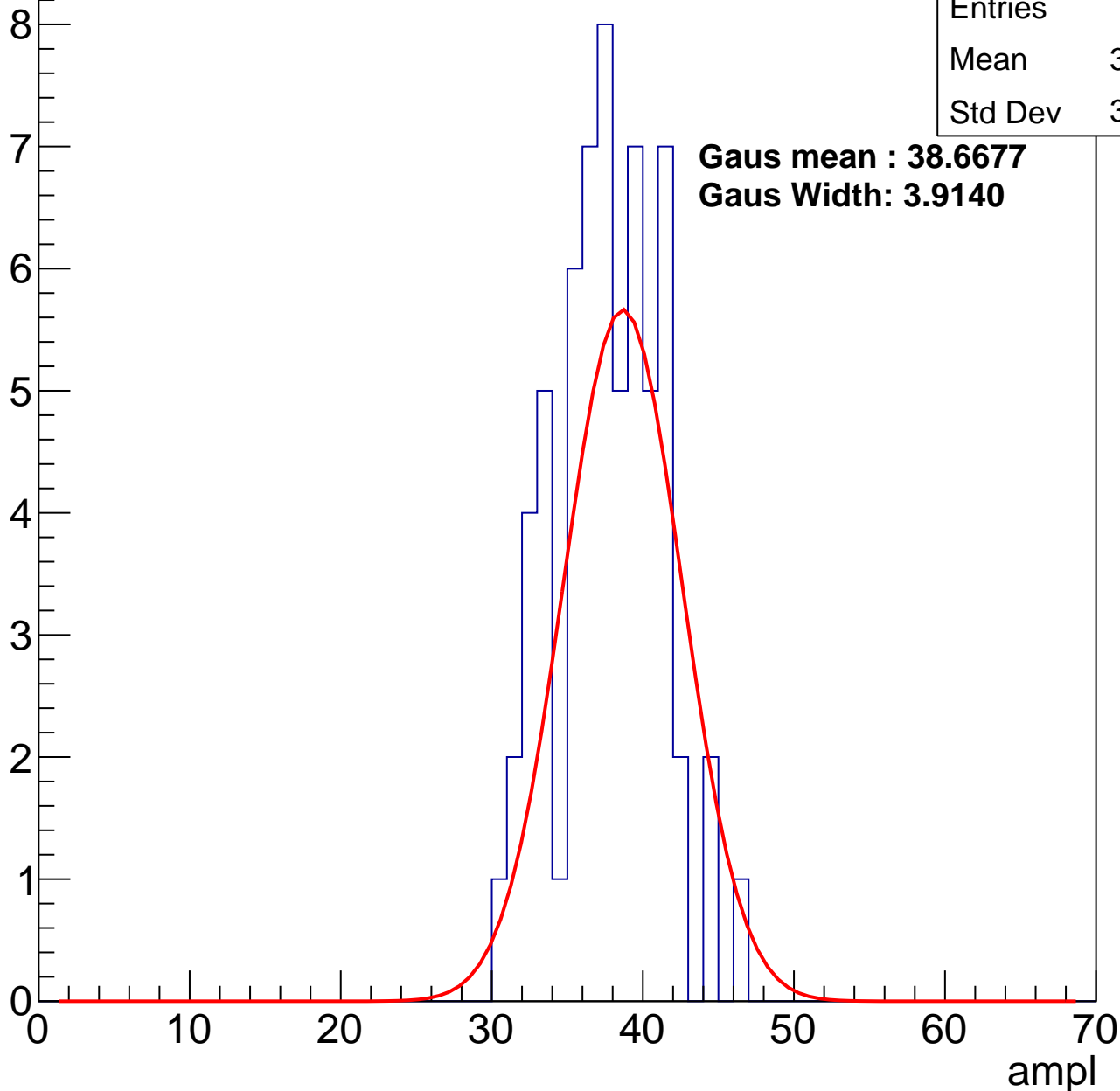
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.22
Std Dev	3.466

Gaus mean : 38.6677

Gaus Width: 3.9140



B1L100S, U10-ch126, adc2

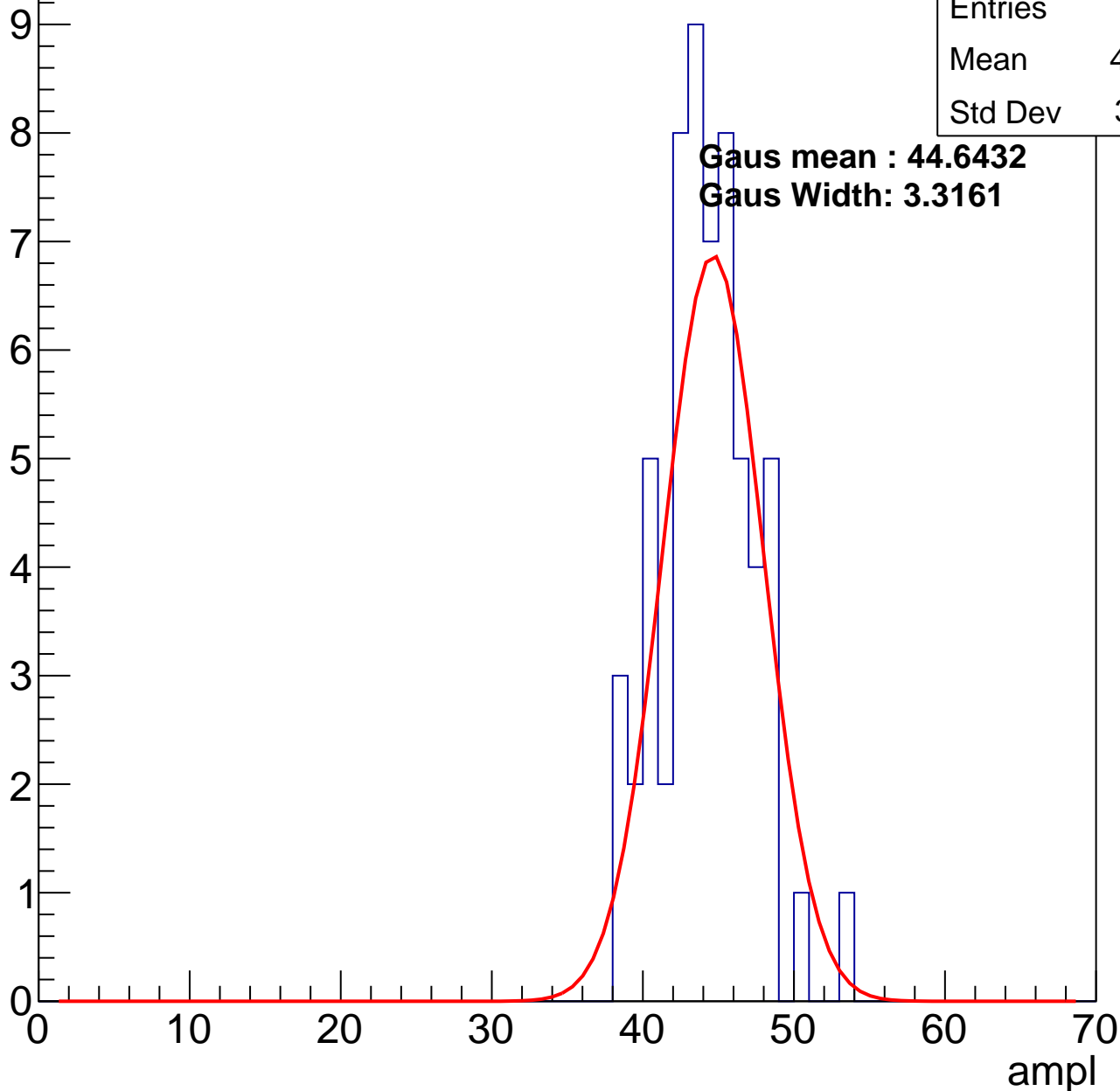
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.77
Std Dev	3.041

Gaus mean : 44.6432

Gaus Width: 3.3161

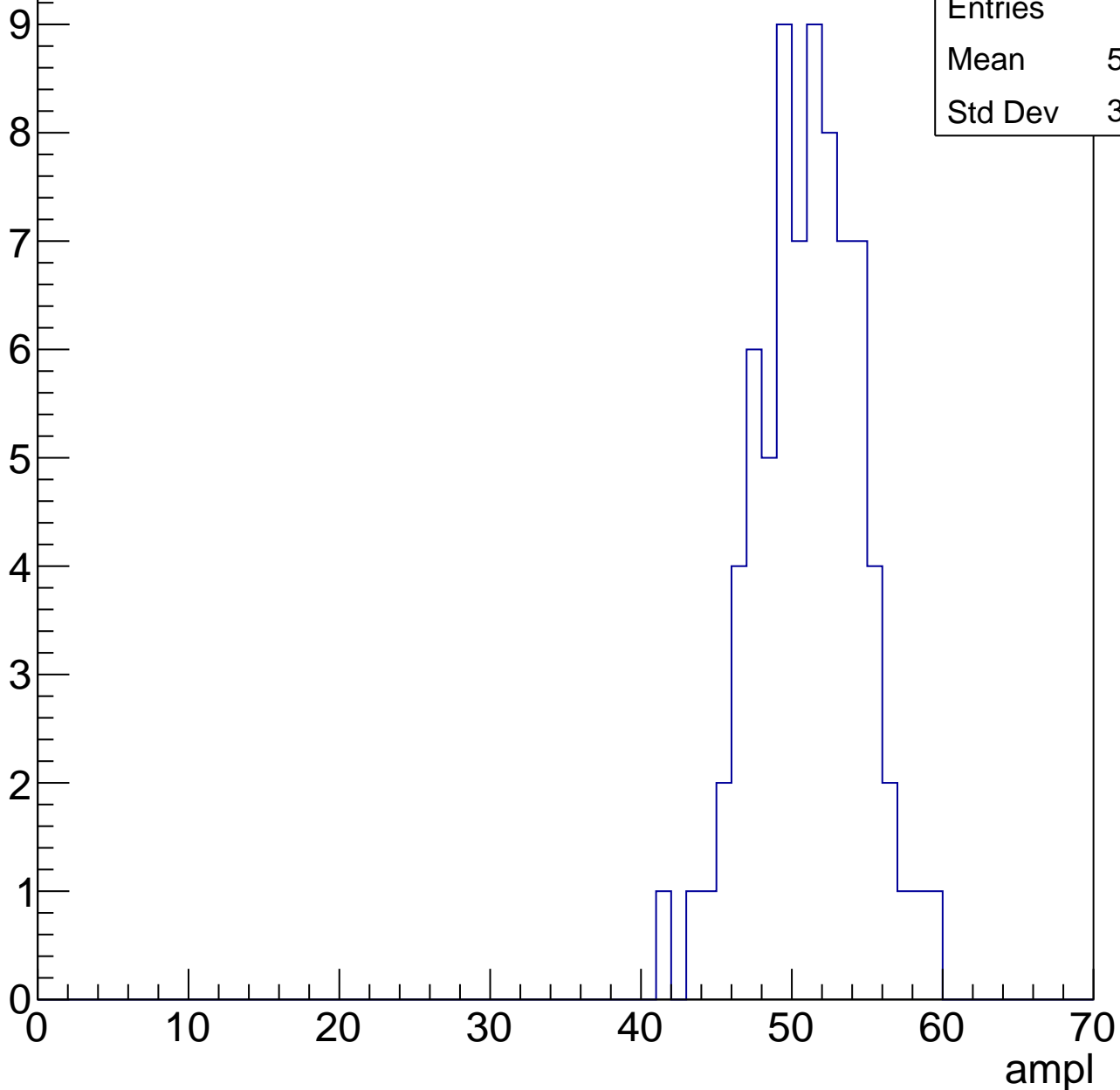


B1L100S, U10-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

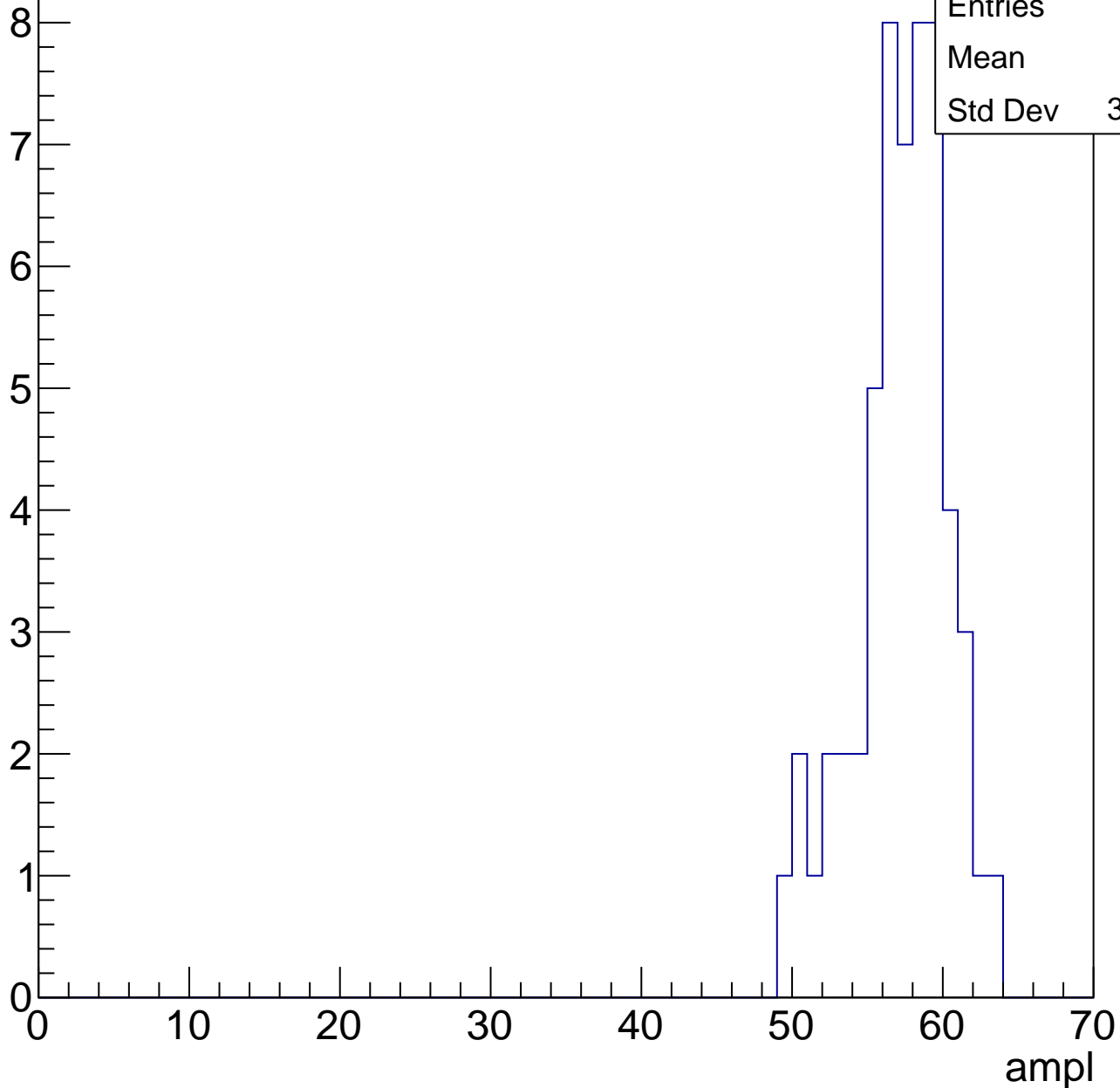
Entries	76
Mean	50.59
Std Dev	3.476



B1L100S, U10-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

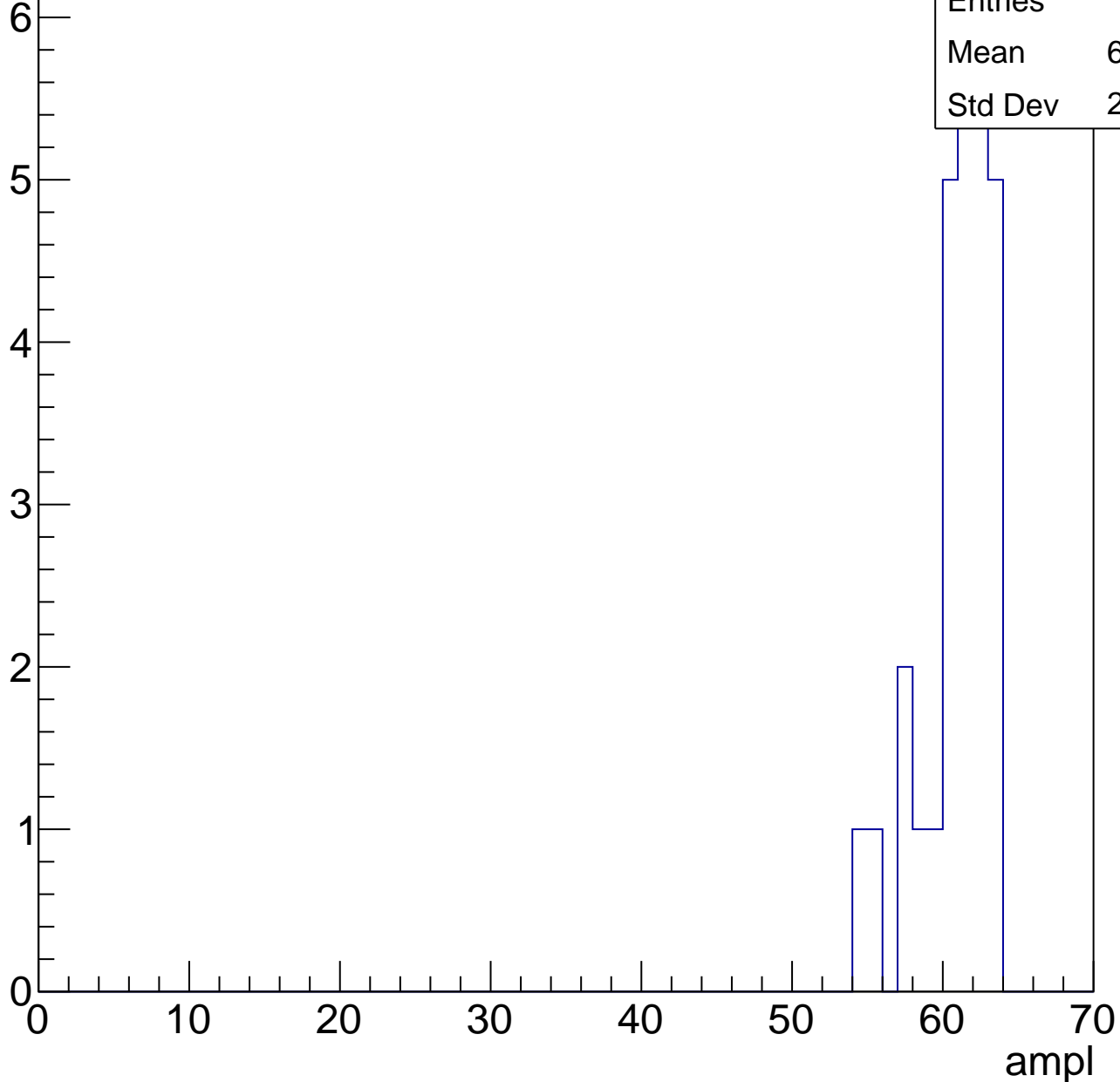
Entry



B1L100S, U10-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

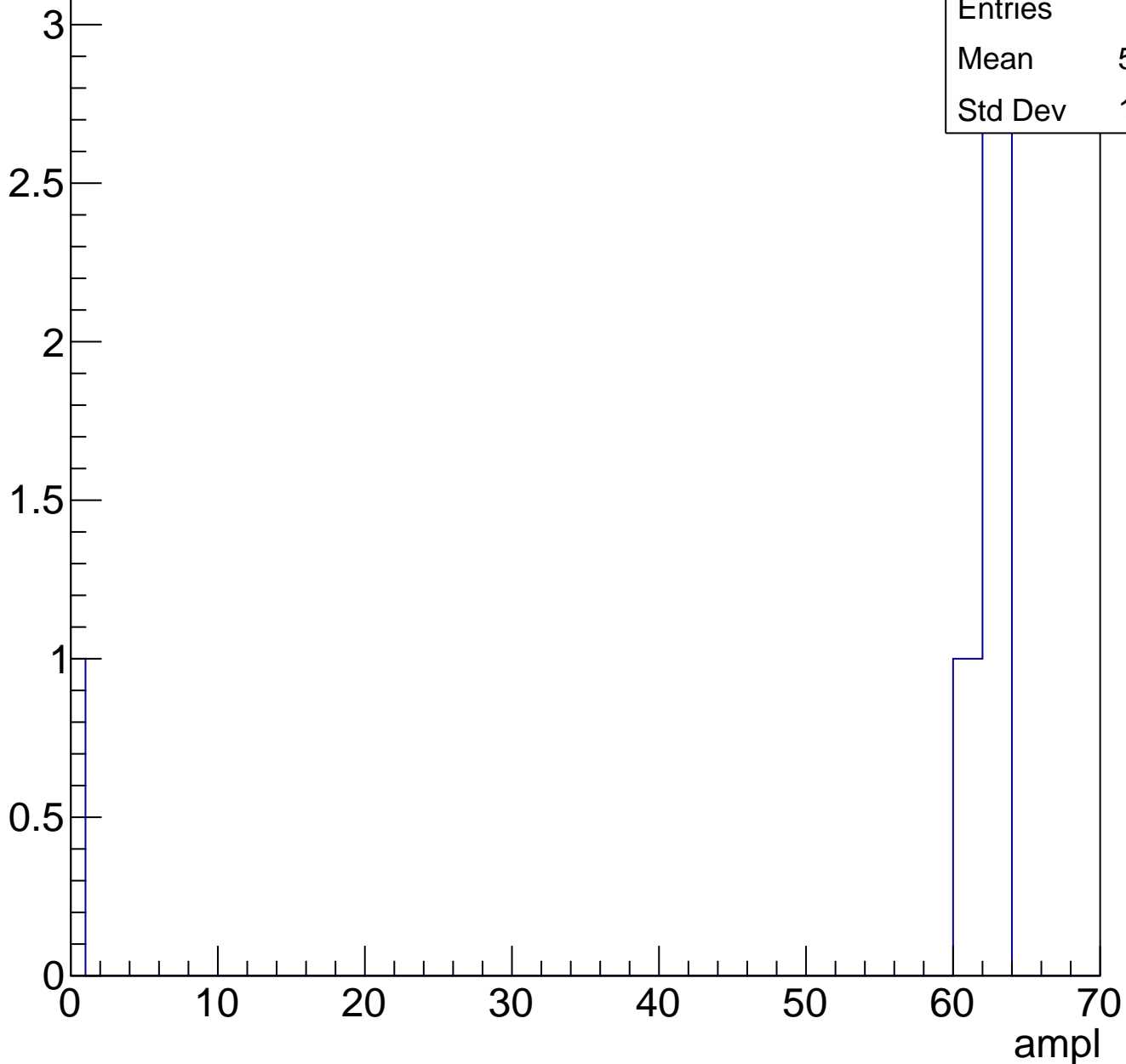
Entry



B1L100S, U10-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch127, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	29.07
Std Dev	4.672

Gaus mean : 30.0596

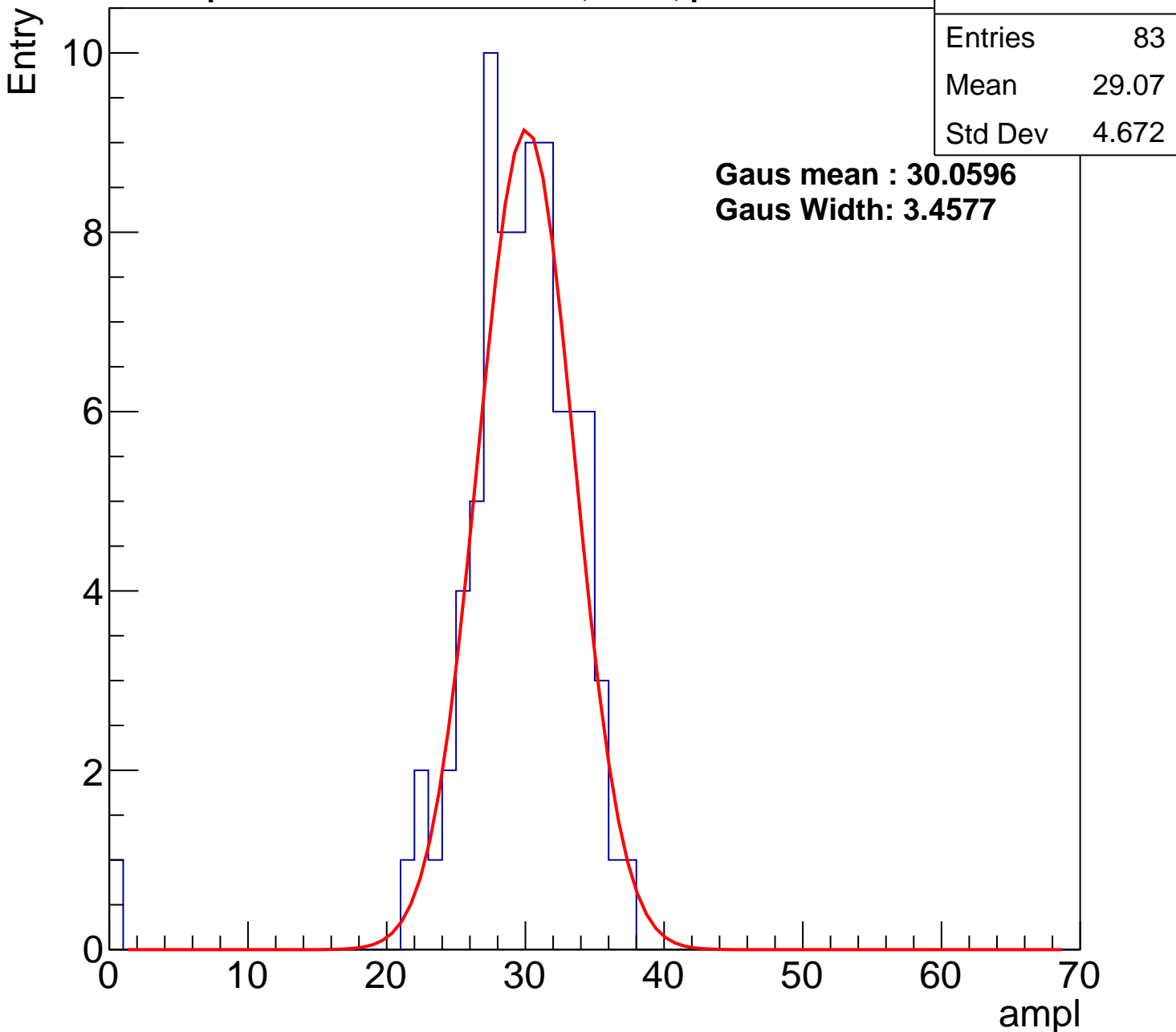
Gaus Width: 3.4577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U10-ch127, adc1

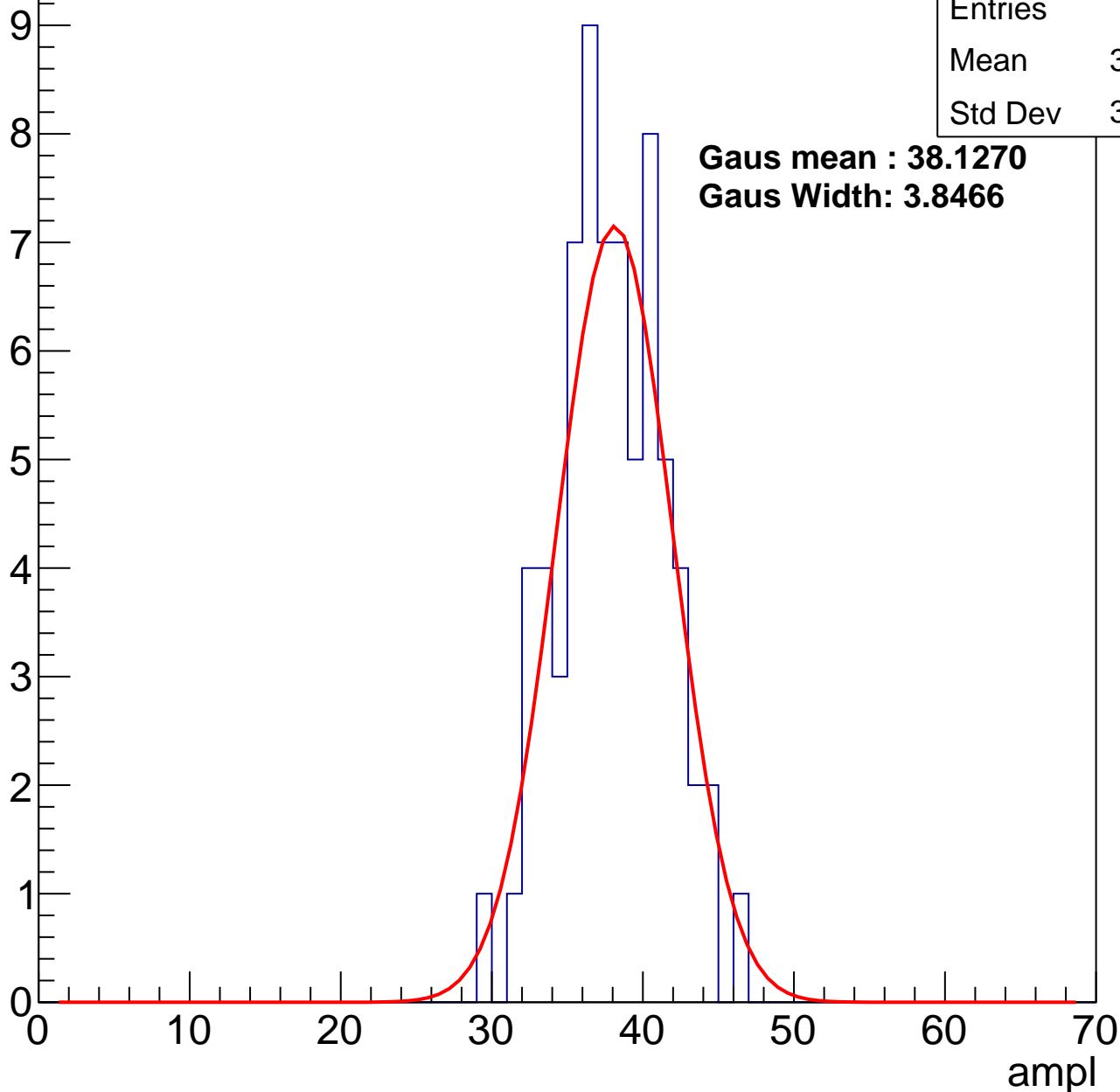
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.49
Std Dev	3.467

Gaus mean : 38.1270

Gaus Width: 3.8466



B1L100S, U10-ch127, adc2

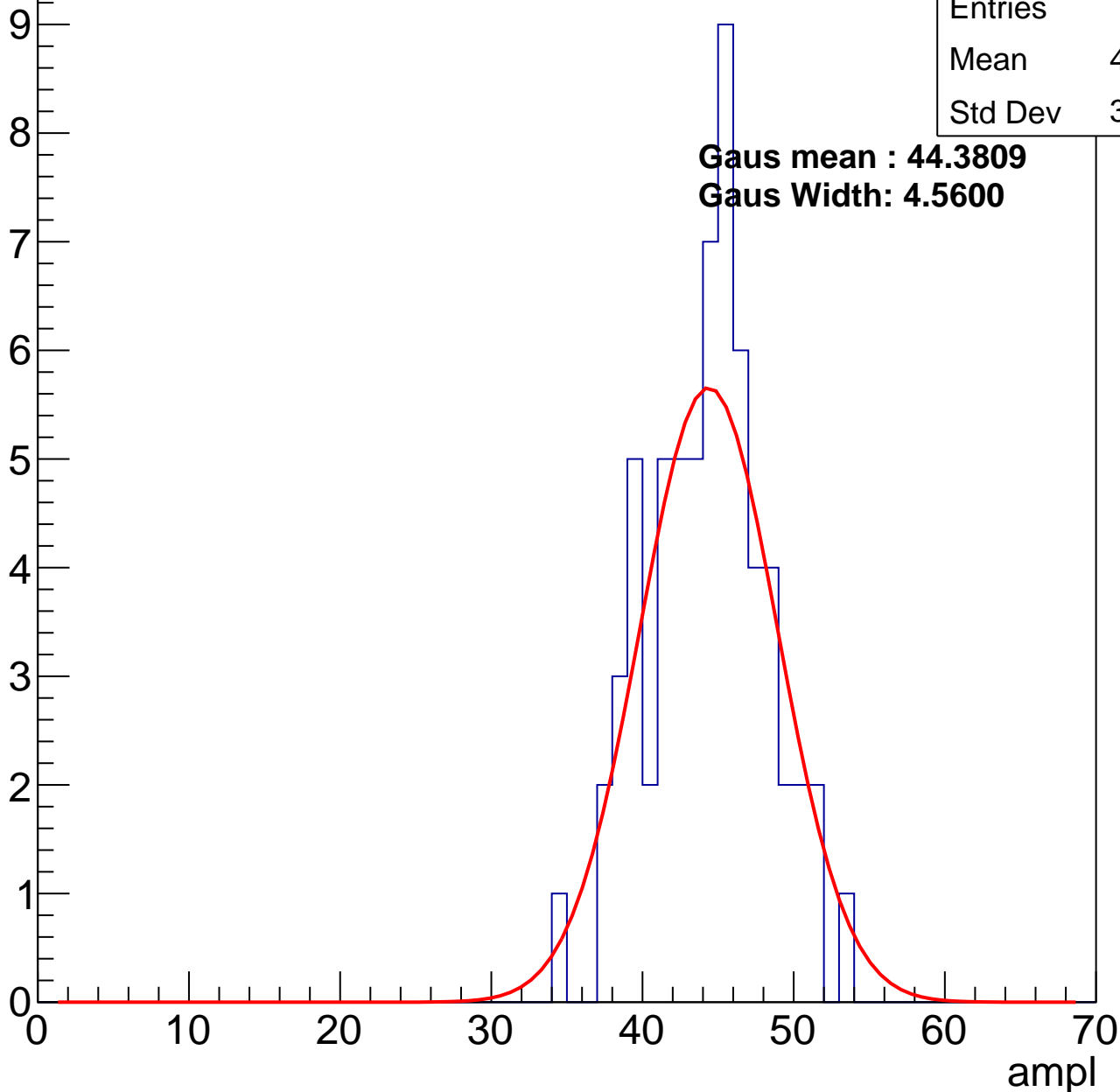
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.83
Std Dev	3.853

Gaus mean : 44.3809

Gaus Width: 4.5600

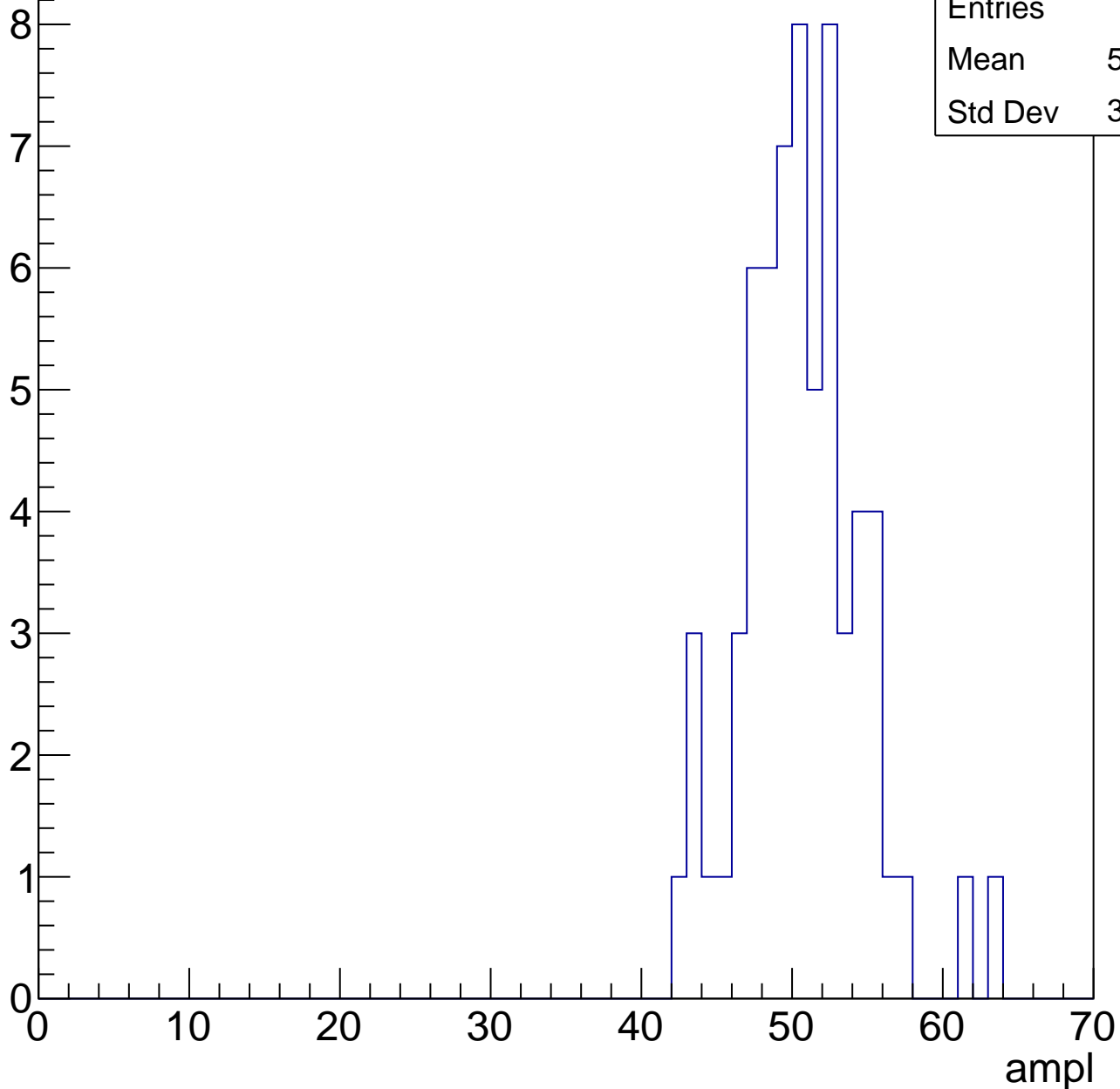


B1L100S, U10-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	50.22
Std Dev	3.963

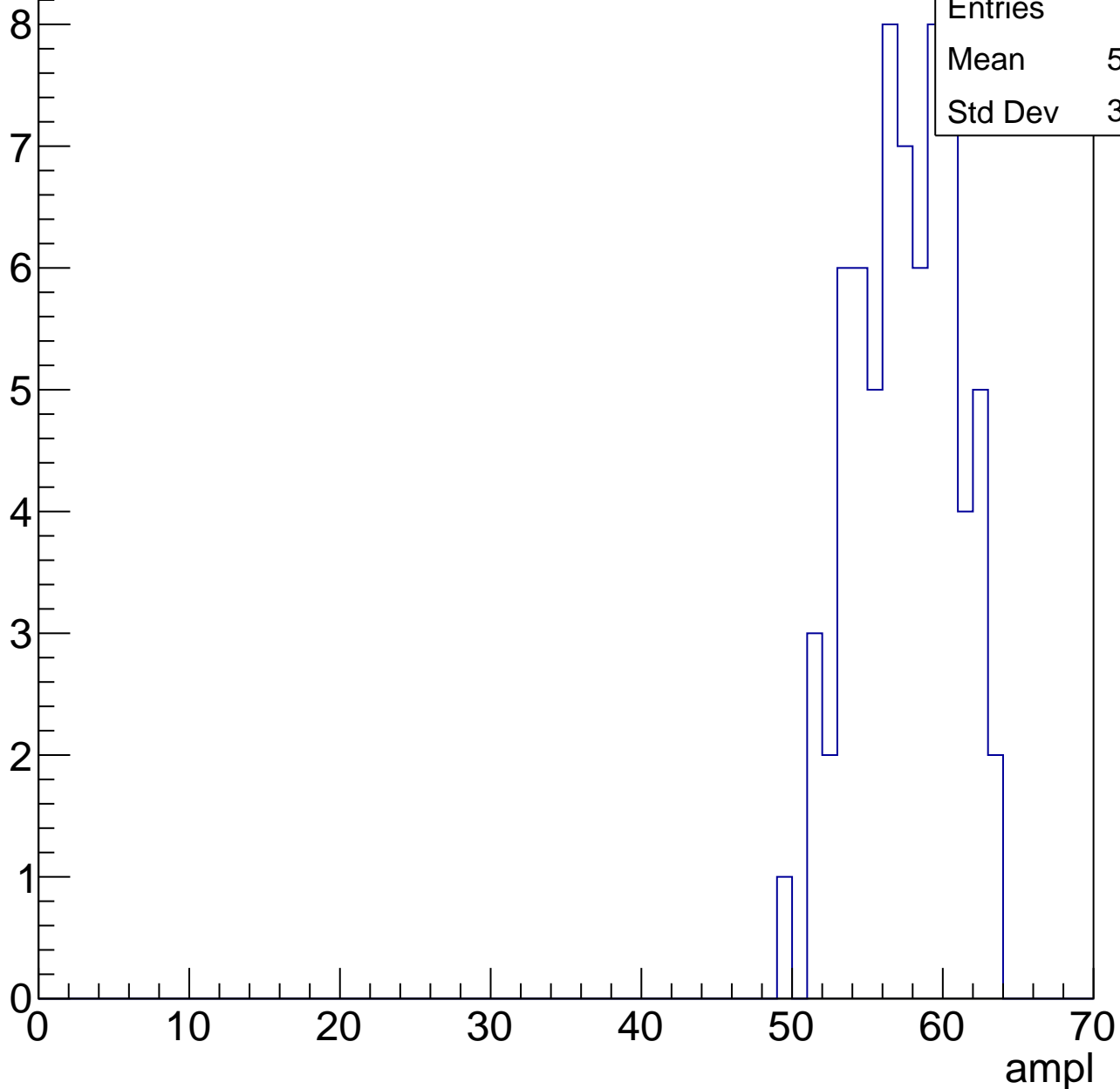


B1L100S, U10-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	57.04
Std Dev	3.295

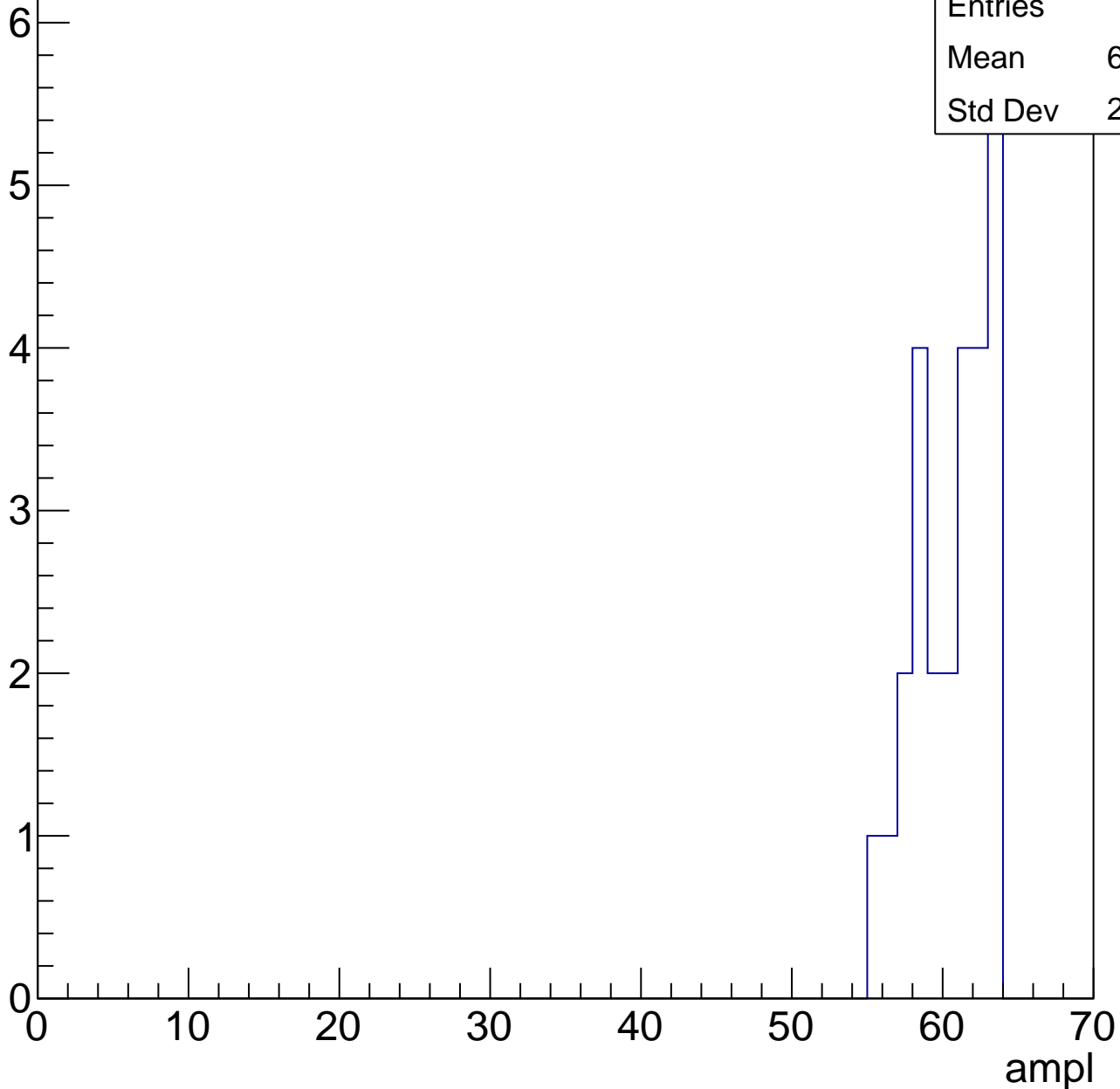


B1L100S, U10-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	60.19
Std Dev	2.402



B1L100S, U10-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

