

B1L103S, U17-ch0, adc0

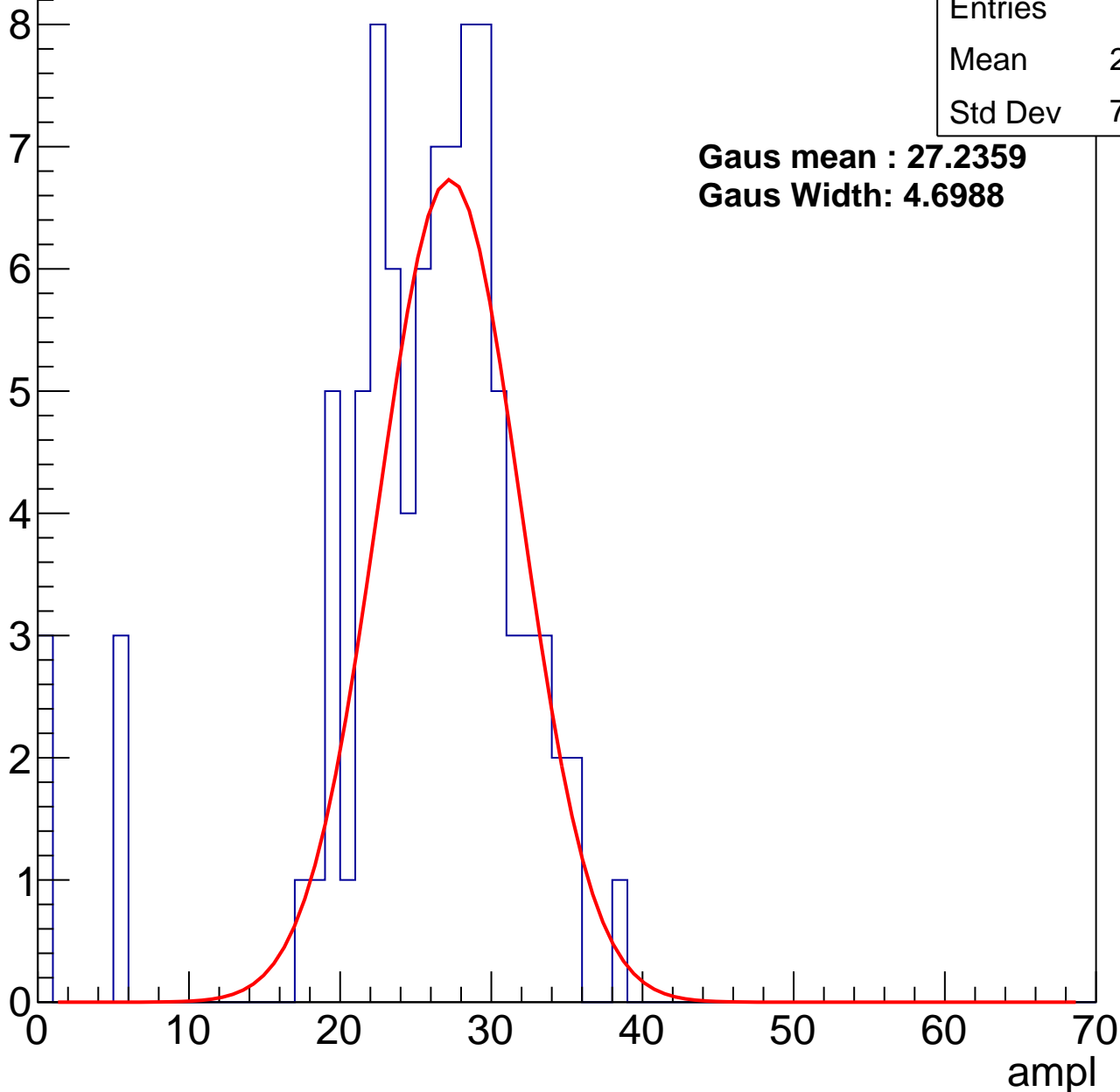
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	92
Mean	24.68
Std Dev	7.299

Gaus mean : 27.2359

Gaus Width: 4.6988



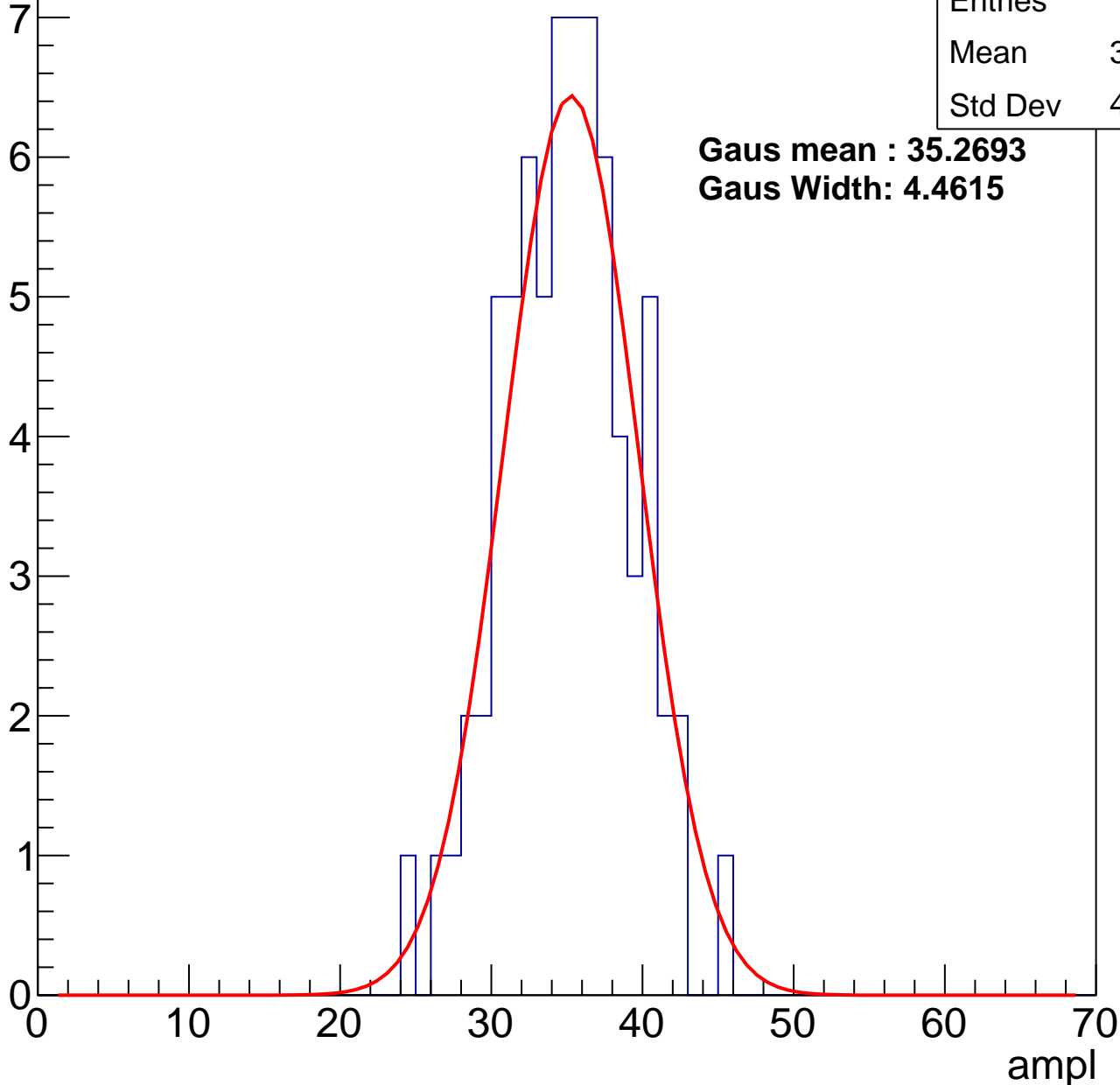
B1L103S, U17-ch0, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	34.58
Std Dev	4.116

Gaus mean : 35.2693
Gaus Width: 4.4615



B1L103S, U17-ch0, adc2

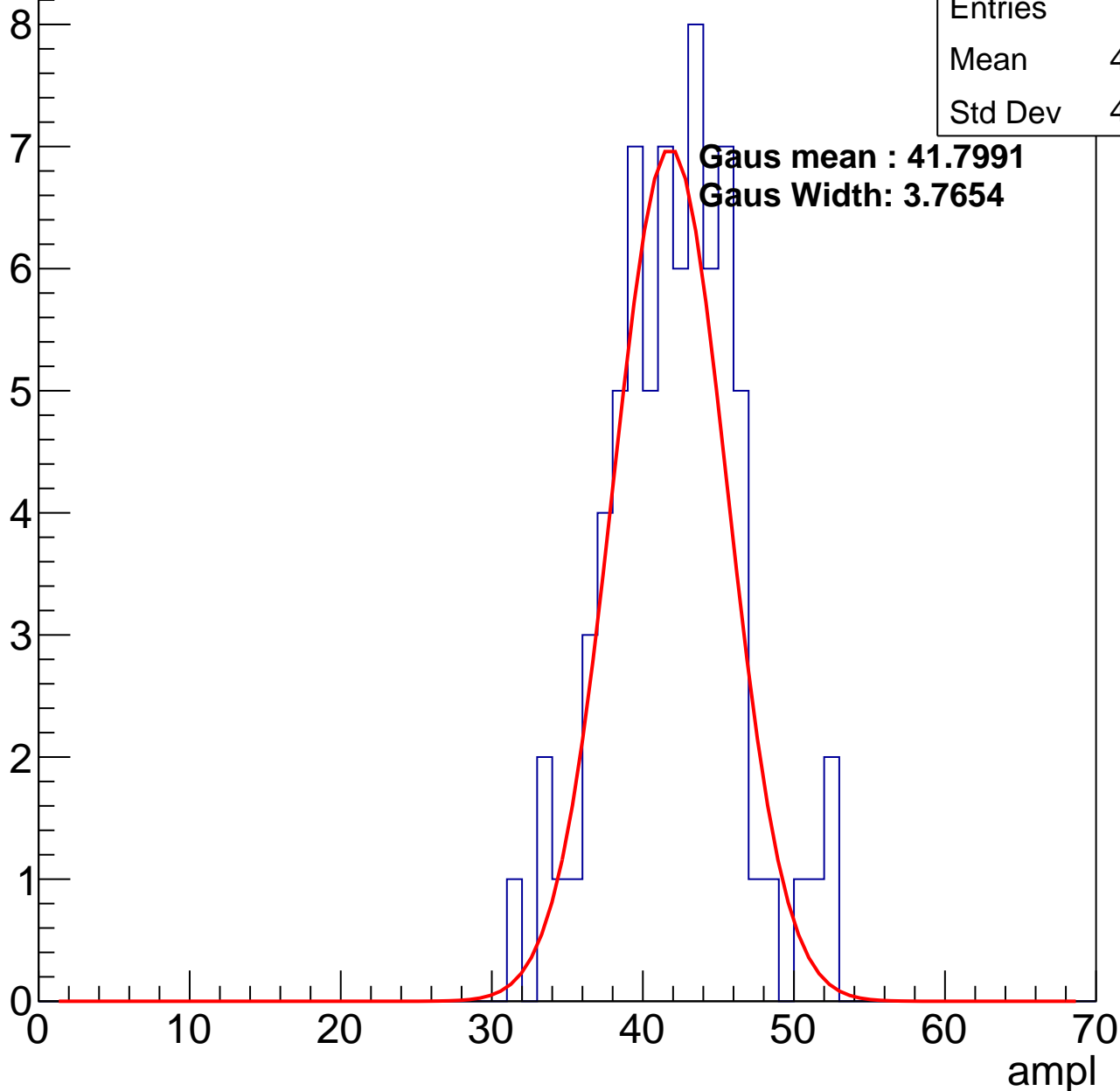
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	41.58
Std Dev	4.259

Gaus mean : 41.7991

Gaus Width: 3.7654

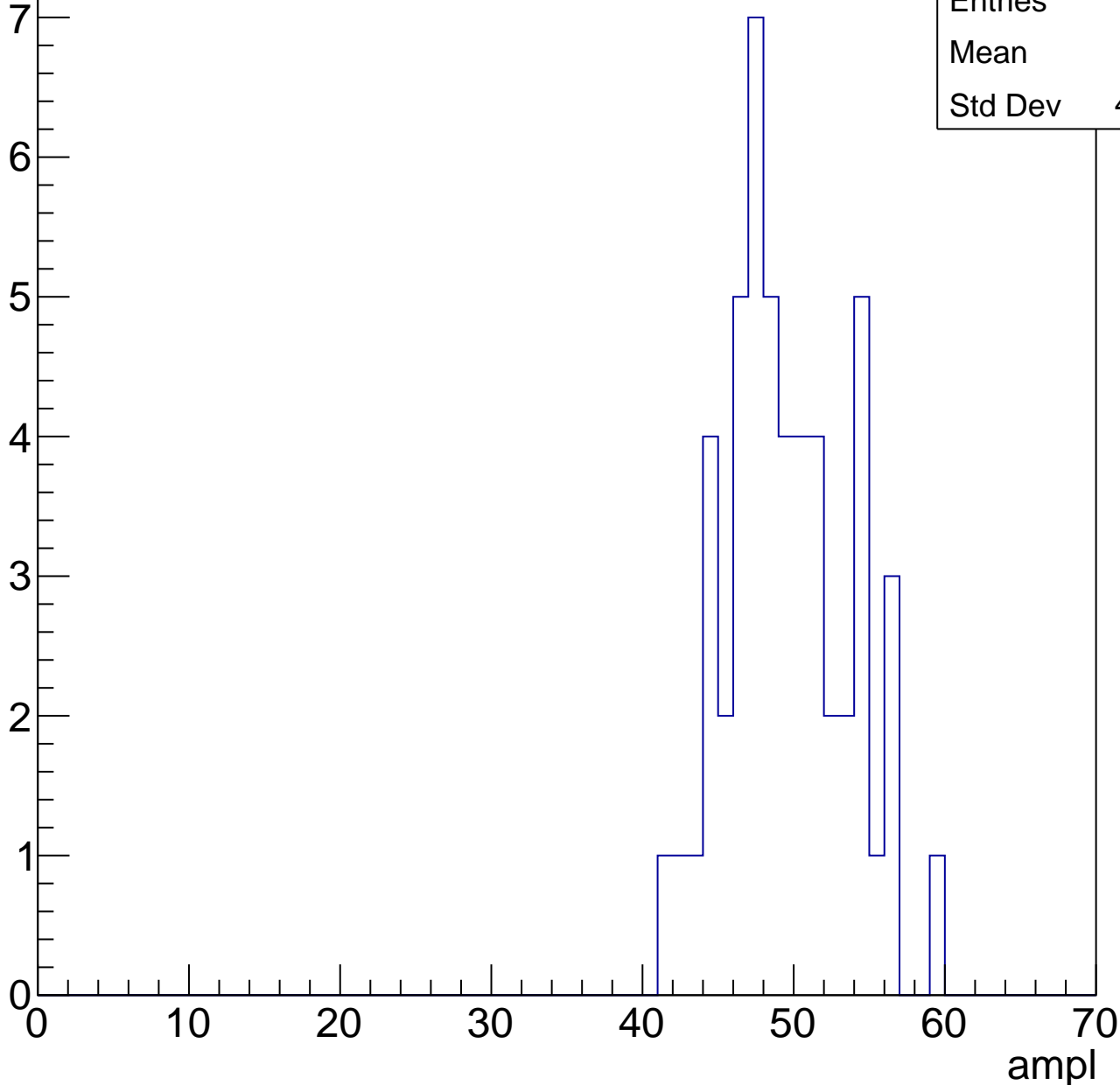


B1L103S, U17-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	49.1
Std Dev	4.011

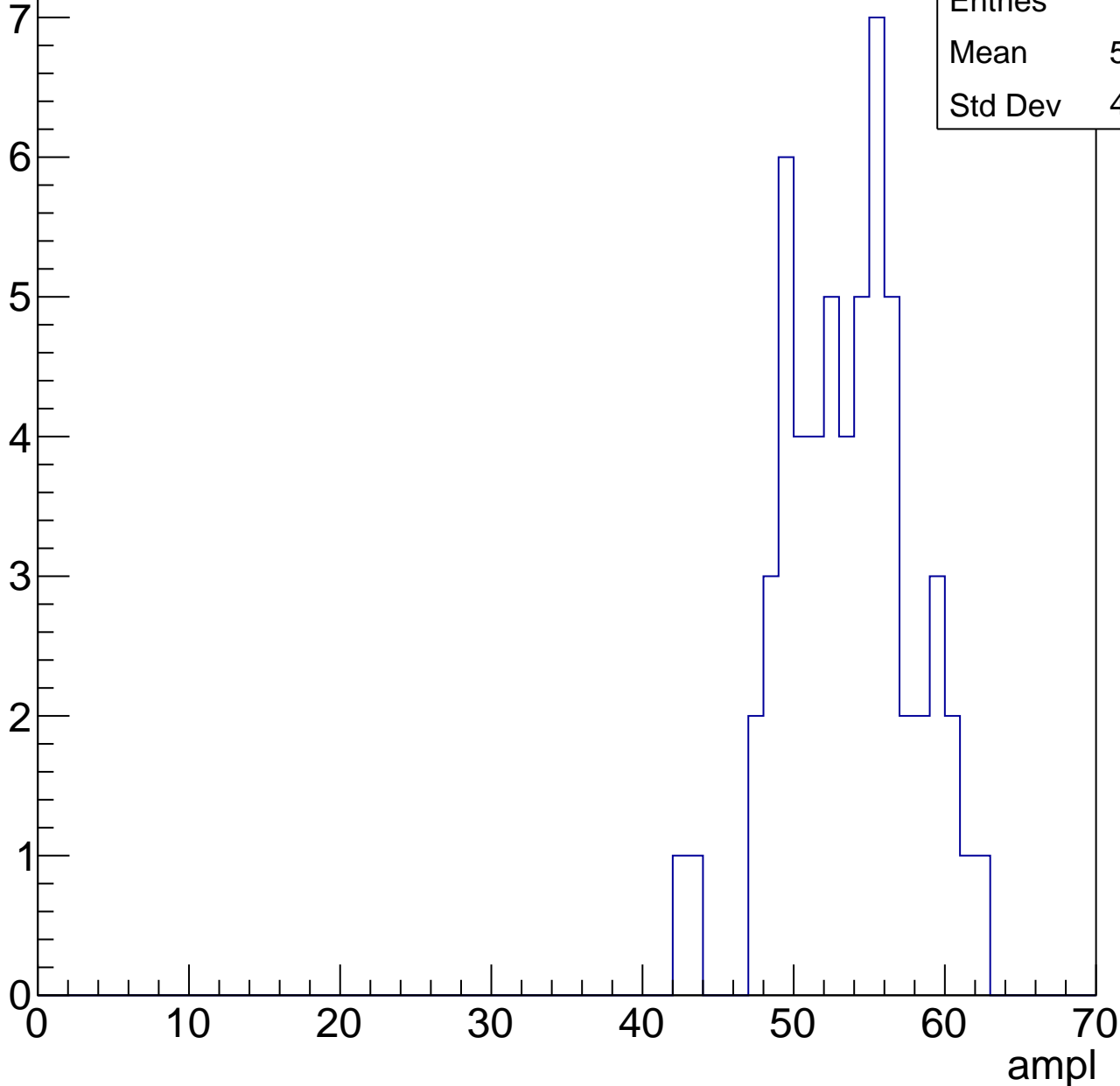


B1L103S, U17-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	53.07
Std Dev	4.218

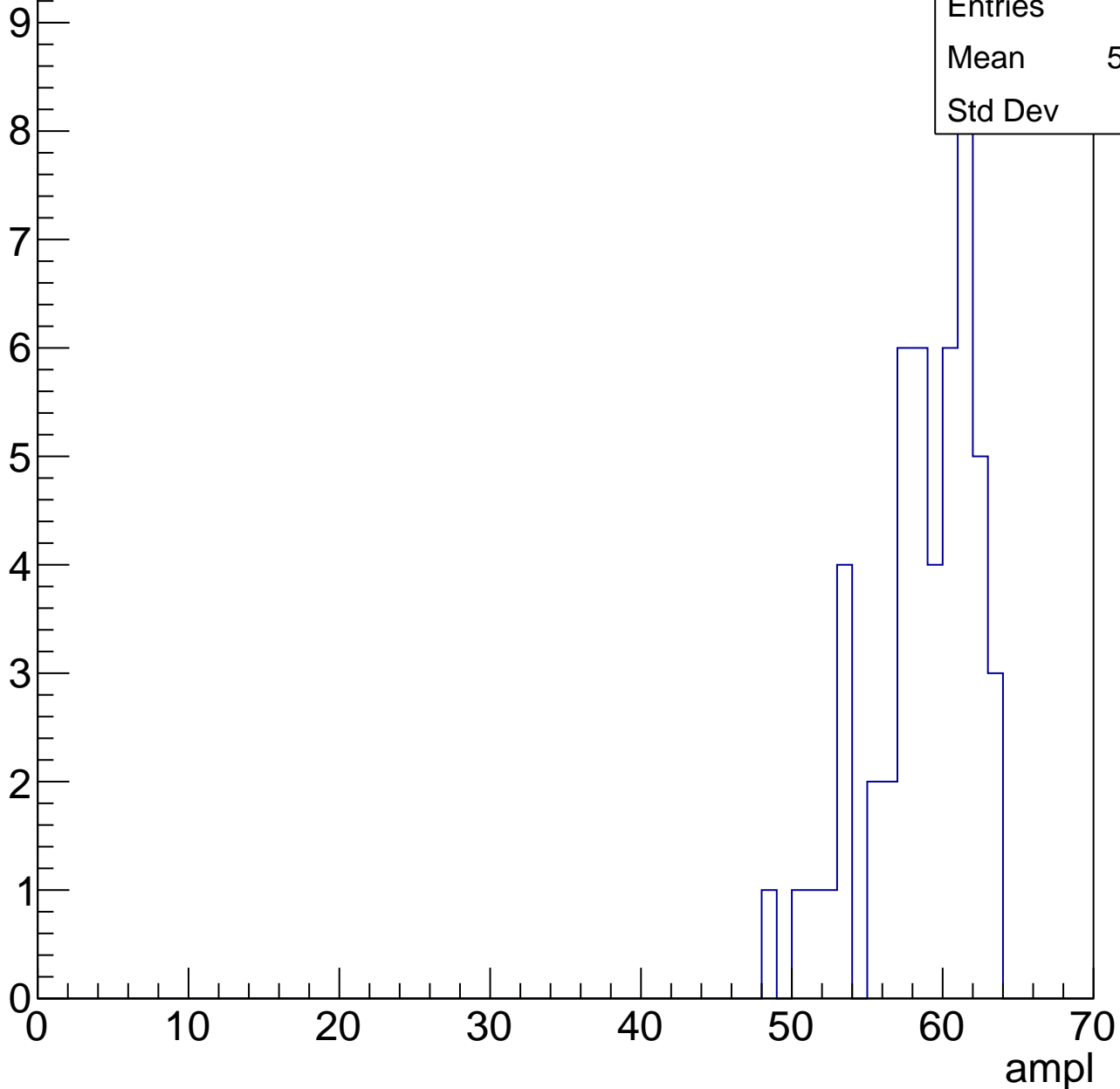


B1L103S, U17-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.22
Std Dev	3.55

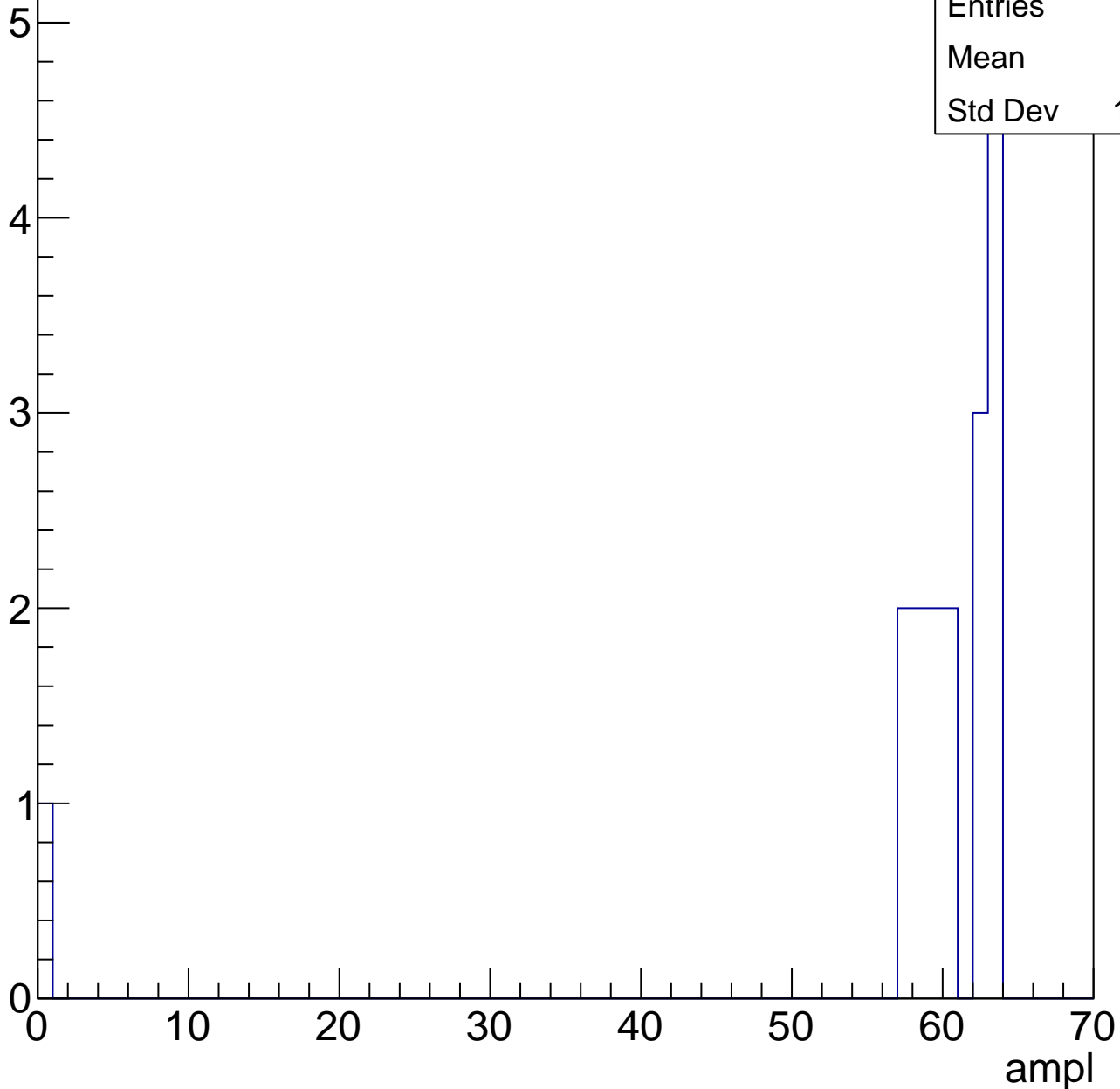


B1L103S, U17-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57
Std Dev	14.41



B1L103S, U17-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U17-ch1, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	89
Mean	29.64
Std Dev	6

Gaus mean : 30.6955

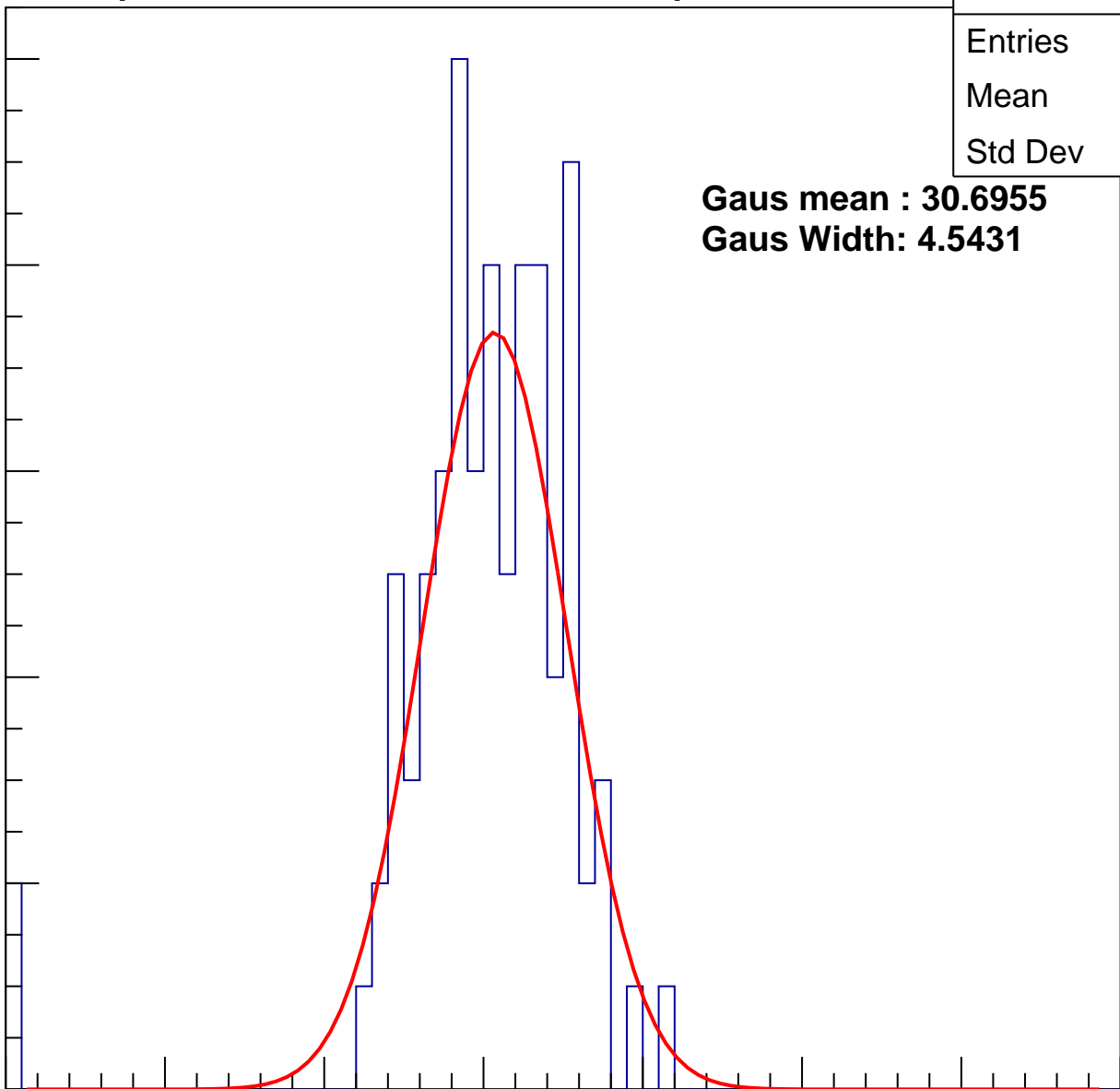
Gaus Width: 4.5431

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch1, adc1

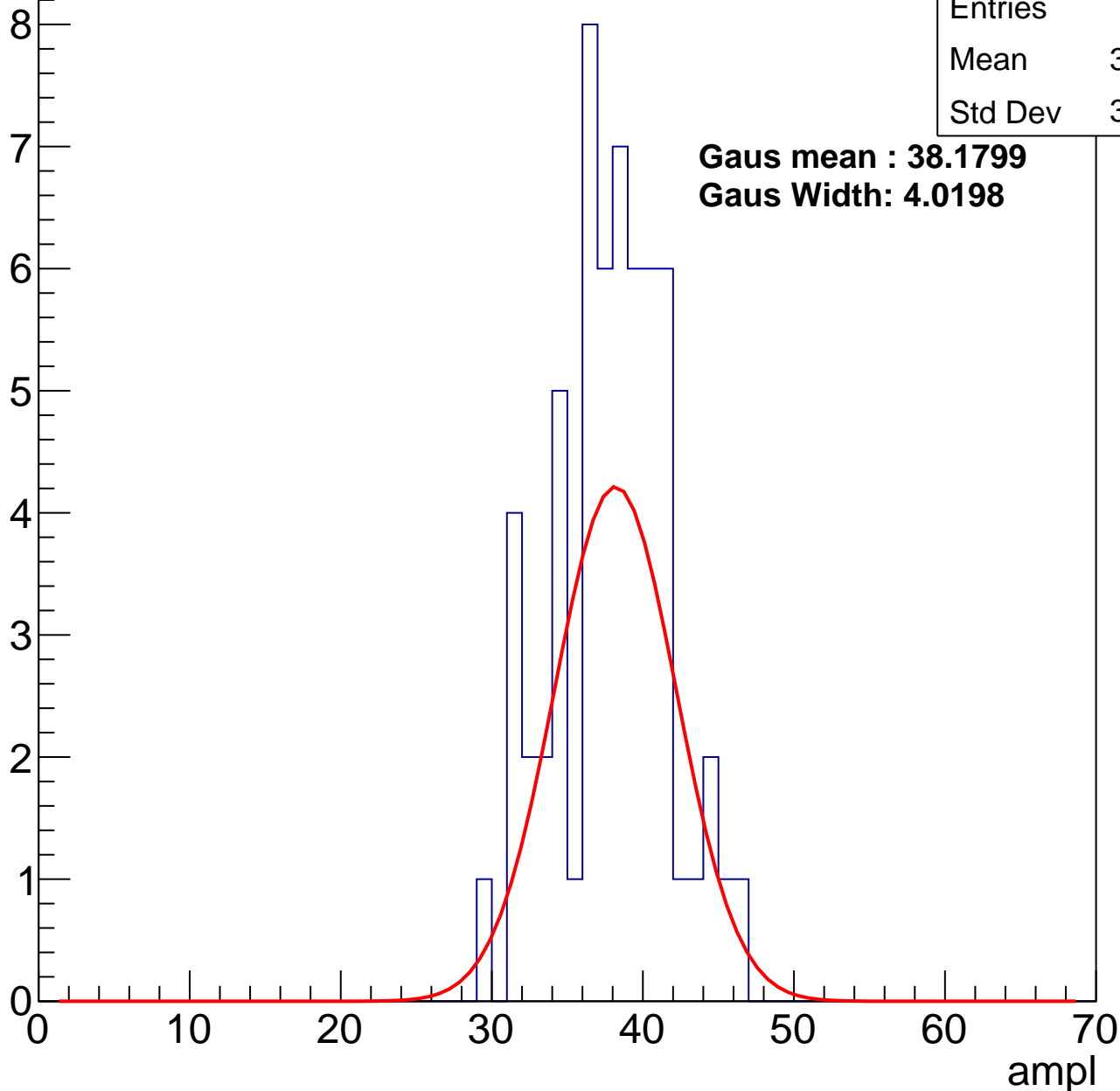
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37.47
Std Dev	3.694

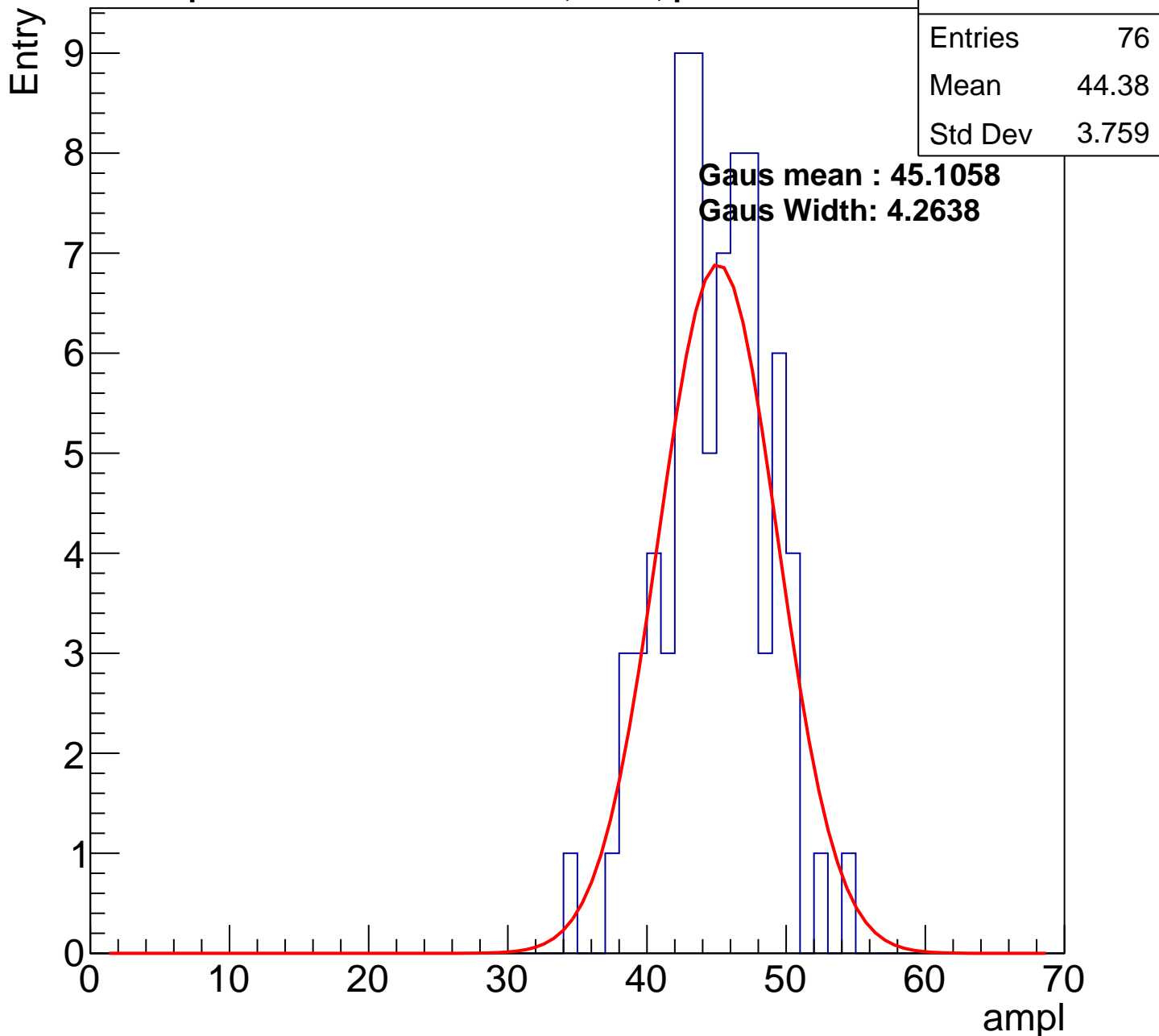
Gaus mean : 38.1799

Gaus Width: 4.0198



B1L103S, U17-ch1, adc2

calib_packv5_042523_0143.root, FC#7, port C2

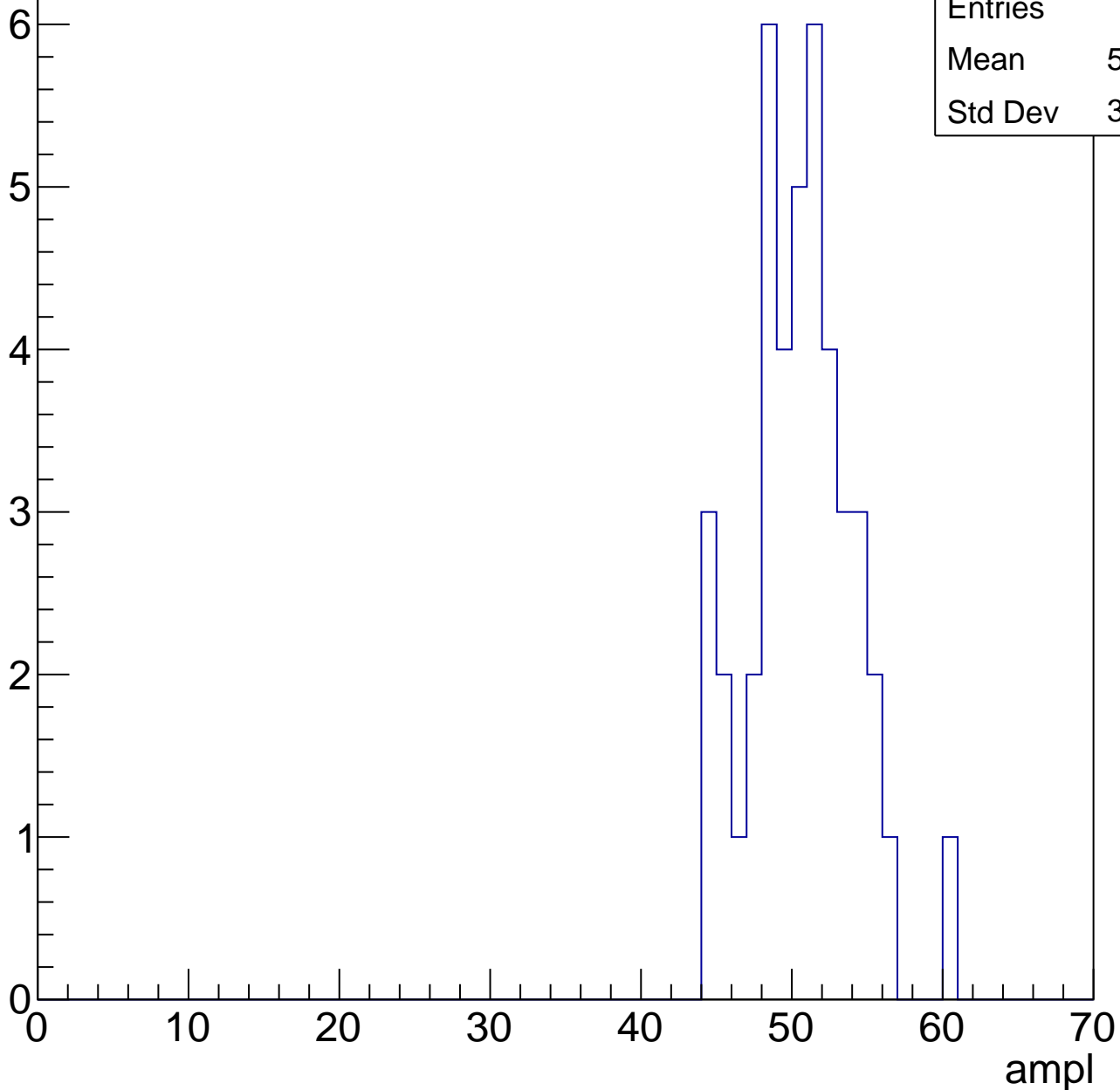


B1L103S, U17-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	50.16
Std Dev	3.416

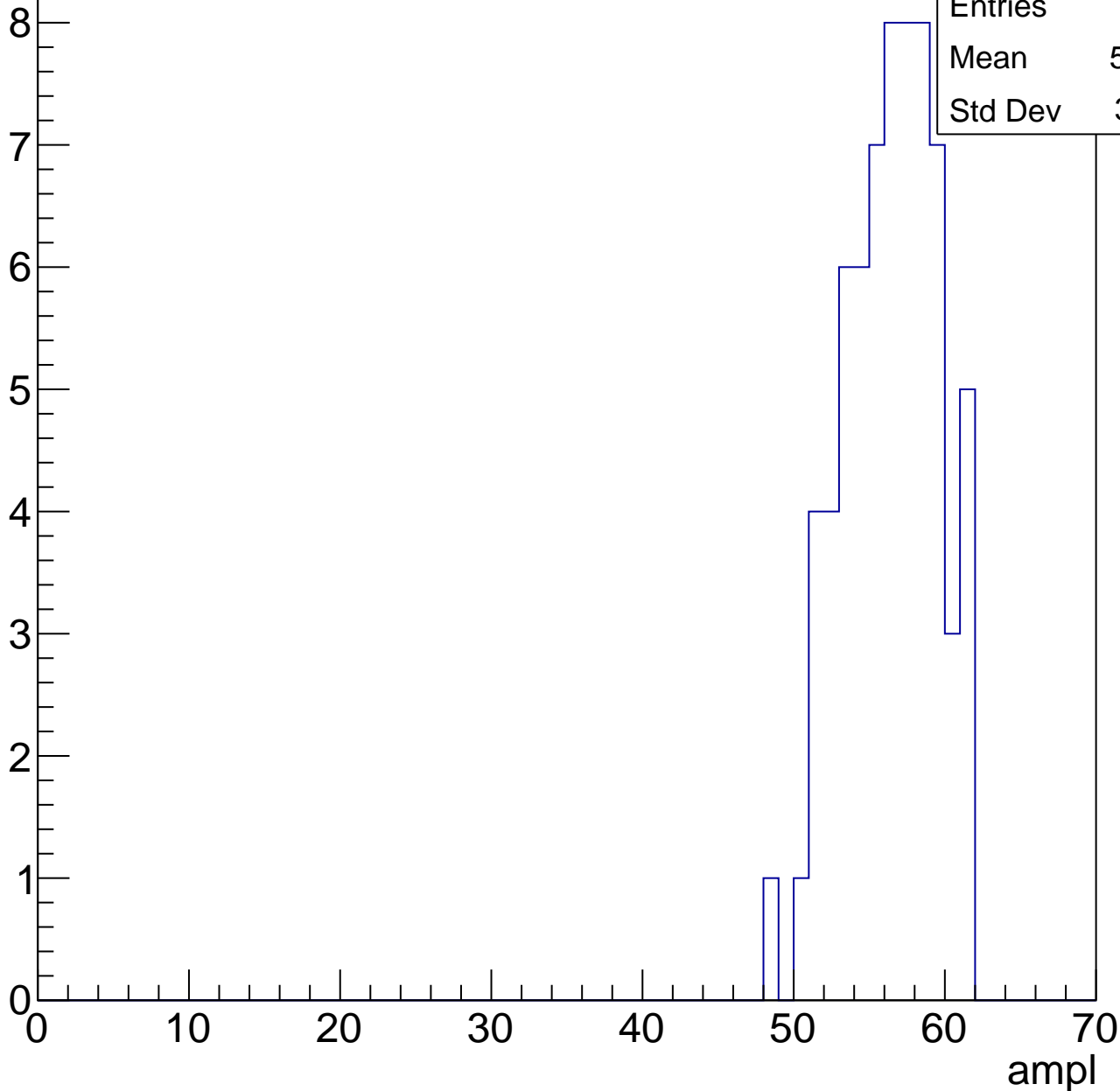


B1L103S, U17-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.93
Std Dev	3.031

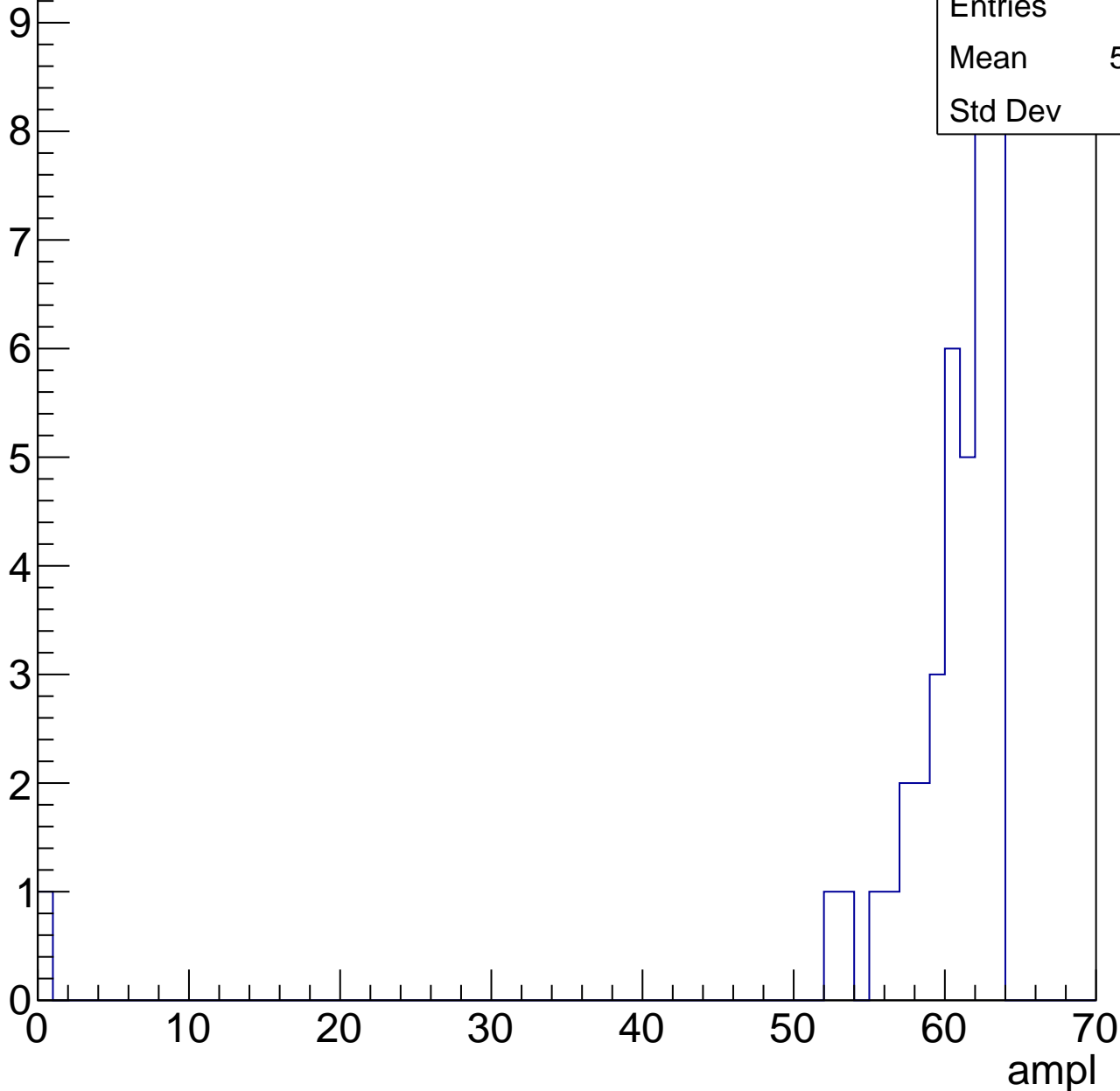


B1L103S, U17-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.75
Std Dev	9.79



B1L103S, U17-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch2, adc0

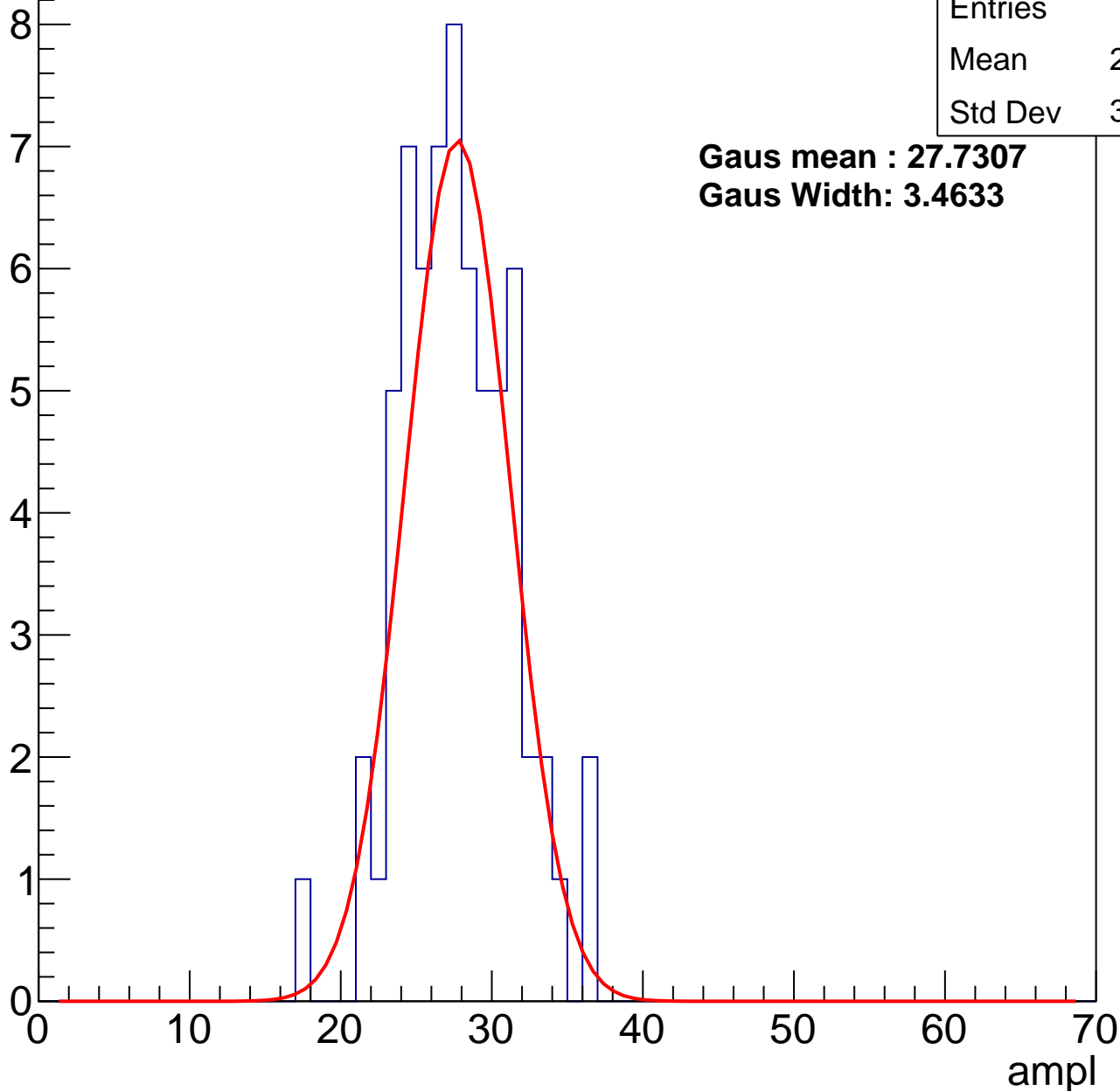
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	27.23
Std Dev	3.626

Gaus mean : 27.7307

Gaus Width: 3.4633



B1L103S, U17-ch2, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	34.23
Std Dev	3.924

Gaus mean : 34.8435

Gaus Width: 3.7007

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U17-ch2, adc2

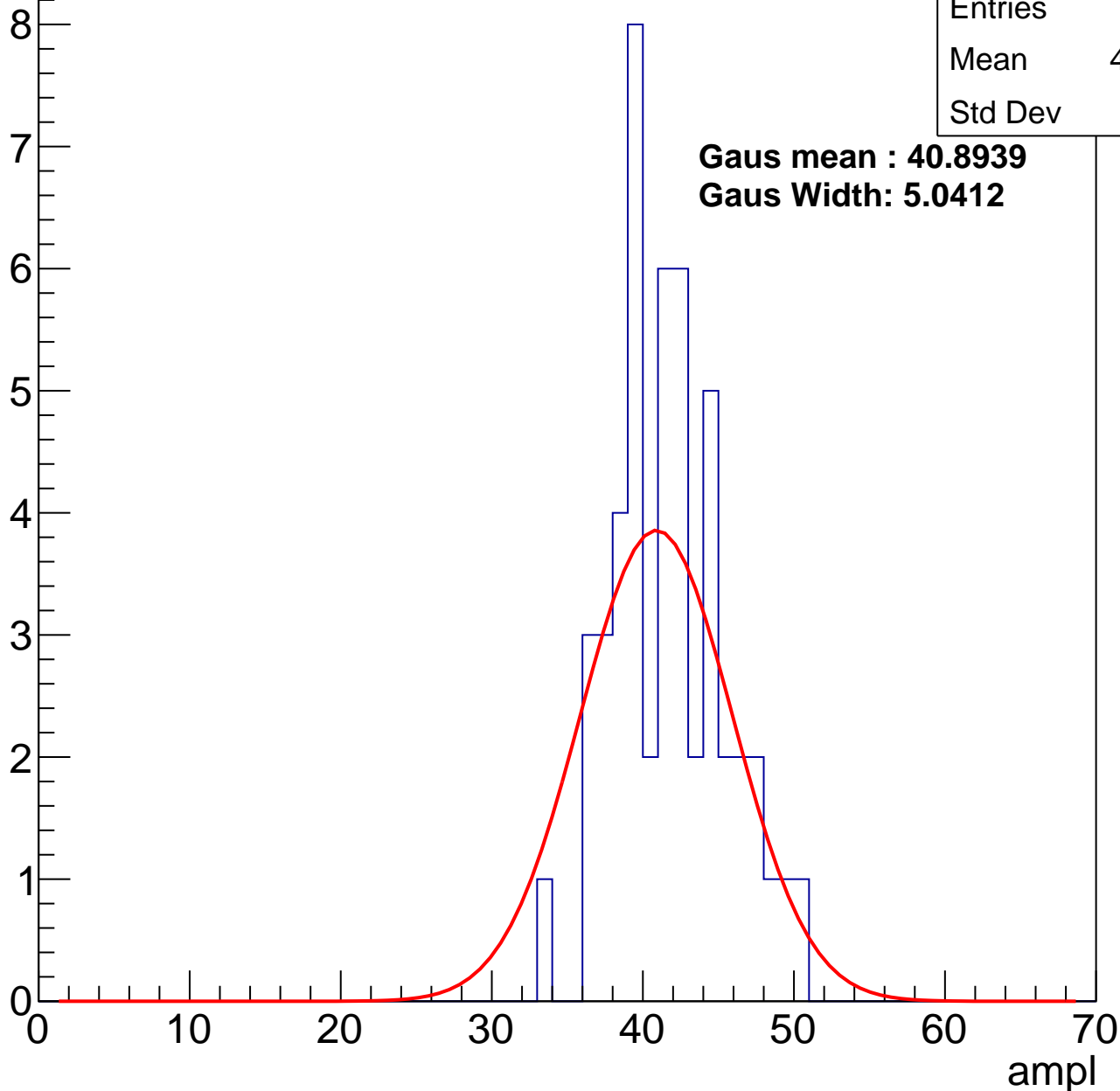
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	41.29
Std Dev	3.67

Gaus mean : 40.8939

Gaus Width: 5.0412

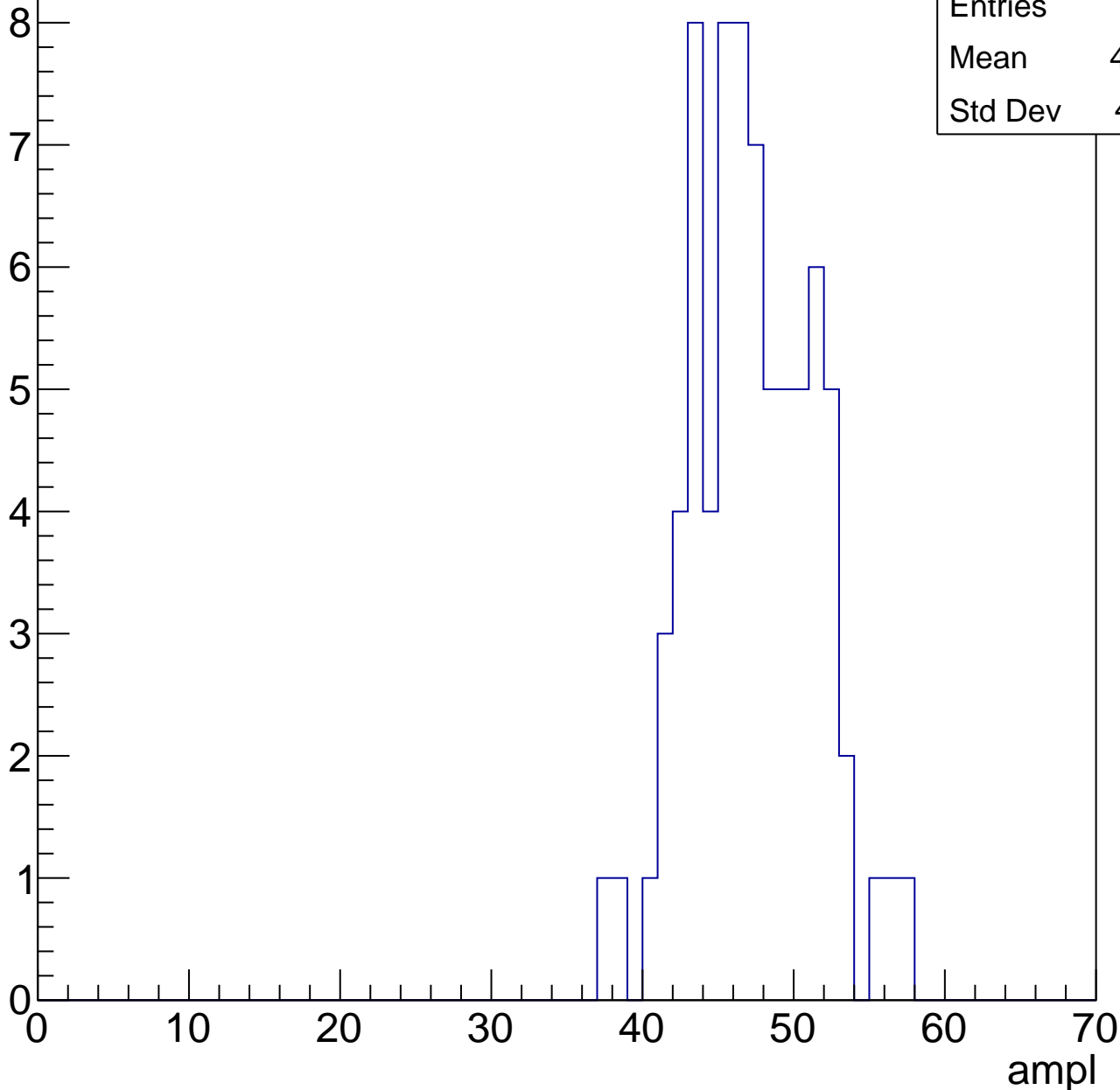


B1L103S, U17-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

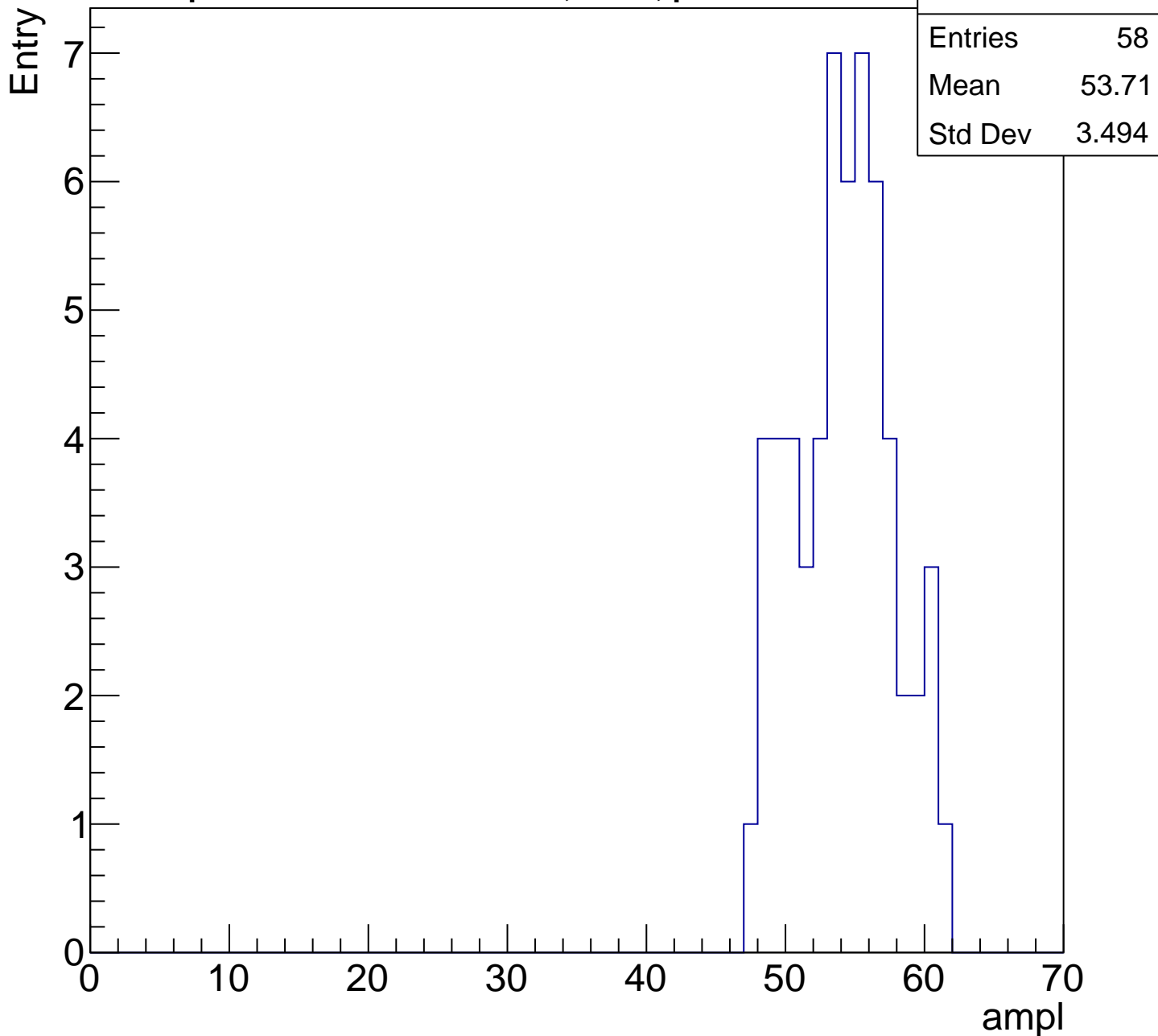
Entry

Entries	76
Mean	46.82
Std Dev	4.051



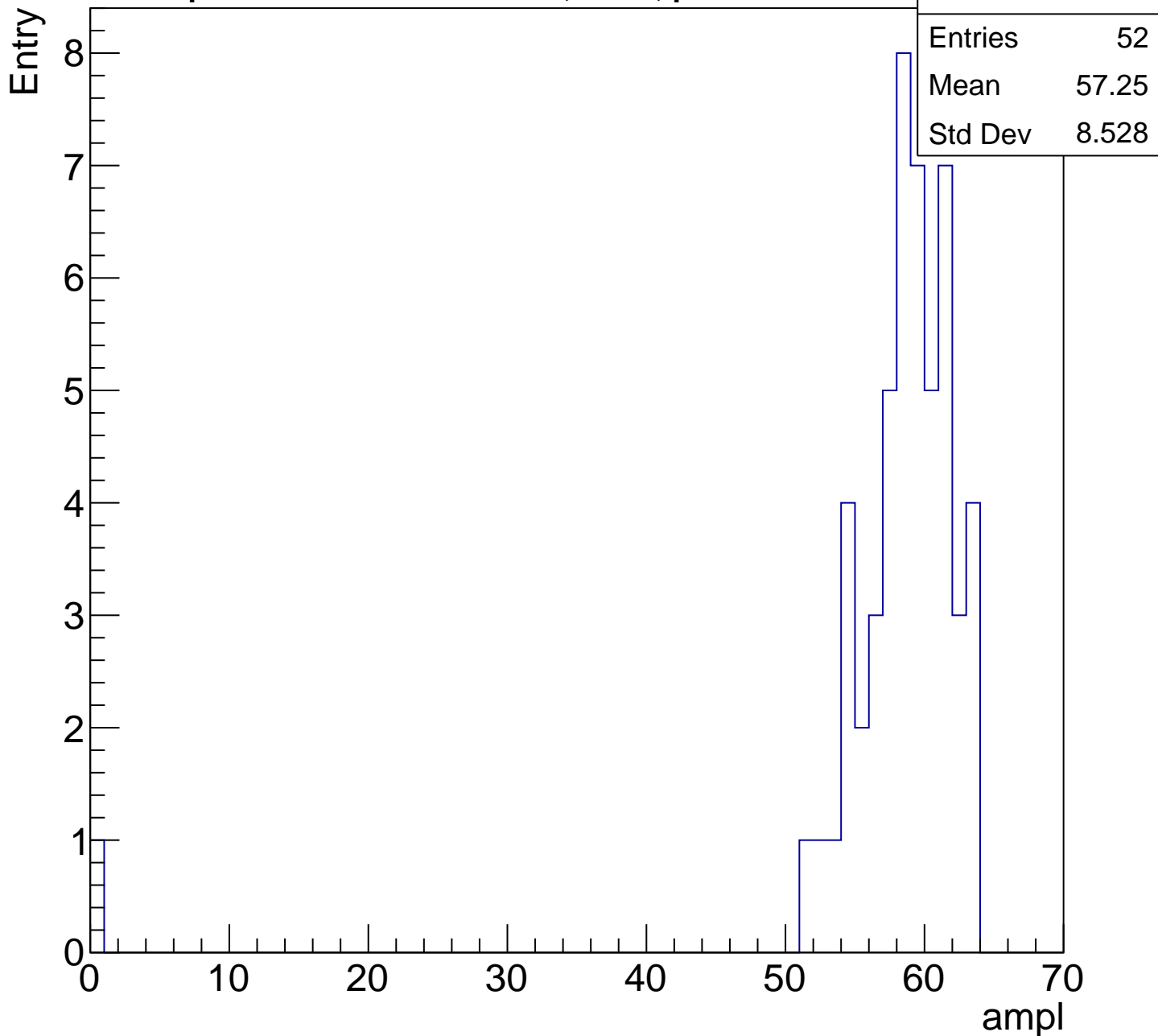
B1L103S, U17-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

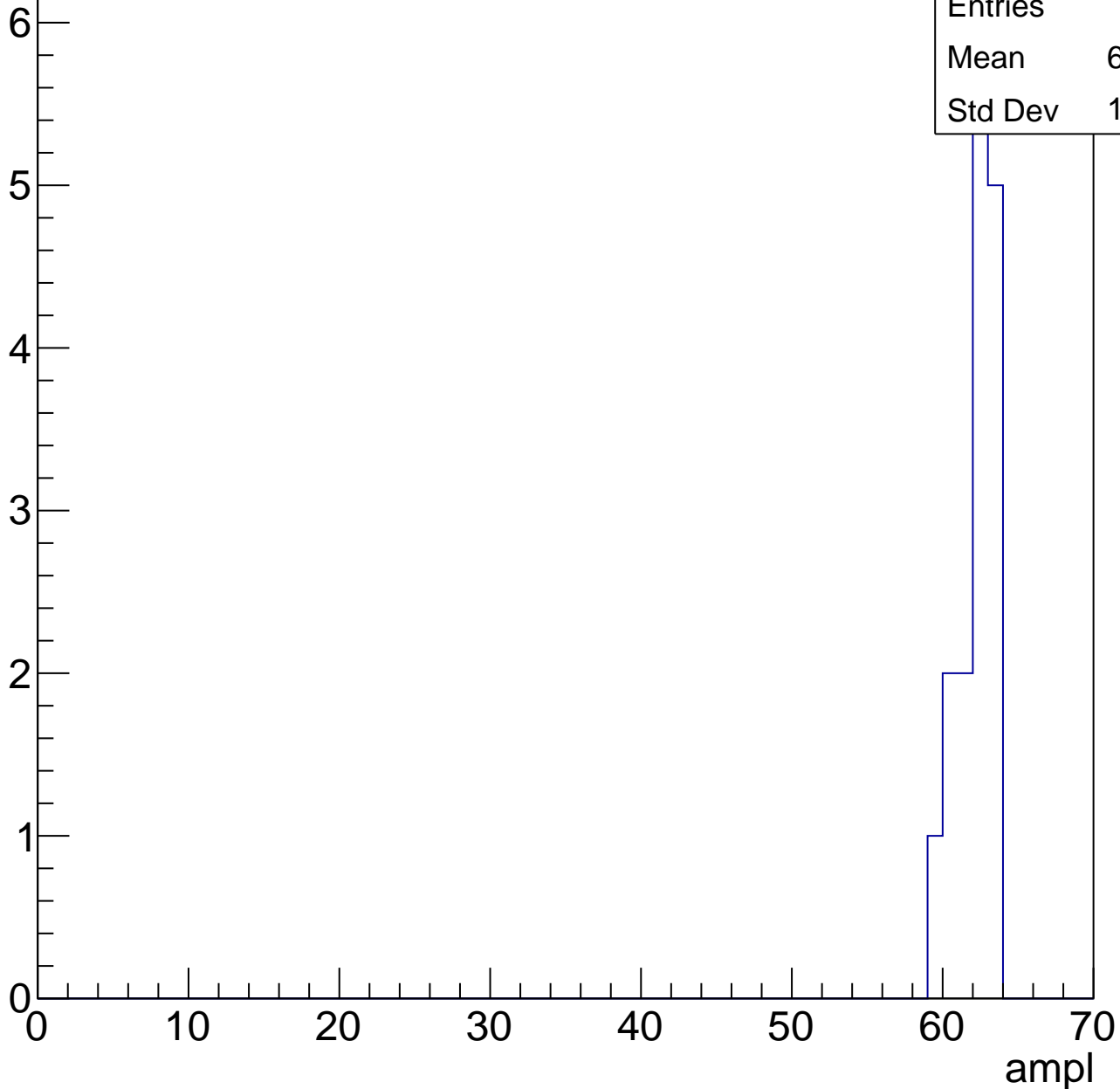


B1L103S, U17-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.75
Std Dev	1.199



B1L103S, U17-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch3, adc0

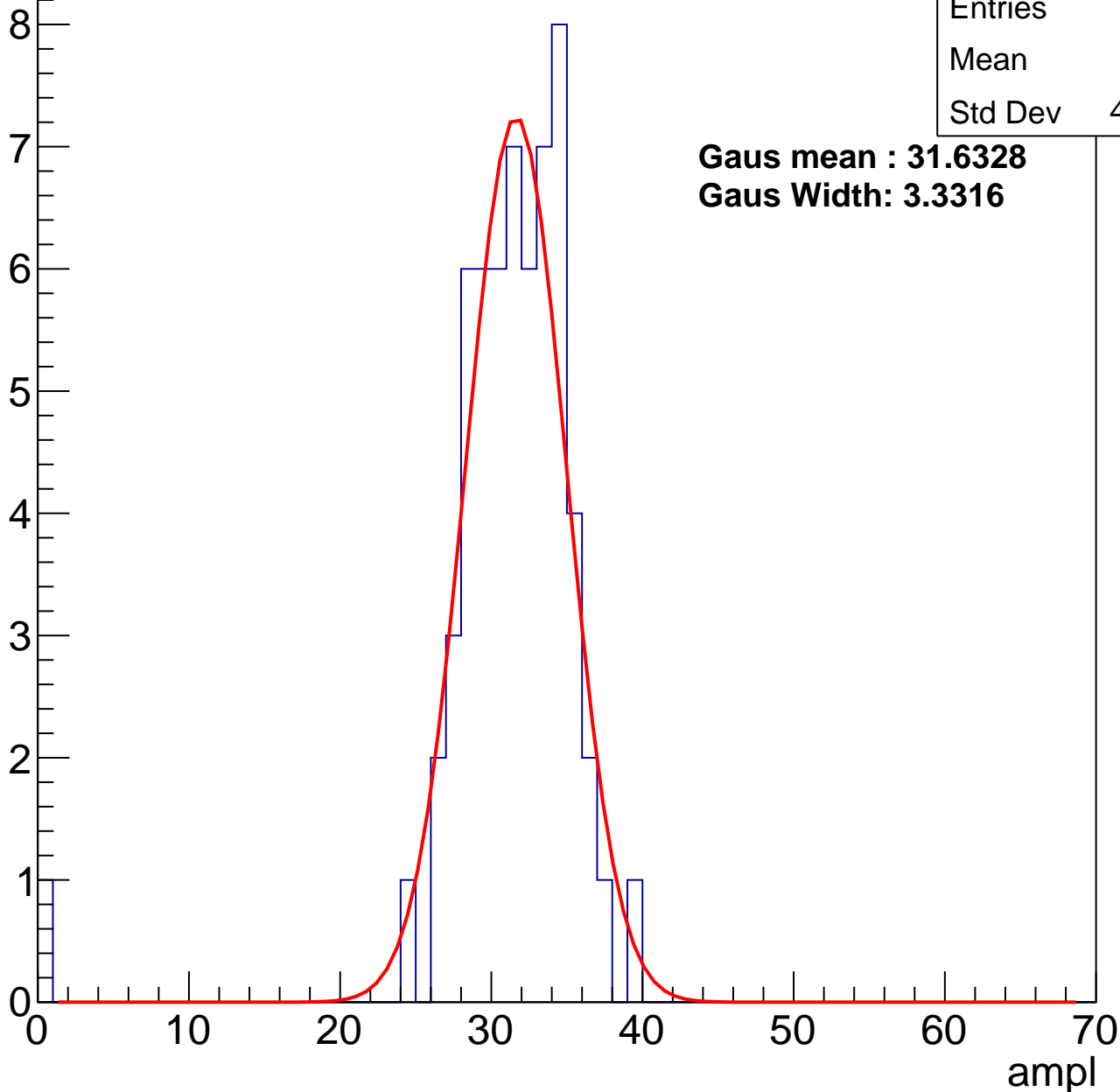
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	30.8
Std Dev	4.975

Gaus mean : 31.6328

Gaus Width: 3.3316



B1L103S, U17-ch3, adc1

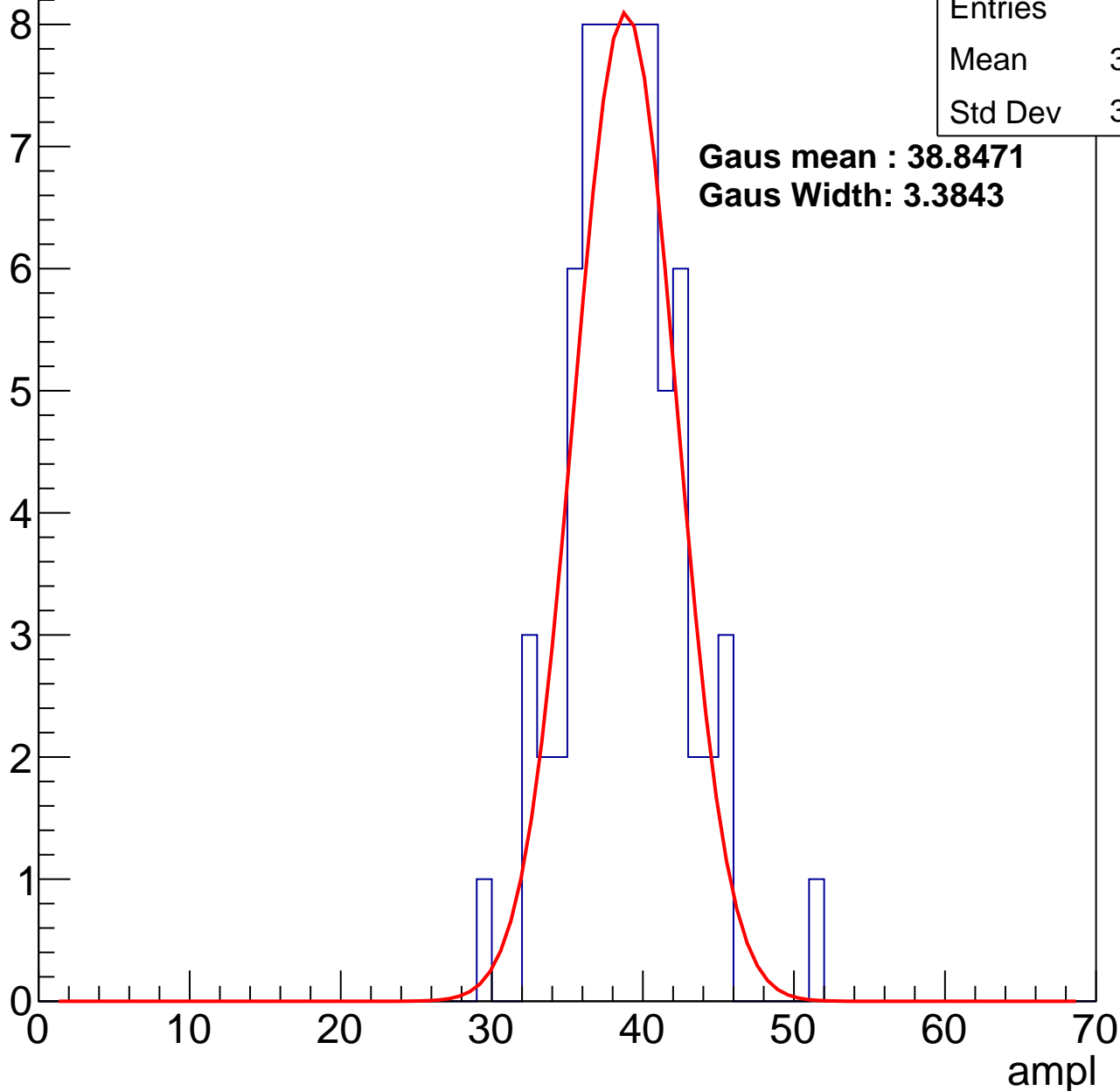
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	38.44
Std Dev	3.645

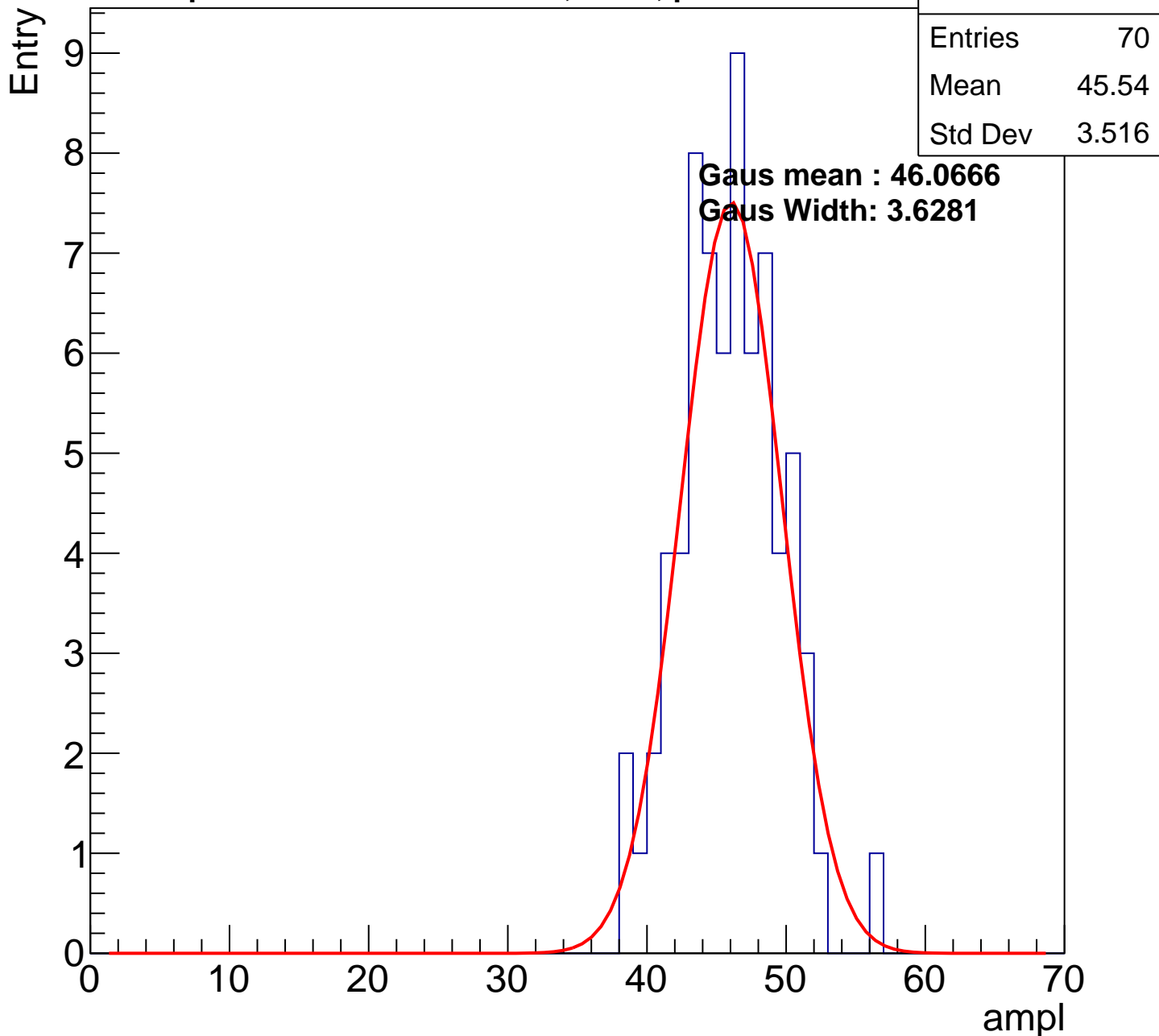
Gaus mean : 38.8471

Gaus Width: 3.3843



B1L103S, U17-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

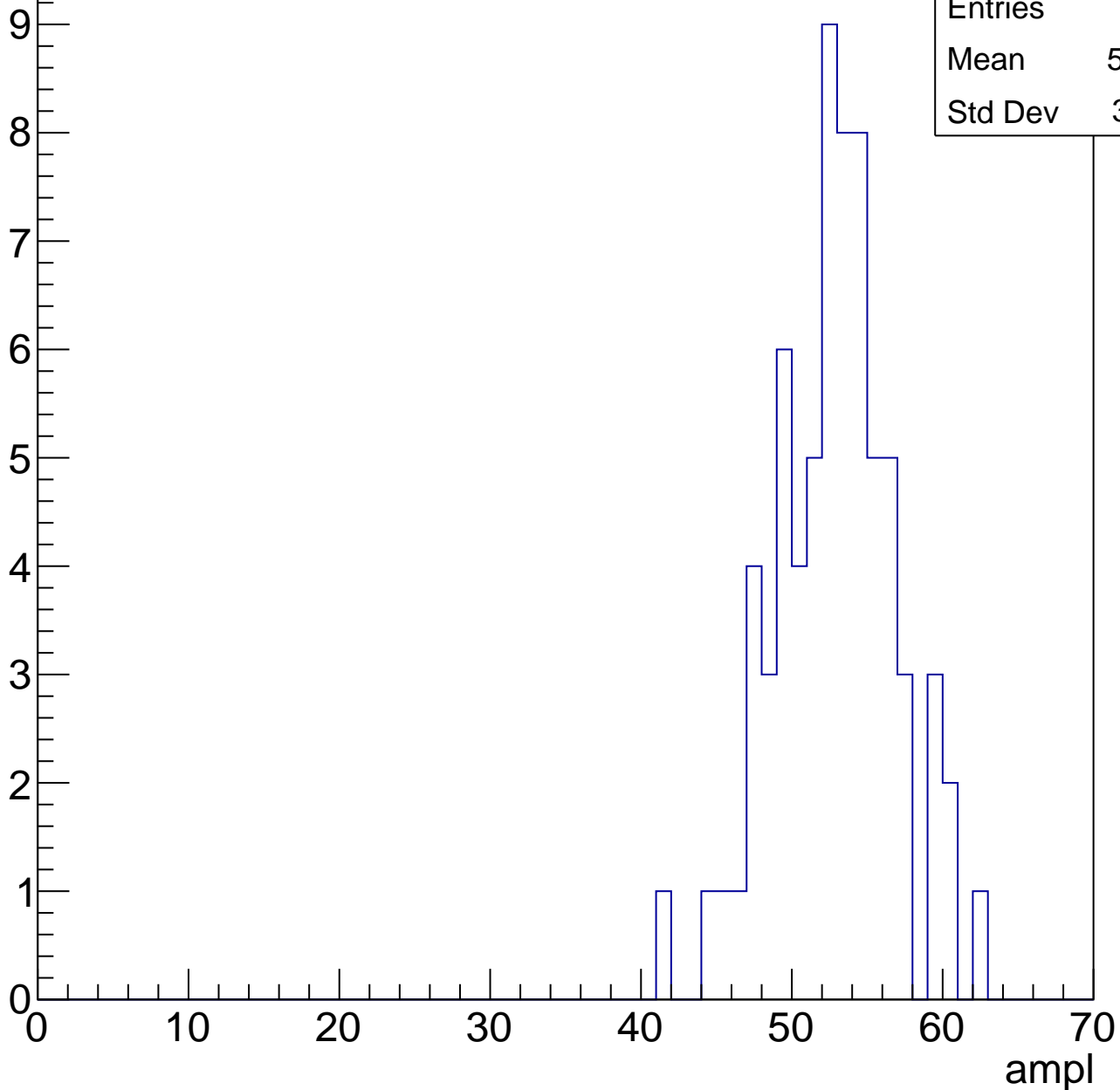


B1L103S, U17-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

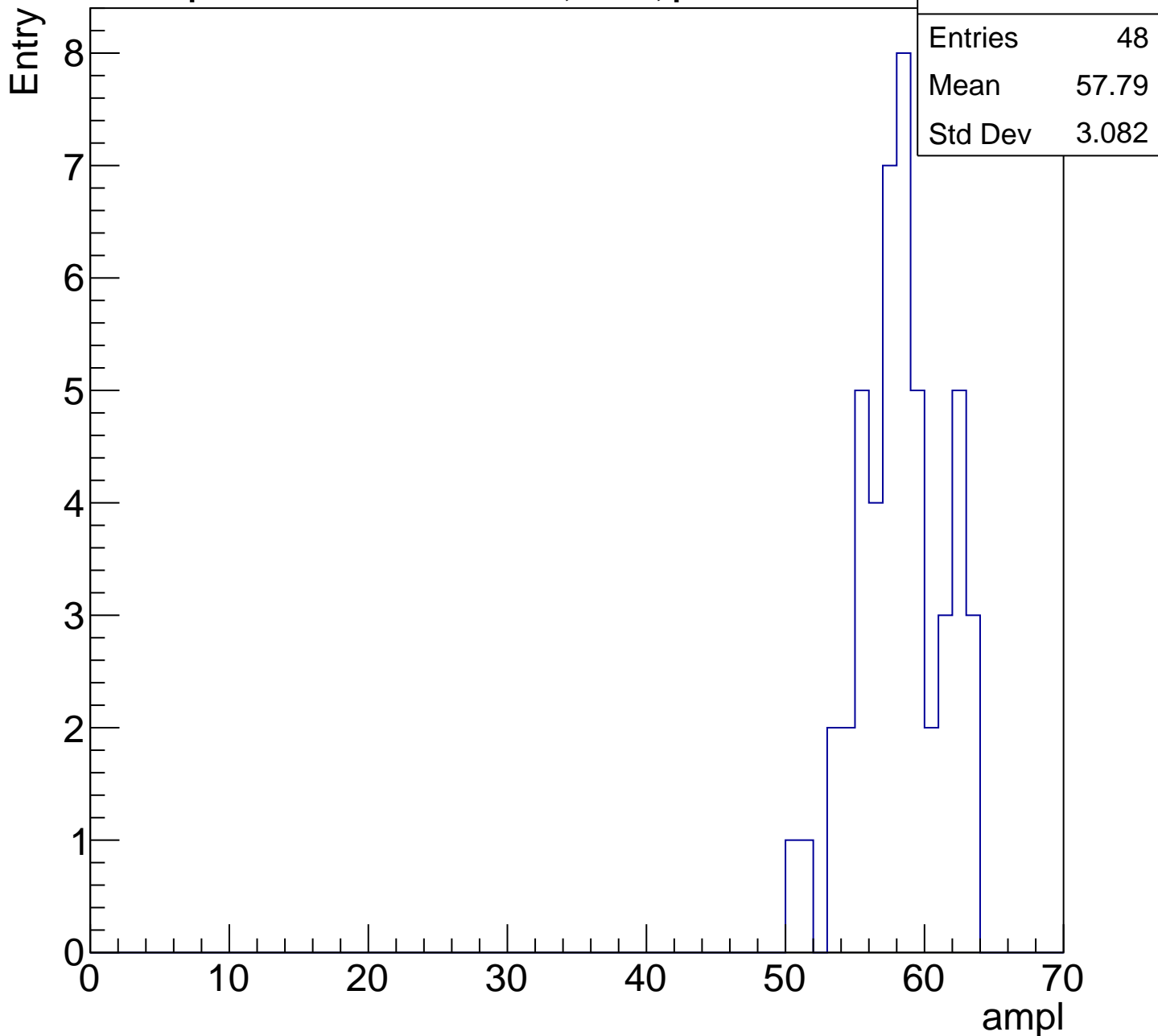
Entry

Entries	70
Mean	52.37
Std Dev	3.961



B1L103S, U17-ch3, adc4

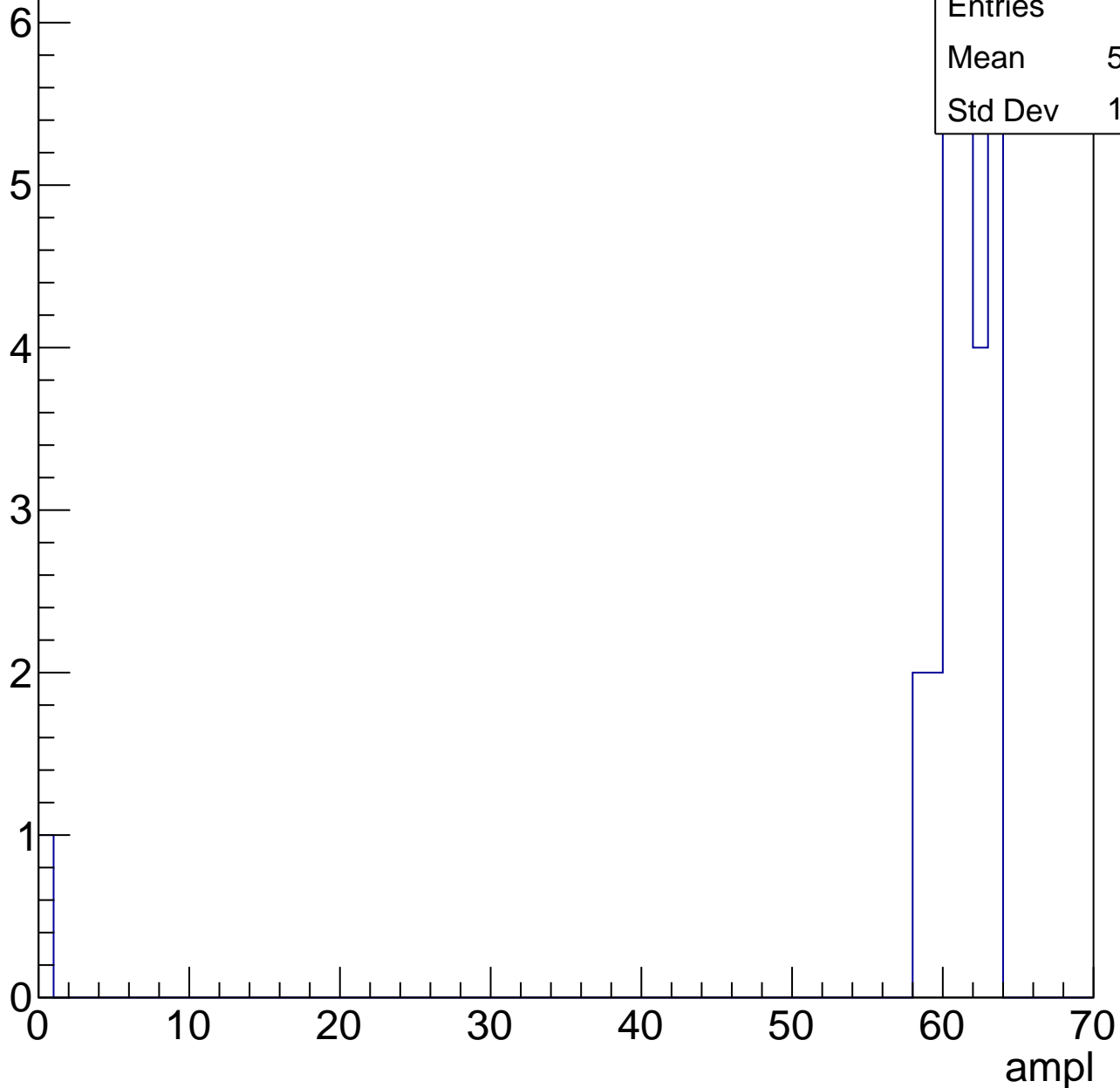
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

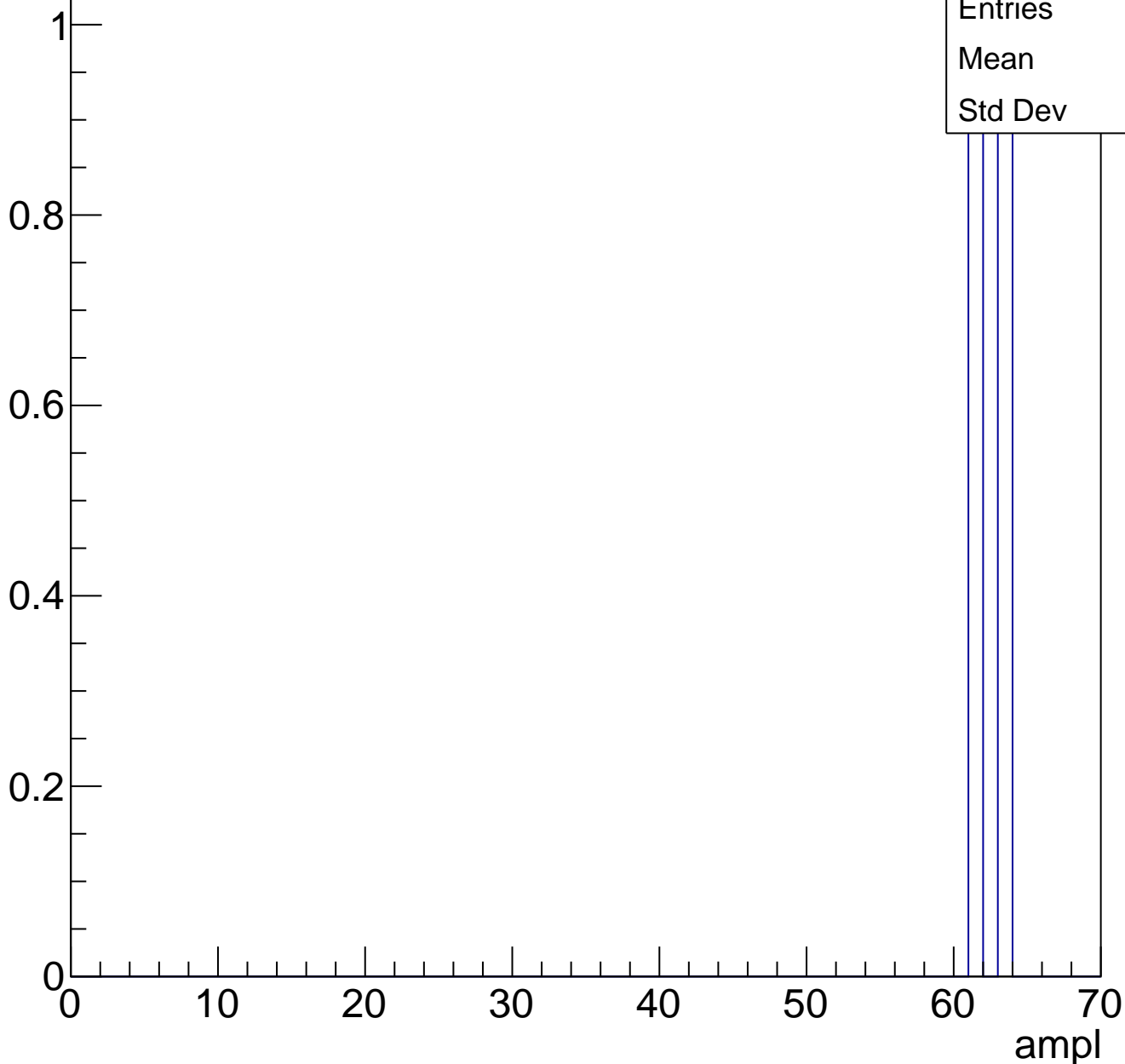
Entry



B1L103S, U17-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch4, adc0

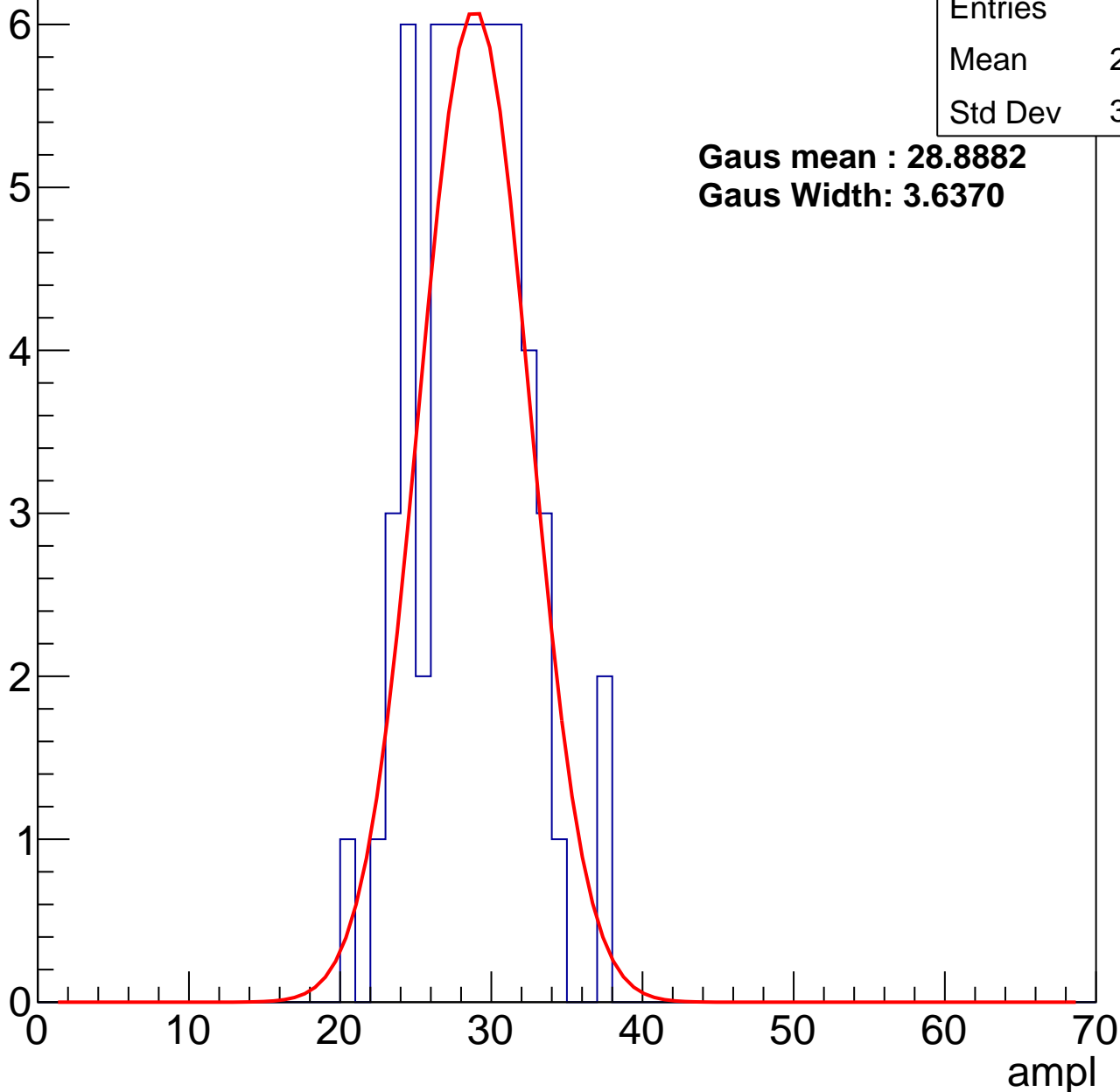
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	28.24
Std Dev	3.534

Gaus mean : 28.8882

Gaus Width: 3.6370



B1L103S, U17-ch4, adc1

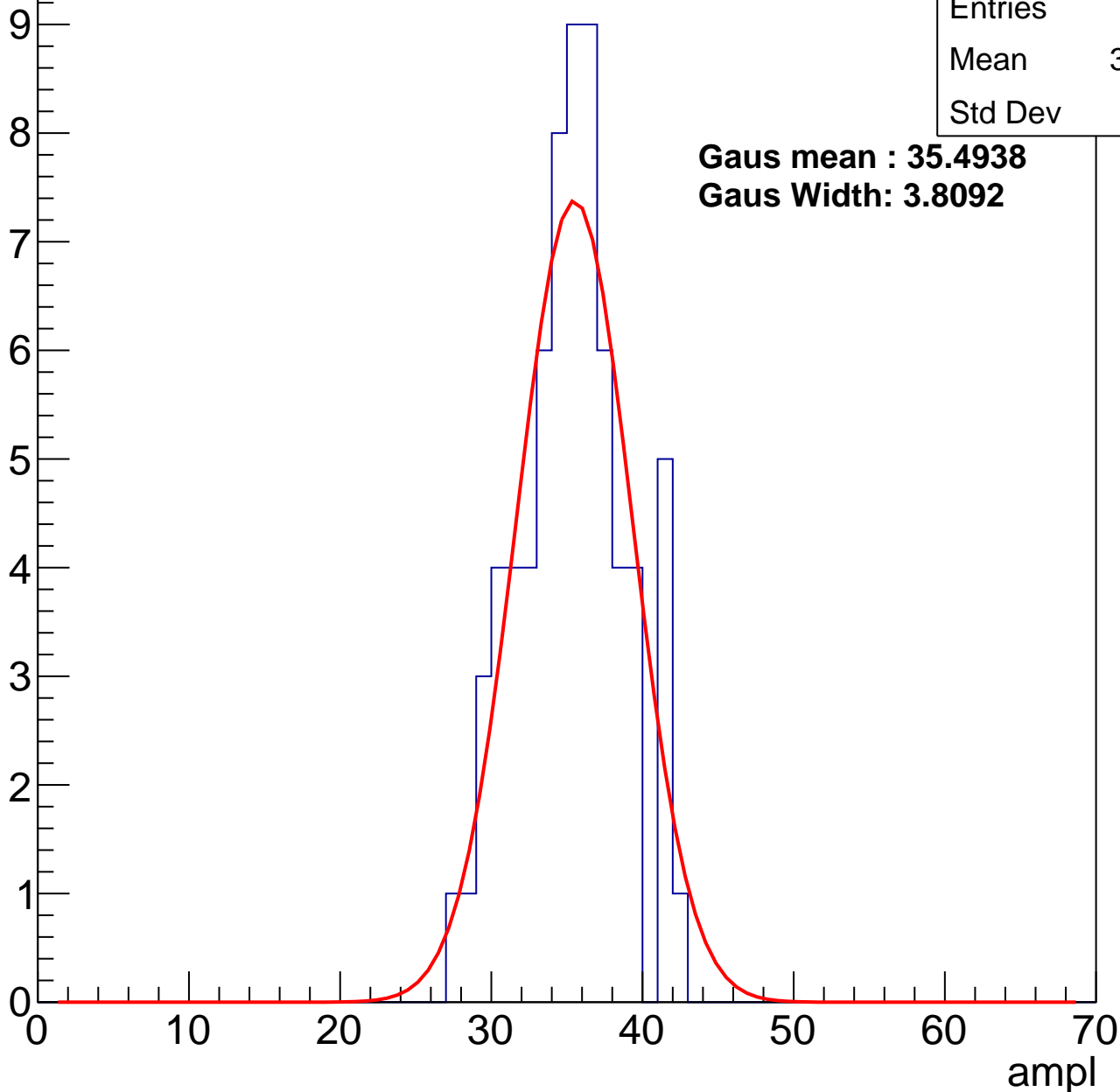
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.78
Std Dev	3.43

Gaus mean : 35.4938

Gaus Width: 3.8092



B1L103S, U17-ch4, adc2

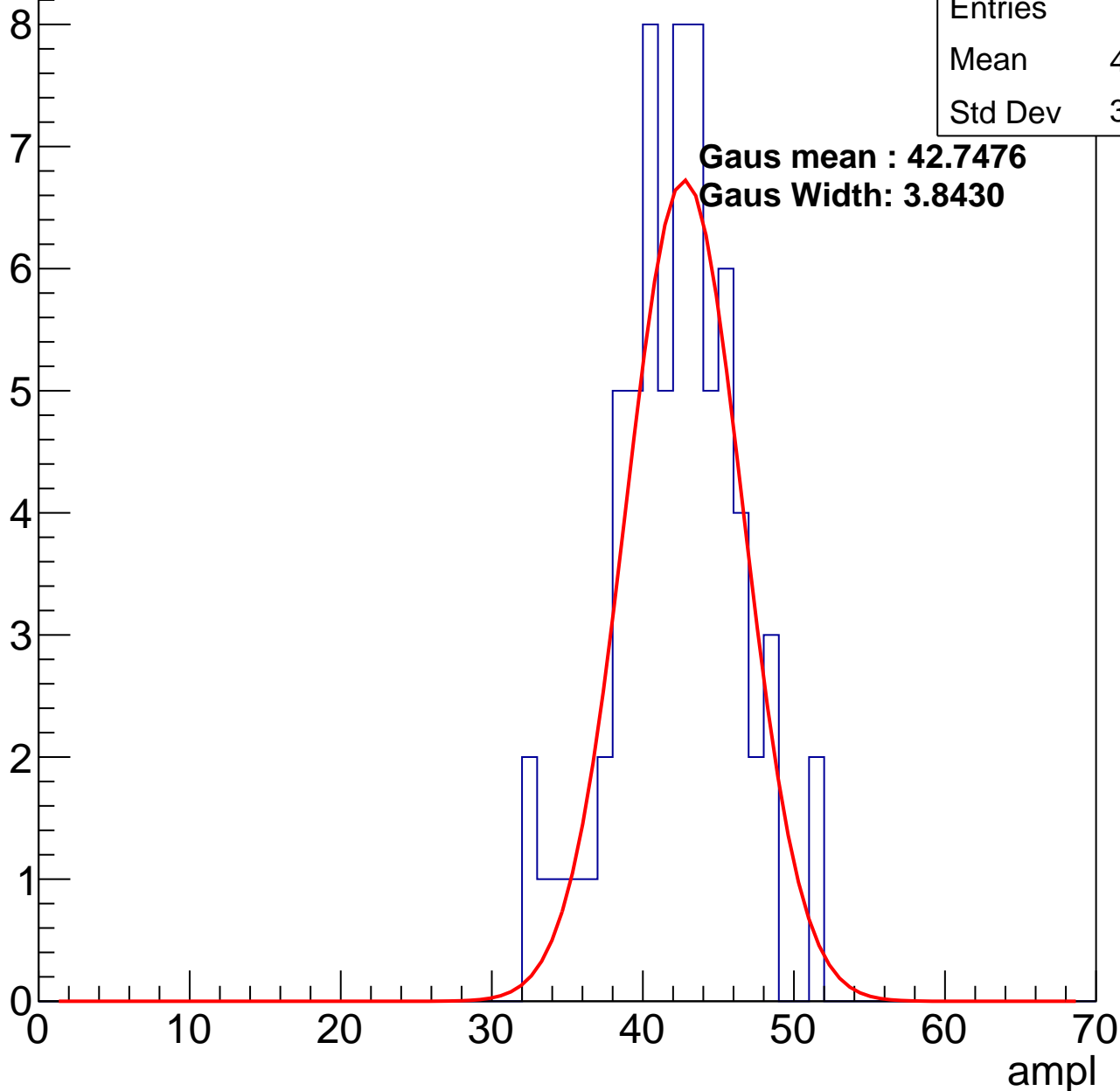
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.74
Std Dev	3.999

Gaus mean : 42.7476

Gaus Width: 3.8430

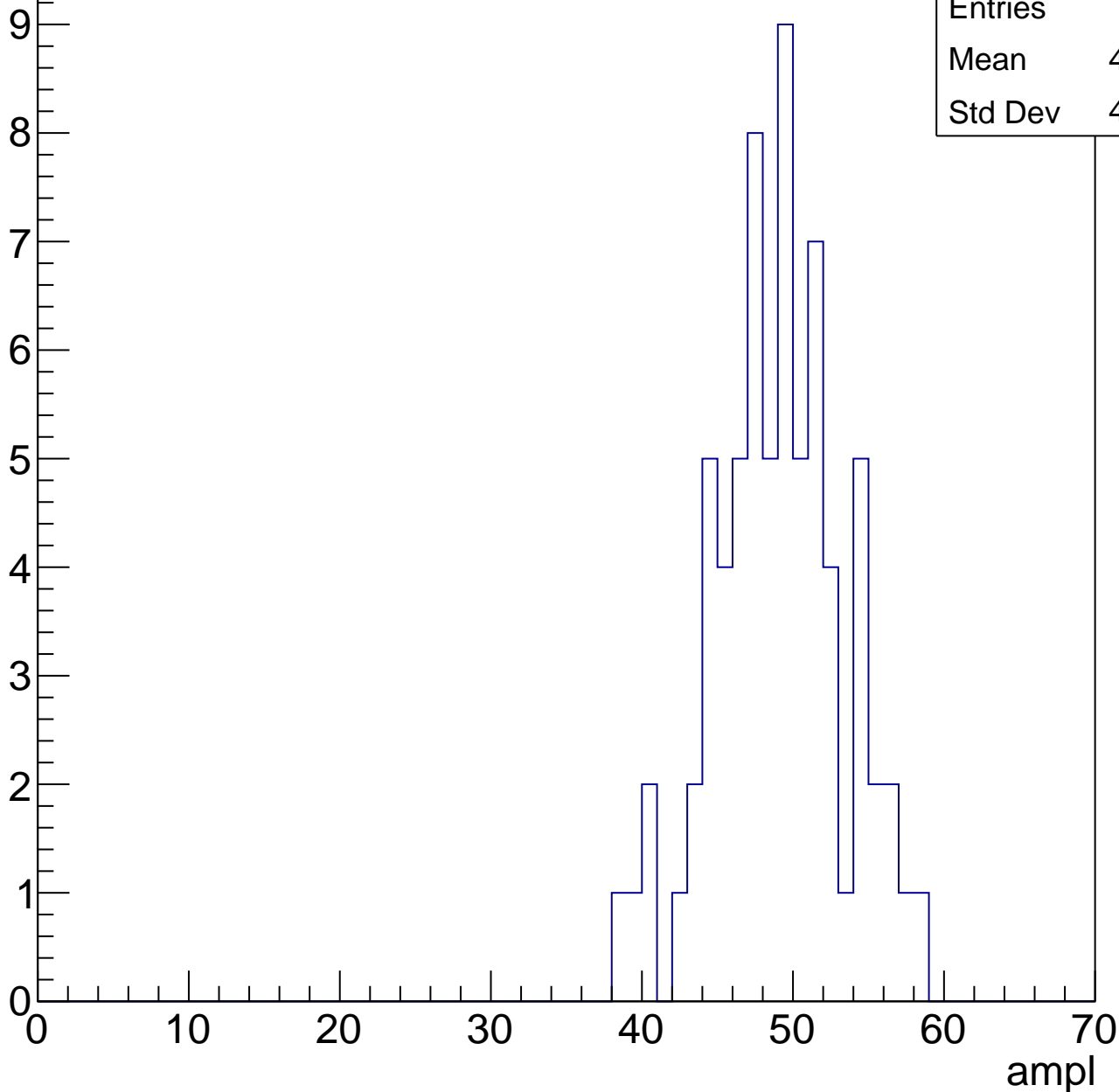


B1L103S, U17-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.55
Std Dev	4.255

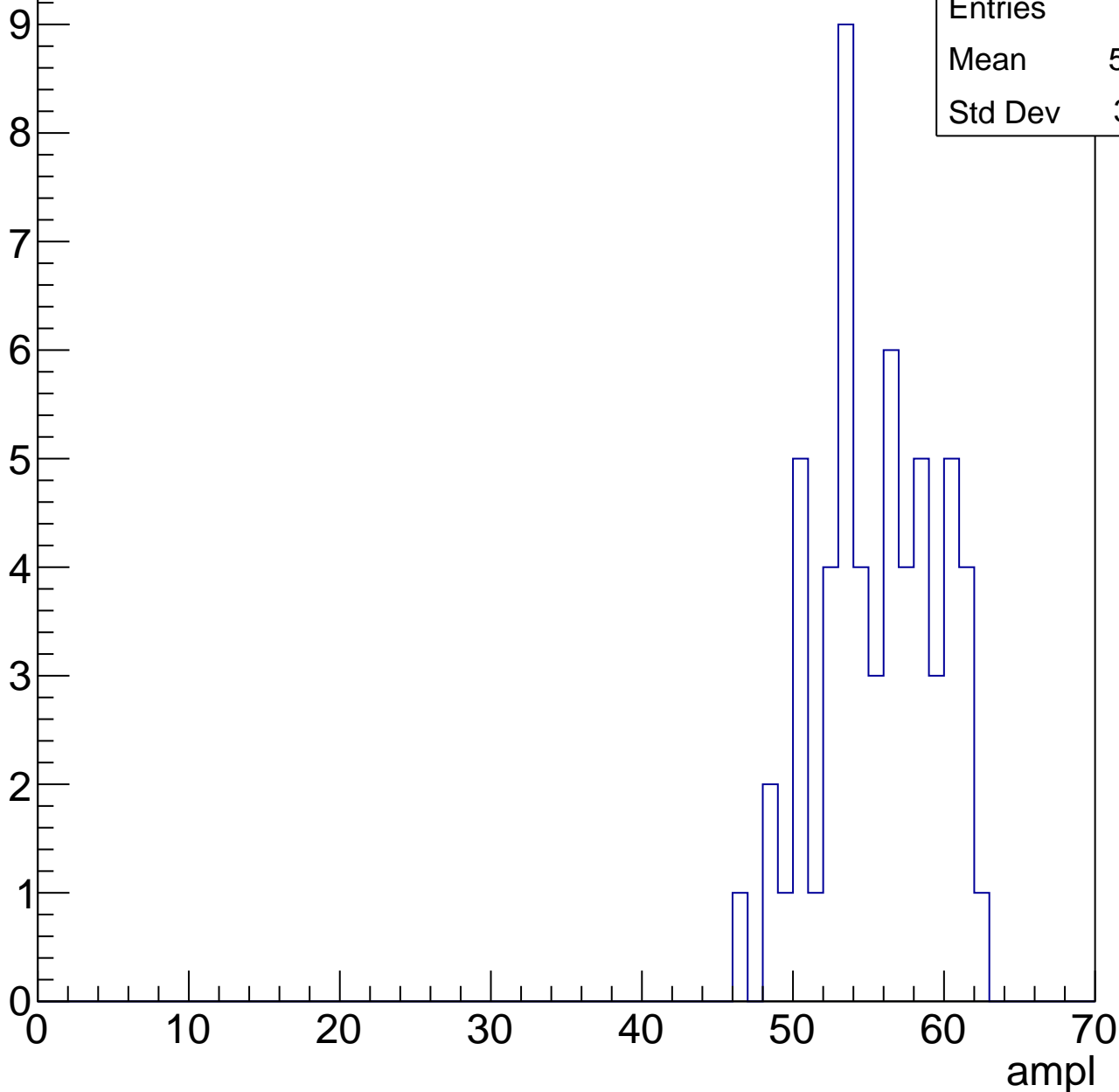


B1L103S, U17-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

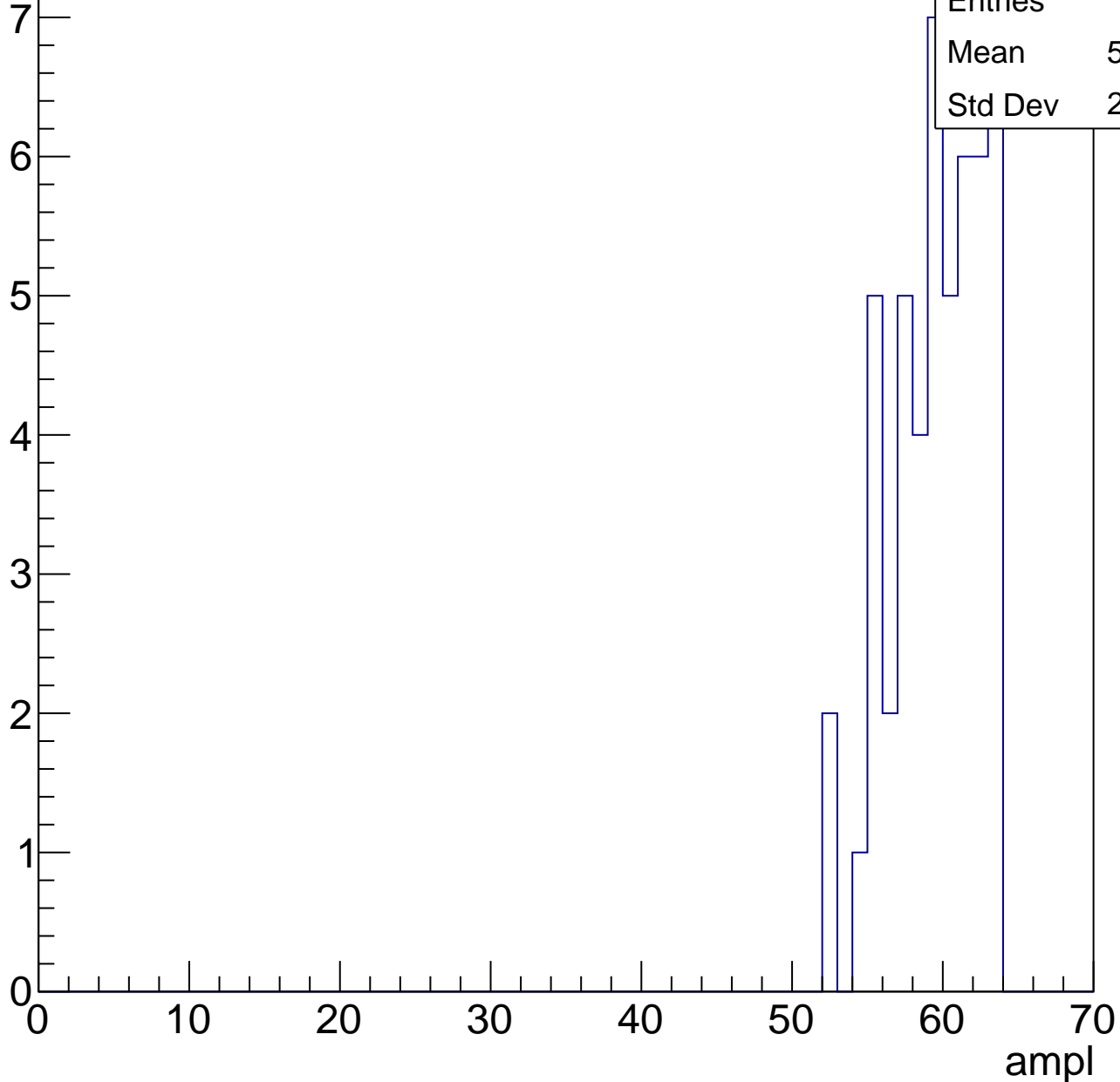
Entries	58
Mean	55.09
Std Dev	3.861



B1L103S, U17-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

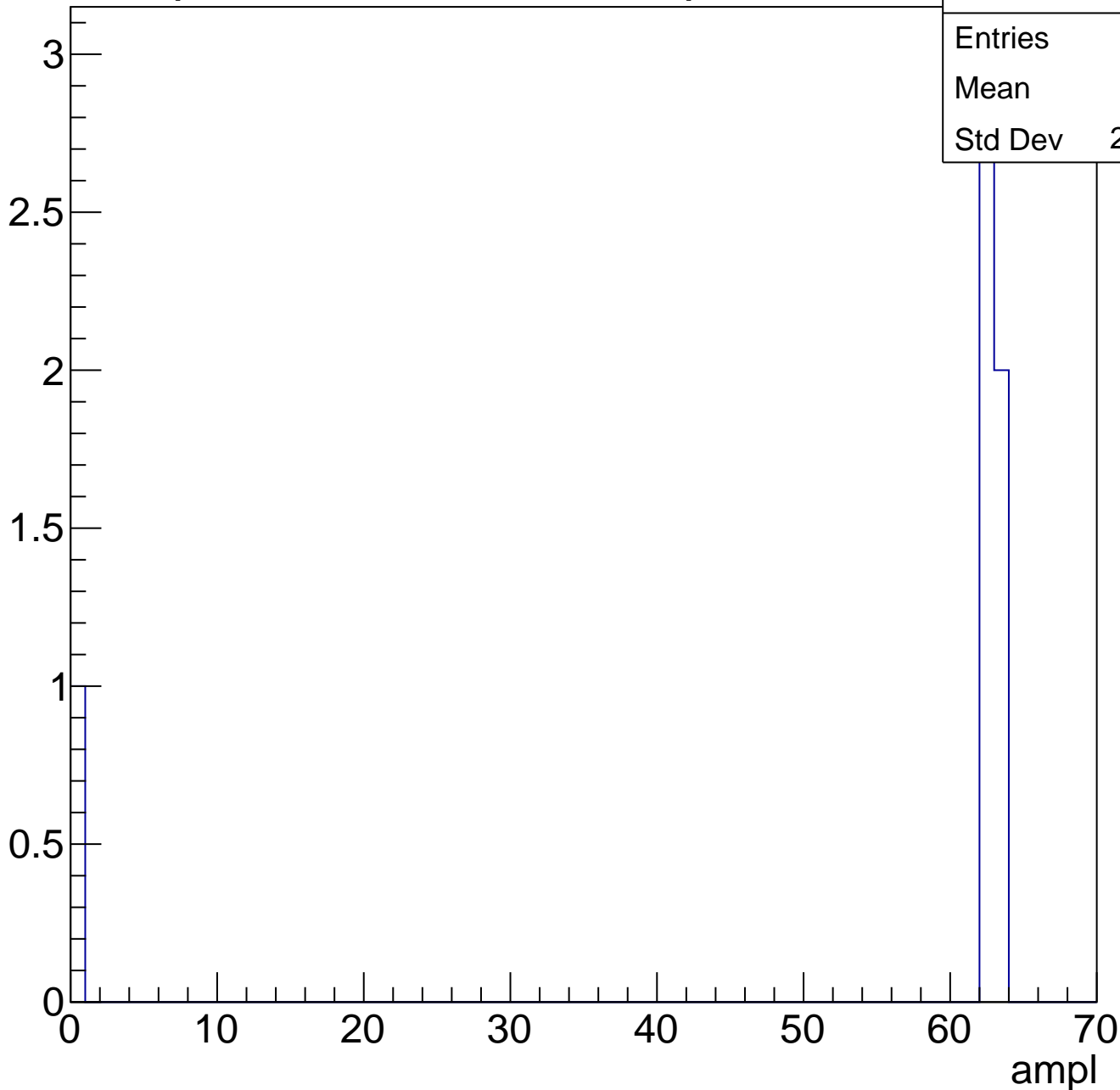
Entry



B1L103S, U17-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

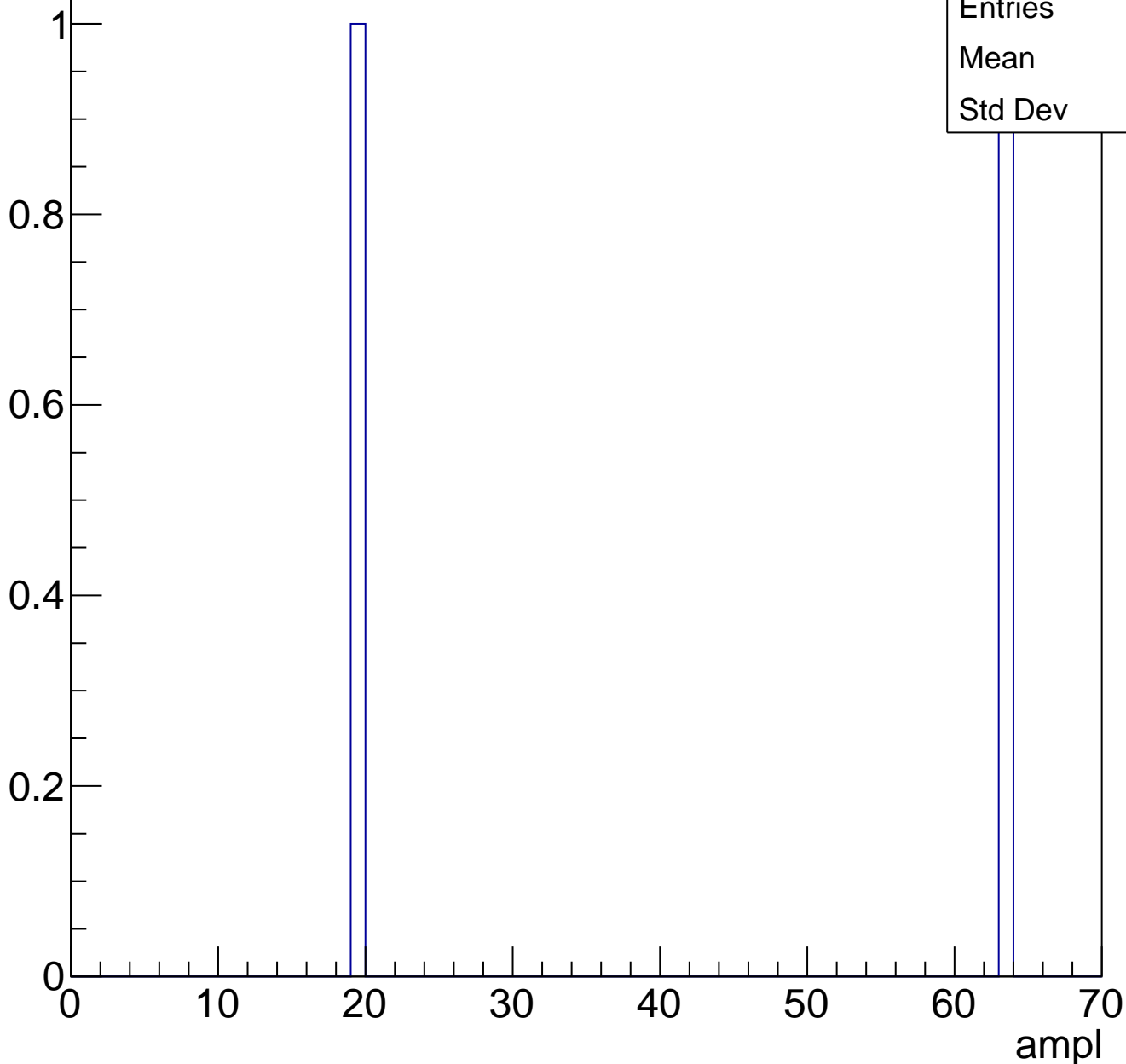
Entry



B1L103S, U17-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch5, adc0

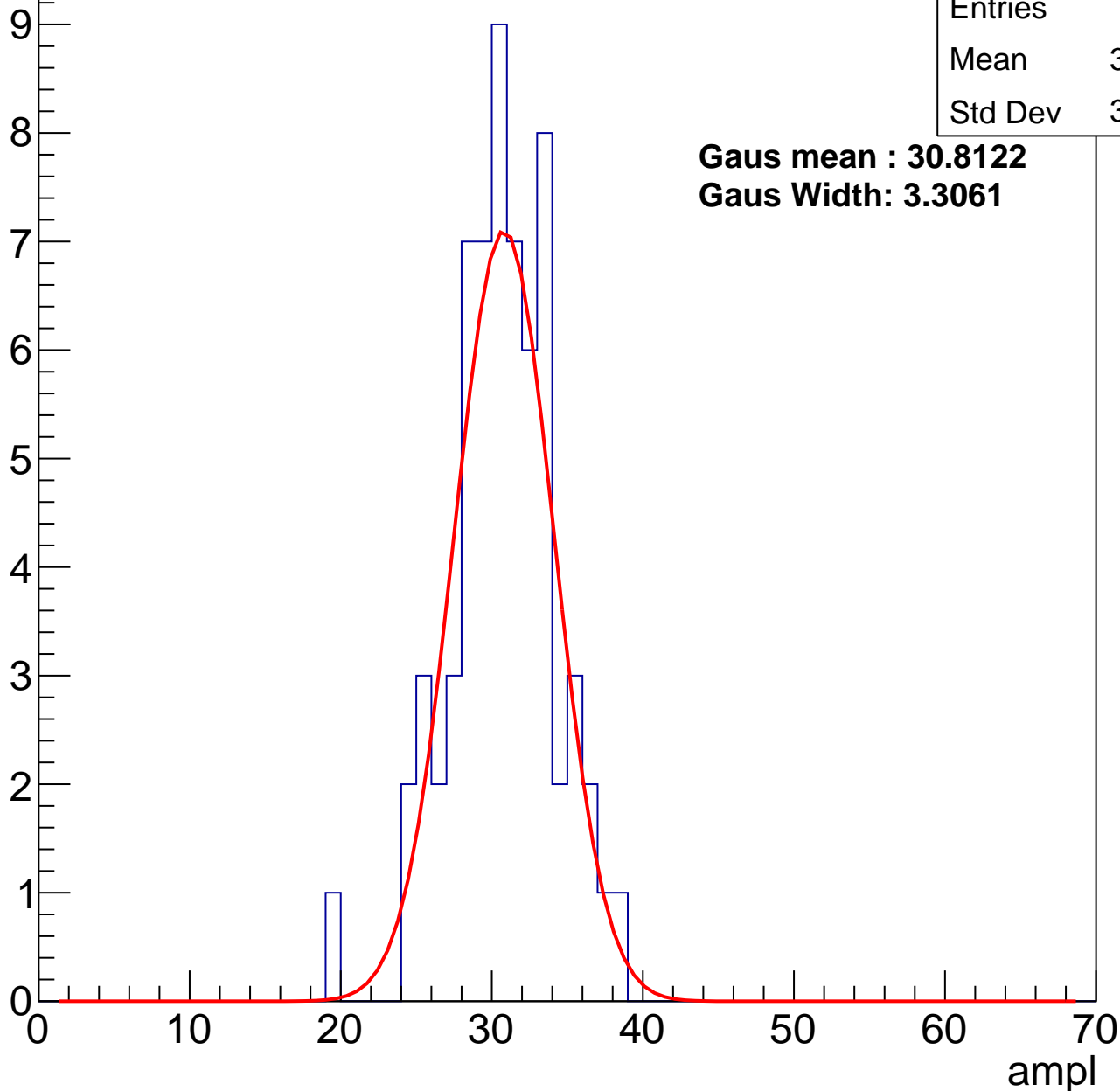
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	30.27
Std Dev	3.429

Gaus mean : 30.8122

Gaus Width: 3.3061



B1L103S, U17-ch5, adc1

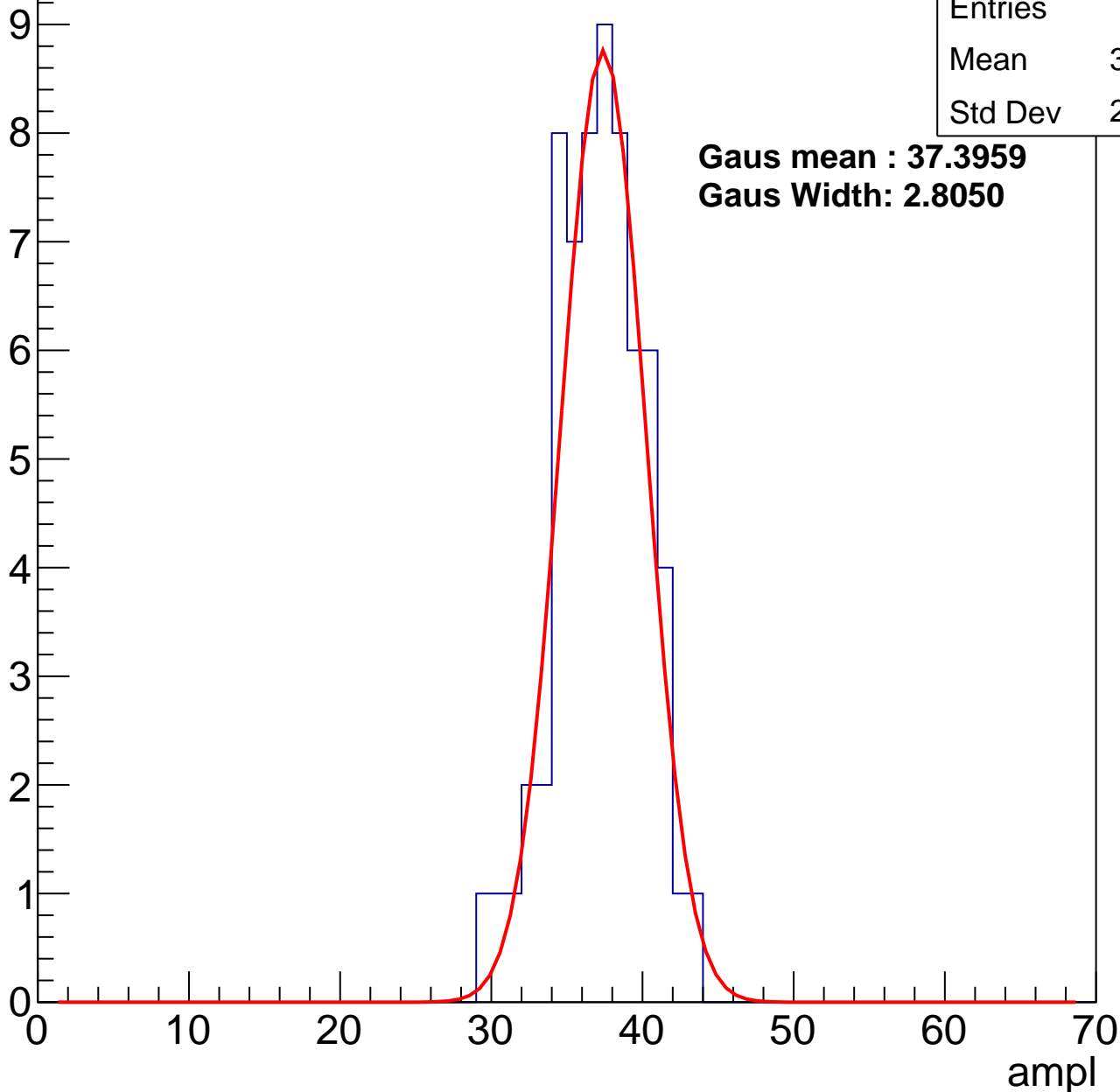
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.69
Std Dev	2.893

Gaus mean : 37.3959

Gaus Width: 2.8050



B1L103S, U17-ch5, adc2

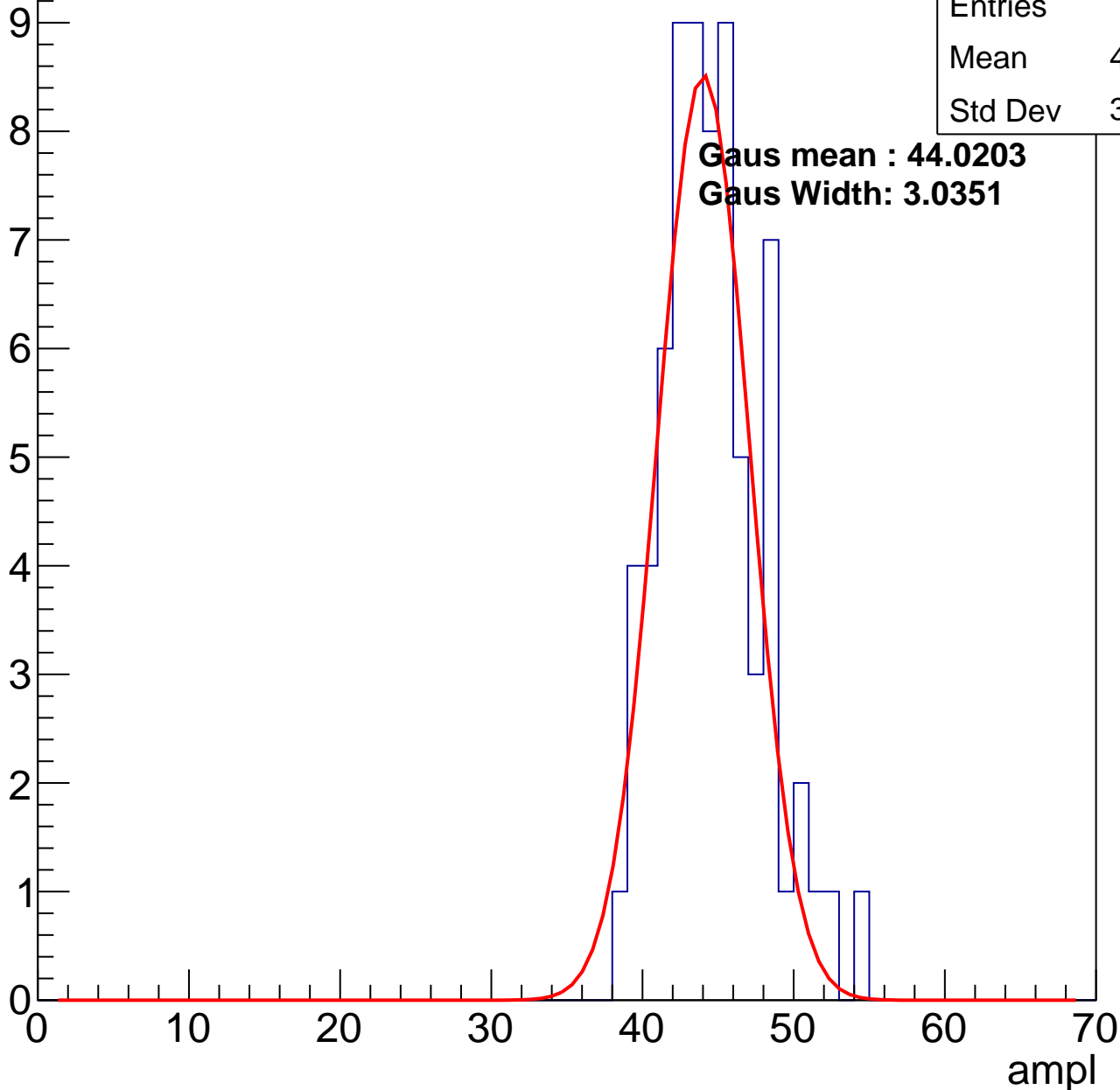
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	44.15
Std Dev	3.304

Gaus mean : 44.0203

Gaus Width: 3.0351

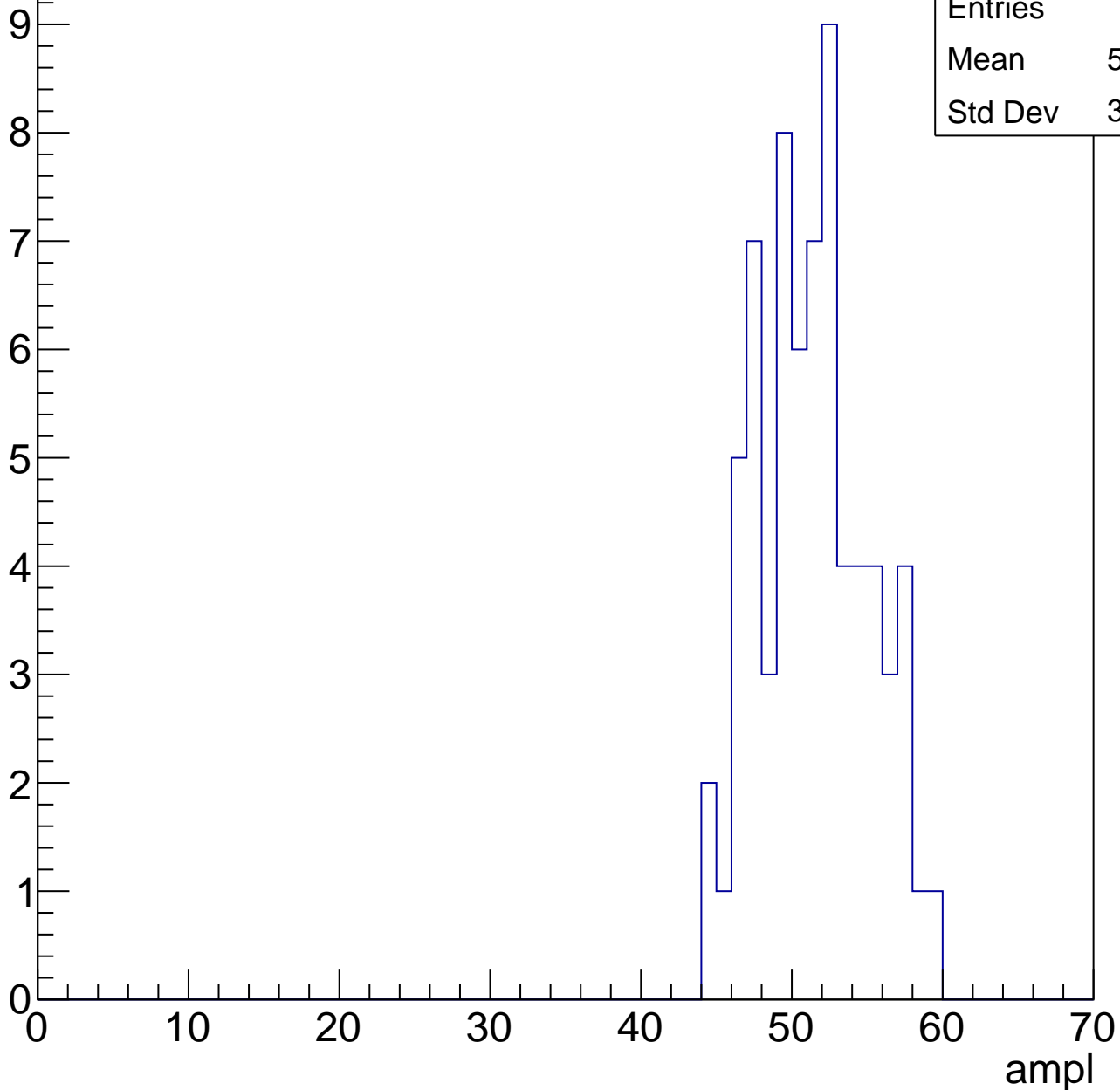


B1L103S, U17-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	50.93
Std Dev	3.597

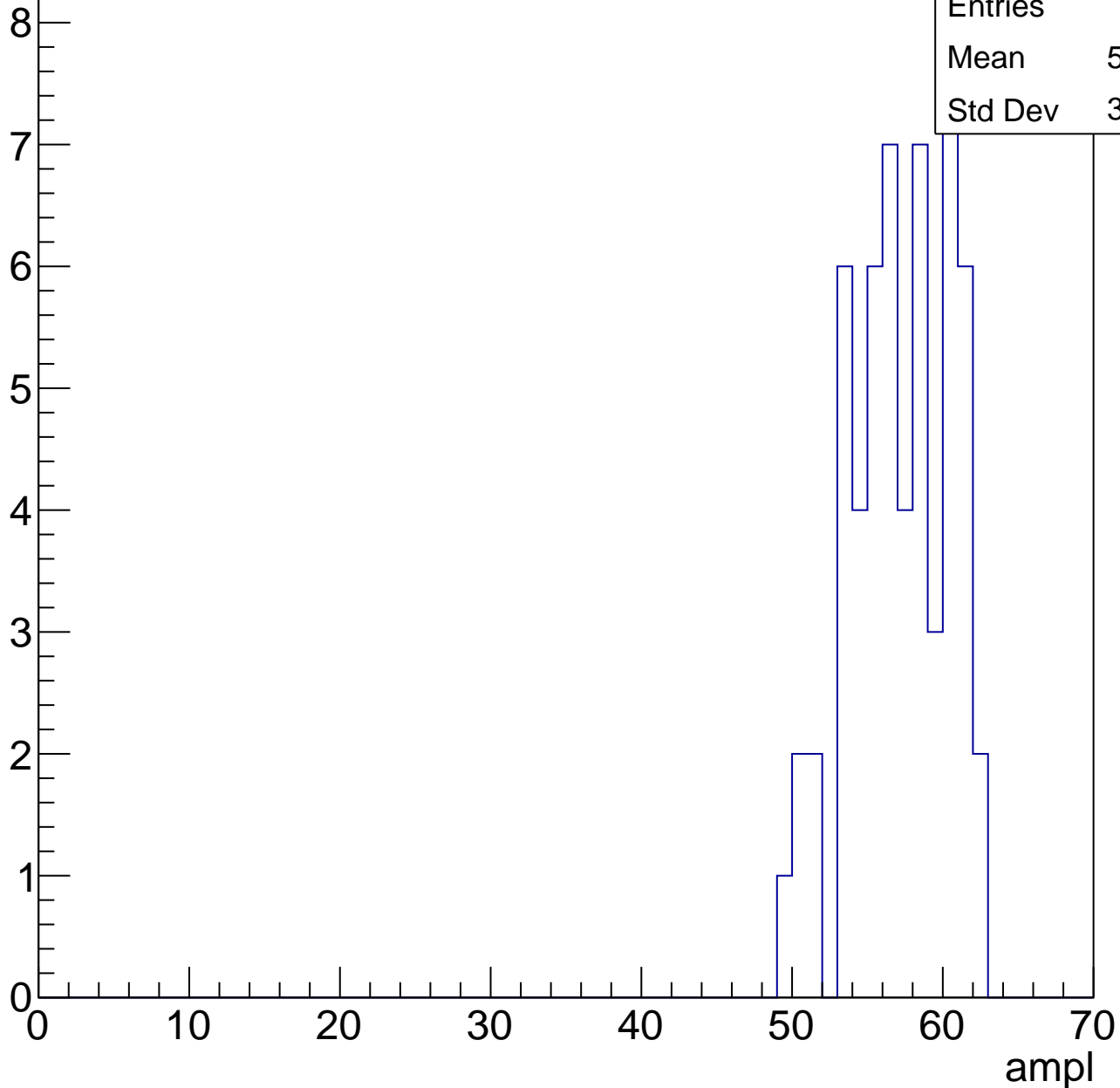


B1L103S, U17-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.69
Std Dev	3.292

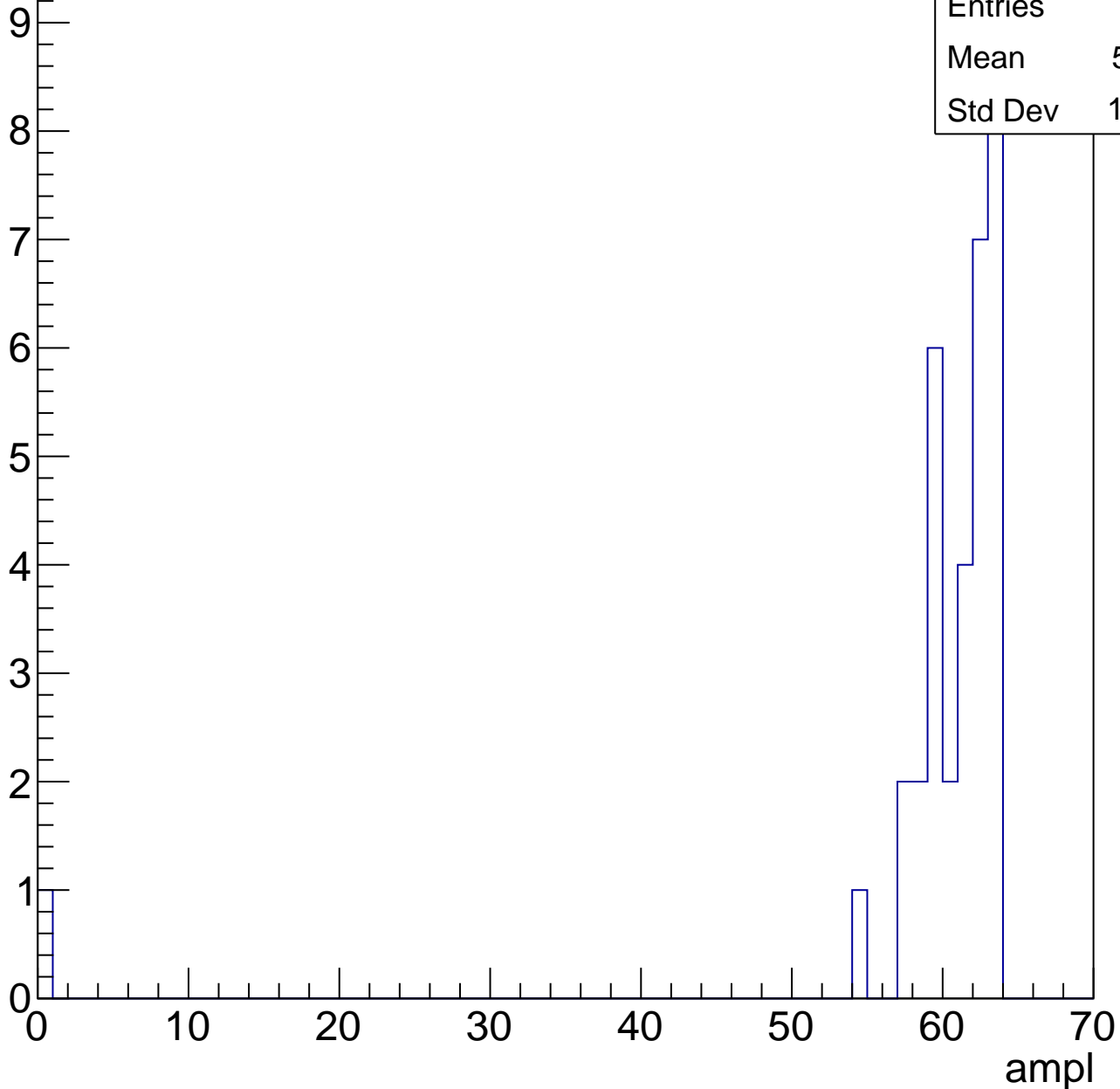


B1L103S, U17-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.91
Std Dev	10.49



B1L103S, U17-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

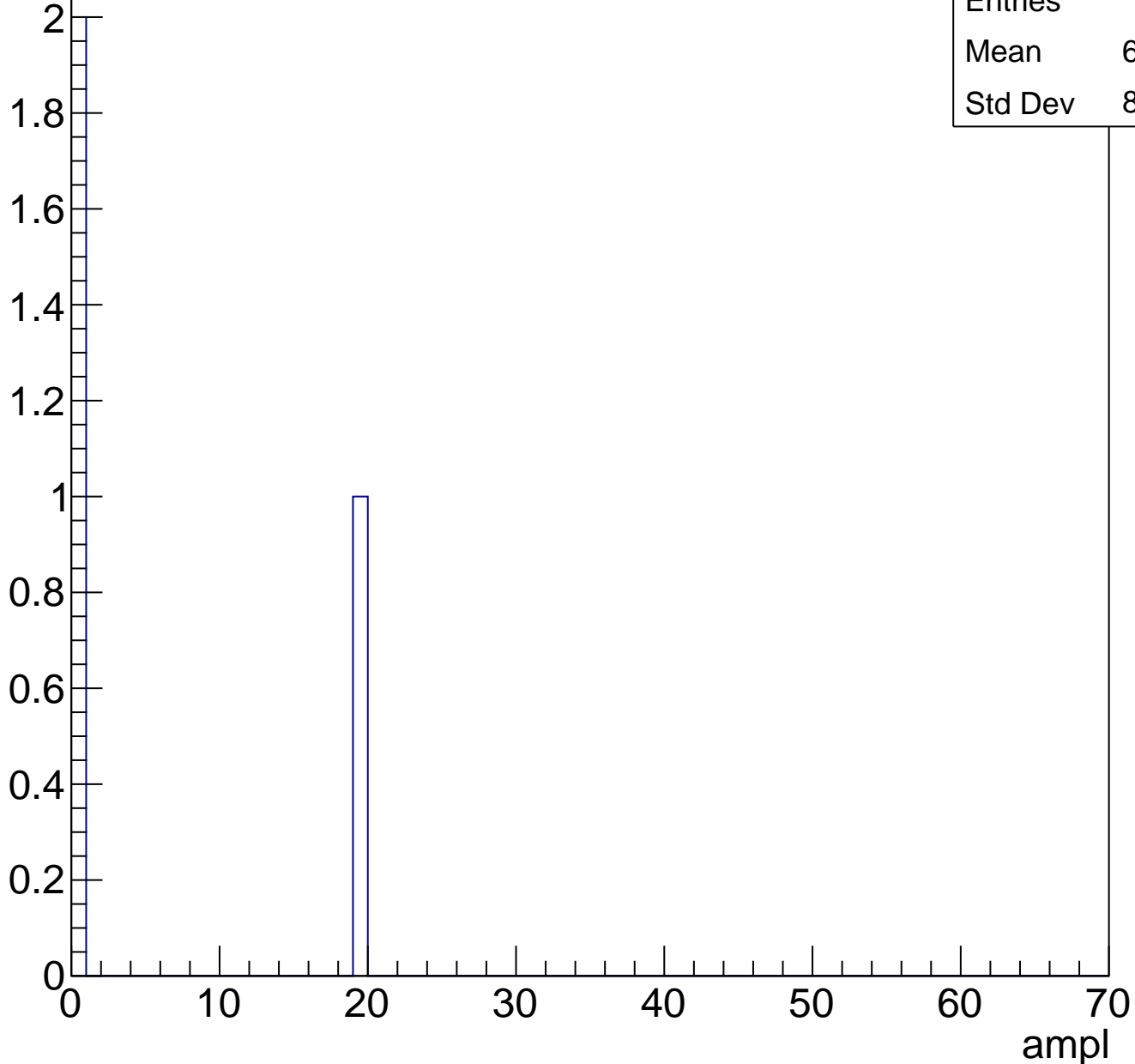
Entry



B1L103S, U17-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L103S, U17-ch6, adc0

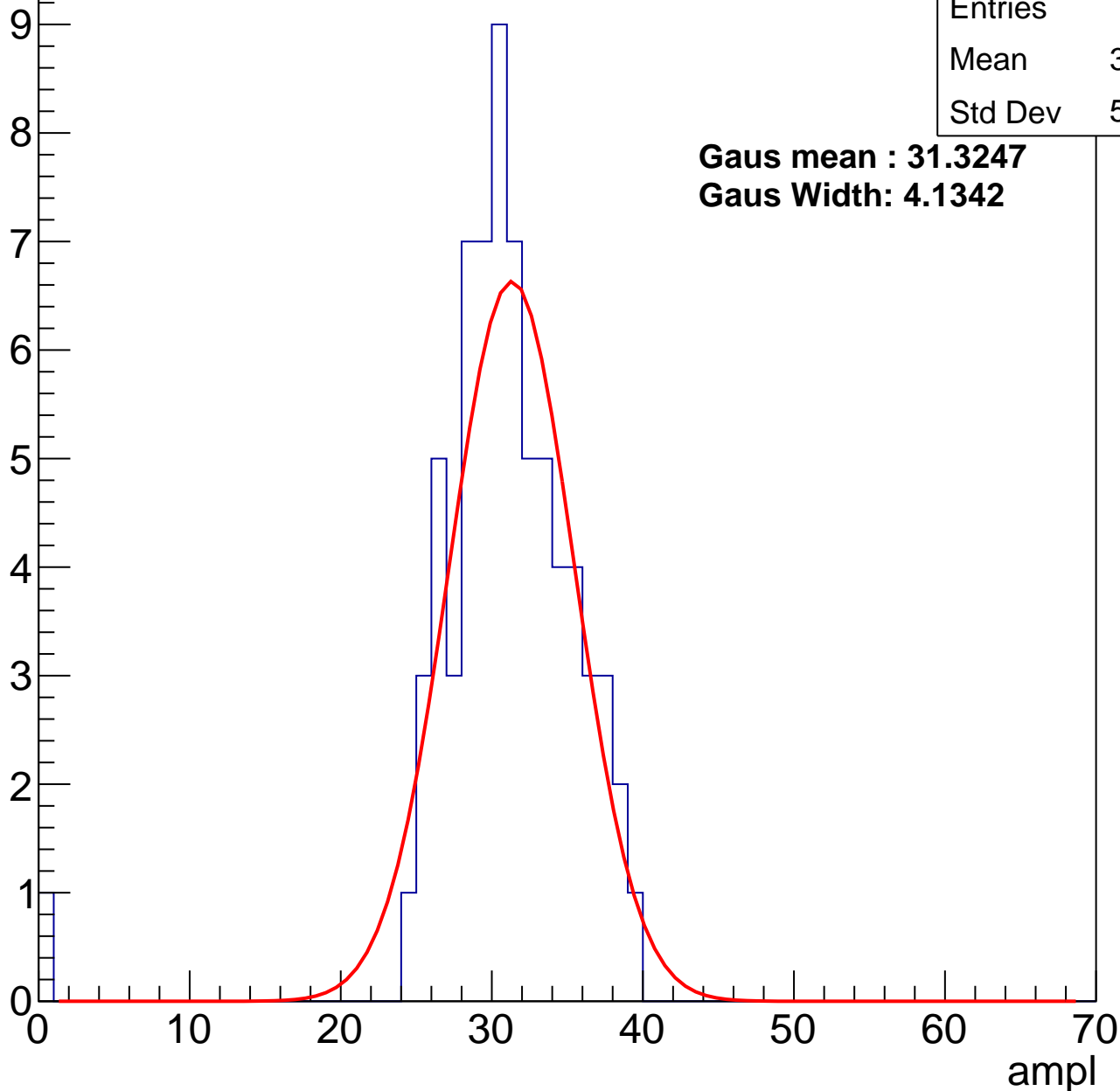
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.44
Std Dev	5.126

Gaus mean : 31.3247

Gaus Width: 4.1342



B1L103S, U17-ch6, adc1

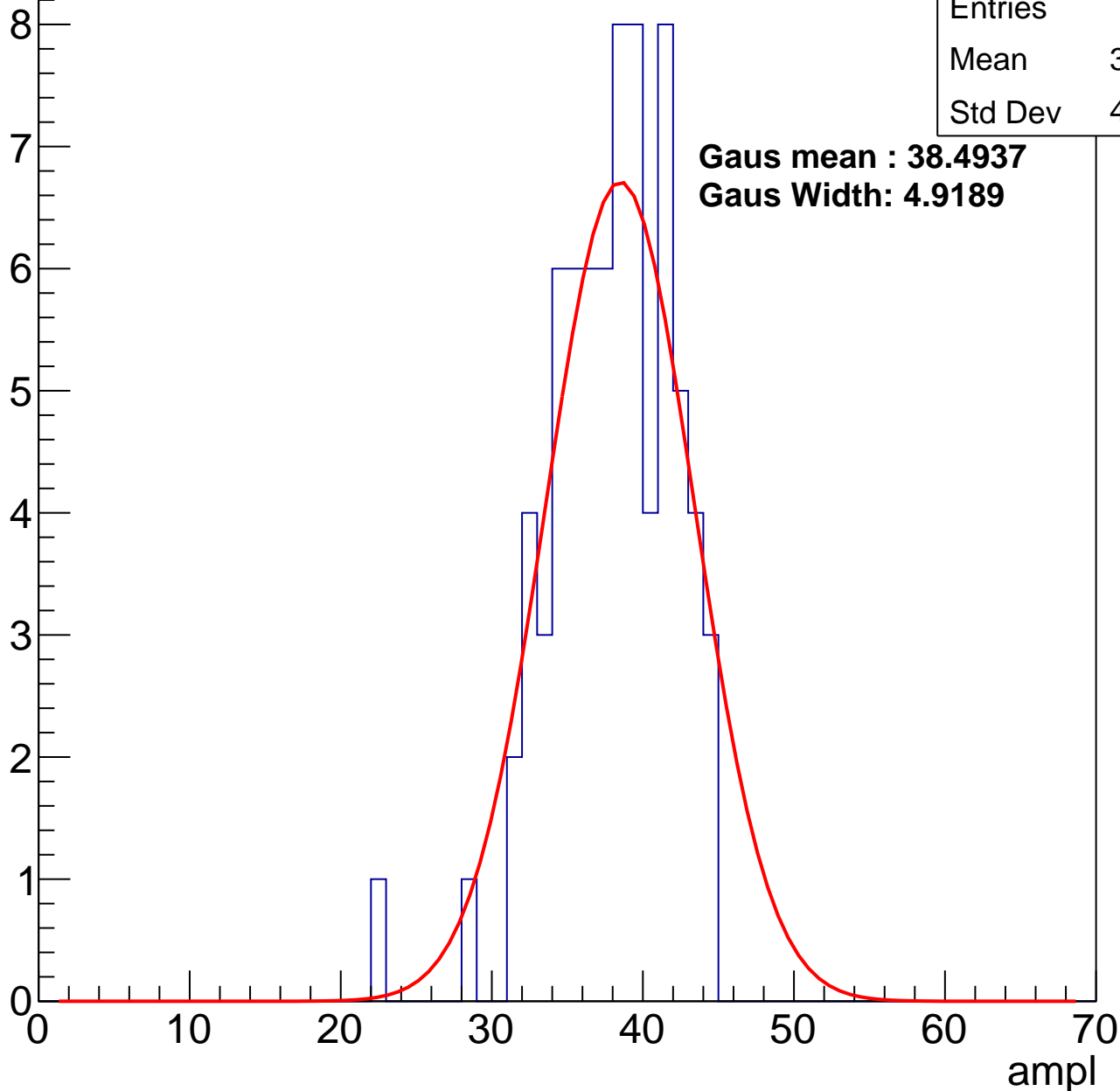
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.45
Std Dev	4.024

Gaus mean : 38.4937

Gaus Width: 4.9189



B1L103S, U17-ch6, adc2

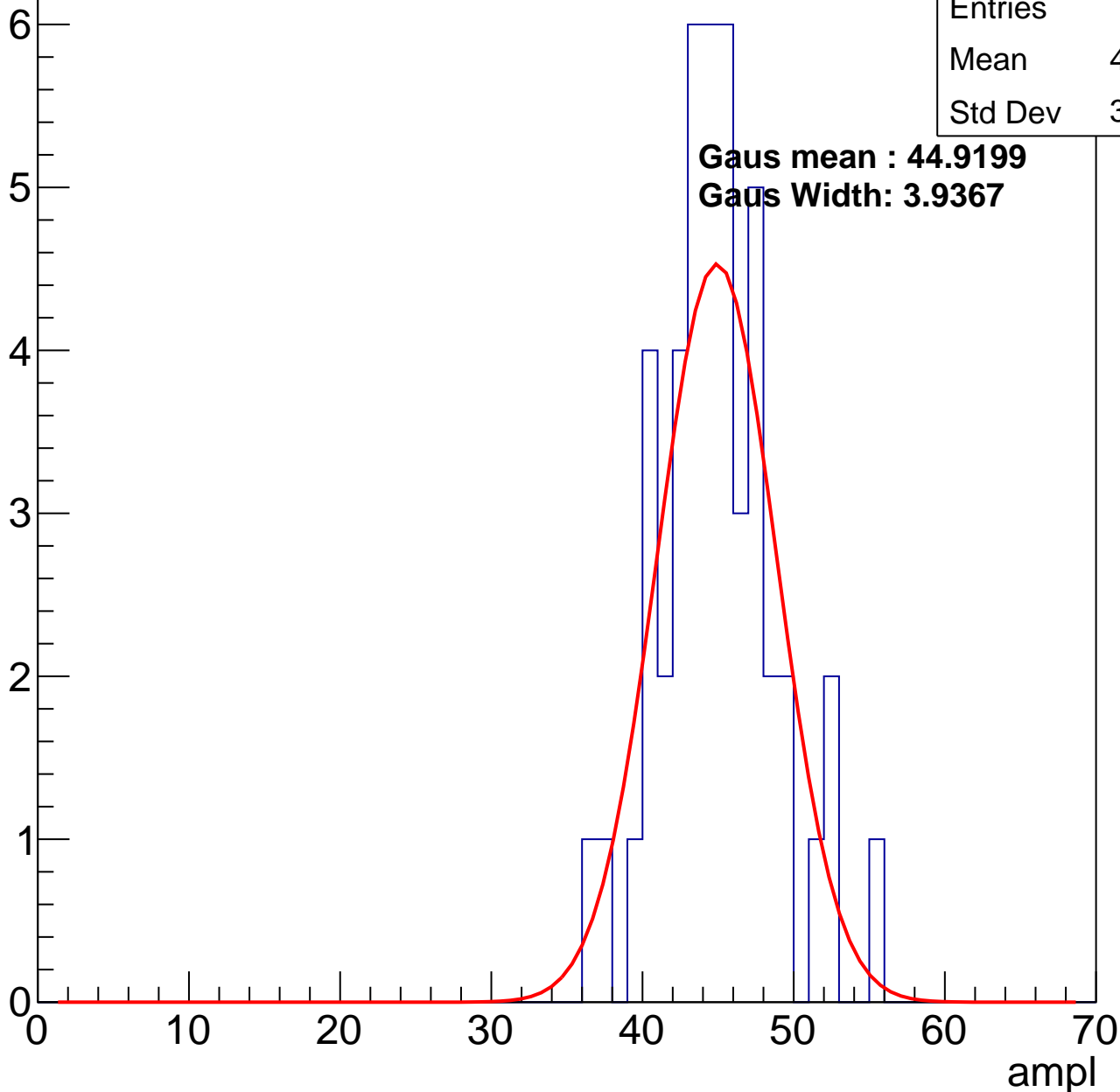
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	44.49
Std Dev	3.786

Gaus mean : 44.9199

Gaus Width: 3.9367

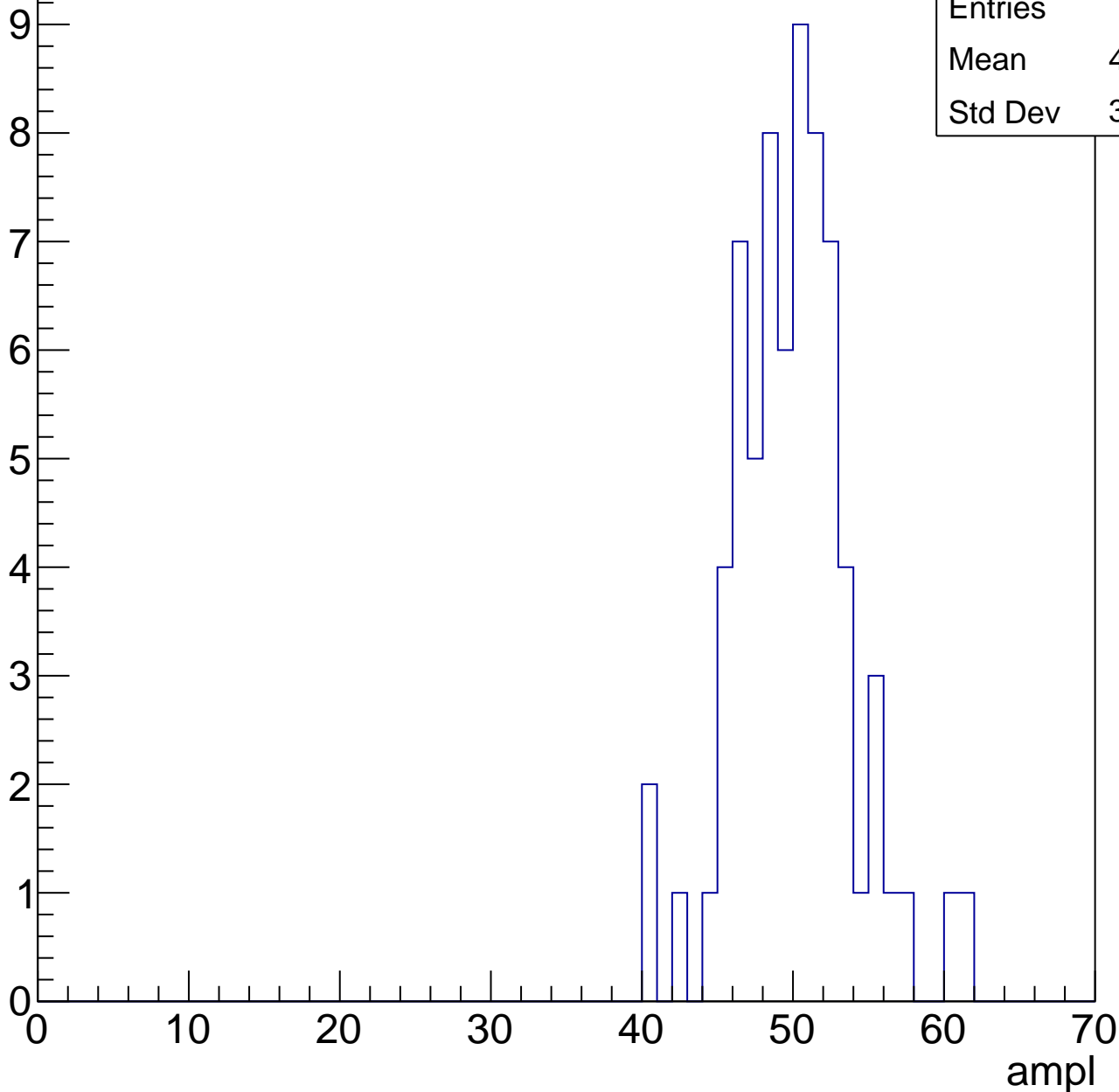


B1L103S, U17-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

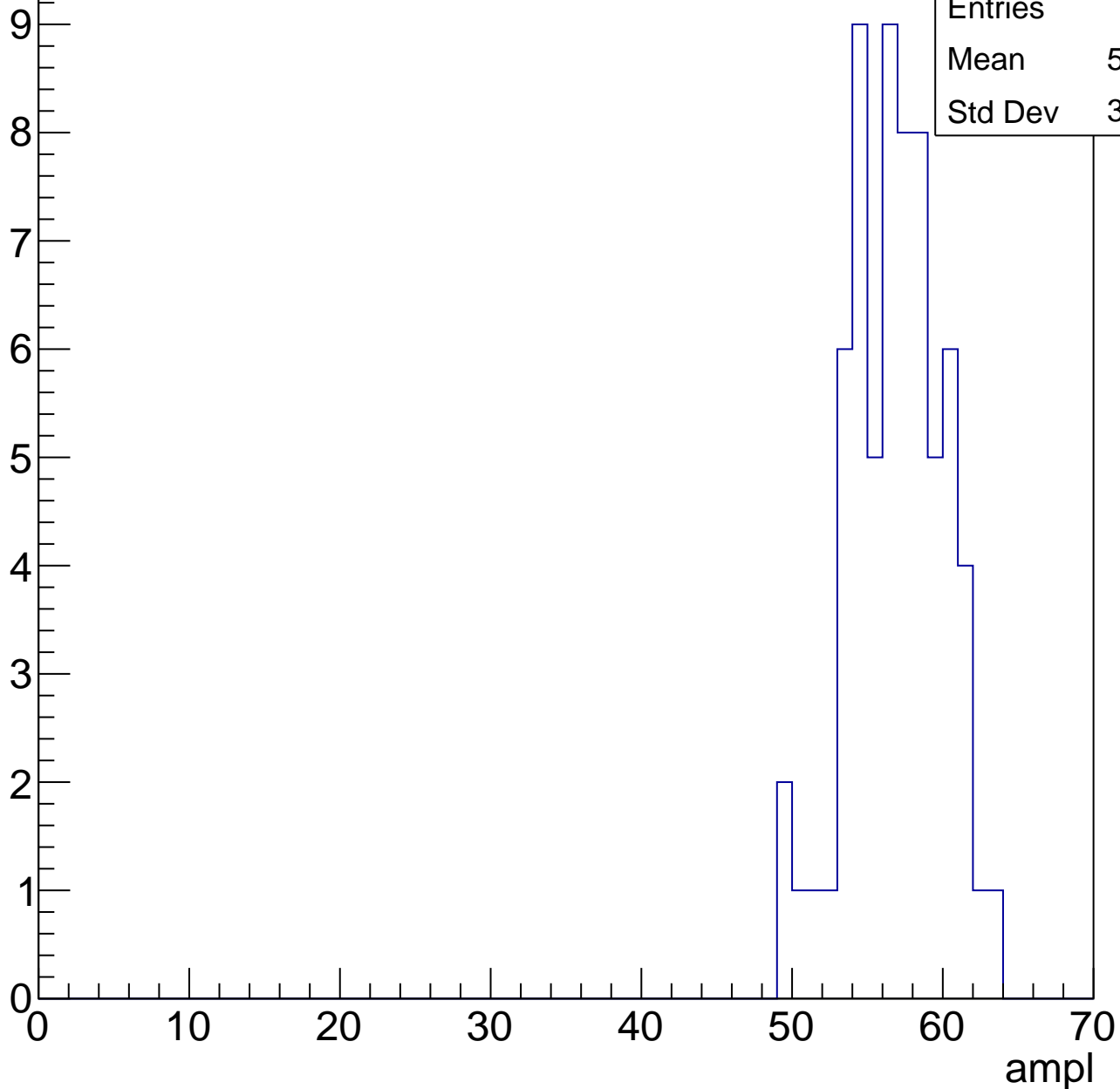
Entries	70
Mean	49.54
Std Dev	3.872



B1L103S, U17-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

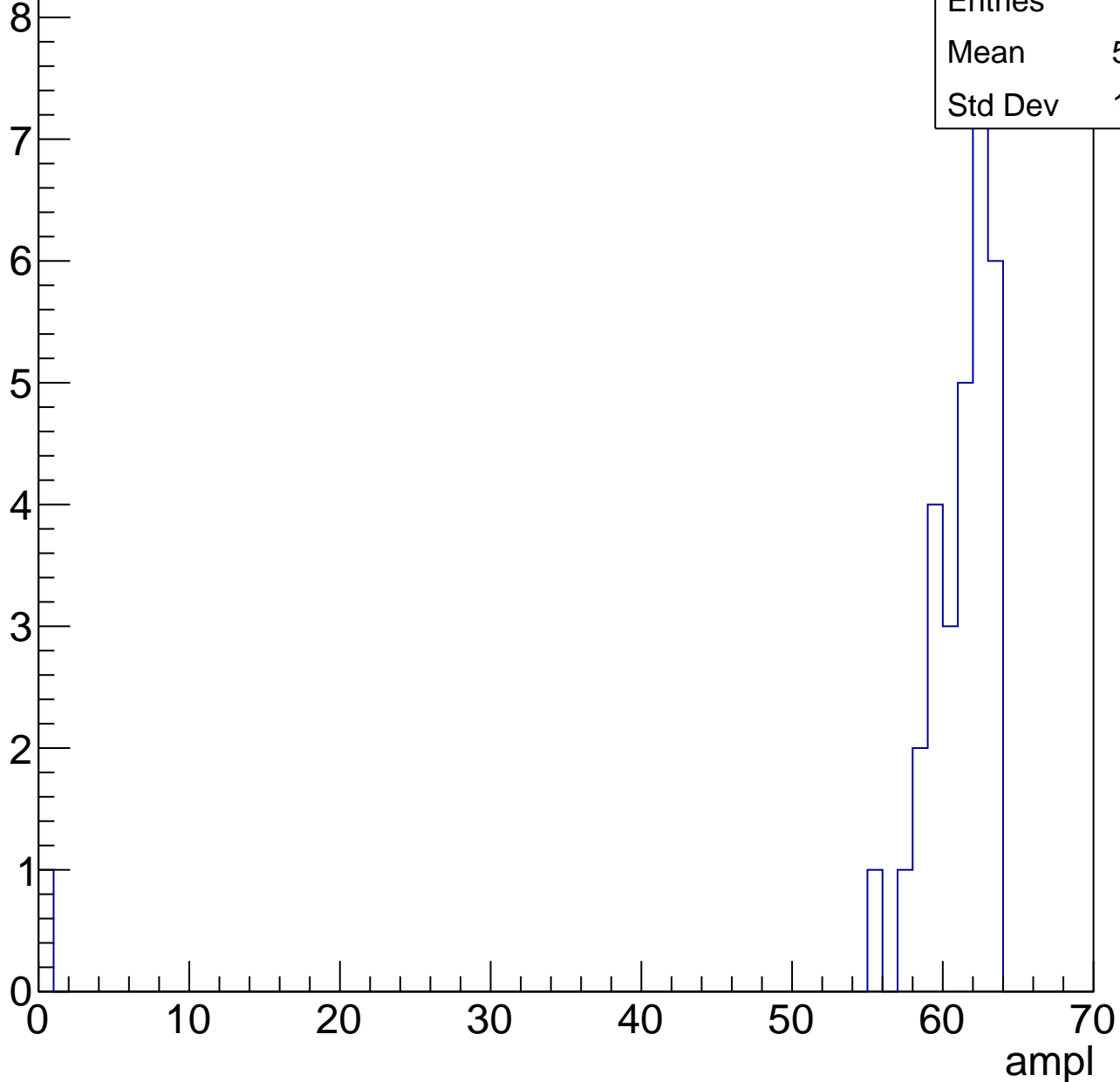


B1L103S, U17-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

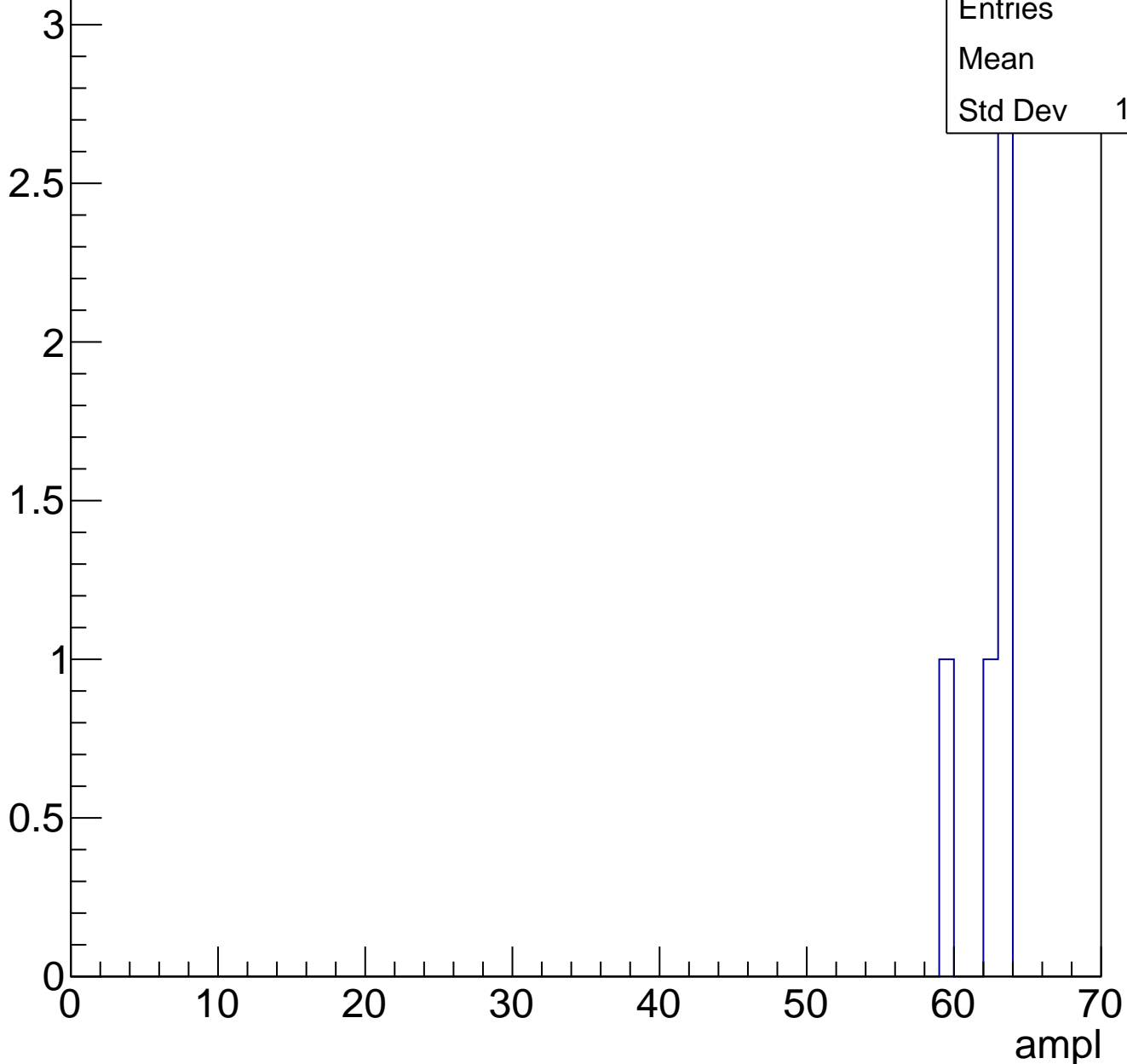
Entries	31
Mean	58.81
Std Dev	10.91



B1L103S, U17-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch7, adc0

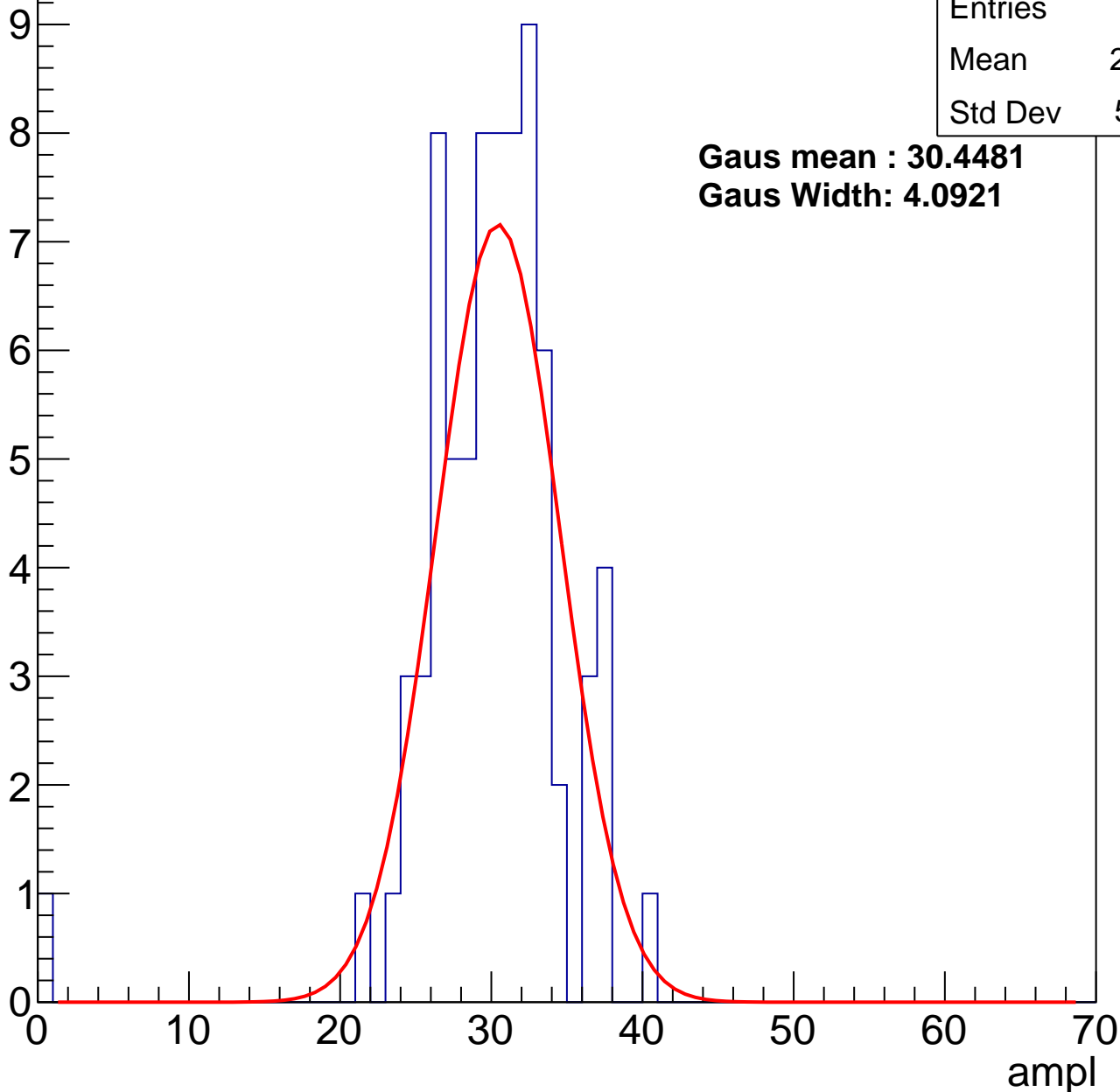
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.53
Std Dev	5.041

Gaus mean : 30.4481

Gaus Width: 4.0921



B1L103S, U17-ch7, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	37.46
Std Dev	3.785

Gaus mean : 37.8302

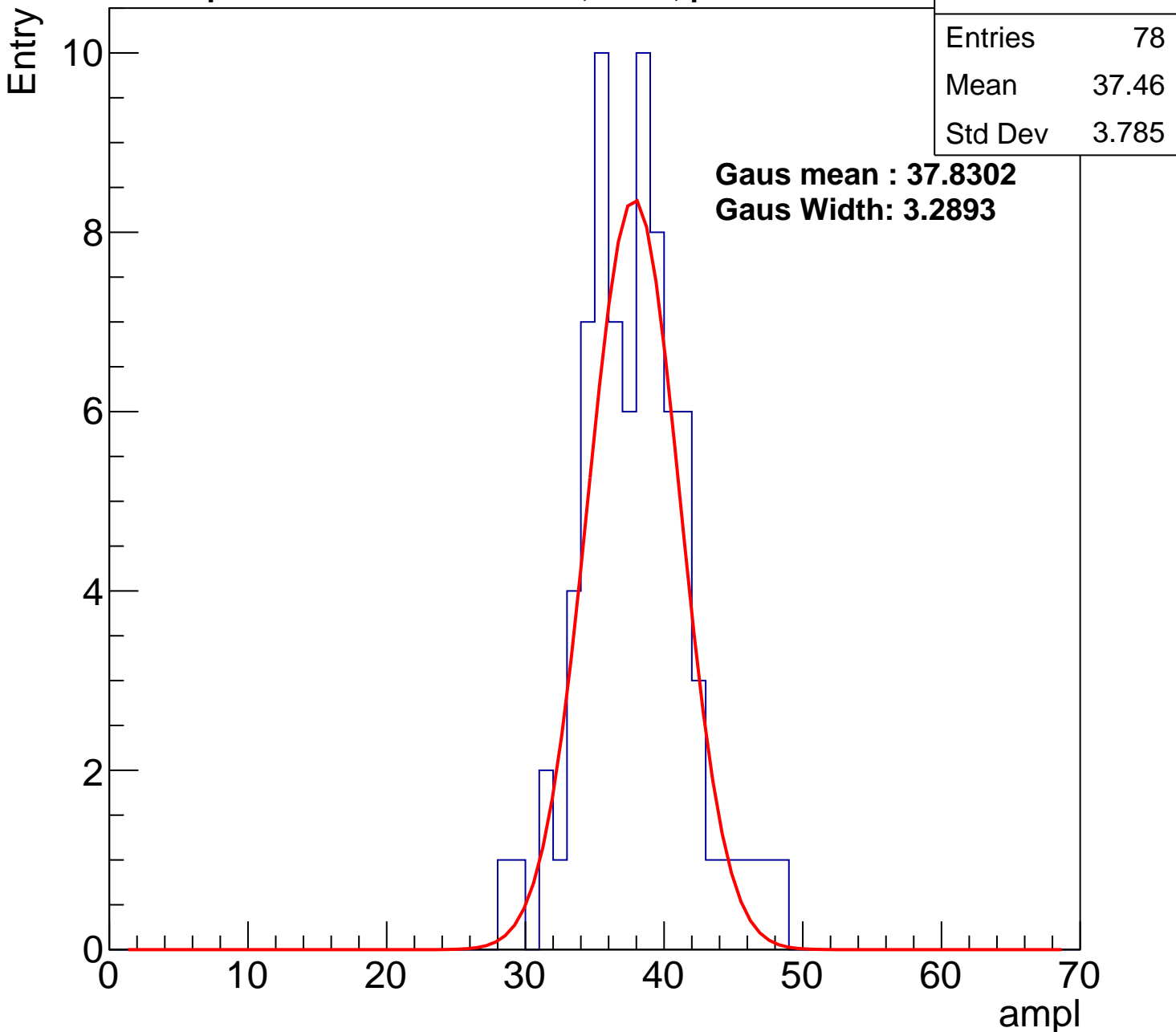
Gaus Width: 3.2893

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch7, adc2

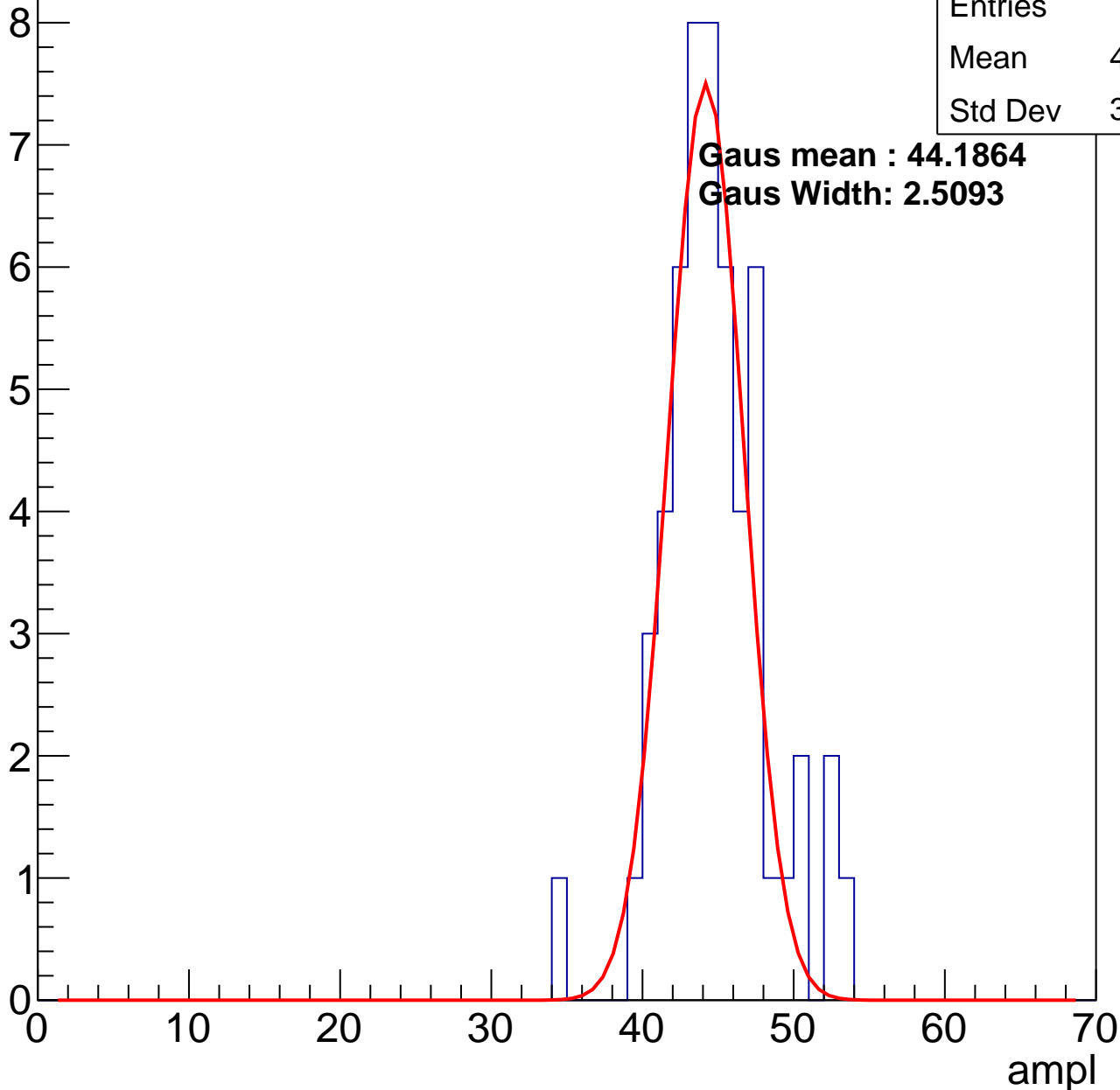
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	44.35
Std Dev	3.422

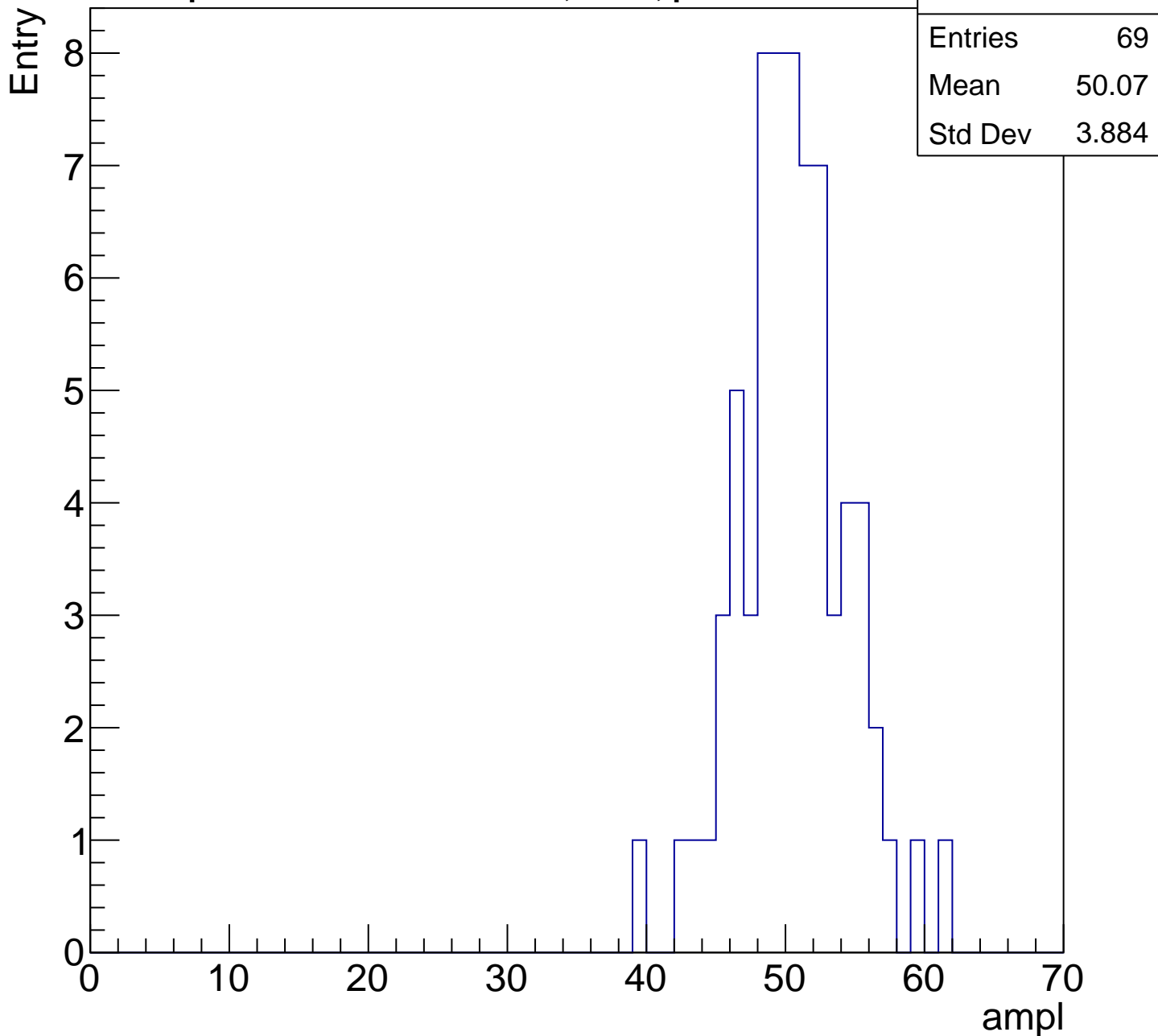
Gaus mean : 44.1864

Gaus Width: 2.5093



B1L103S, U17-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

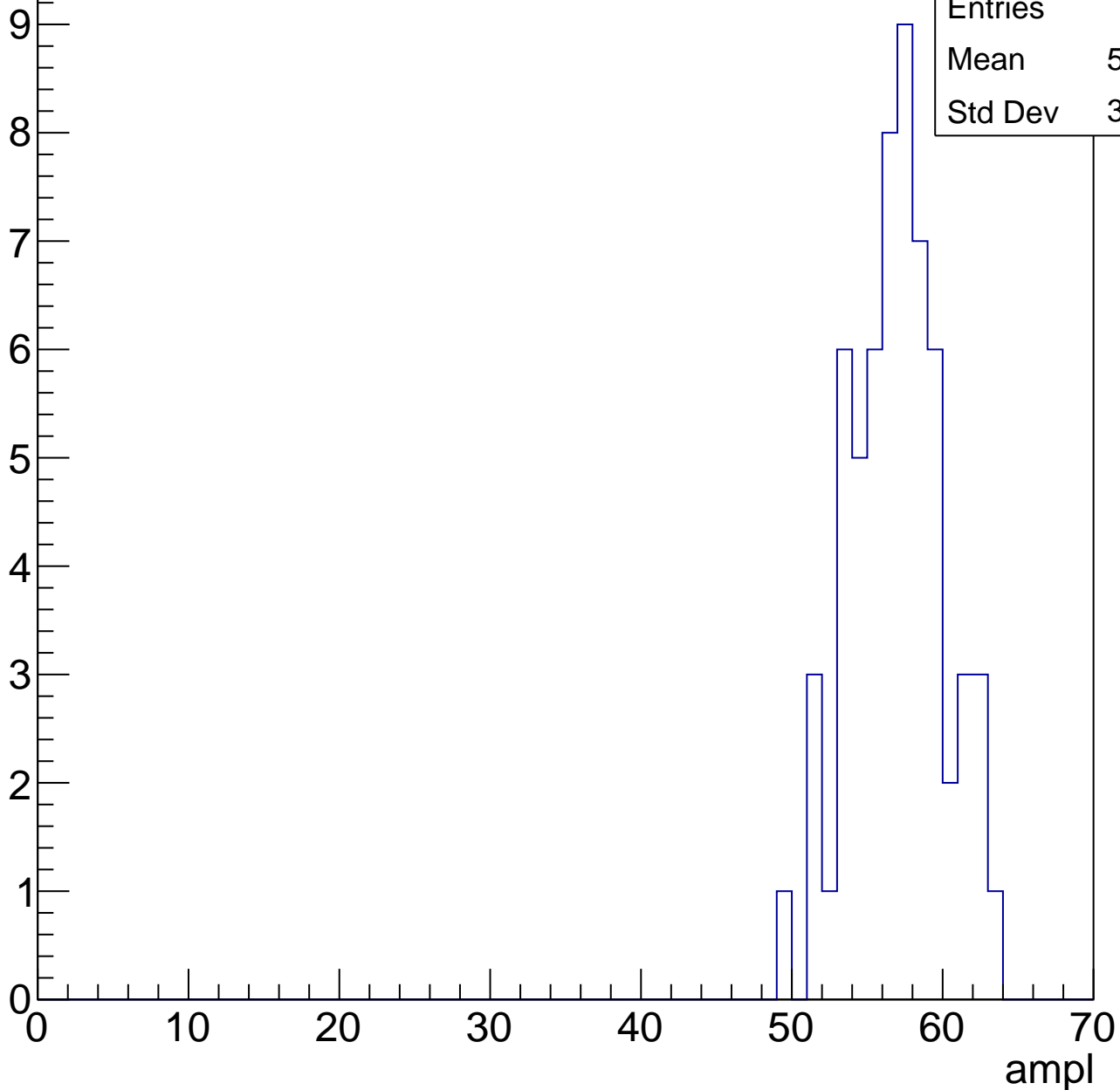


B1L103S, U17-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	56.48
Std Dev	3.033

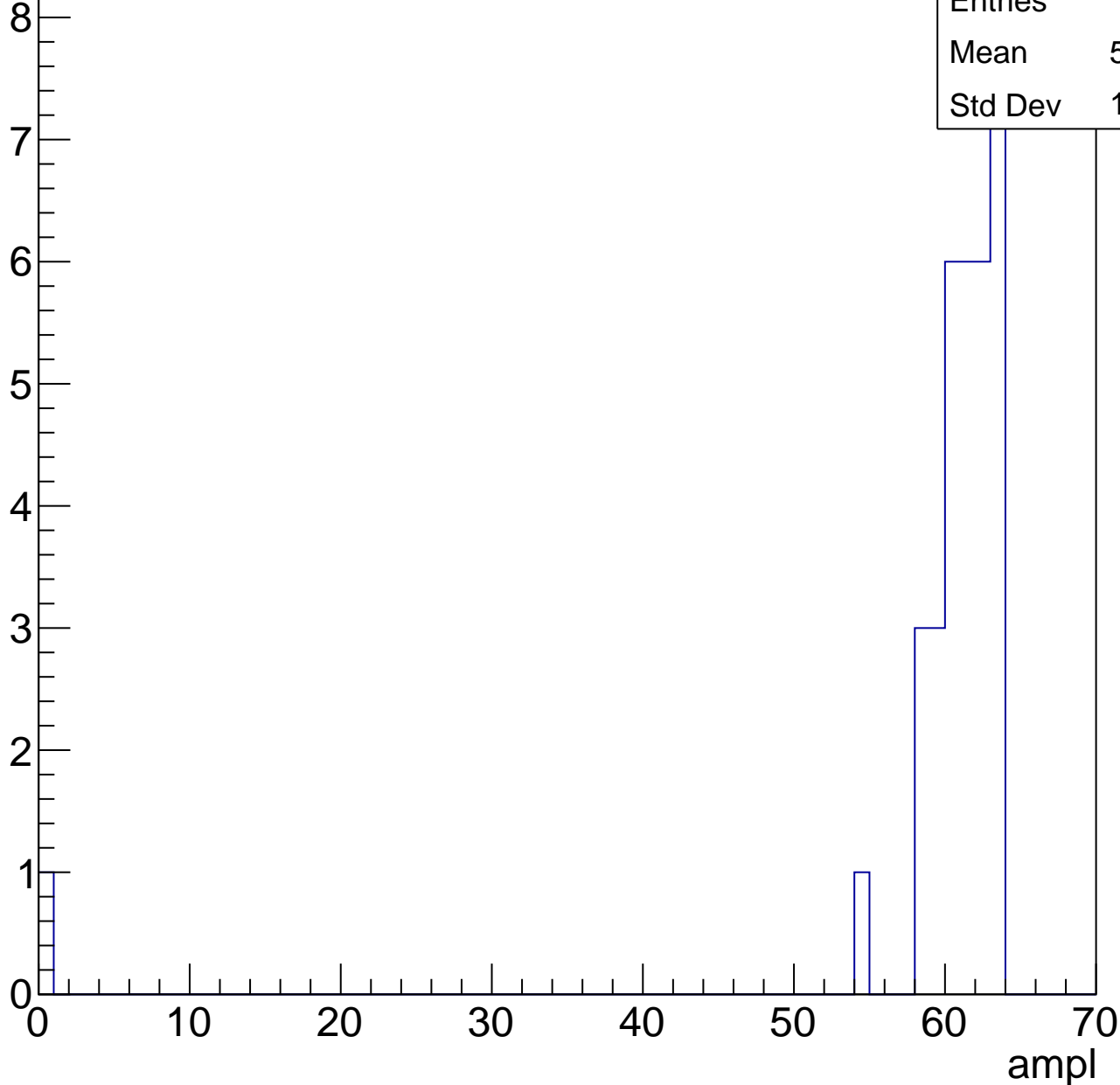


B1L103S, U17-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	59.03
Std Dev	10.46



B1L103S, U17-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

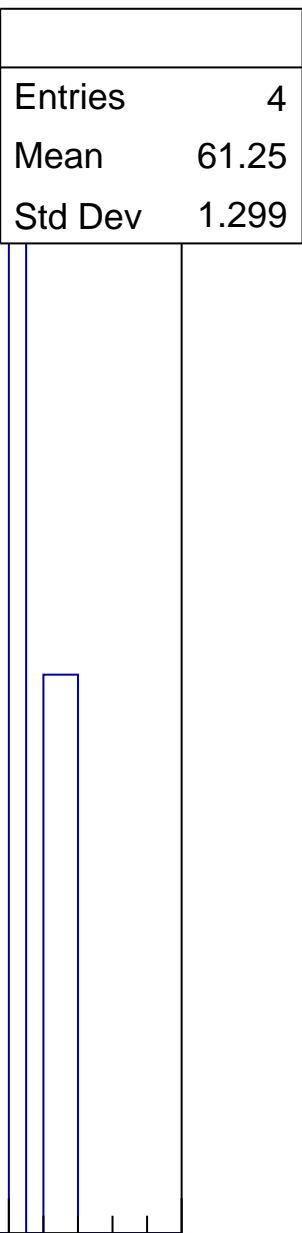
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.299

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch8, adc0

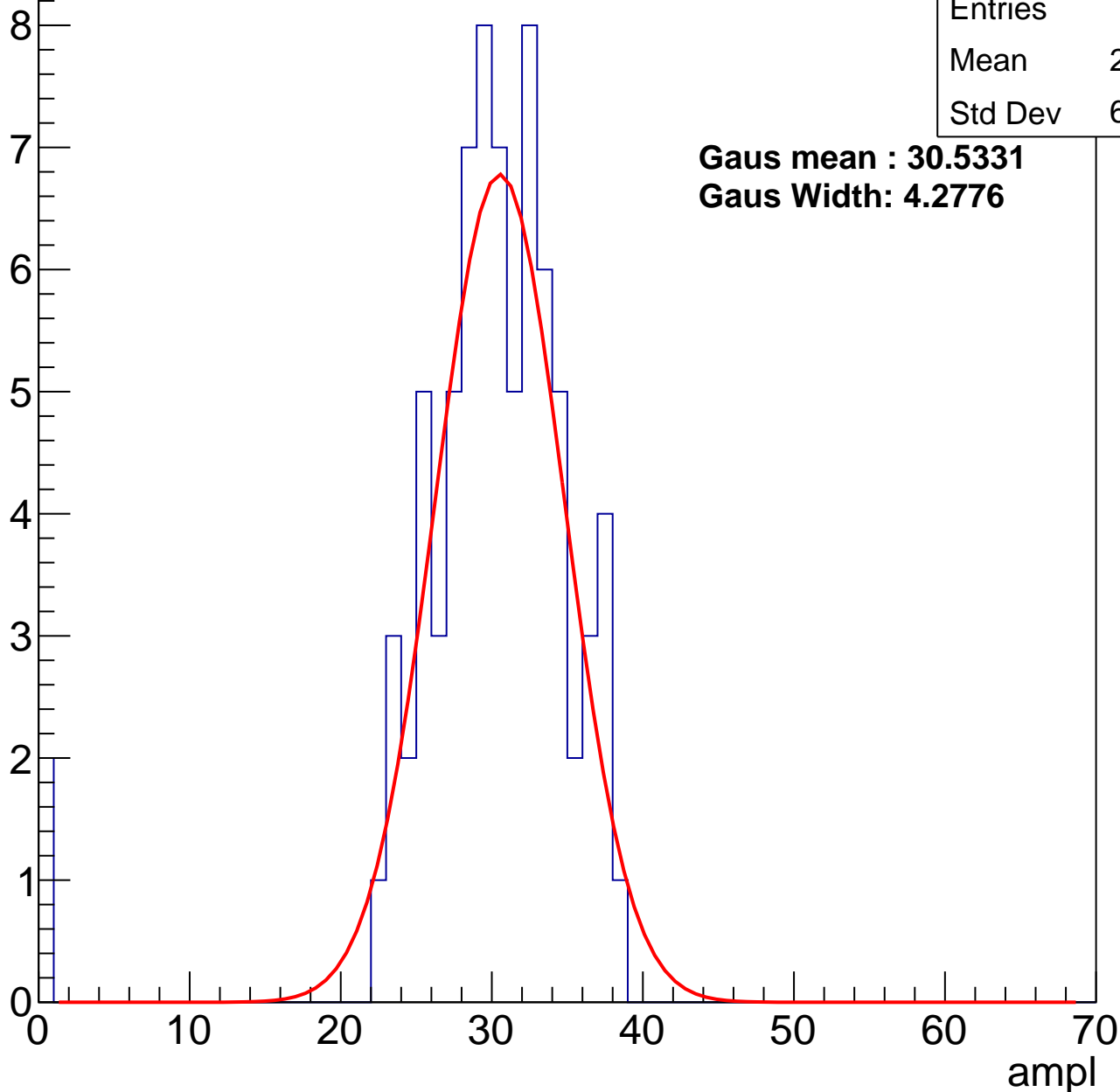
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	29.32
Std Dev	6.127

Gaus mean : 30.5331

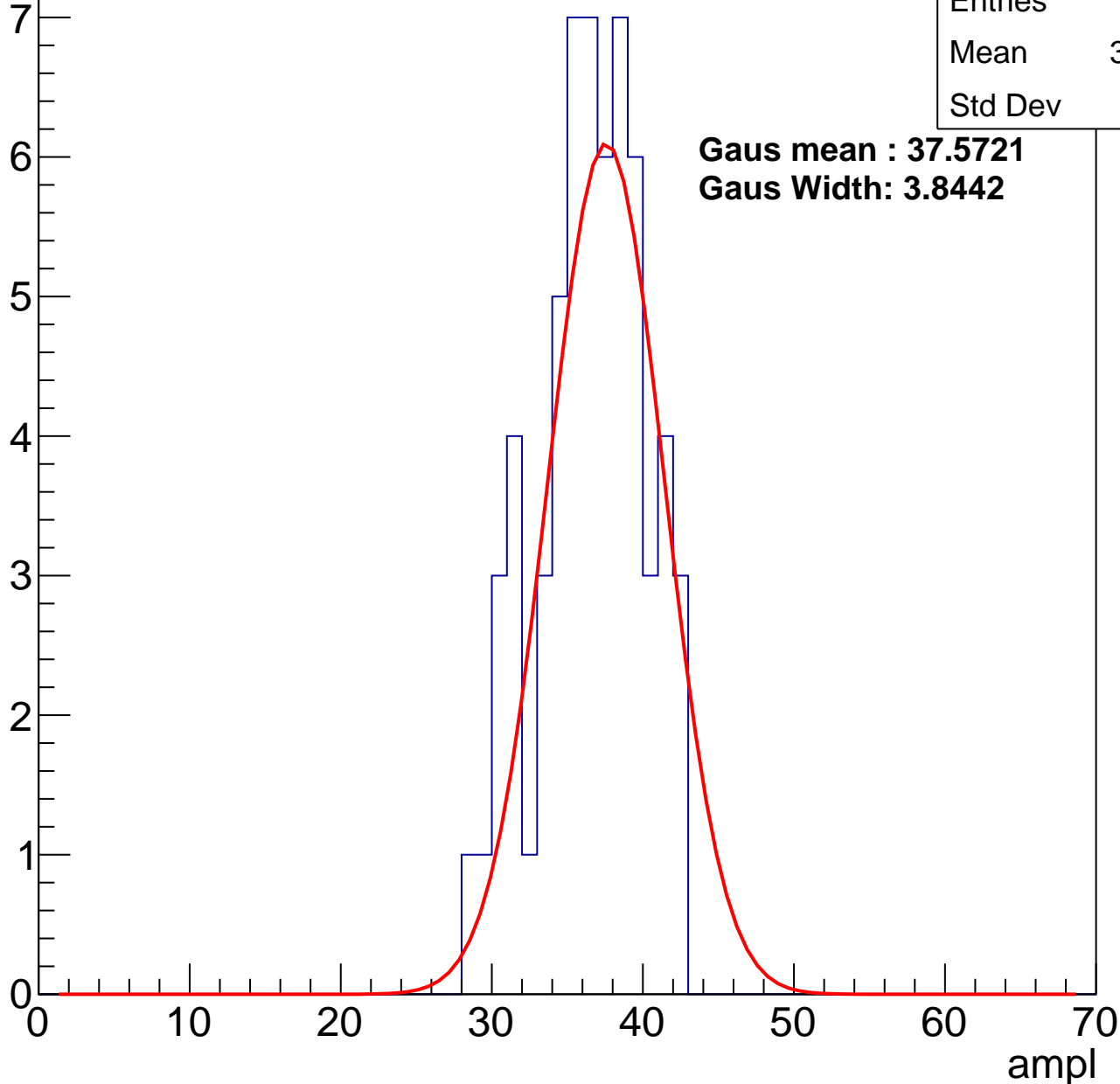
Gaus Width: 4.2776



B1L103S, U17-ch8, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch8, adc2

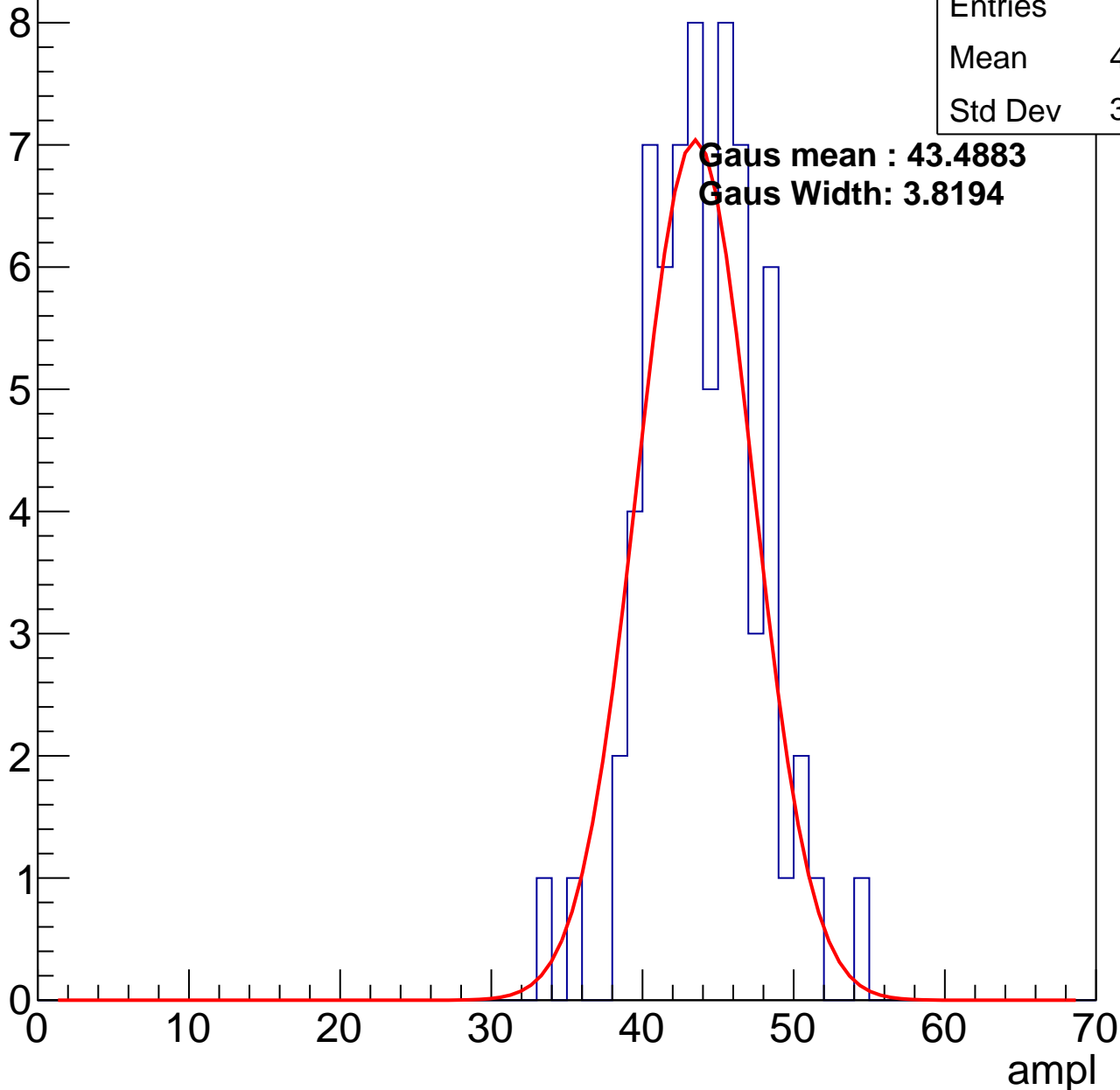
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.56
Std Dev	3.717

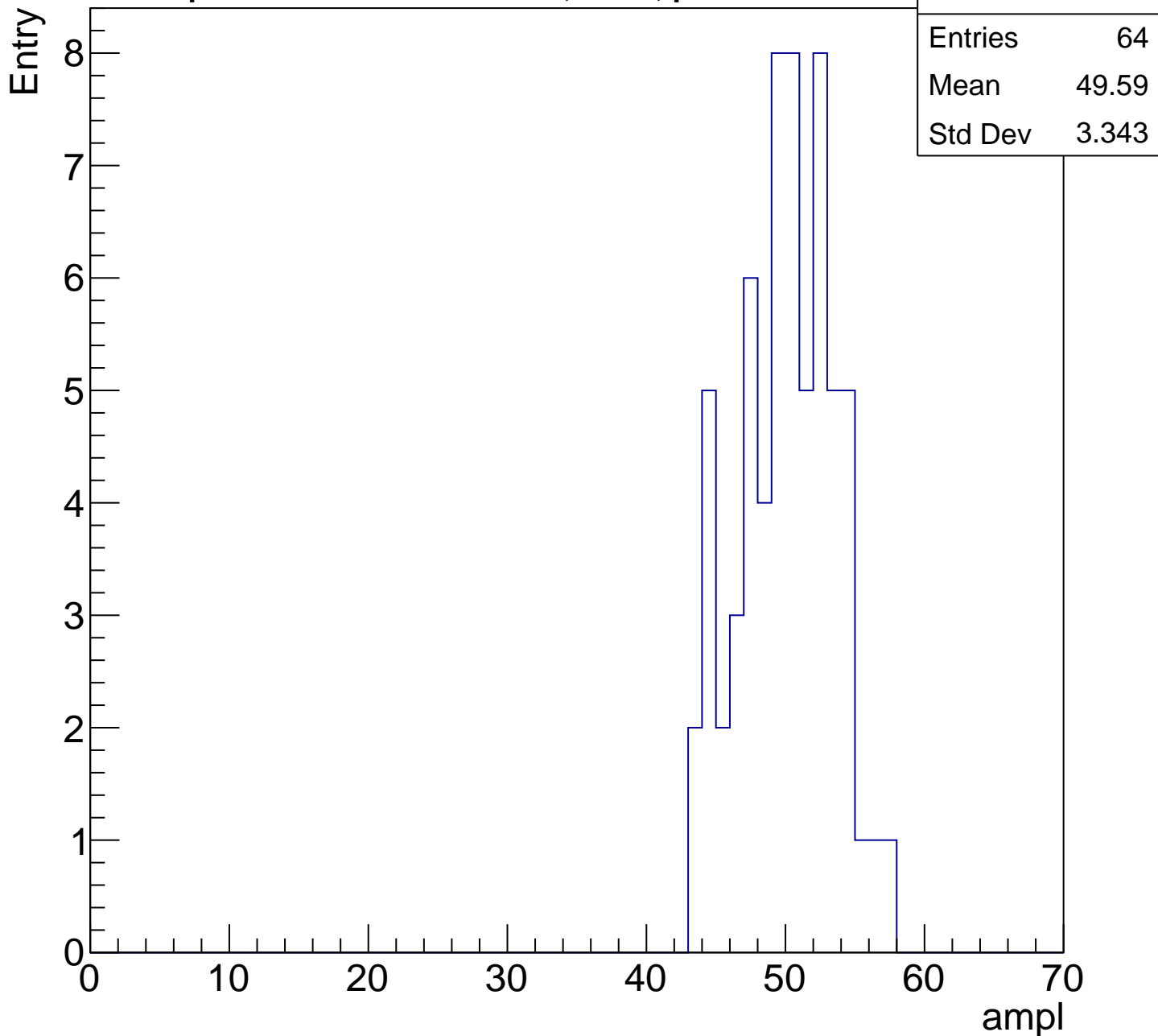
Gaus mean : 43.4883

Gaus Width: 3.8194



B1L103S, U17-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

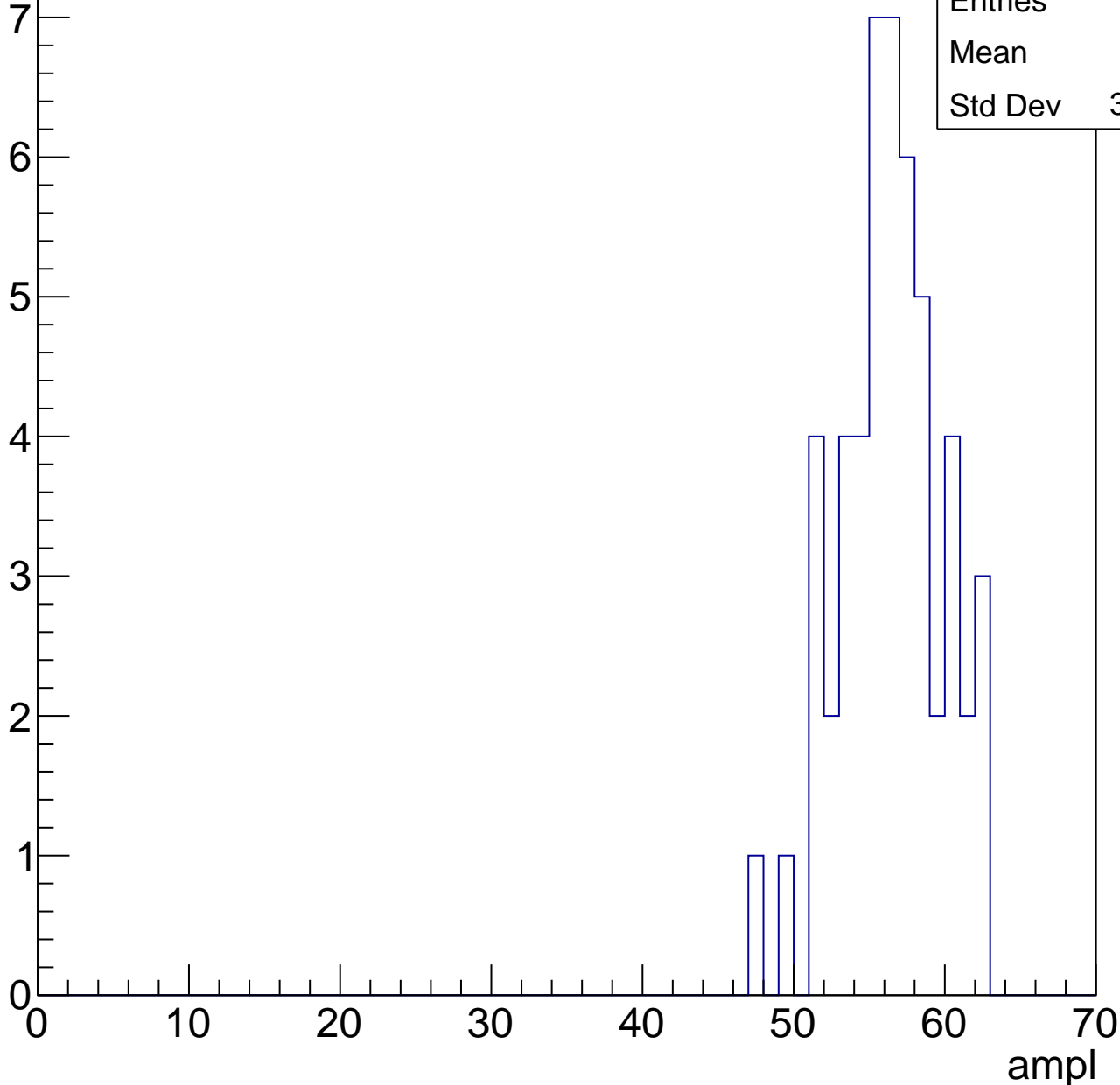


B1L103S, U17-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

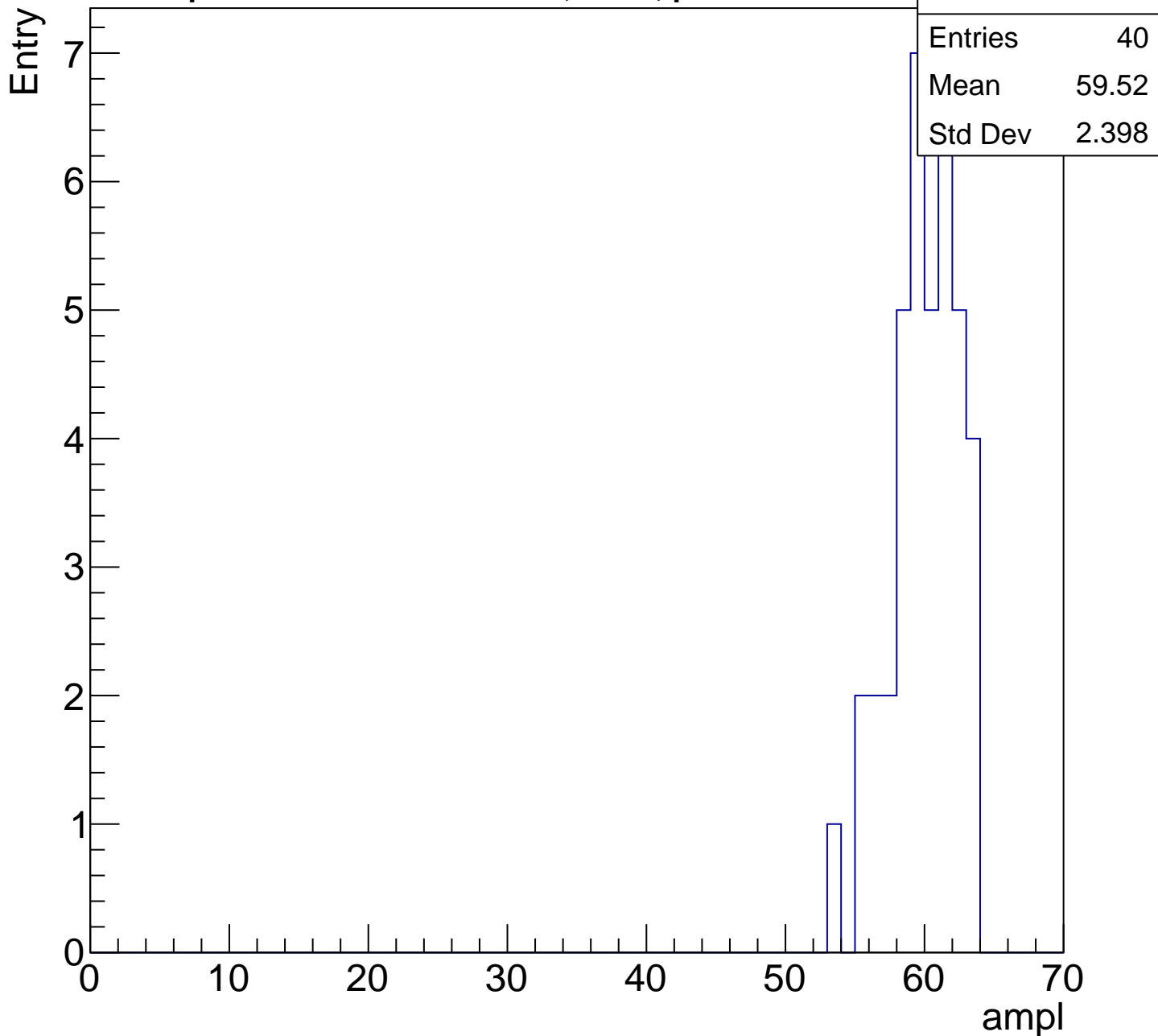
Entry

Entries	52
Mean	55.9
Std Dev	3.358



B1L103S, U17-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

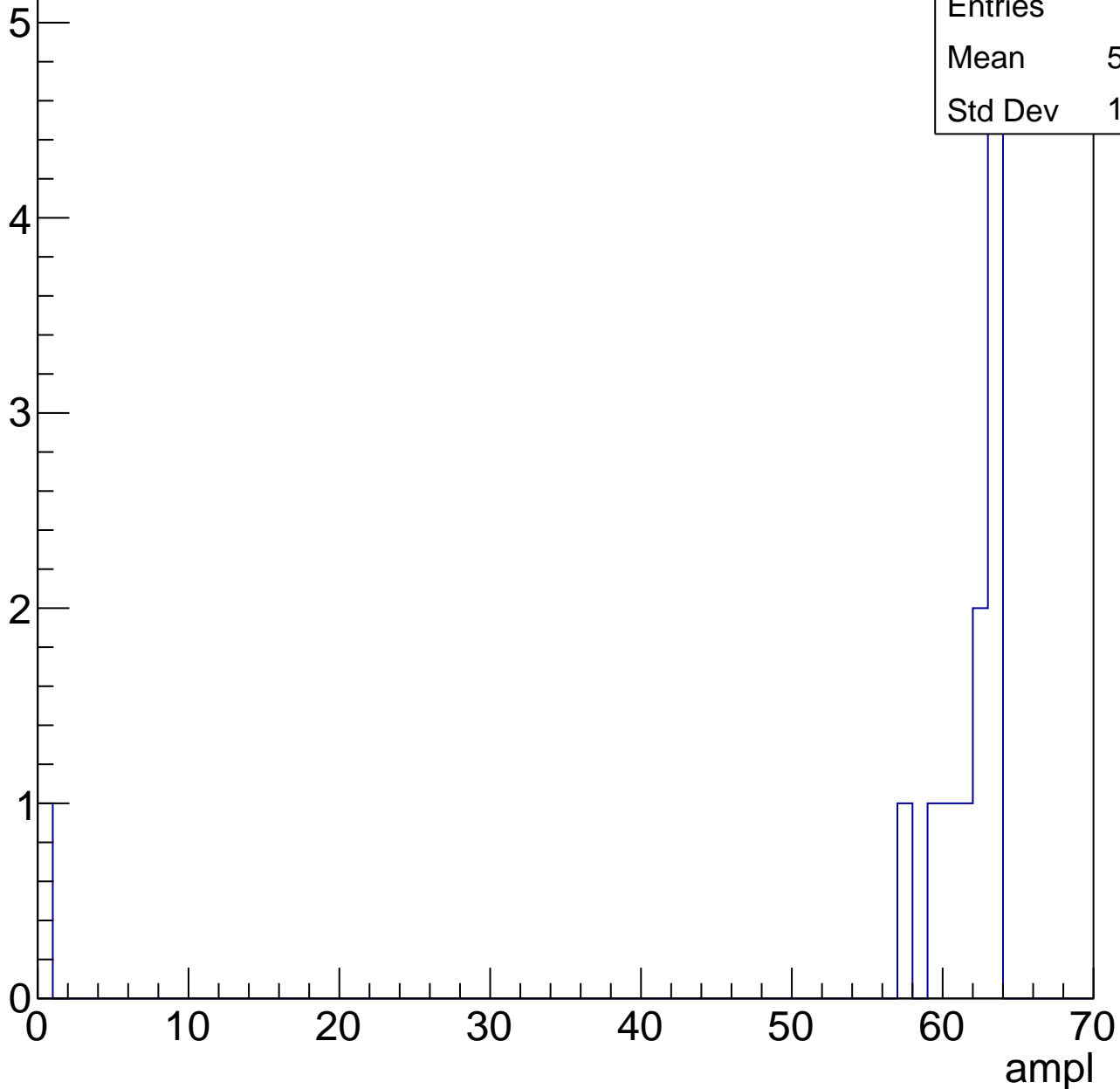


B1L103S, U17-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.33
Std Dev	17.08



B1L103S, U17-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch9, adc0

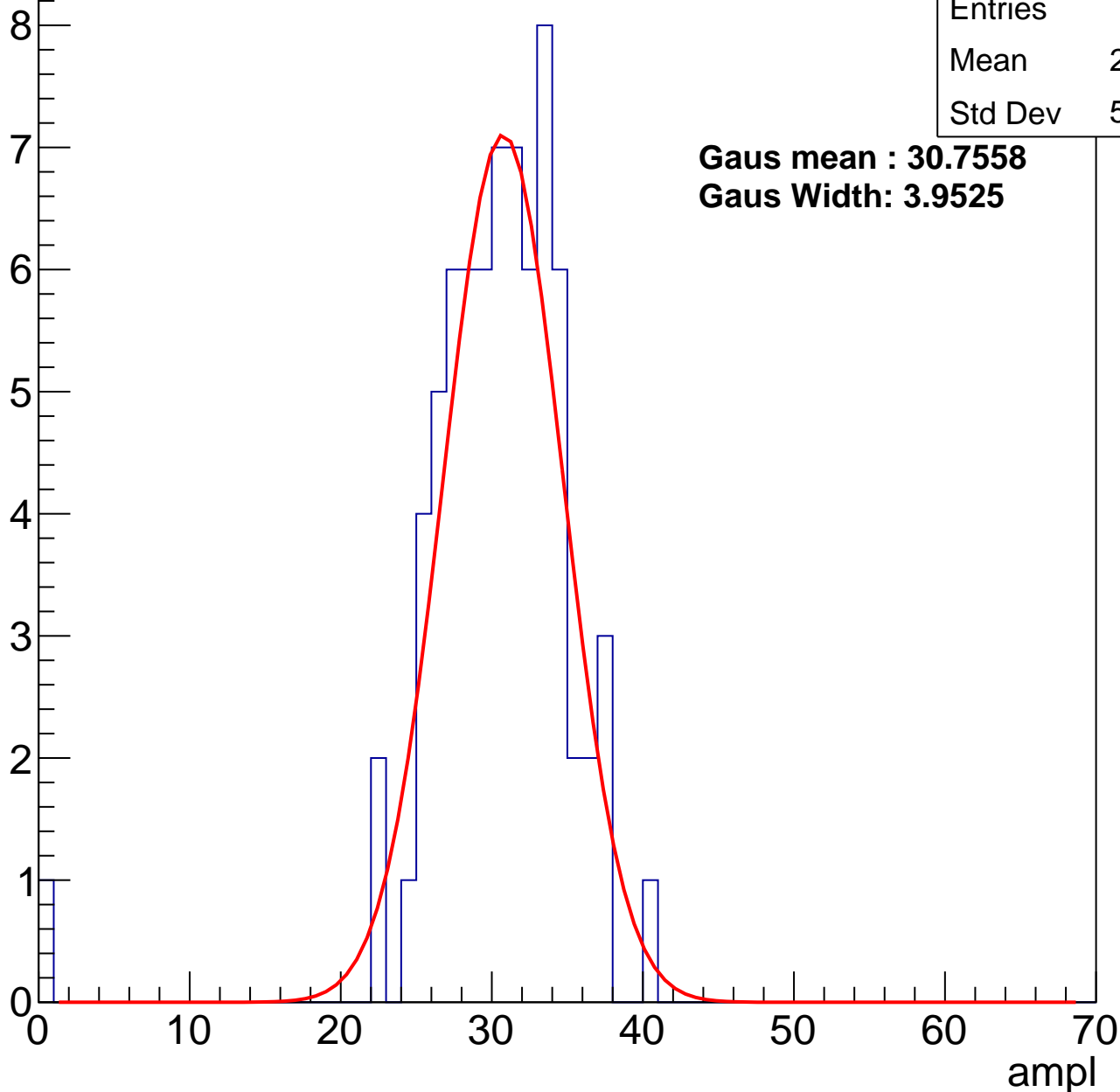
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.89
Std Dev	5.095

Gaus mean : 30.7558

Gaus Width: 3.9525



B1L103S, U17-ch9, adc1

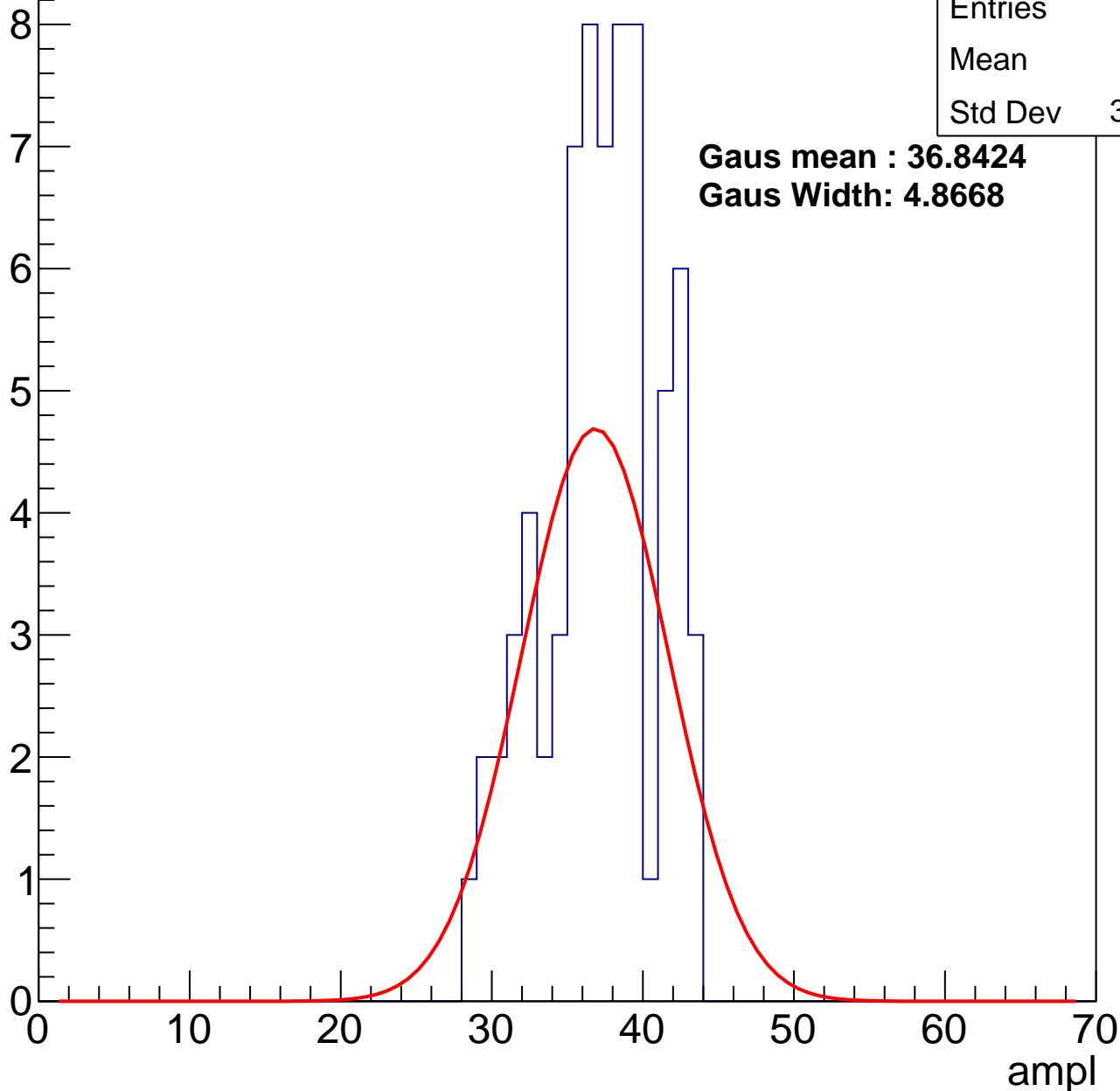
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.7
Std Dev	3.762

Gaus mean : 36.8424

Gaus Width: 4.8668



B1L103S, U17-ch9, adc2

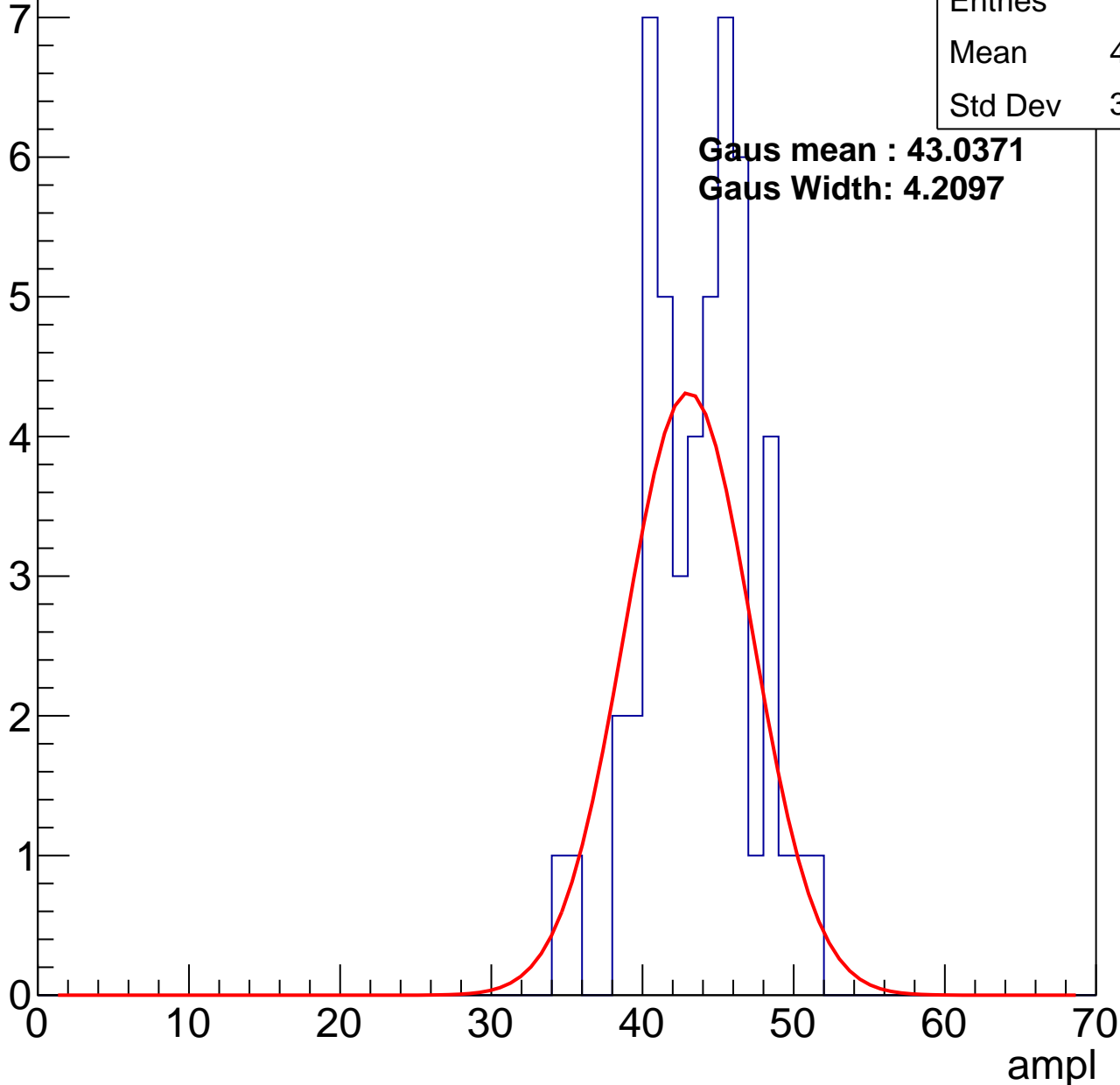
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	43.25
Std Dev	3.602

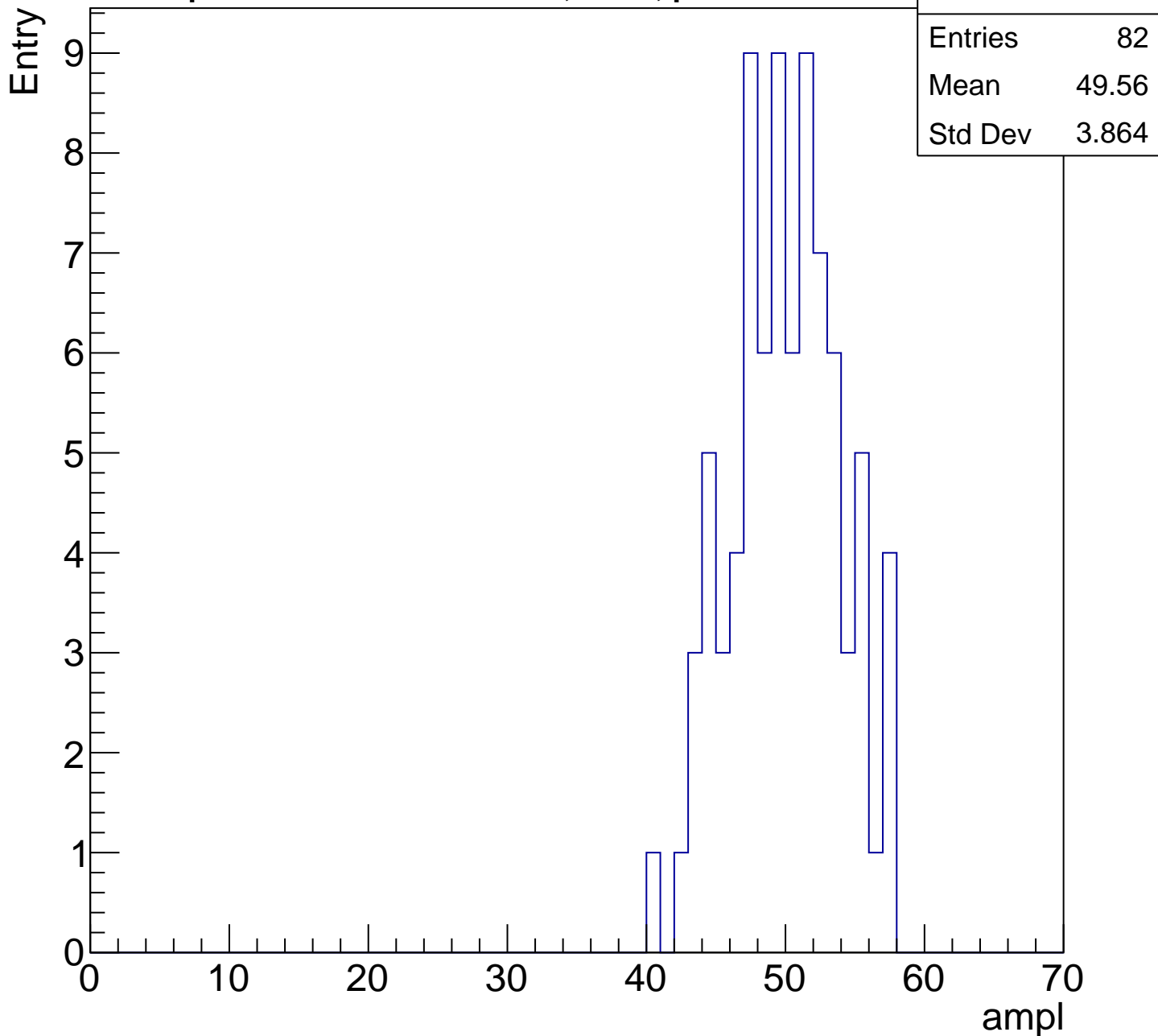
Gaus mean : 43.0371

Gaus Width: 4.2097



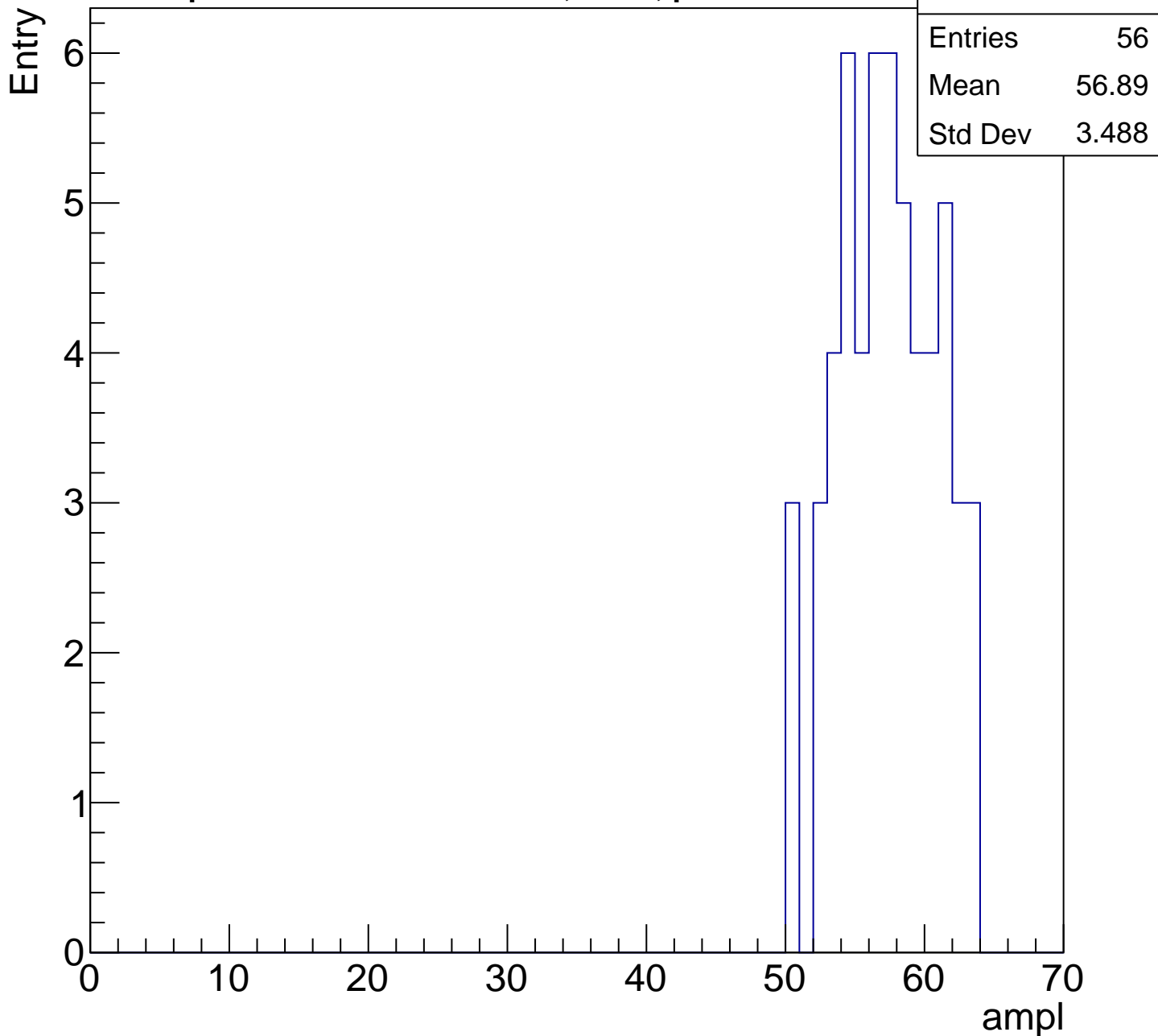
B1L103S, U17-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2



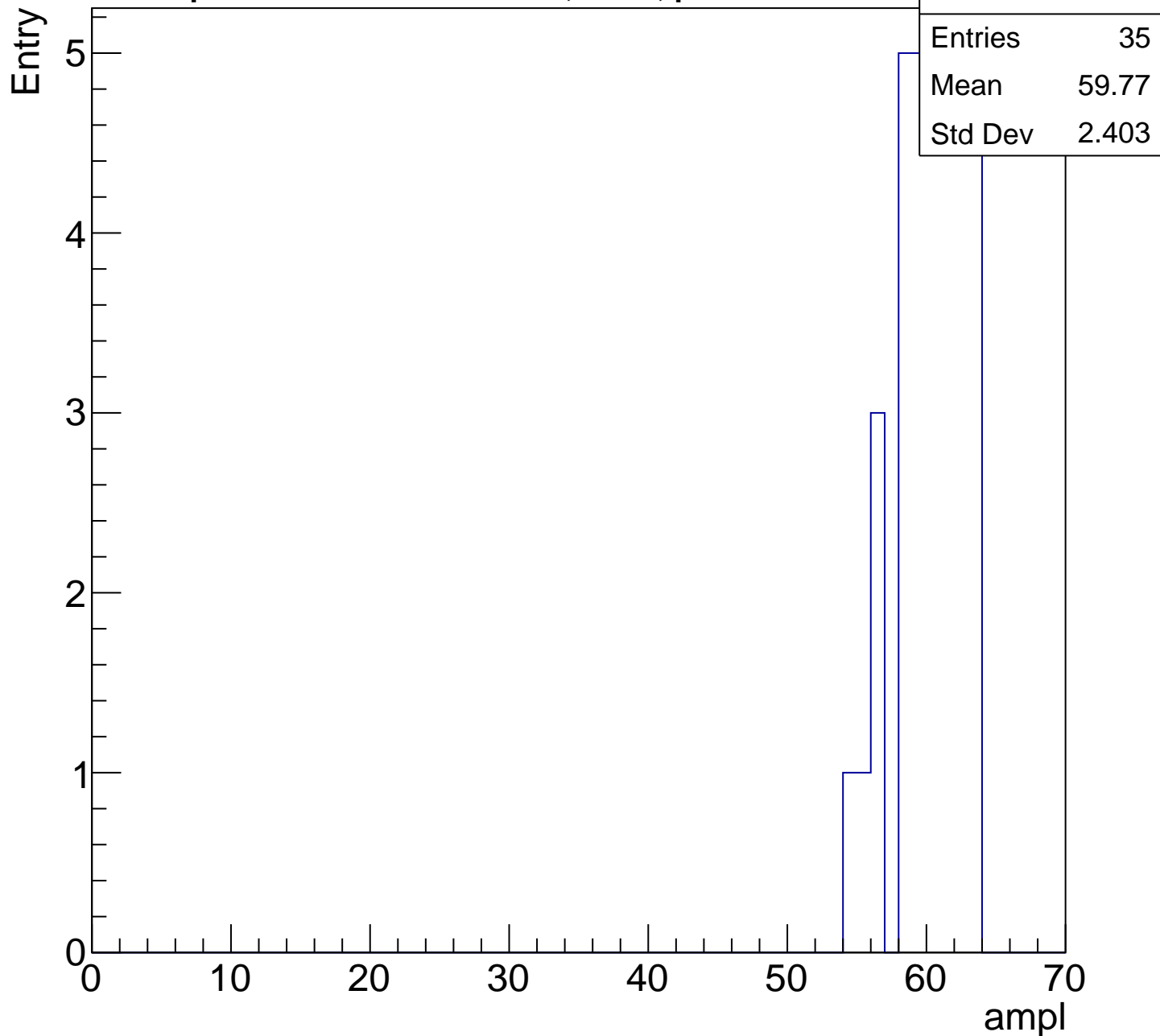
B1L103S, U17-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch9, adc5

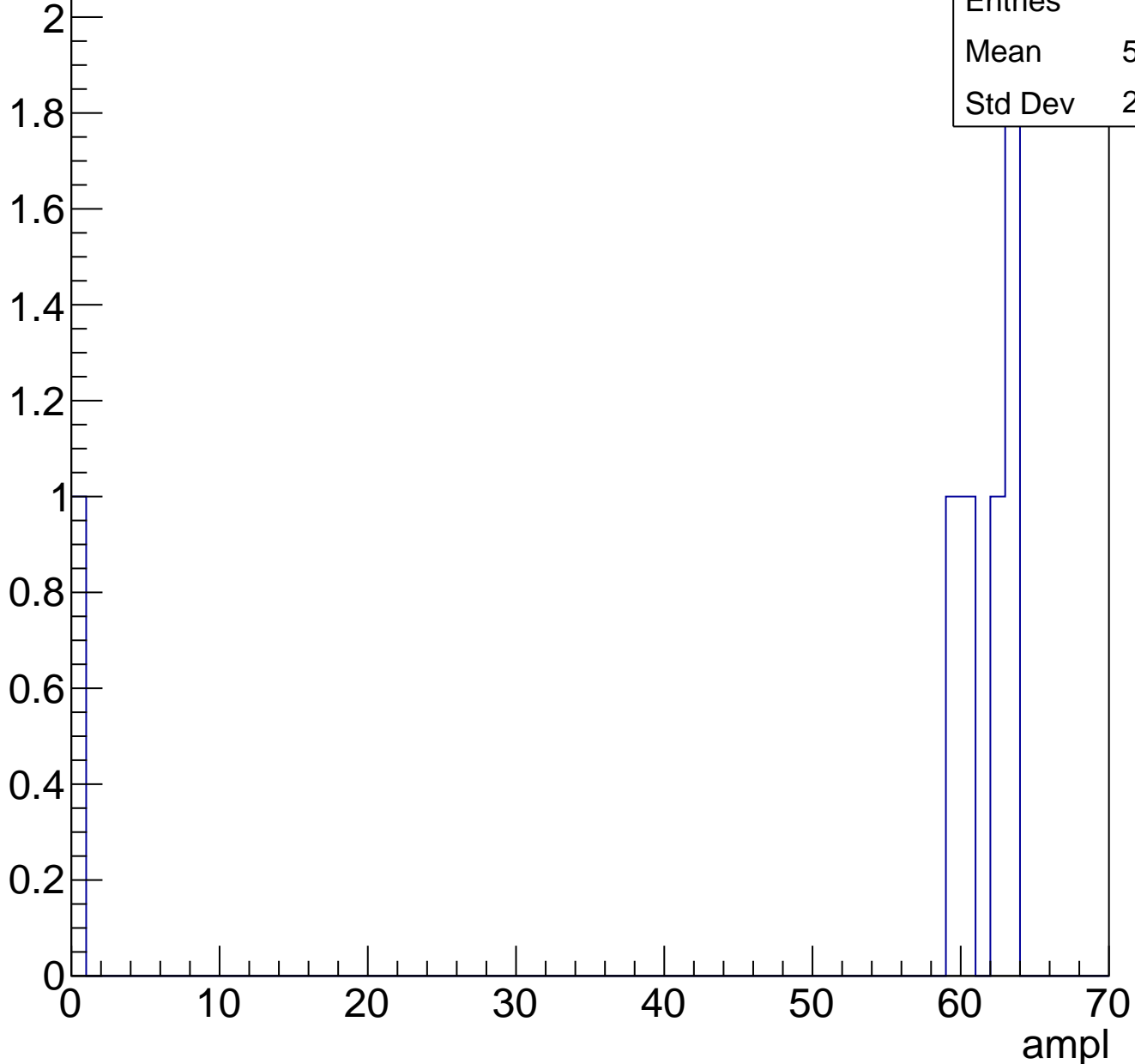
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	51.17
Std Dev	22.93

B1L103S, U17-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch10, adc0

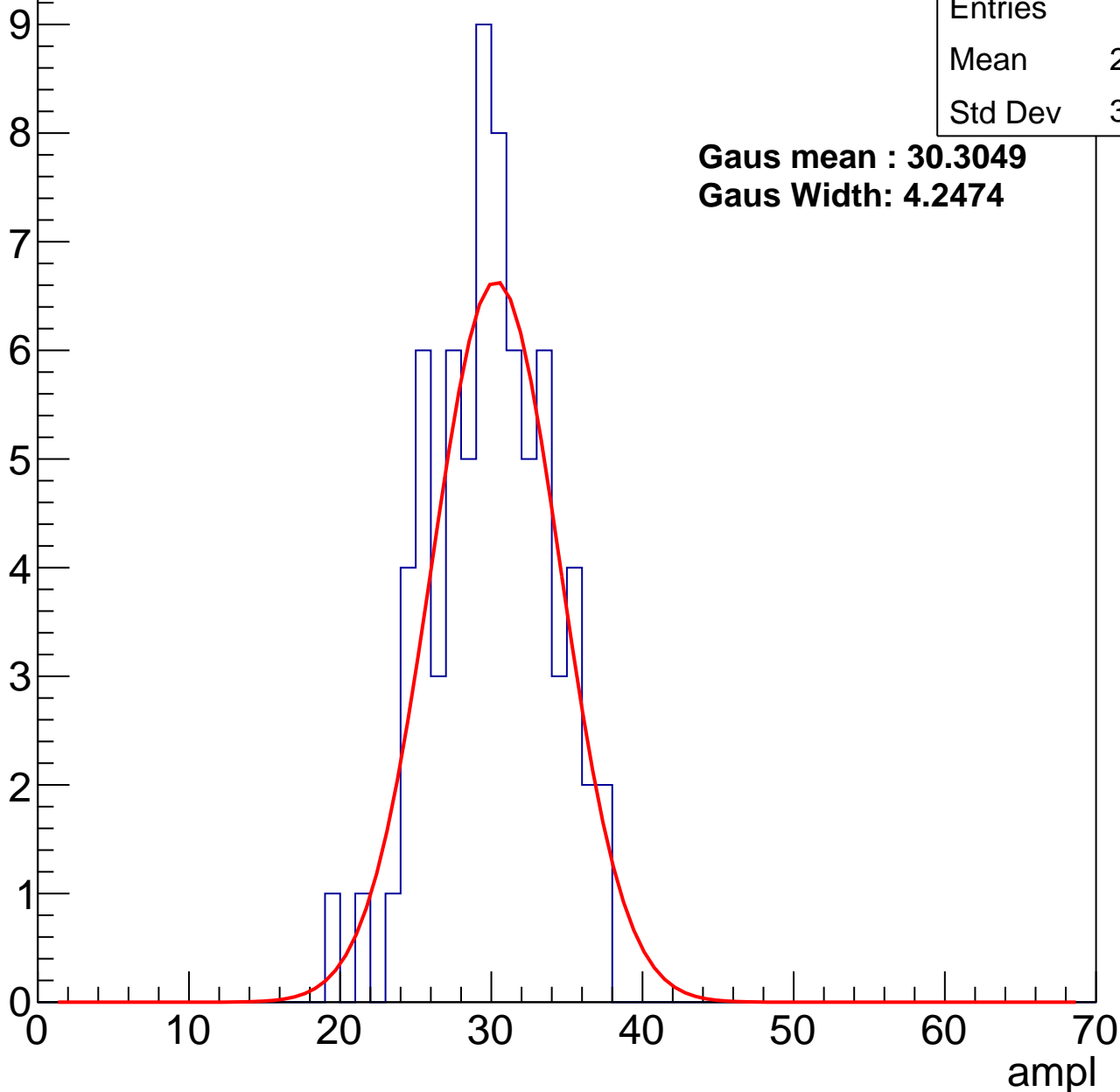
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.47
Std Dev	3.826

Gaus mean : 30.3049

Gaus Width: 4.2474



B1L103S, U17-ch10, adc1

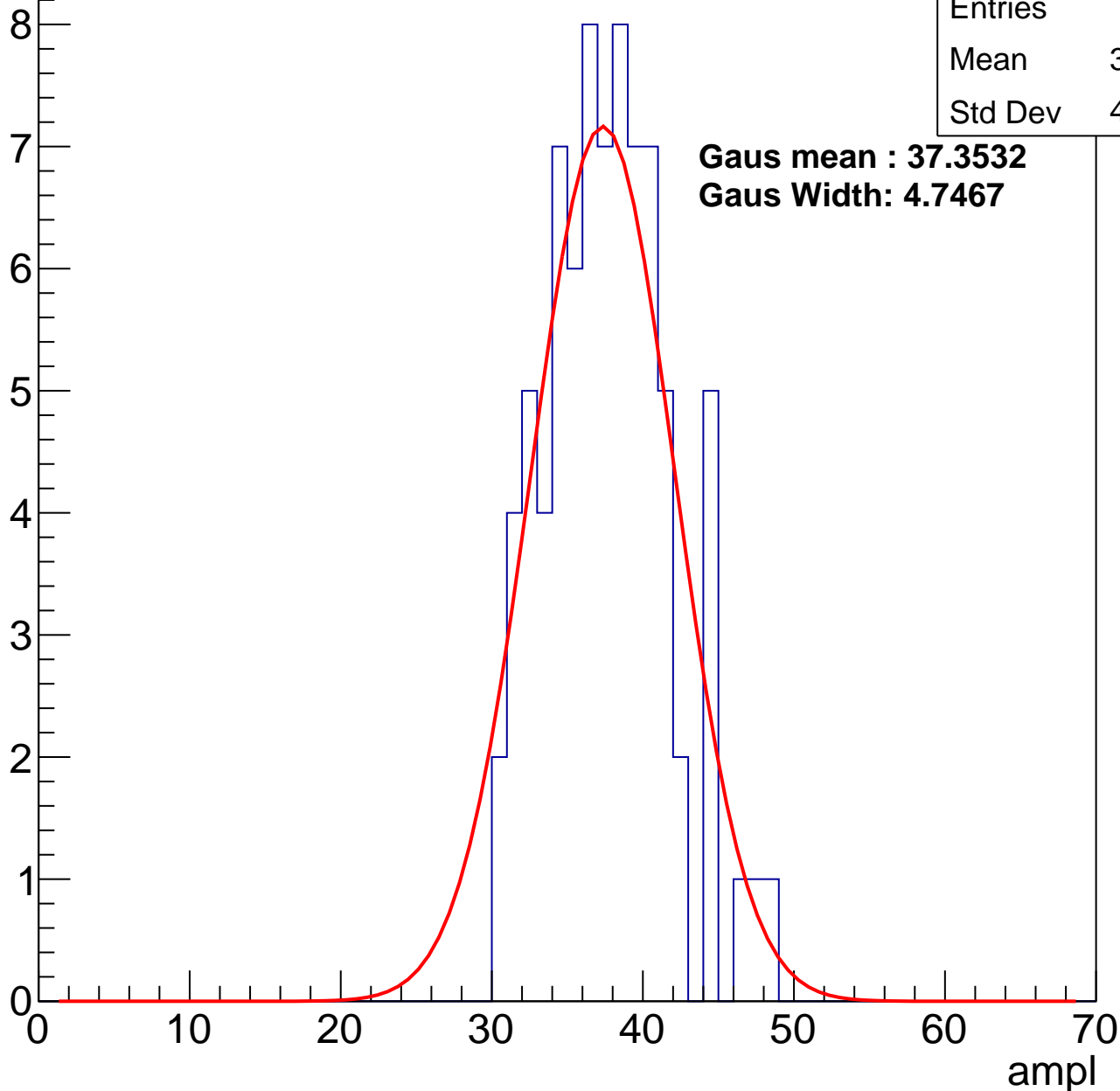
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	37.23
Std Dev	4.028

Gaus mean : 37.3532

Gaus Width: 4.7467



B1L103S, U17-ch10, adc2

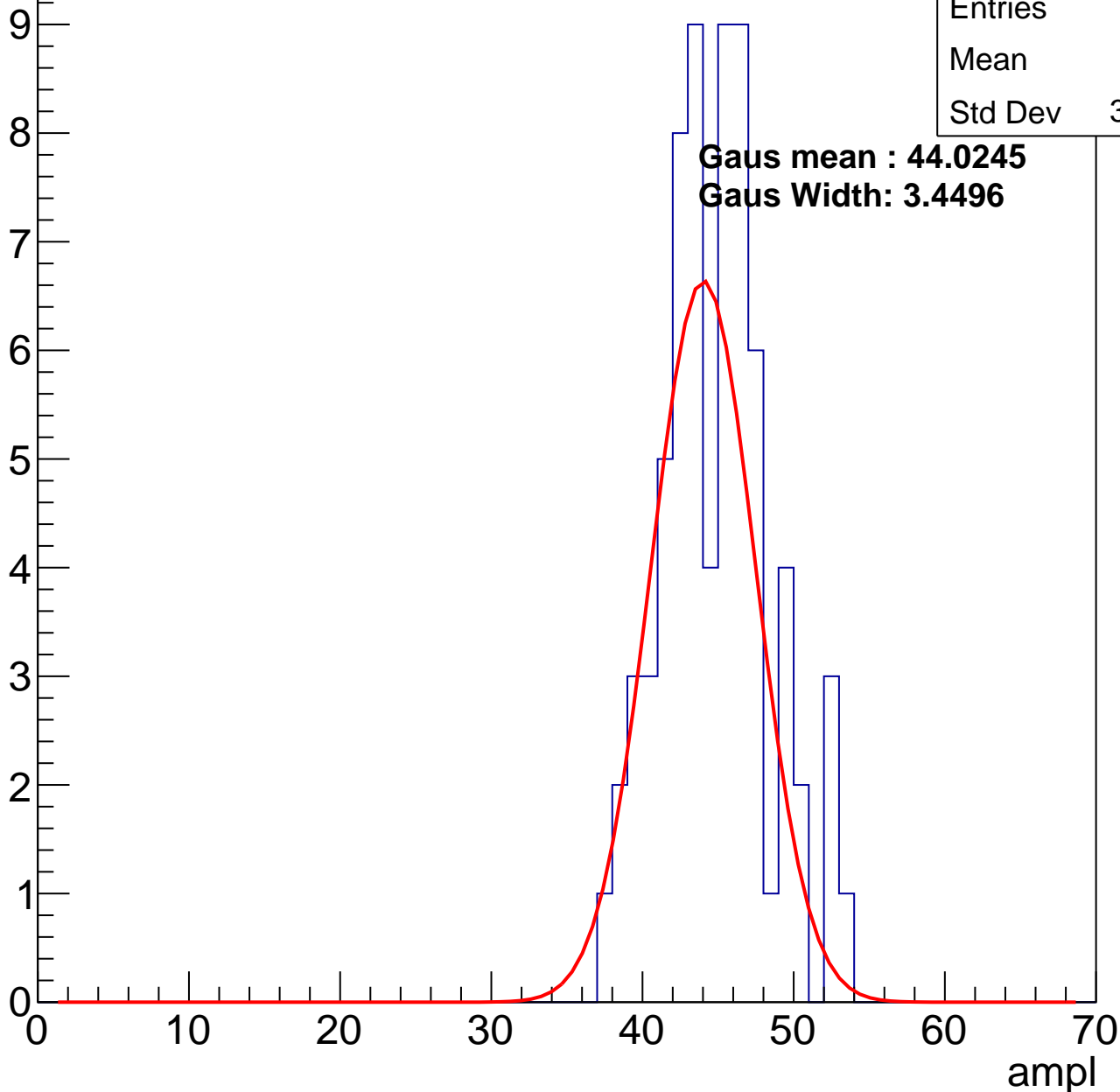
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.4
Std Dev	3.543

Gaus mean : 44.0245

Gaus Width: 3.4496

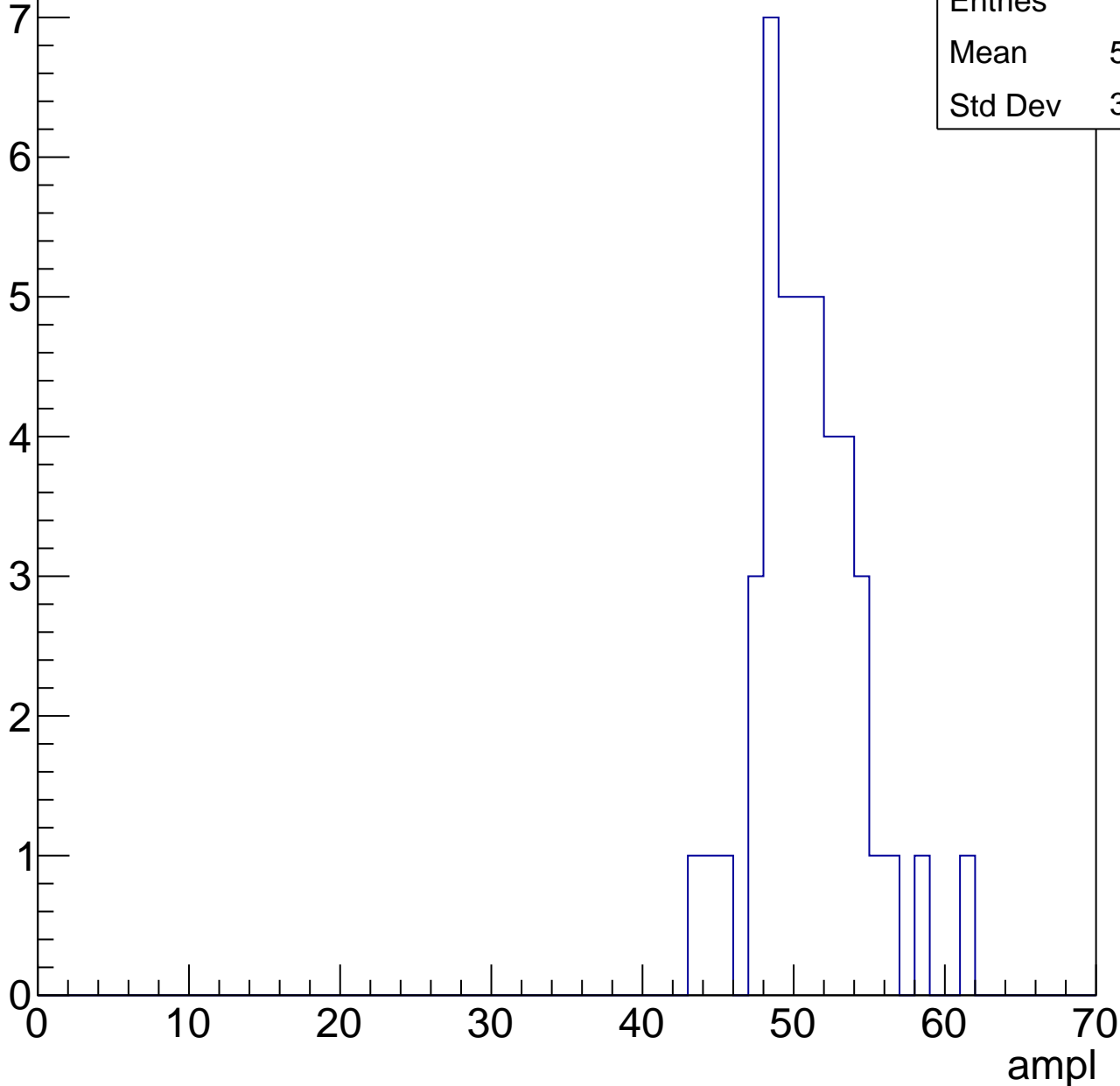


B1L103S, U17-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	50.49
Std Dev	3.446

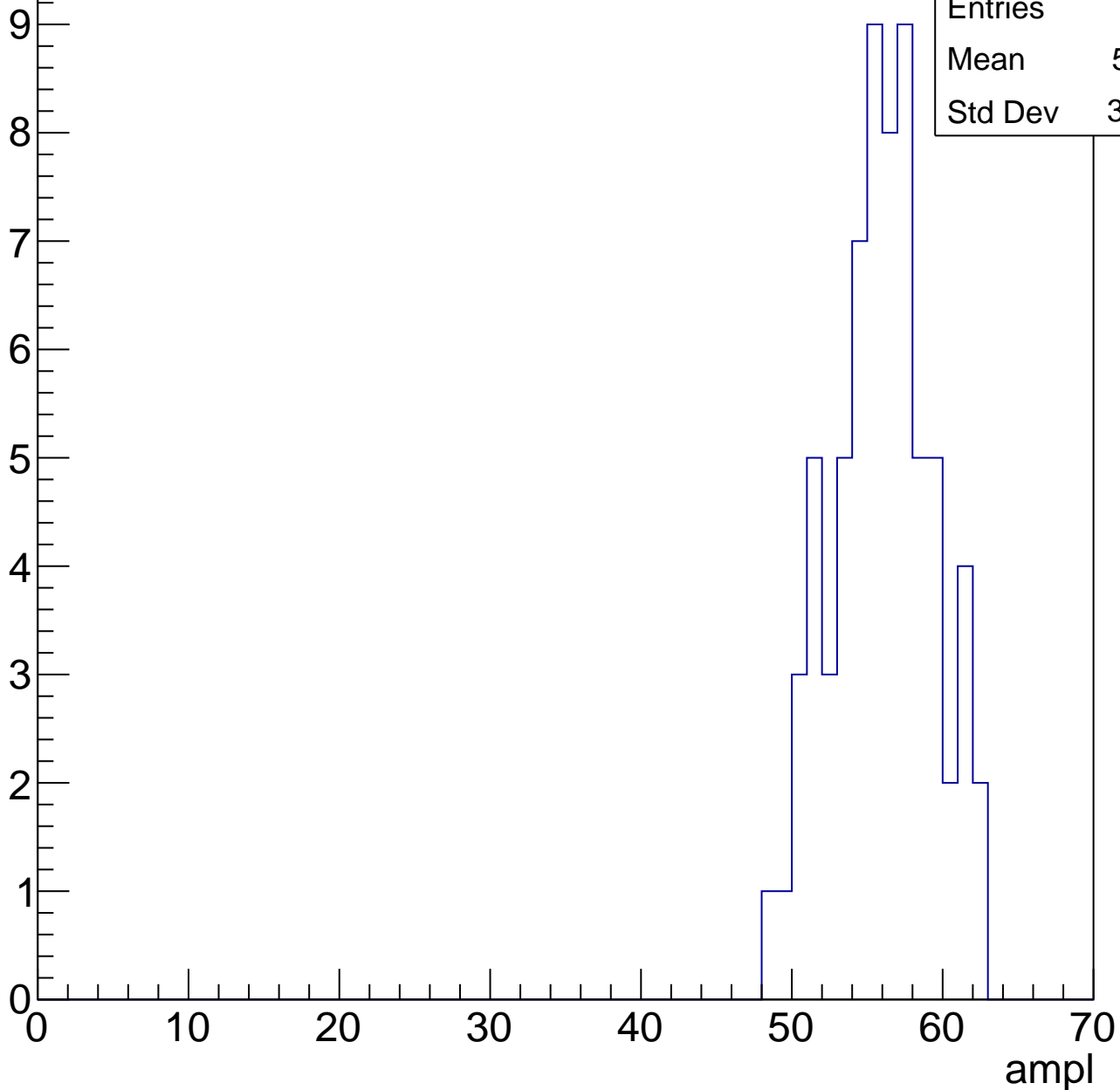


B1L103S, U17-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	55.51
Std Dev	3.269

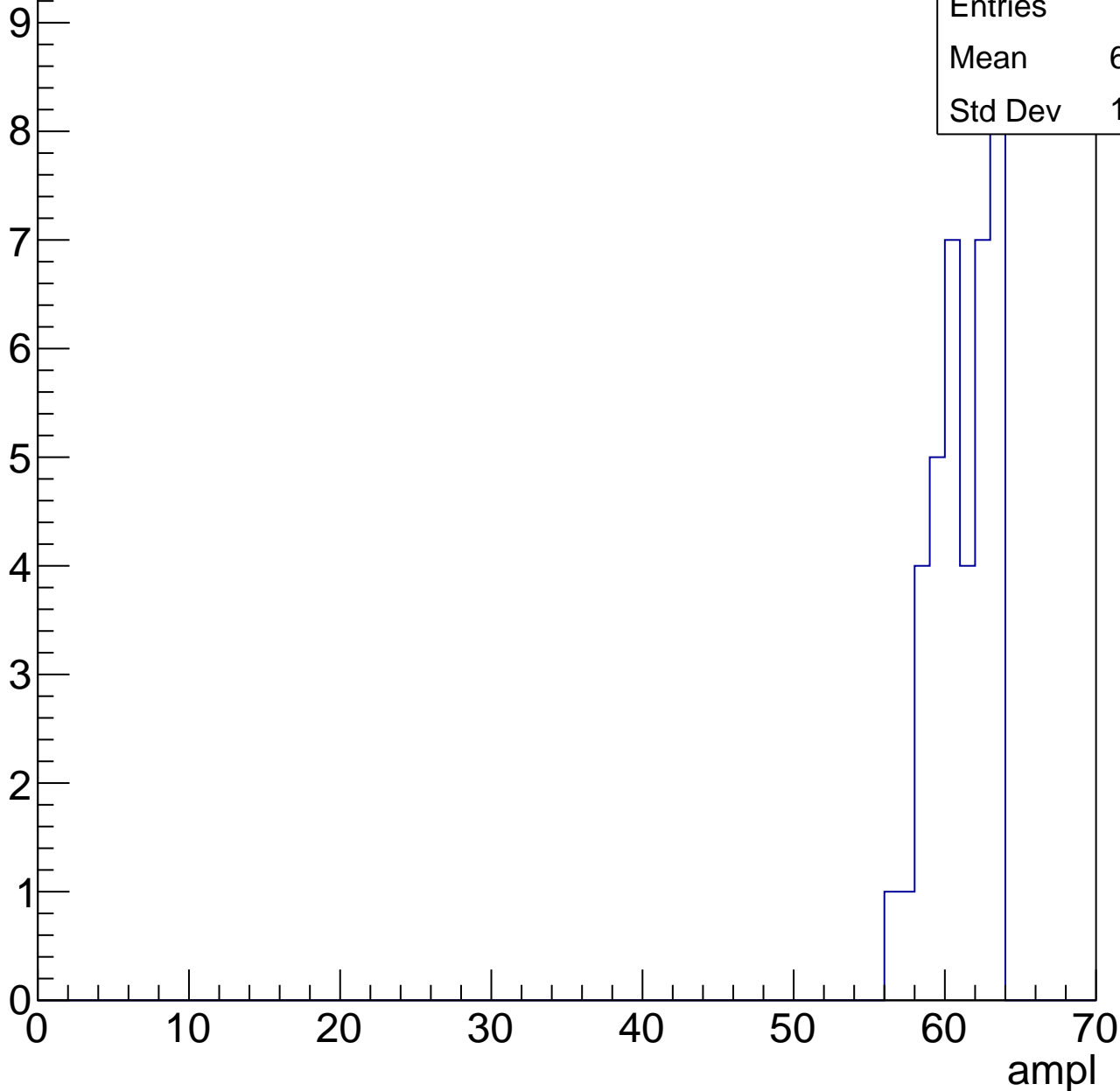


B1L103S, U17-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

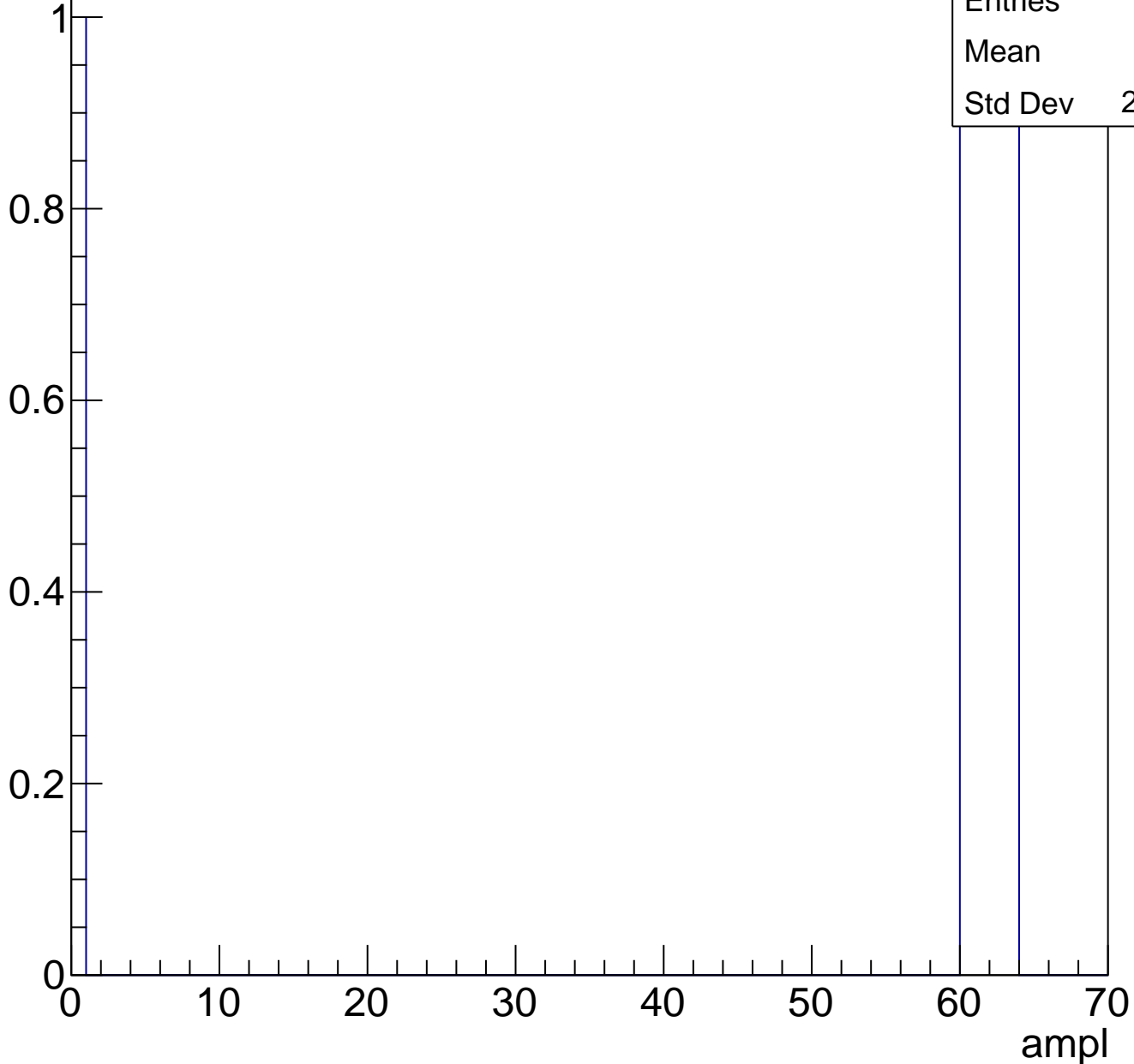
Entries	38
Mean	60.66
Std Dev	1.937



B1L103S, U17-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch11, adc0

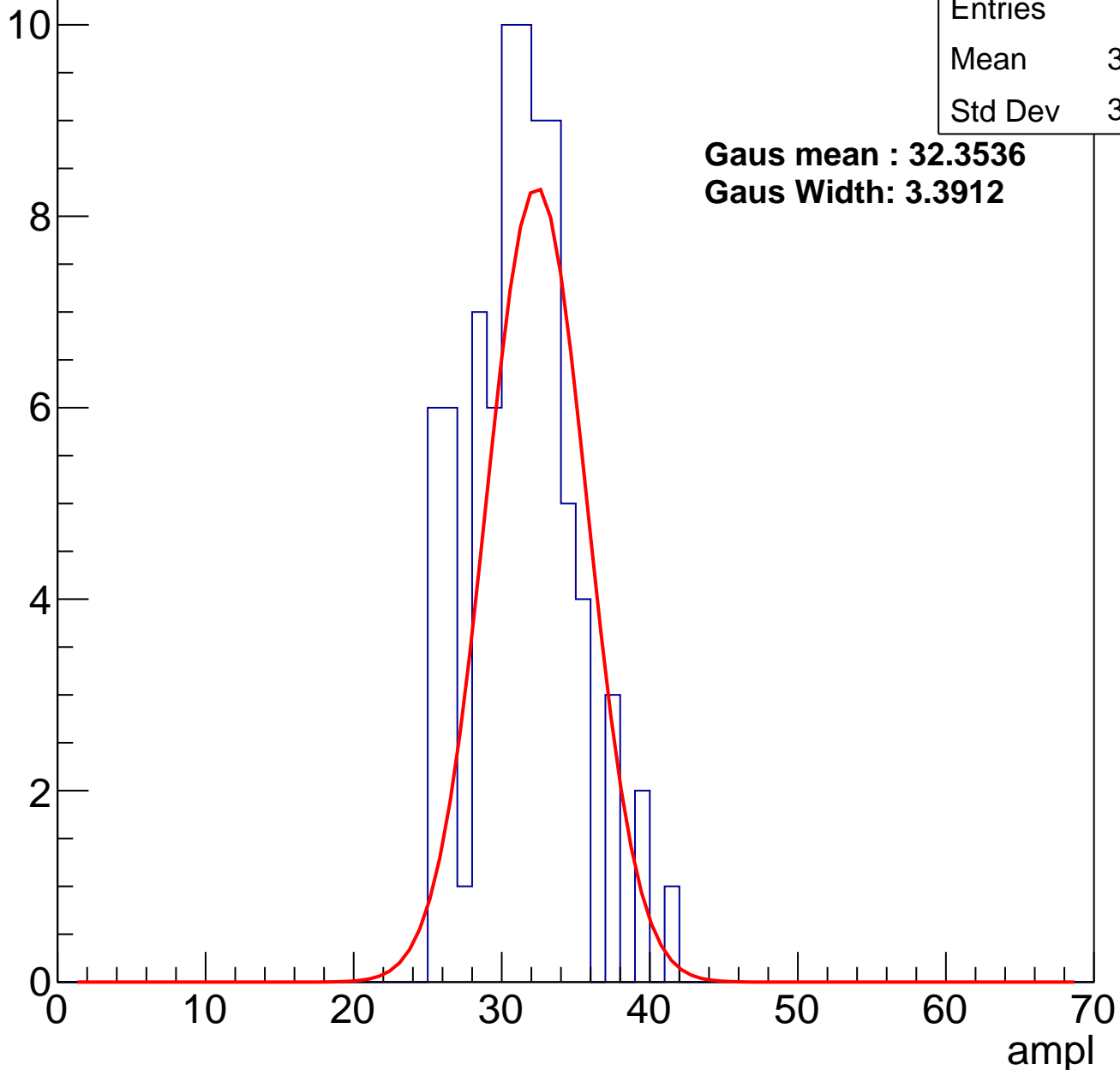
calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	30.86
Std Dev	3.499

Gaus mean : 32.3536

Gaus Width: 3.3912

Entry



B1L103S, U17-ch11, adc1

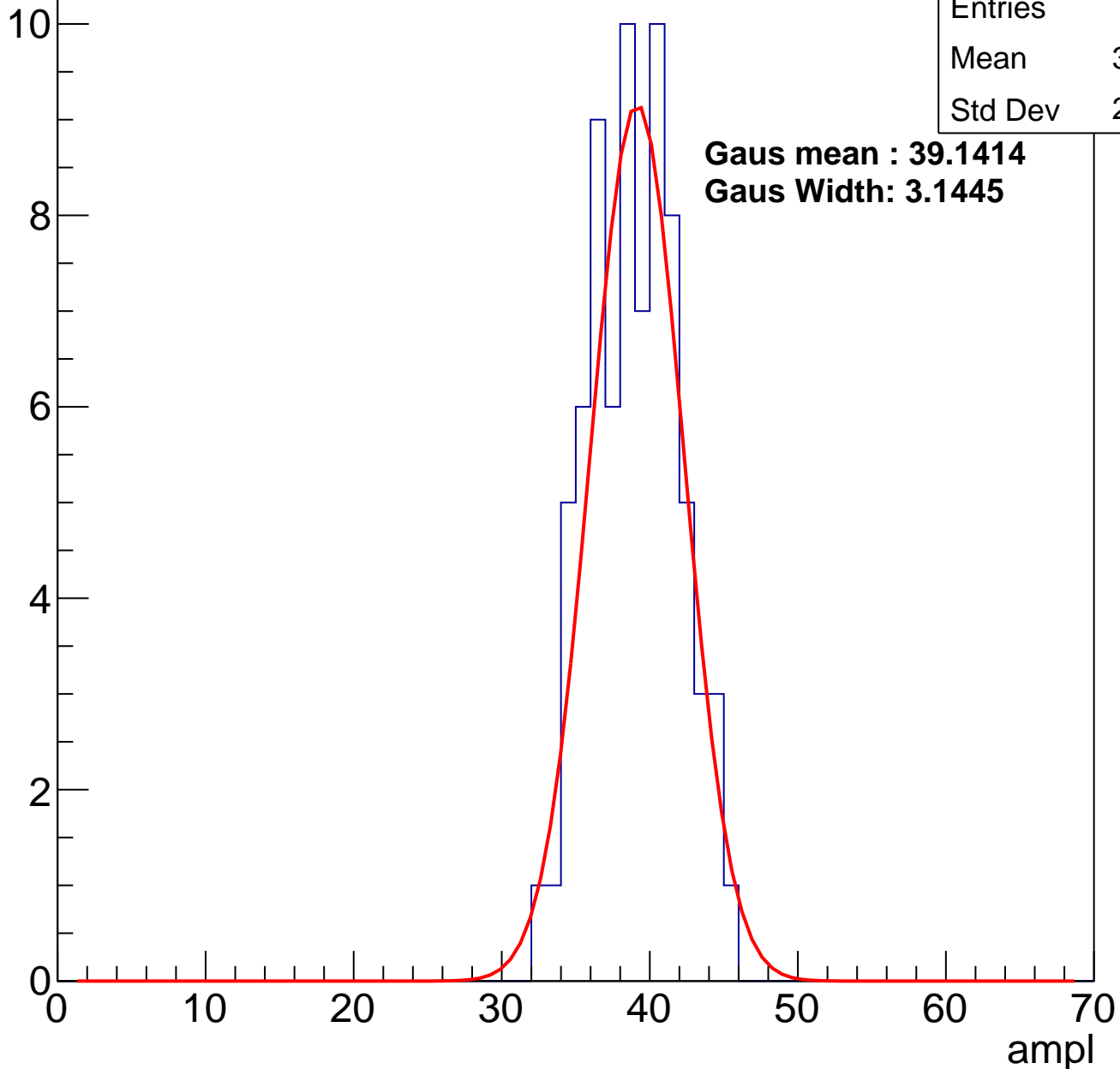
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	38.51
Std Dev	2.941

Gaus mean : 39.1414

Gaus Width: 3.1445

Entry



B1L103S, U17-ch11, adc2

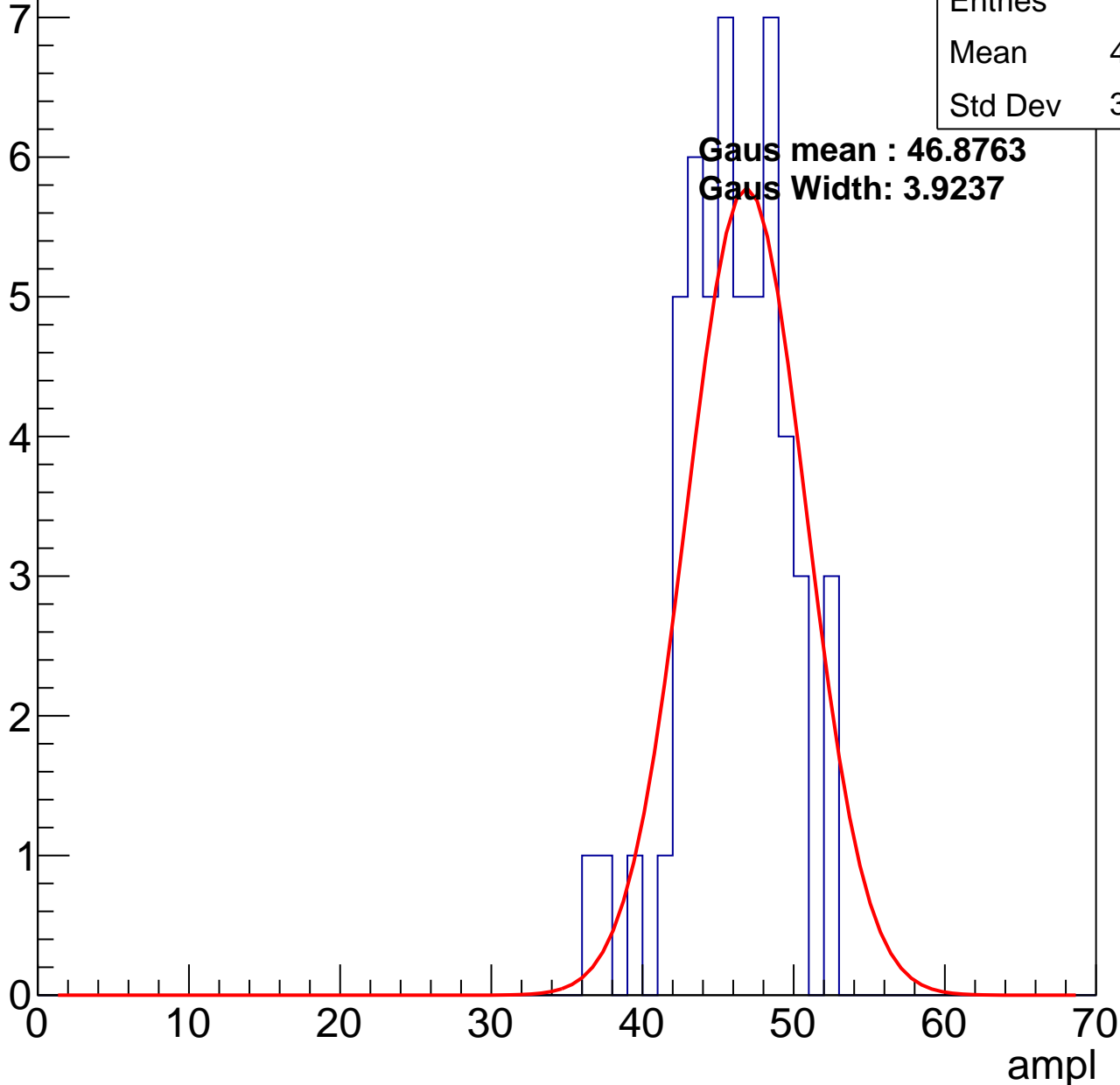
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	45.54
Std Dev	3.414

Gaus mean : 46.8763

Gaus Width: 3.9237



B1L103S, U17-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	51.14
Std Dev	3.803

Entry

10

8

6

4

2

0

0

10

20

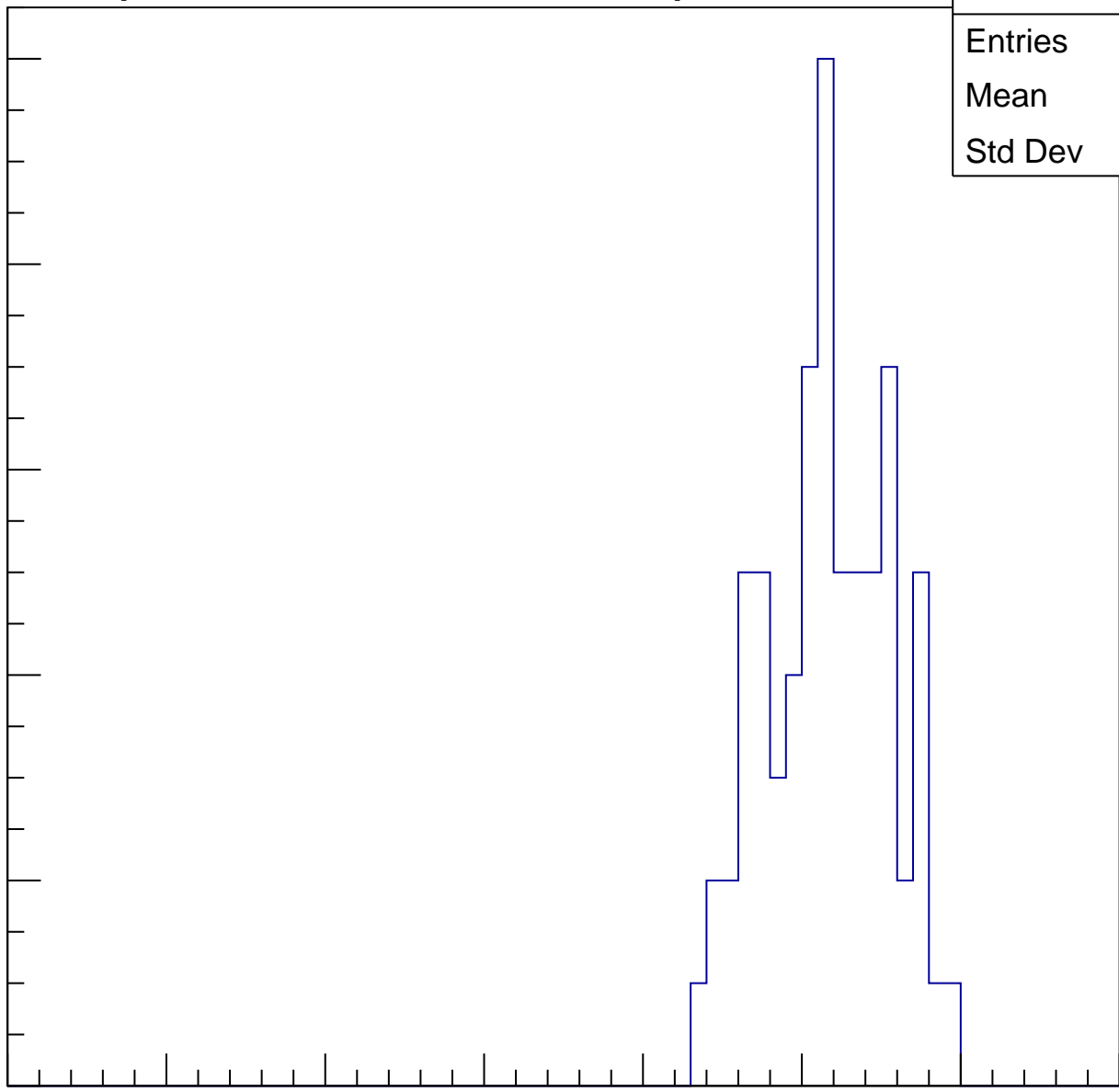
30

40

50

60

ampl

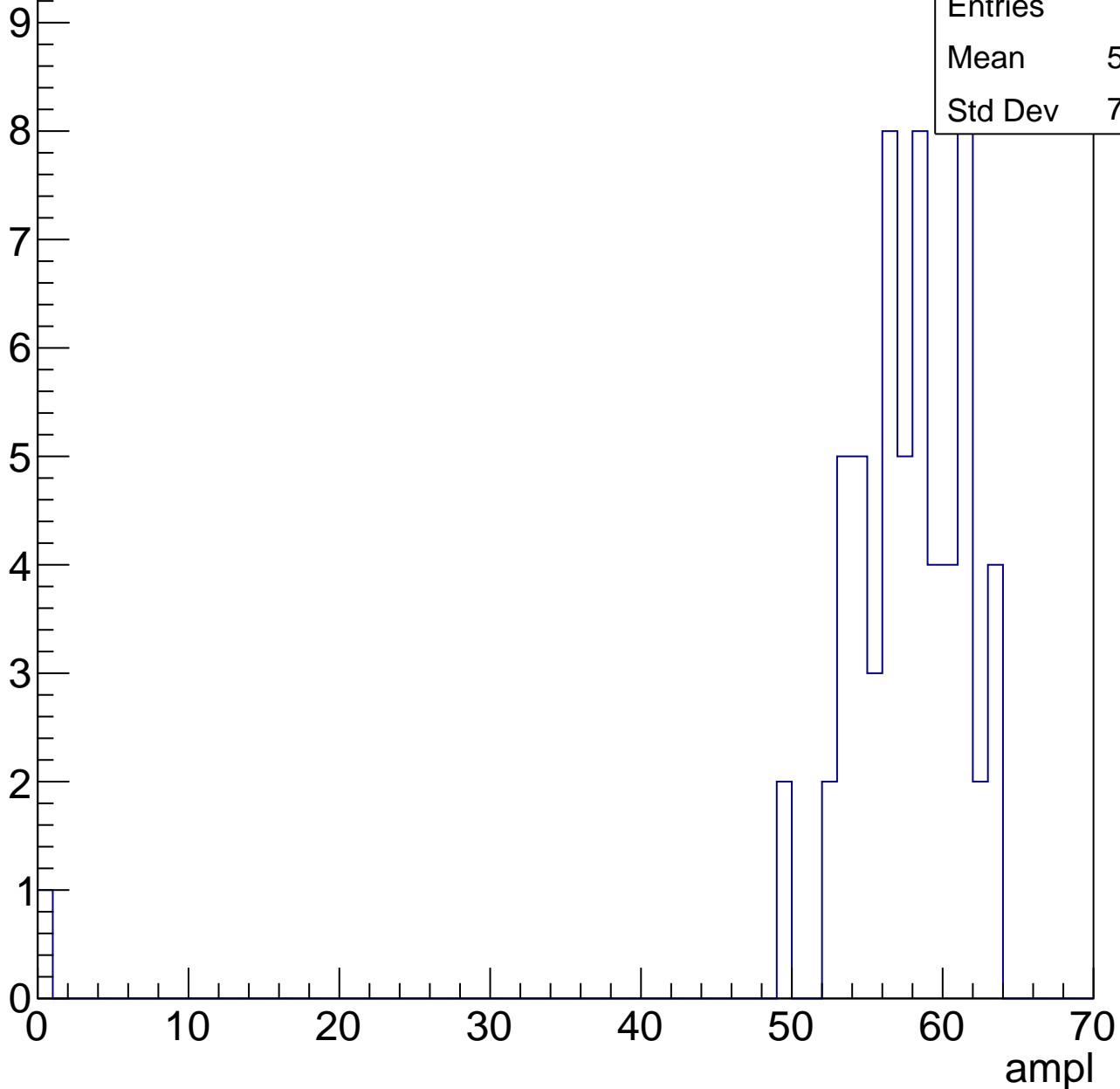


B1L103S, U17-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.45
Std Dev	7.983

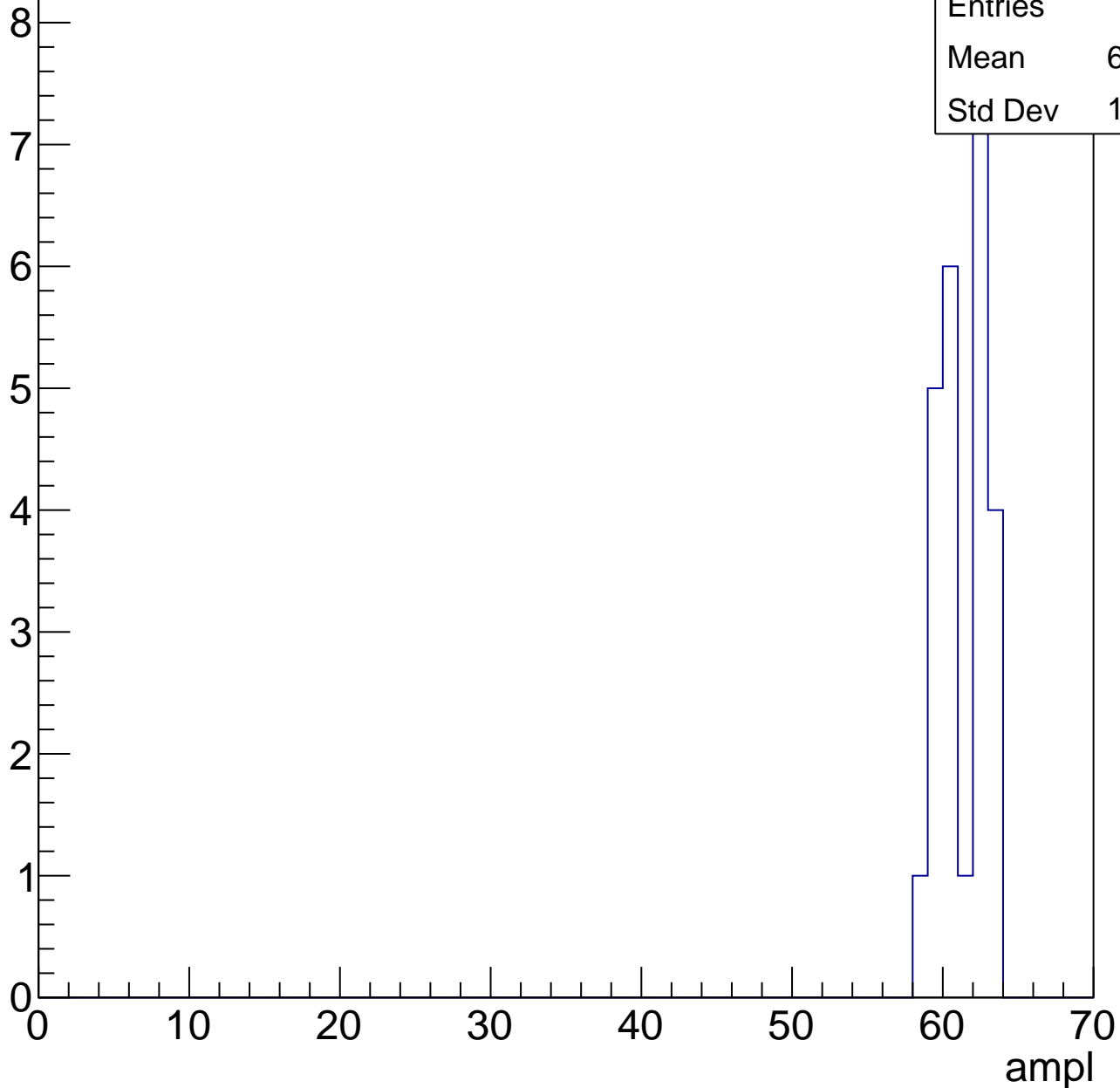


B1L103S, U17-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	60.88
Std Dev	1.532



B1L103S, U17-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch12, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	30.37
Std Dev	6.506

Gaus mean : 31.5931

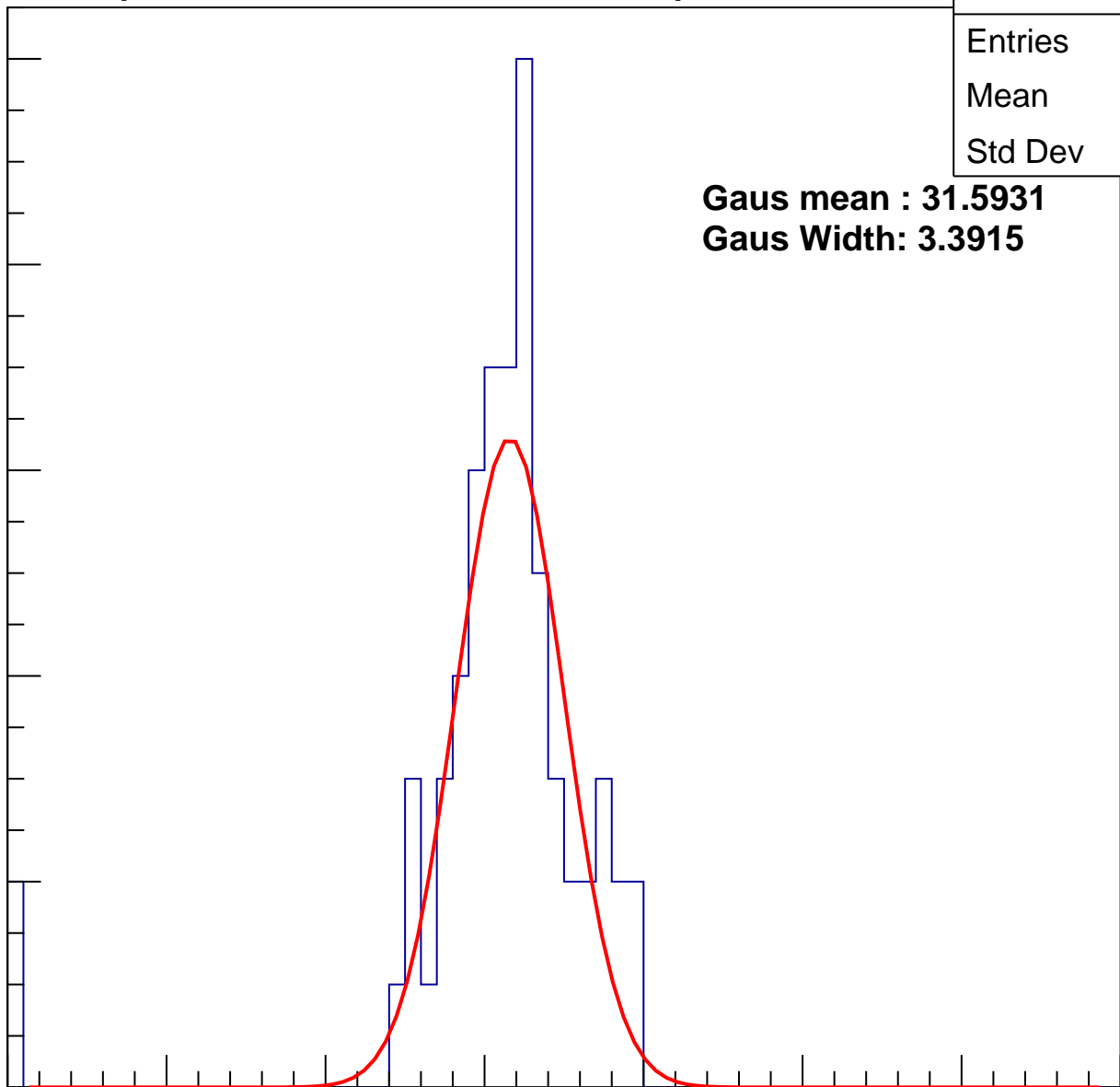
Gaus Width: 3.3915

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch12, adc1

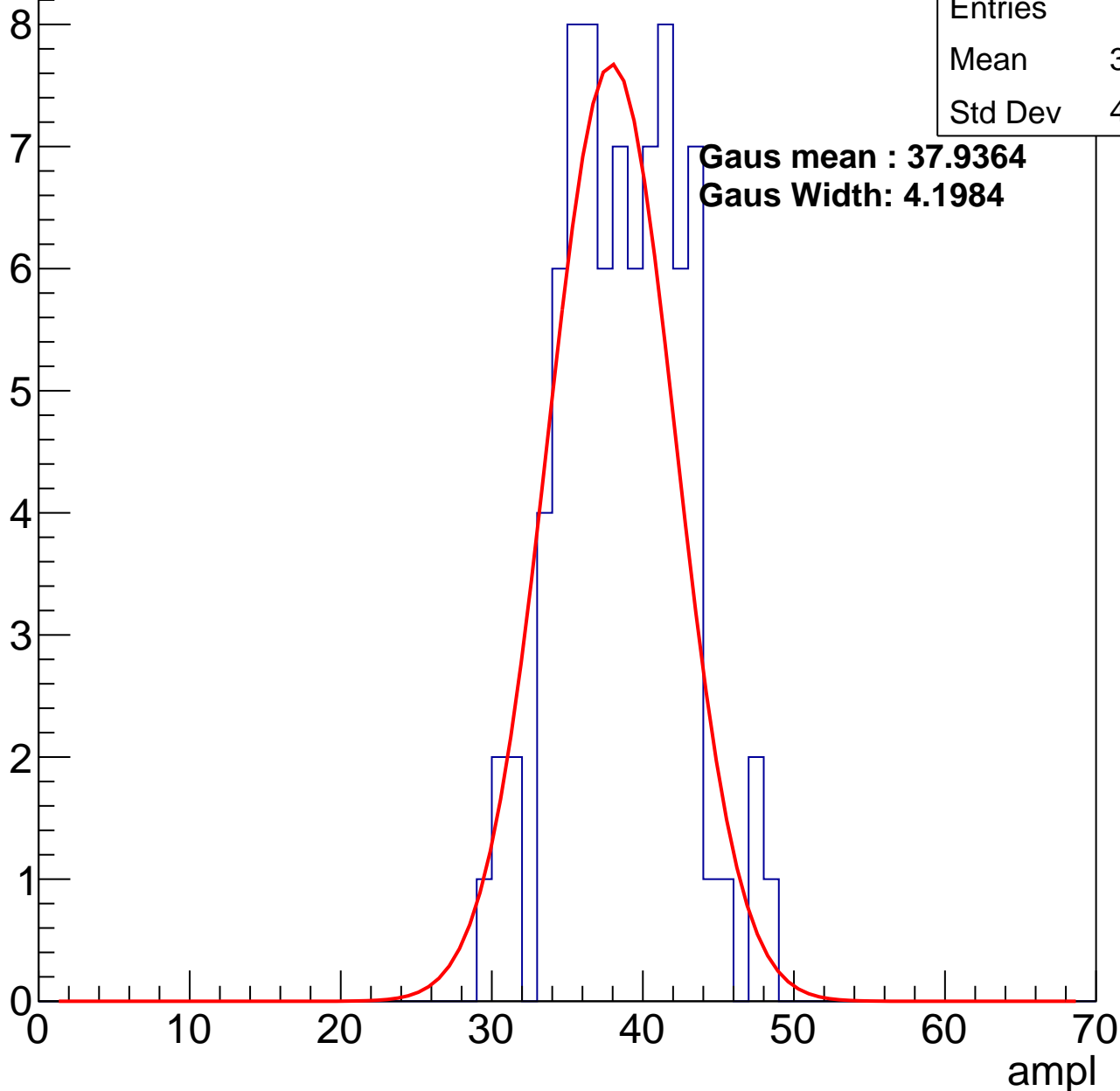
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	38.18
Std Dev	4.009

Gaus mean : 37.9364

Gaus Width: 4.1984



B1L103S, U17-ch12, adc2

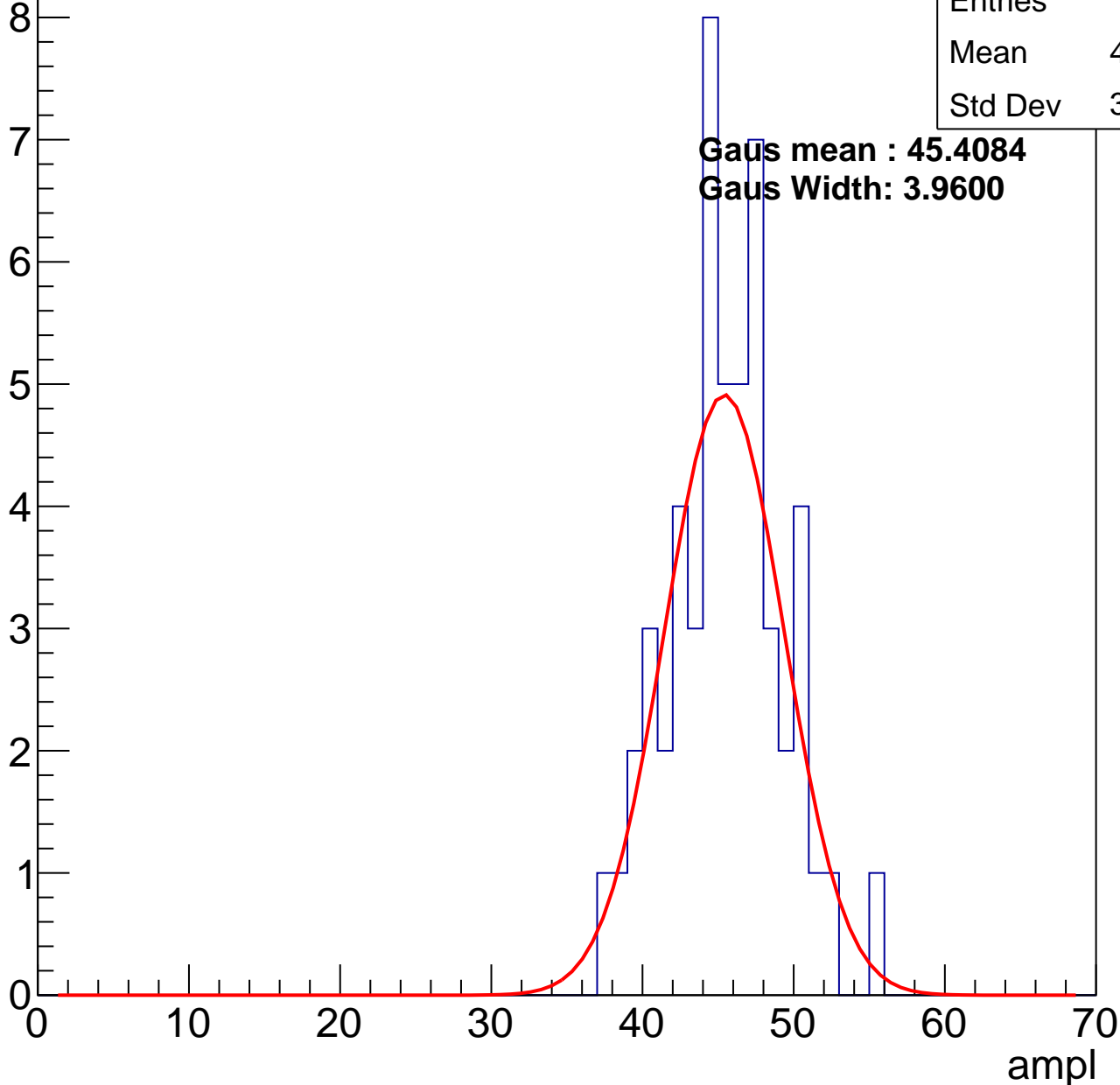
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	45.06
Std Dev	3.698

Gaus mean : 45.4084

Gaus Width: 3.9600

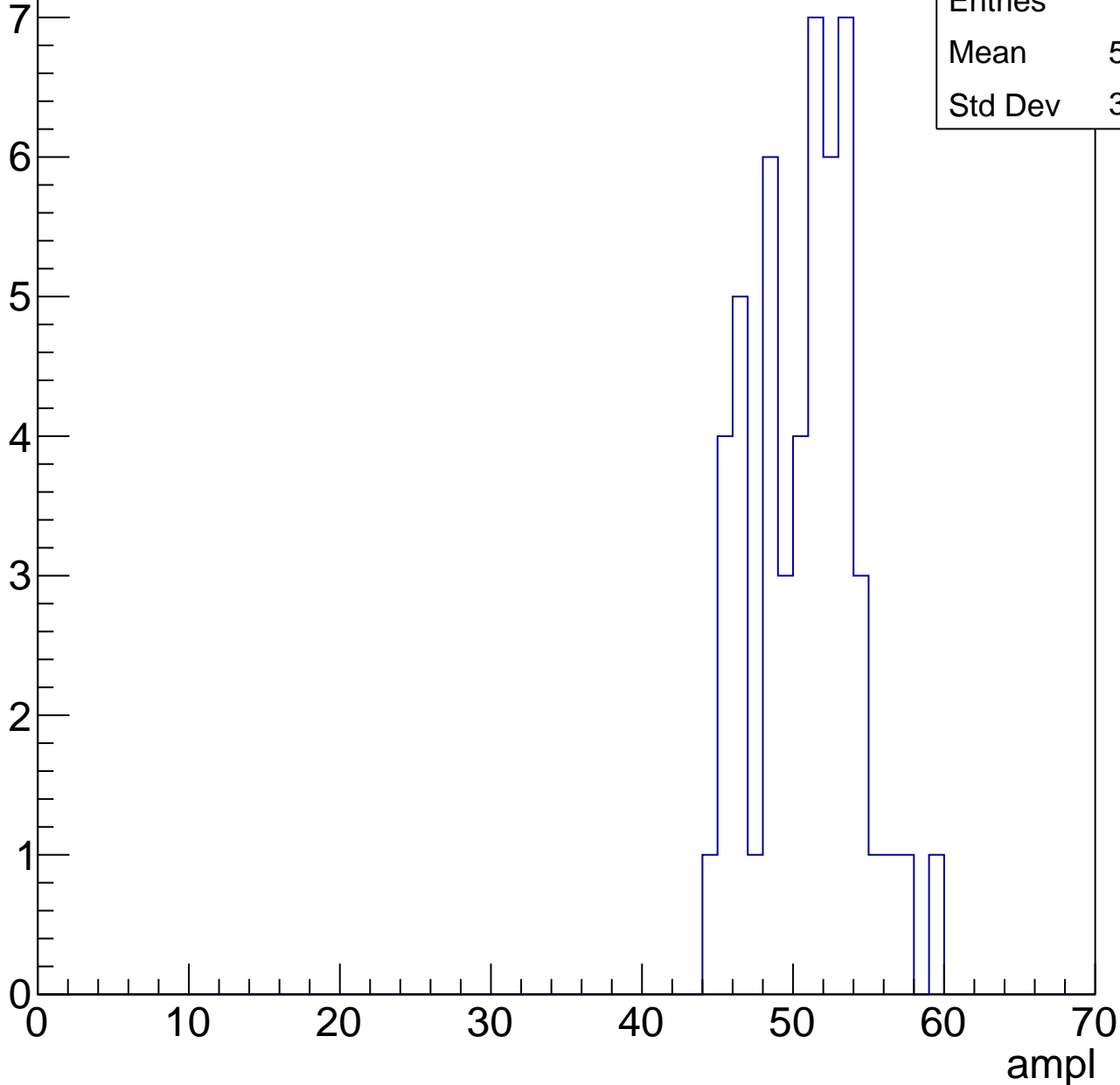


B1L103S, U17-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	50.29
Std Dev	3.374

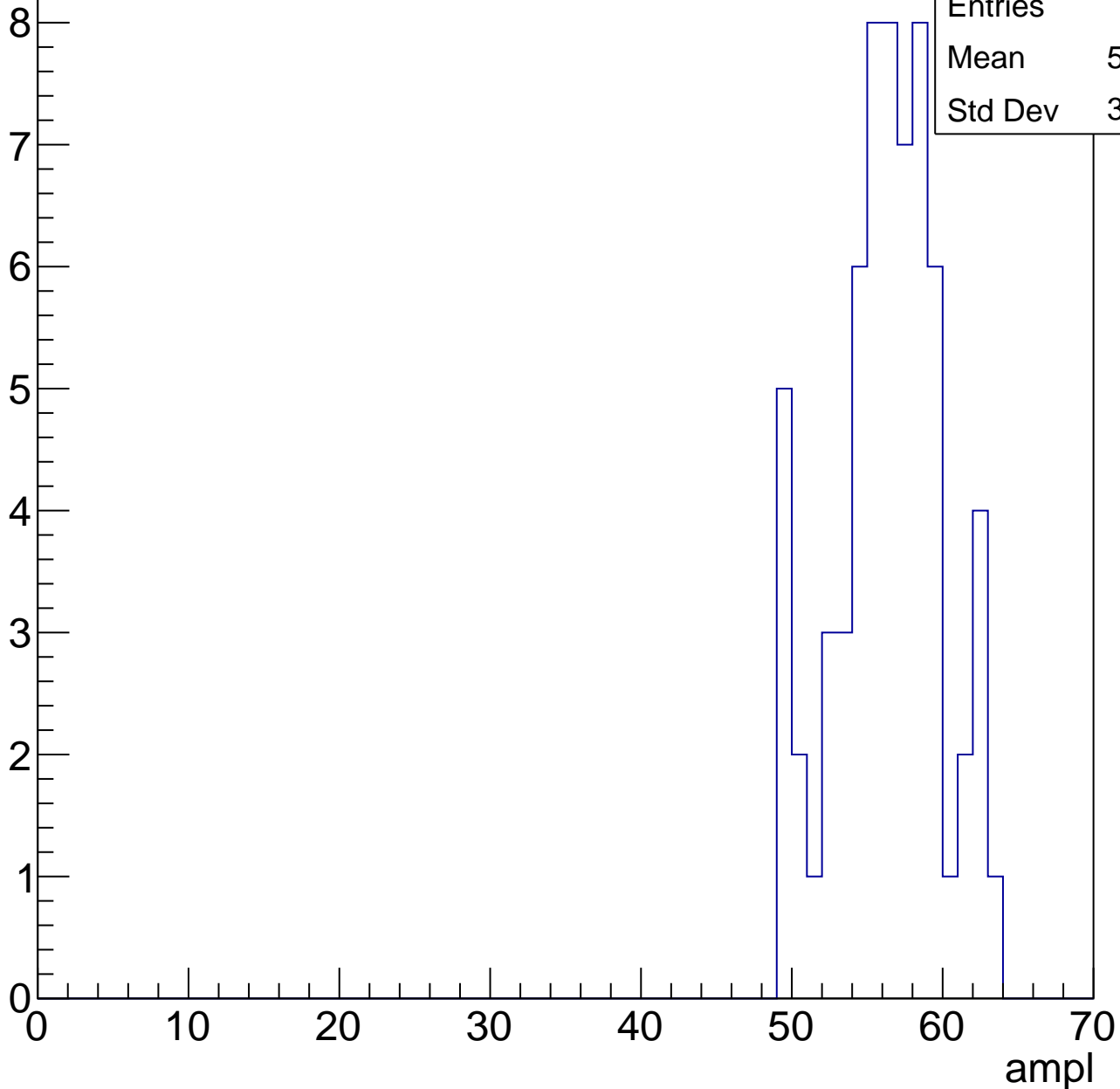


B1L103S, U17-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.89
Std Dev	3.509

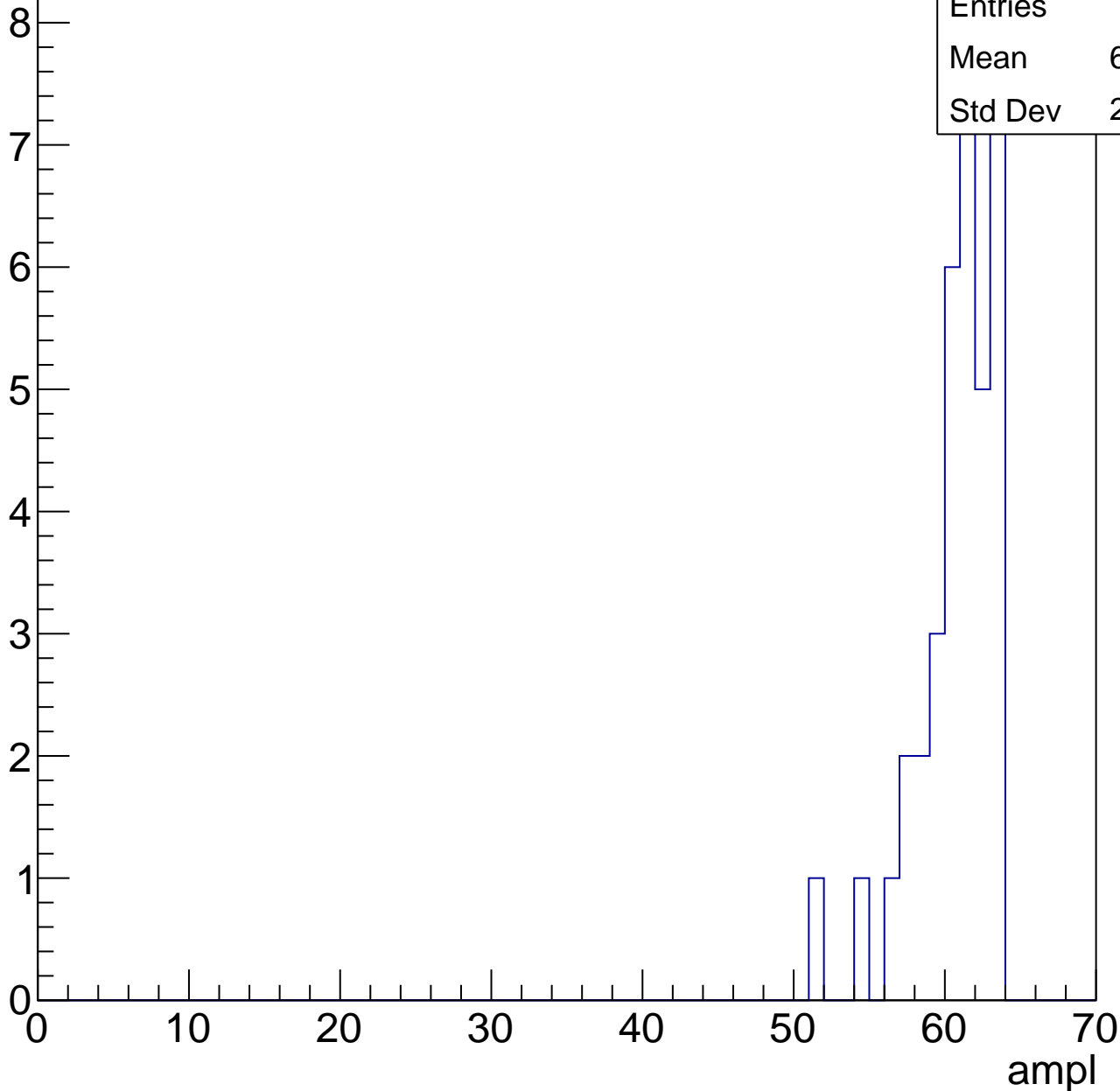


B1L103S, U17-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

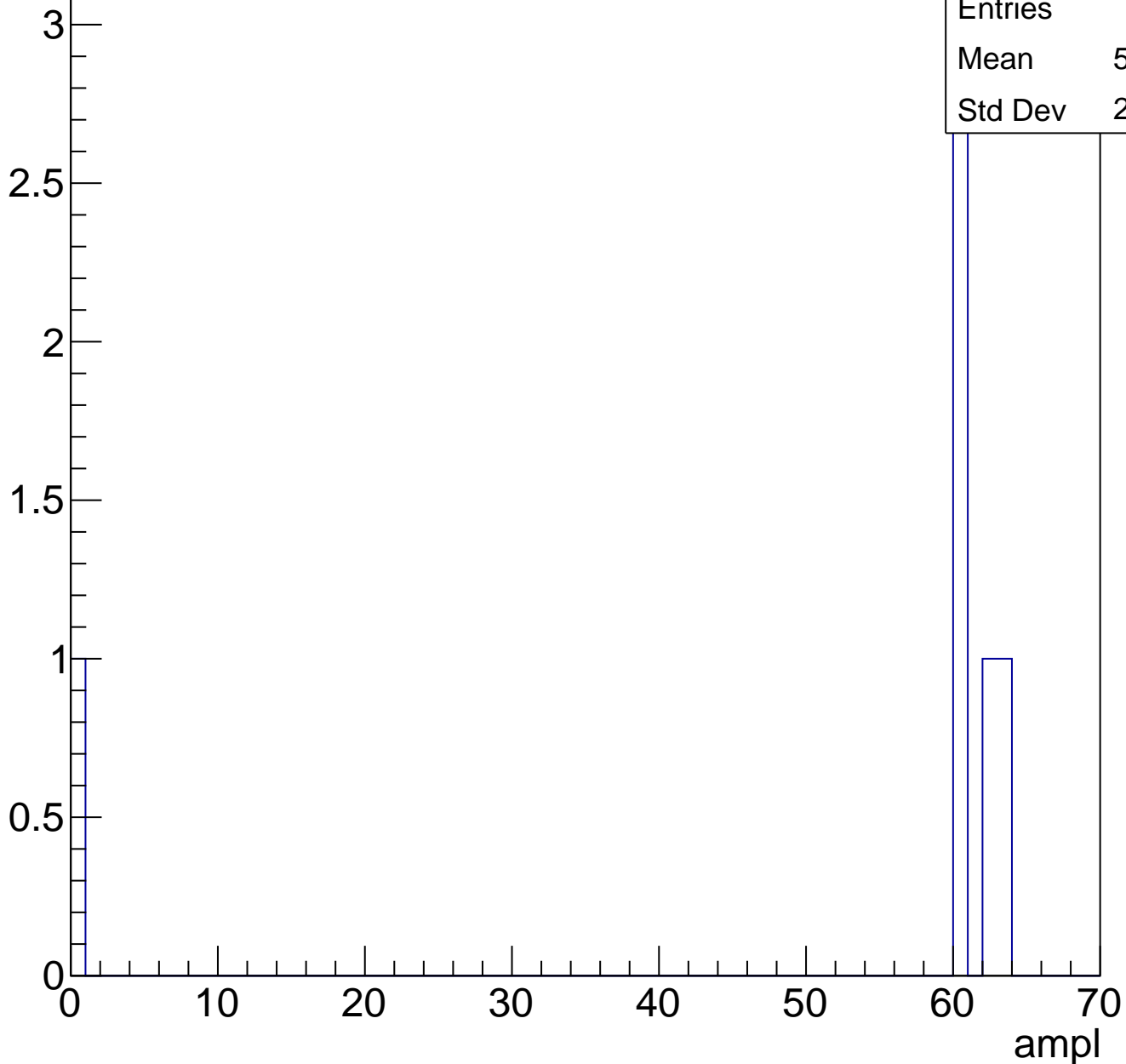
Entries	37
Mean	60.27
Std Dev	2.647



B1L103S, U17-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	50.83
Std Dev	22.76

B1L103S, U17-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch13, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	29.12
Std Dev	4.901

Gaus mean : 29.6884

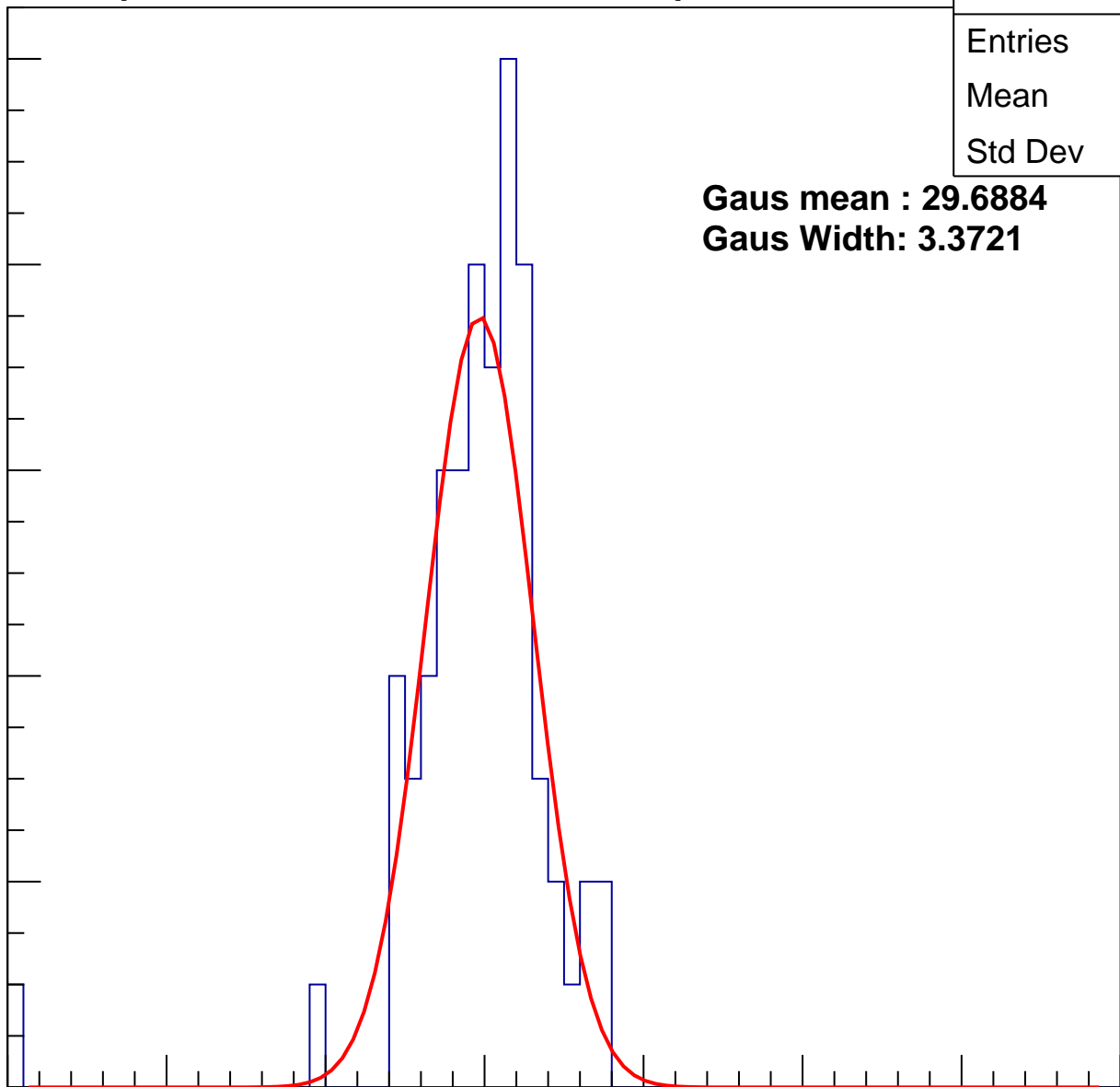
Gaus Width: 3.3721

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch13, adc1

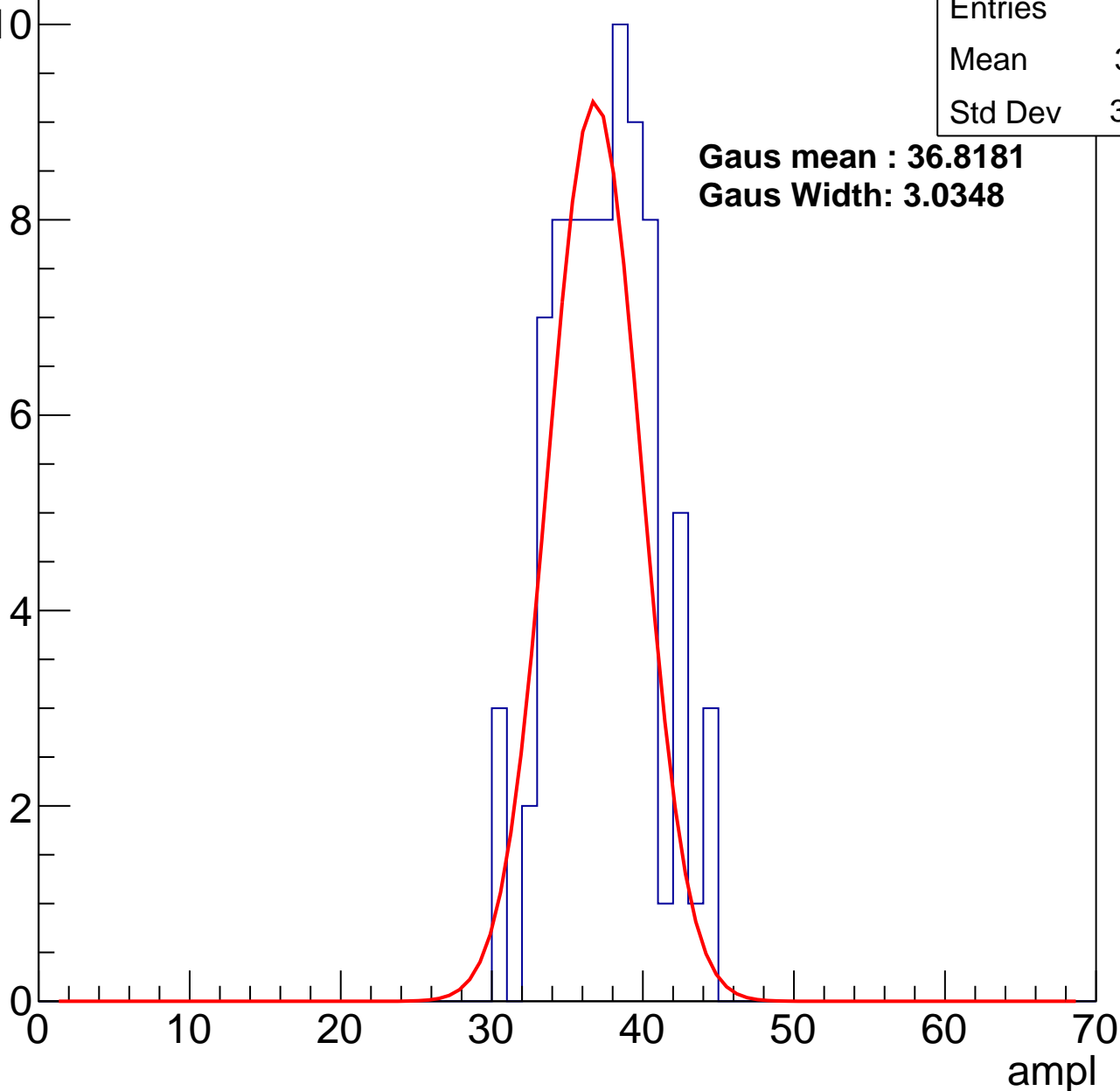
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	37.01
Std Dev	3.264

Gaus mean : 36.8181

Gaus Width: 3.0348



B1L103S, U17-ch13, adc2

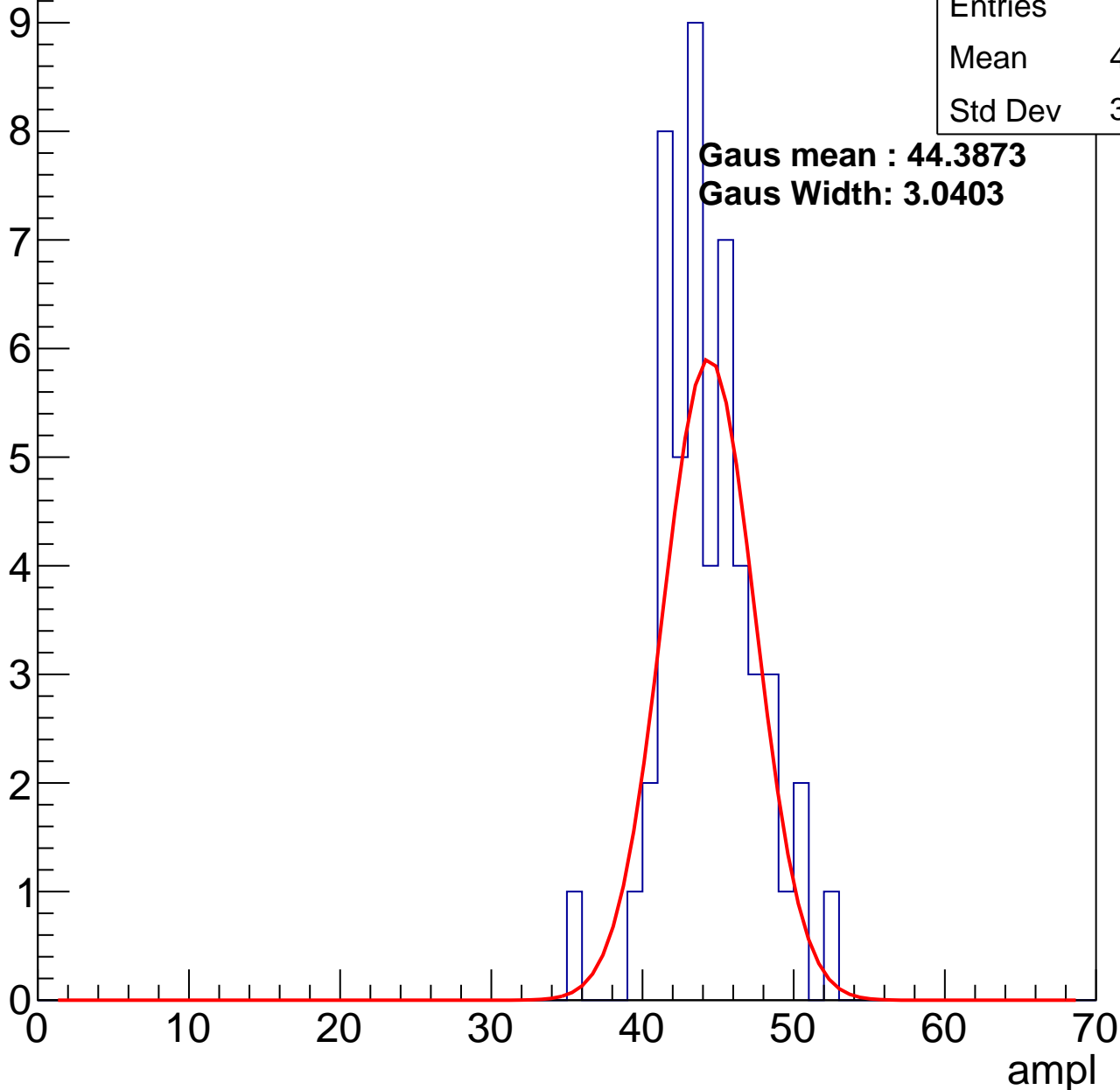
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	43.92
Std Dev	3.124

Gaus mean : 44.3873

Gaus Width: 3.0403

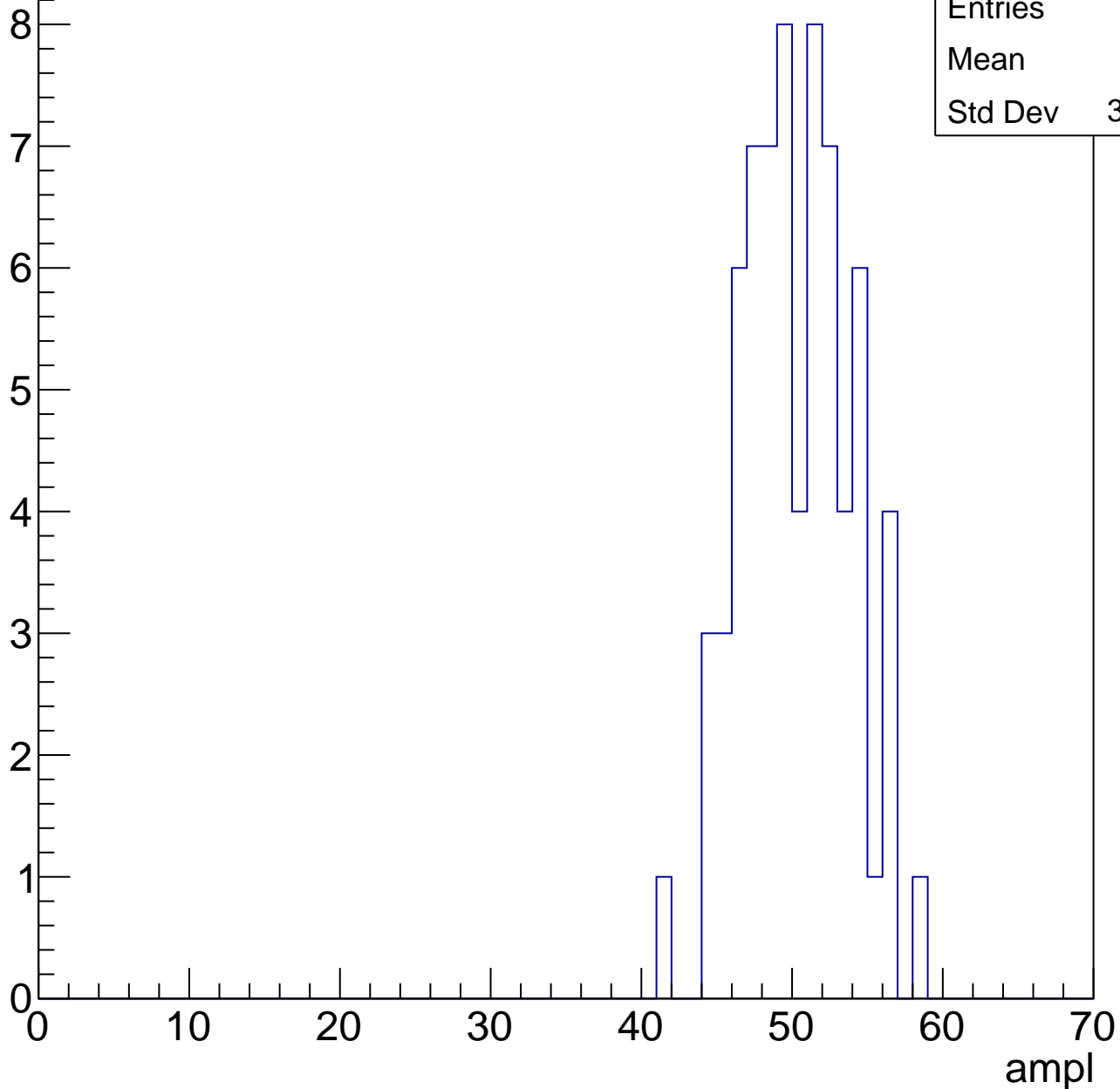


B1L103S, U17-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.8
Std Dev	3.499

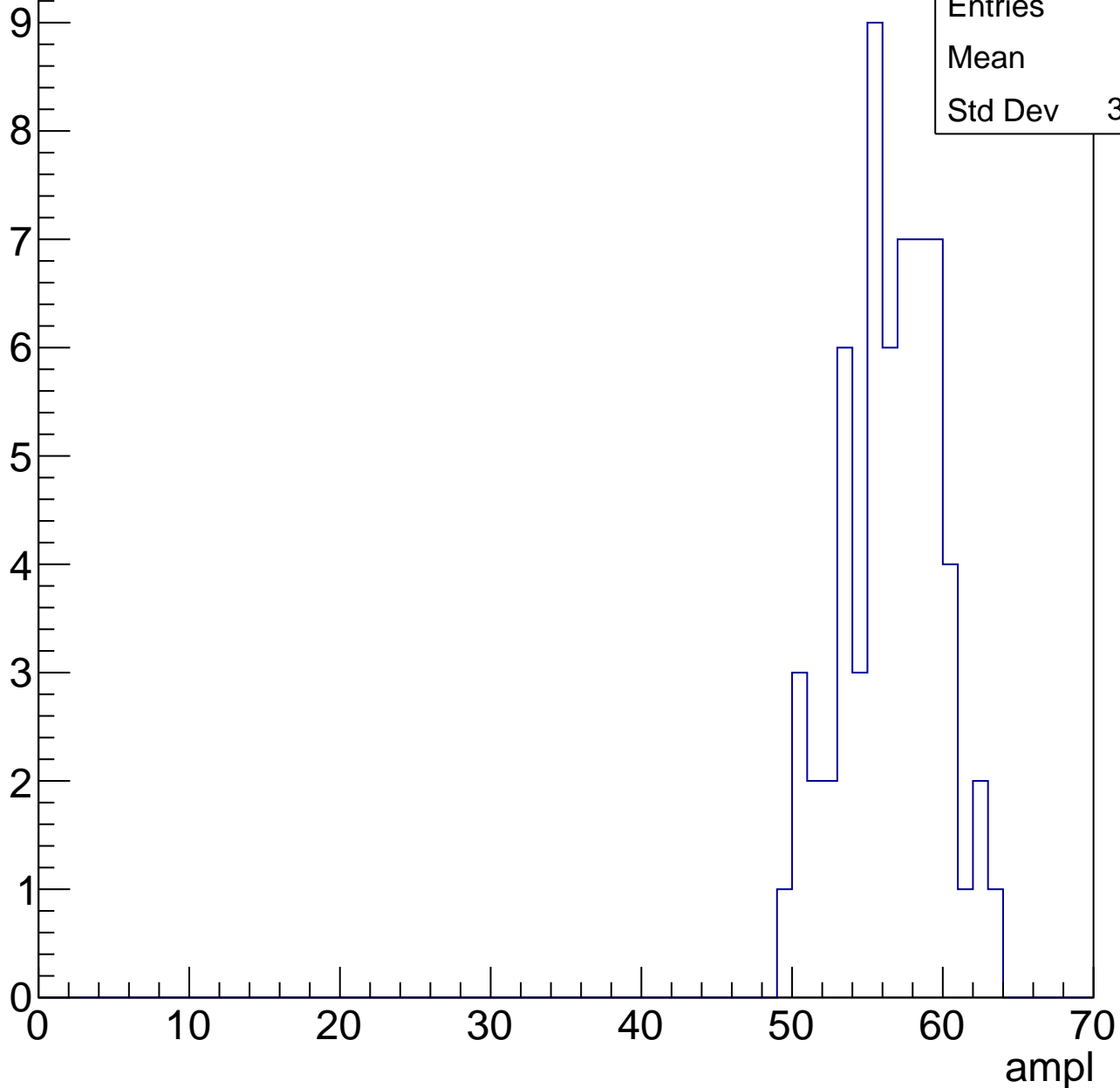


B1L103S, U17-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	56.1
Std Dev	3.192

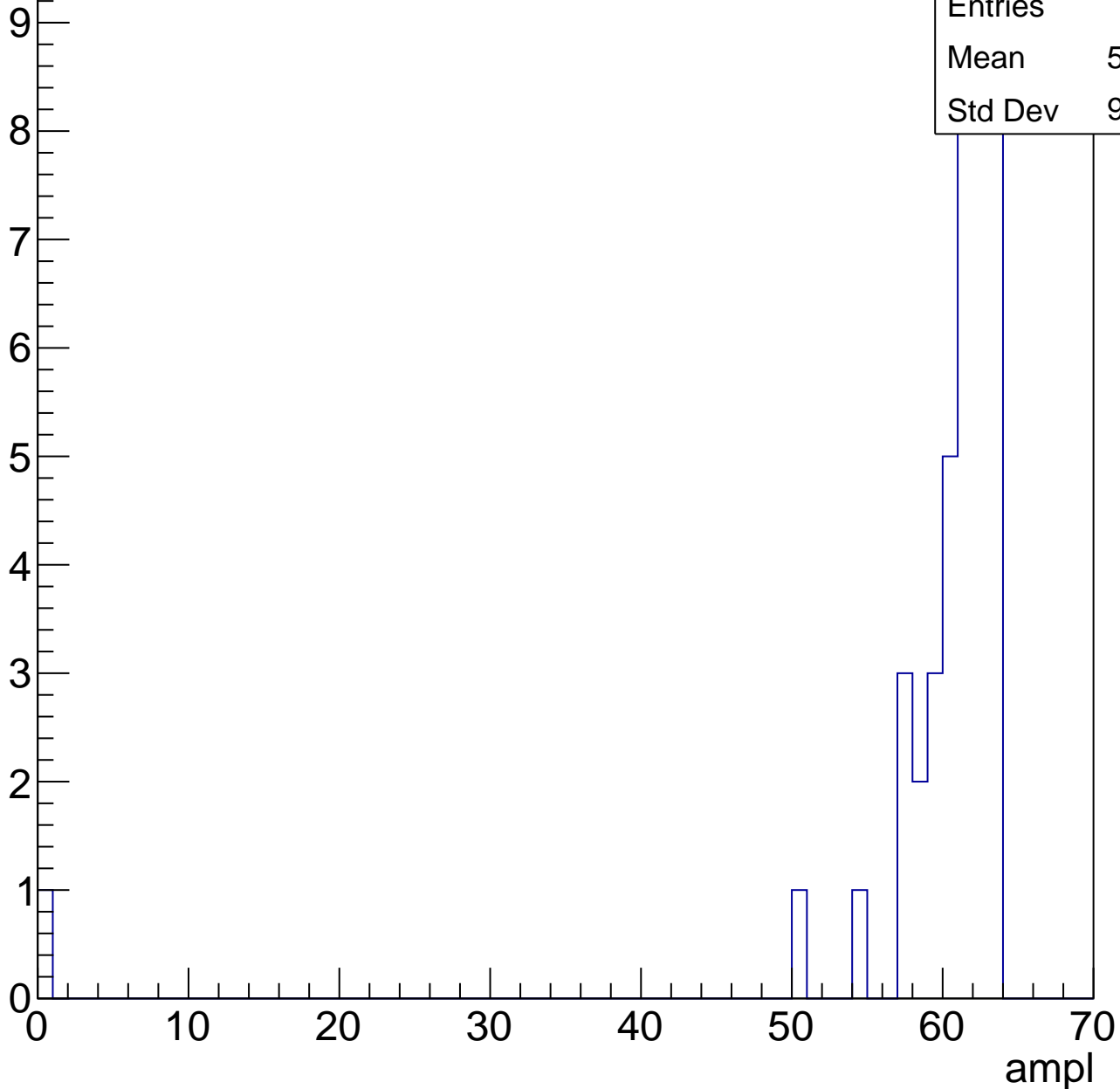


B1L103S, U17-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.05
Std Dev	9.584



B1L103S, U17-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch14, adc0

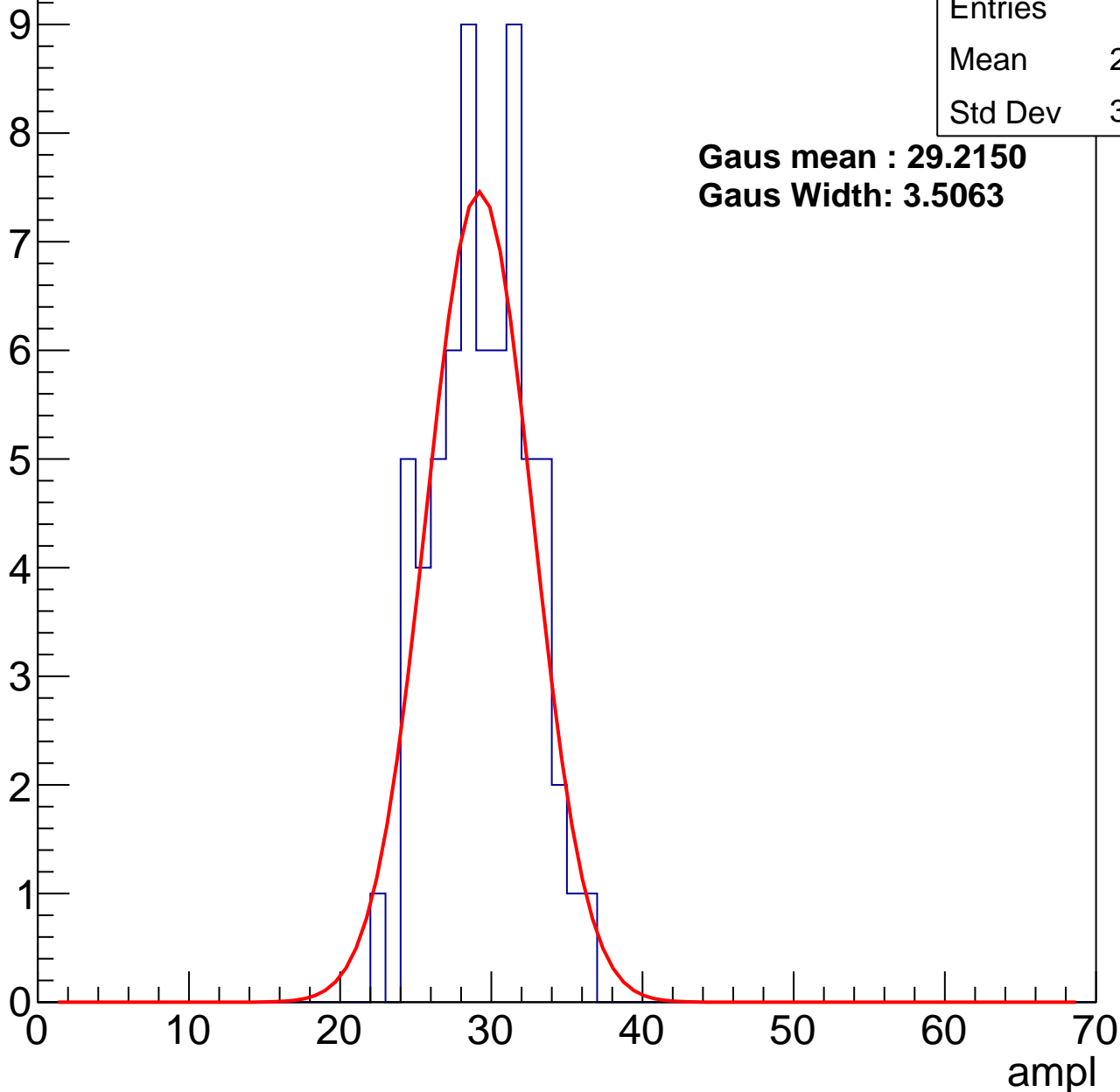
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.97
Std Dev	3.083

Gaus mean : 29.2150

Gaus Width: 3.5063



B1L103S, U17-ch14, adc1

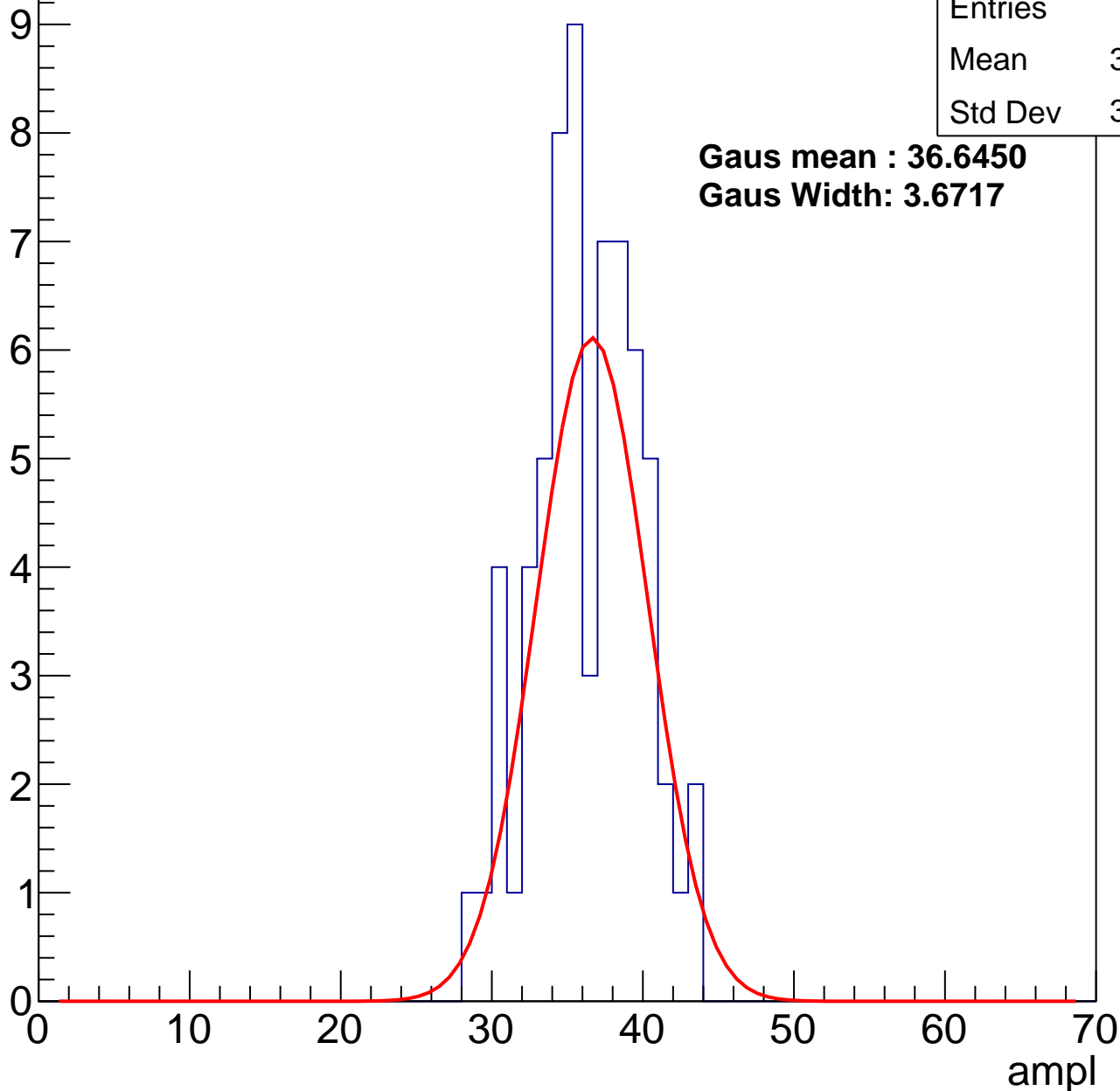
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.83
Std Dev	3.445

Gaus mean : 36.6450

Gaus Width: 3.6717



B1L103S, U17-ch14, adc2

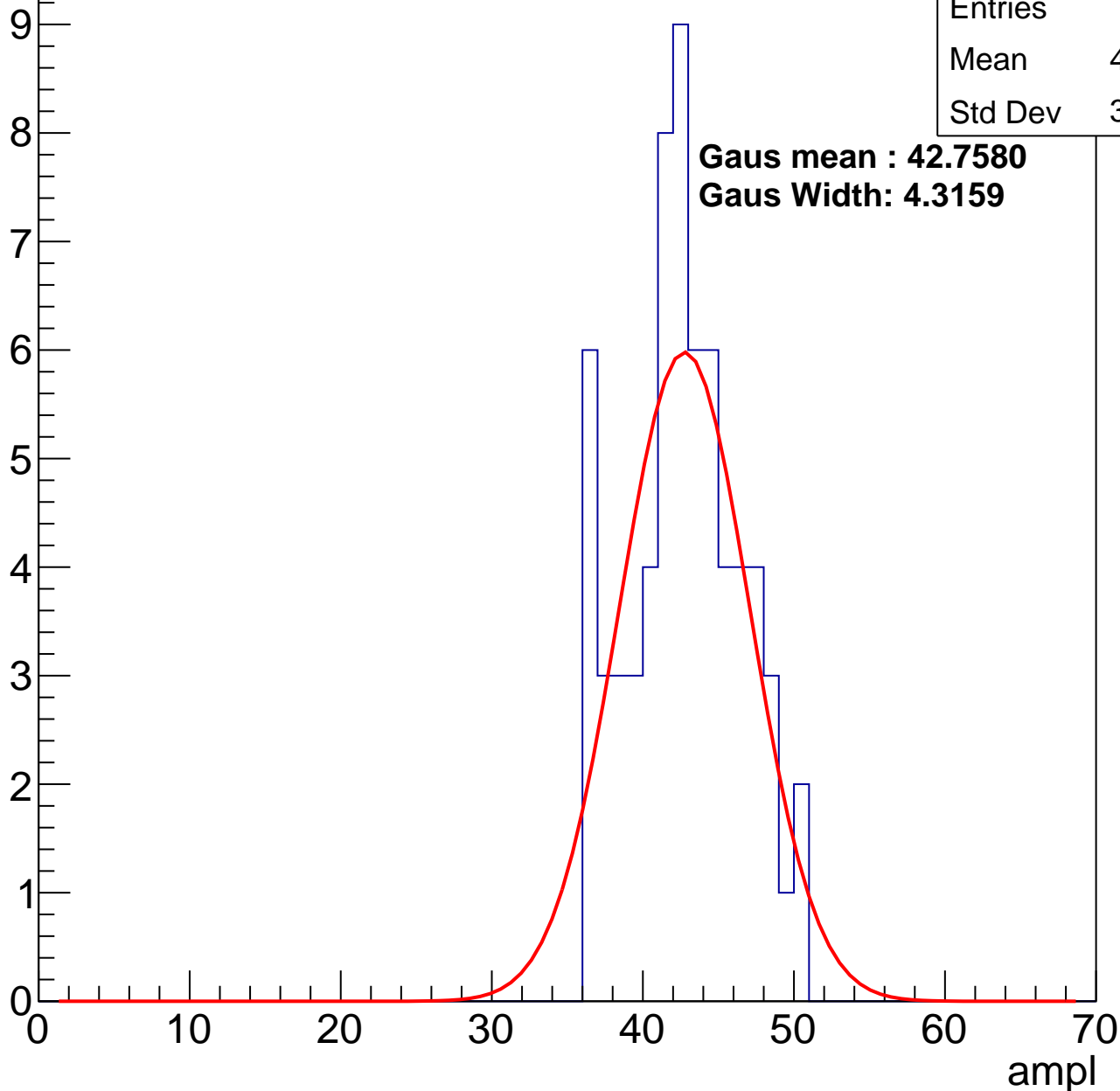
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.29
Std Dev	3.692

Gaus mean : 42.7580

Gaus Width: 4.3159

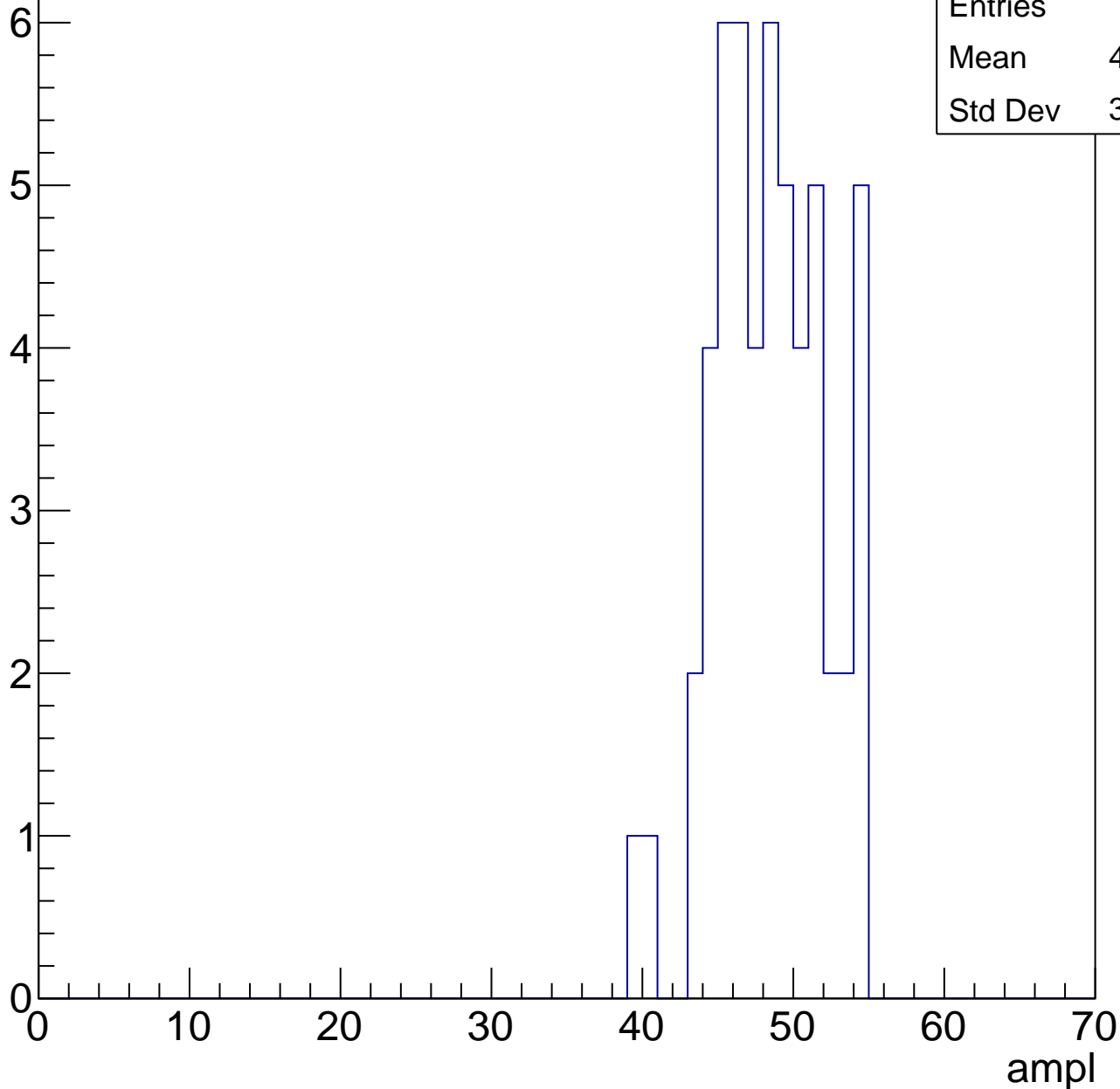


B1L103S, U17-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	47.98
Std Dev	3.553

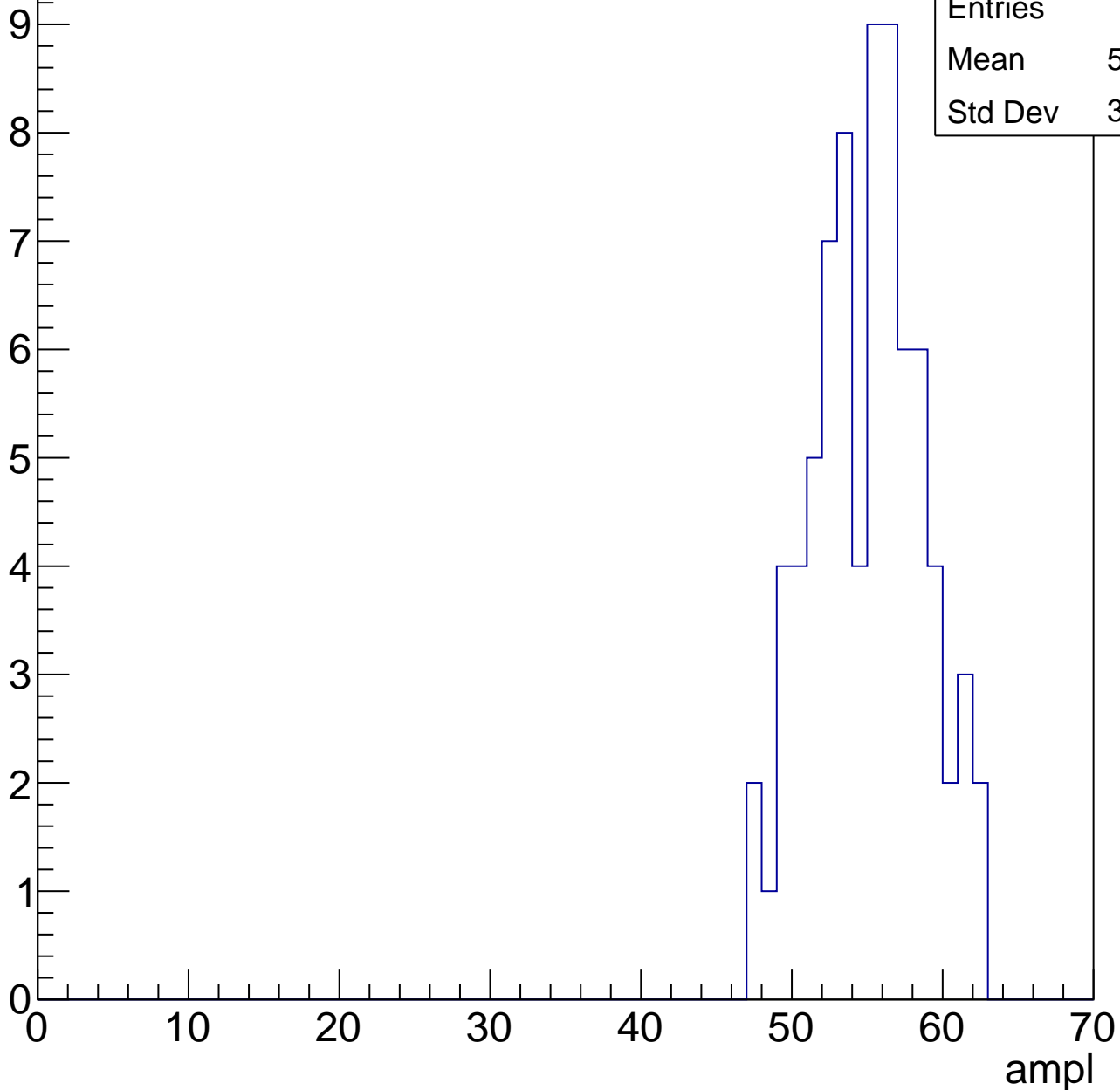


B1L103S, U17-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	54.59
Std Dev	3.617

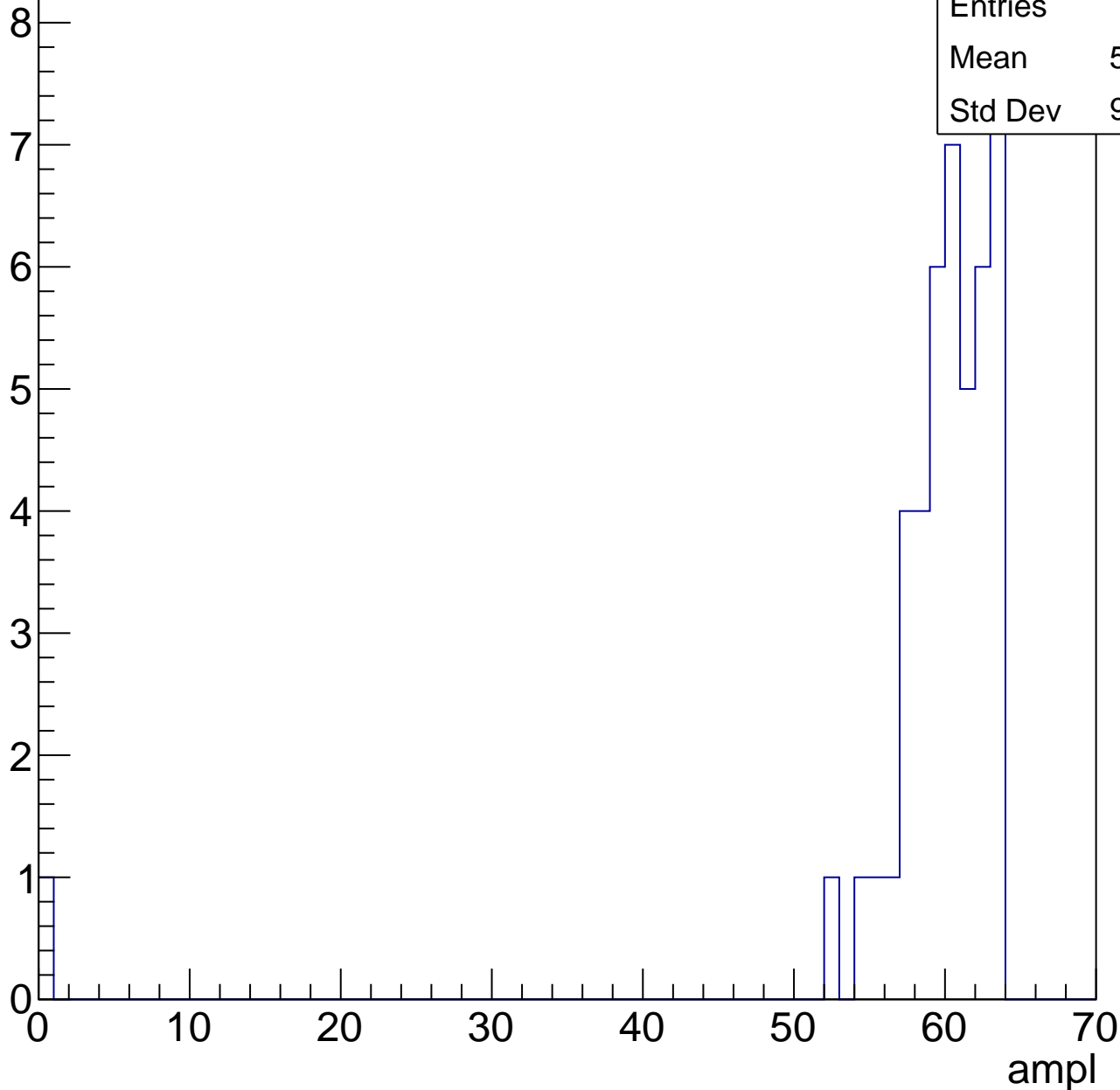


B1L103S, U17-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.49
Std Dev	9.186



B1L103S, U17-ch14, adc6

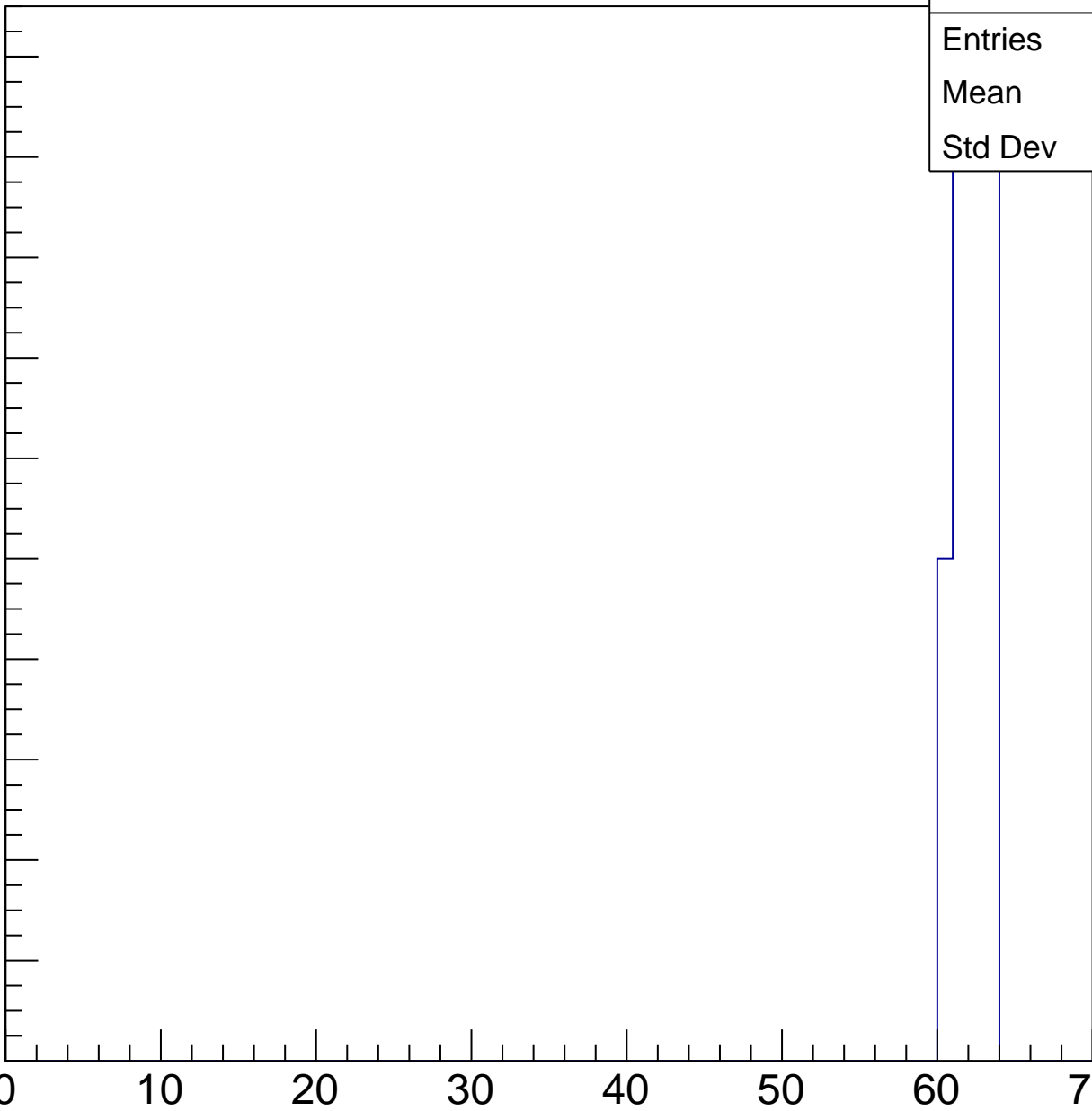
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.71
Std Dev	1.03

ampl



B1L103S, U17-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch15, adc0

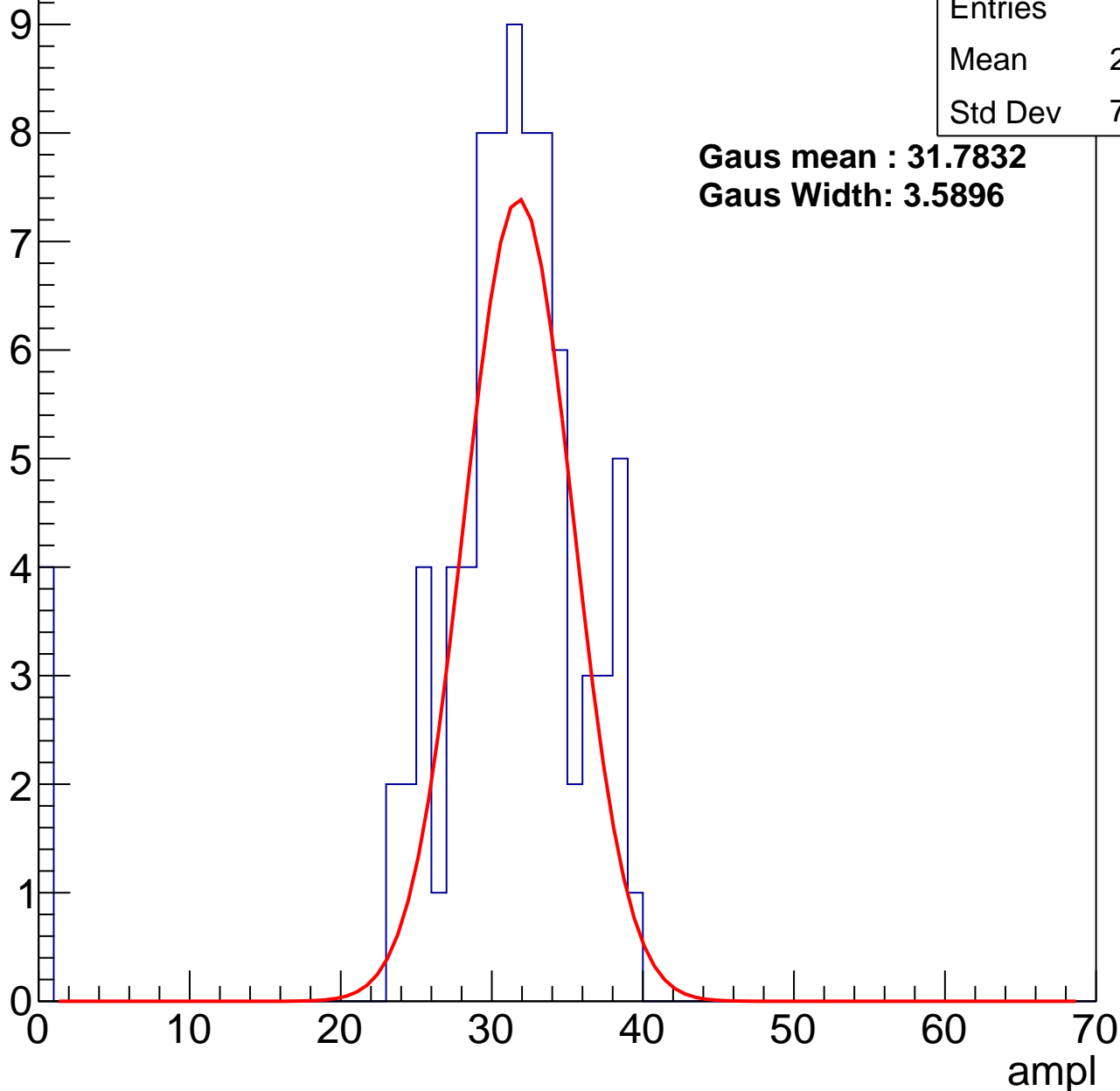
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	29.67
Std Dev	7.692

Gaus mean : 31.7832

Gaus Width: 3.5896



B1L103S, U17-ch15, adc1

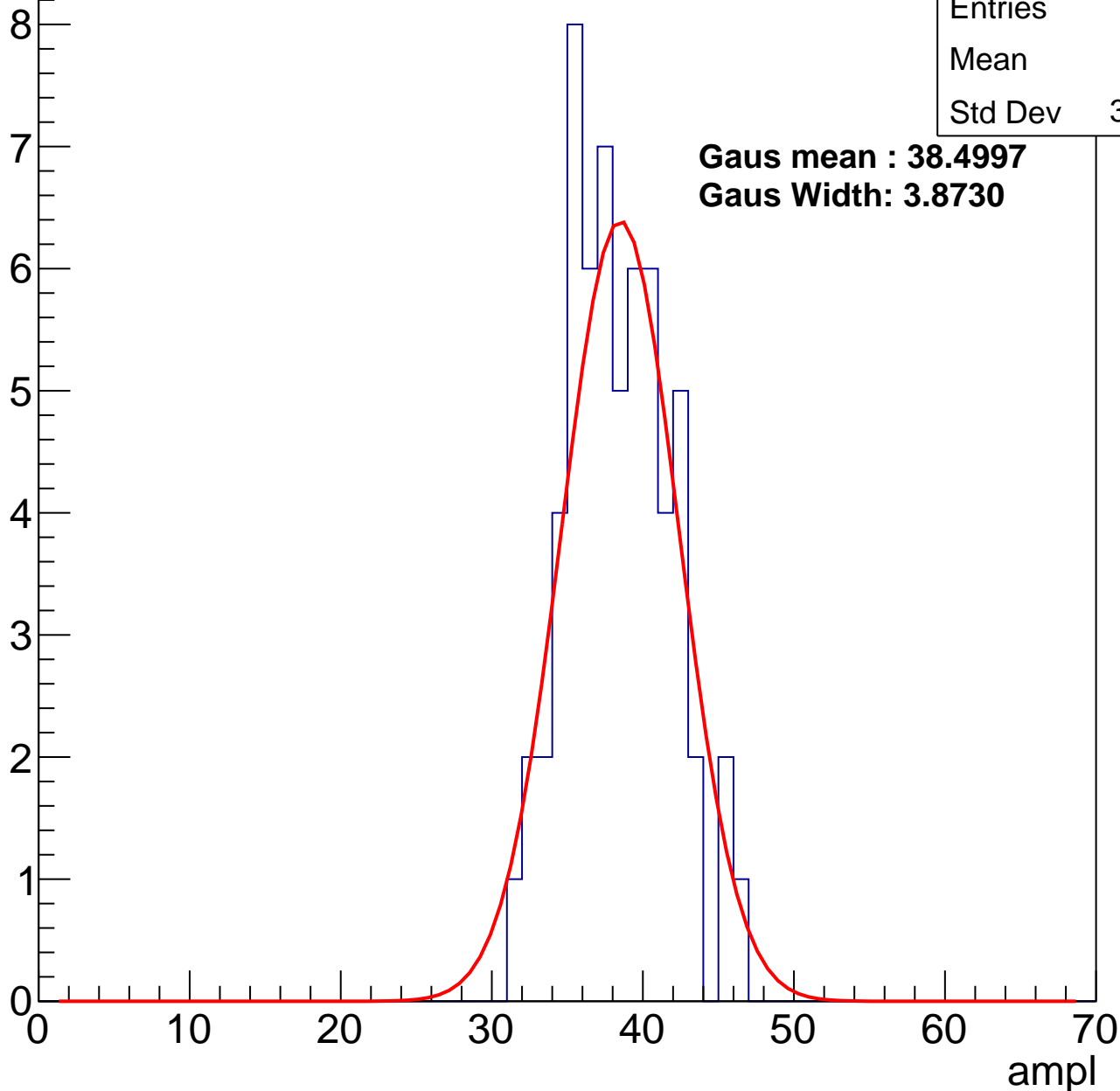
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.9
Std Dev	3.376

Gaus mean : 38.4997

Gaus Width: 3.8730



B1L103S, U17-ch15, adc2

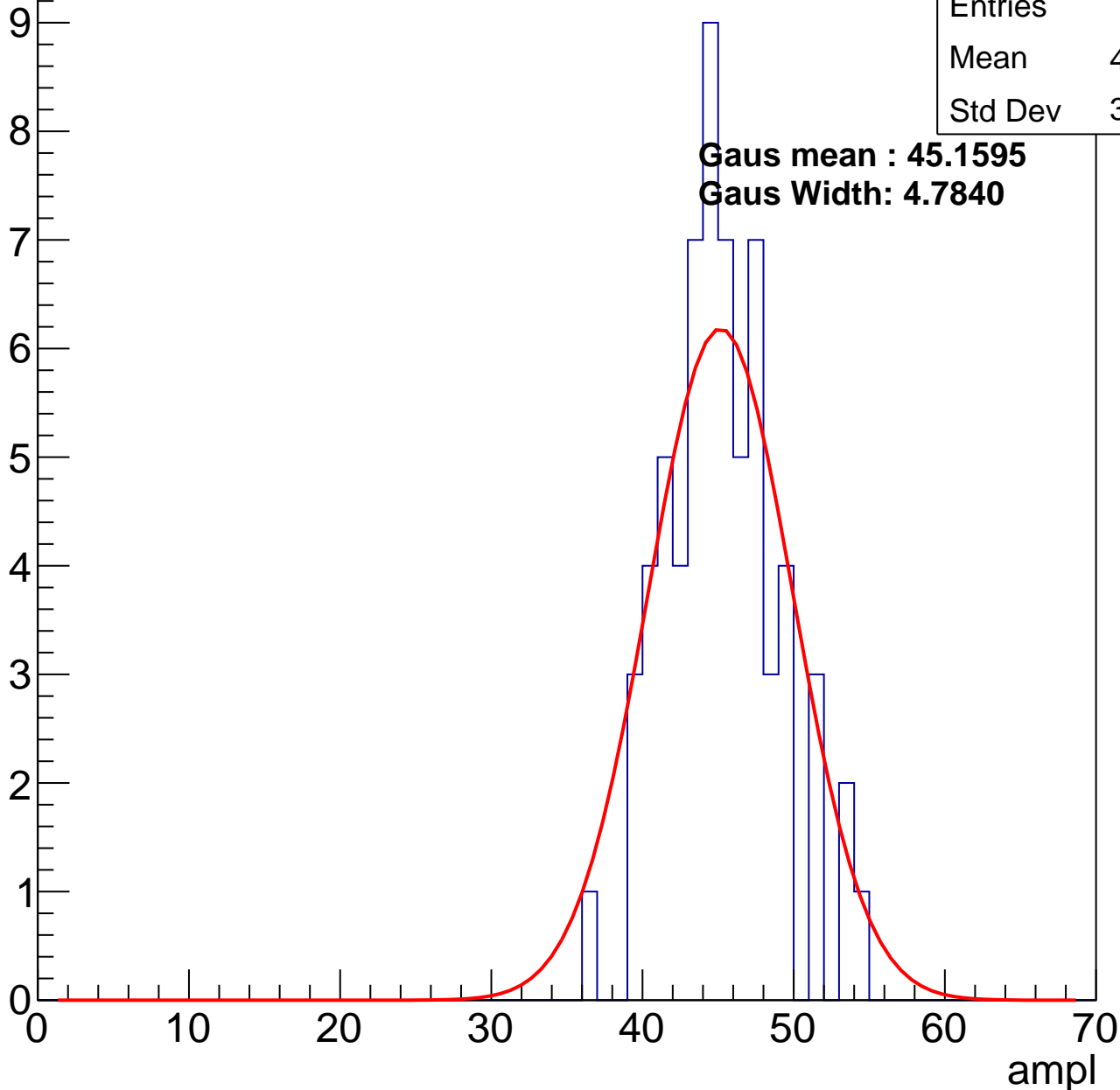
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.77
Std Dev	3.678

Gaus mean : 45.1595

Gaus Width: 4.7840

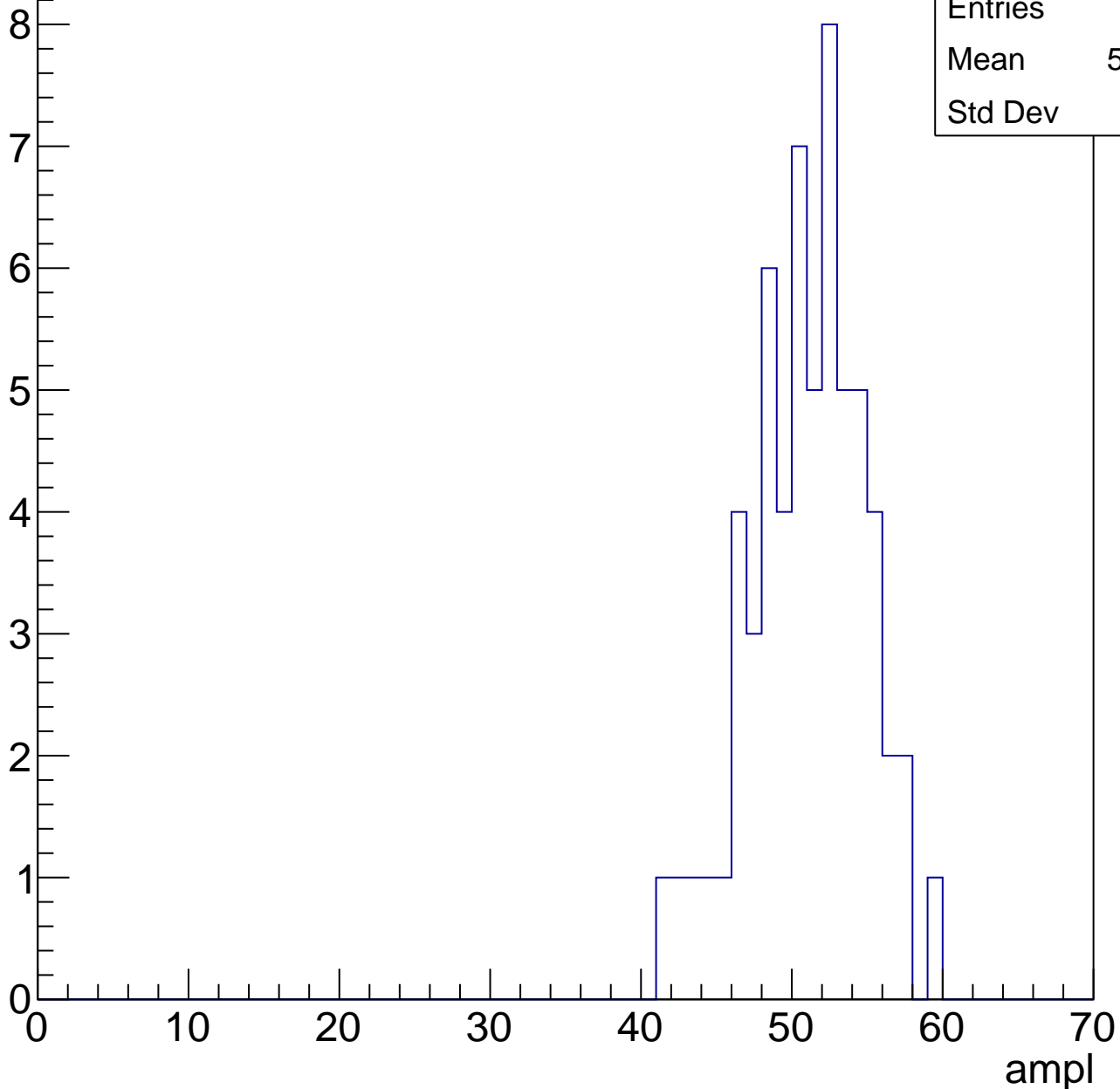


B1L103S, U17-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.57
Std Dev	3.77

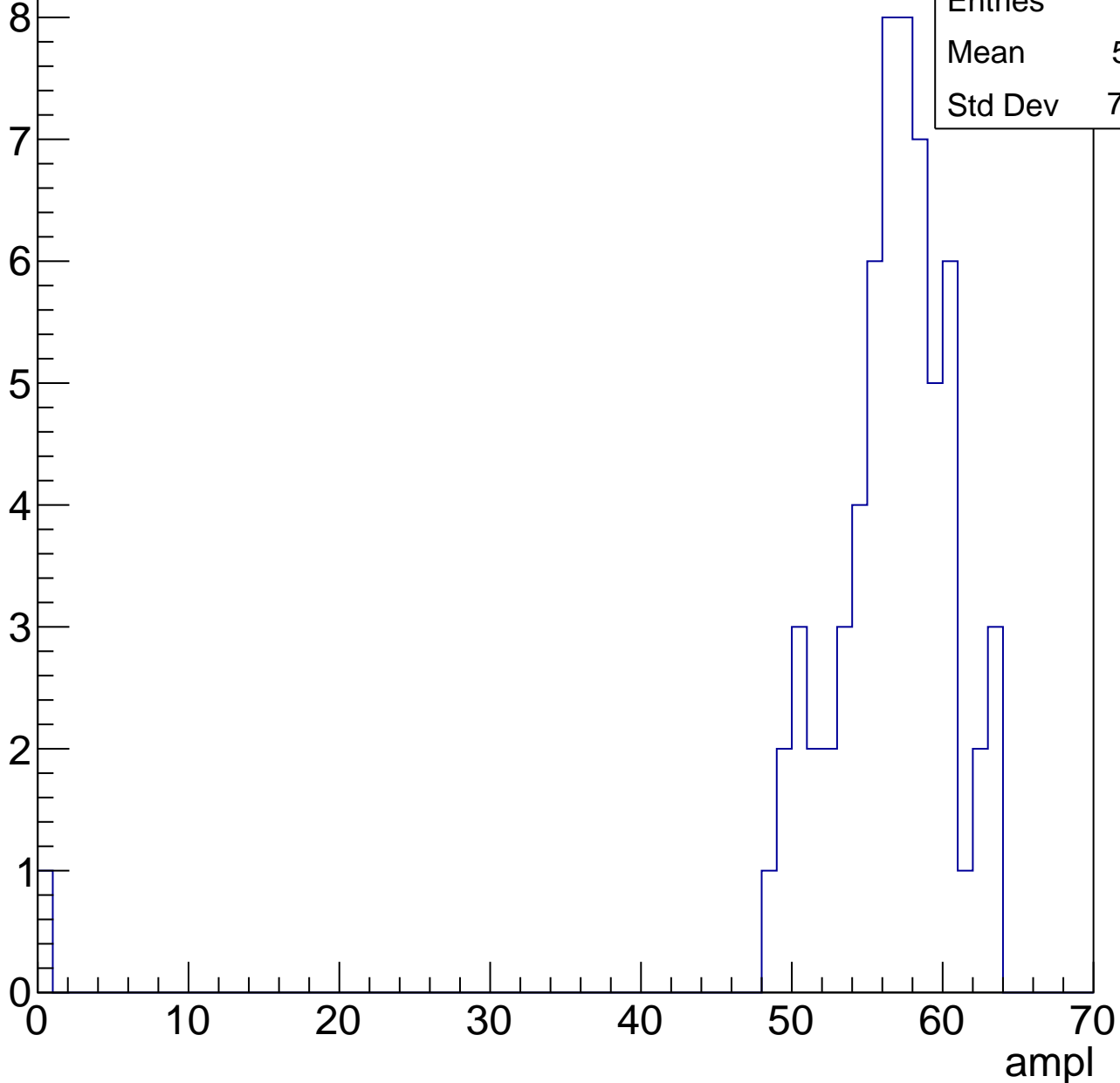


B1L103S, U17-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.41
Std Dev	7.842

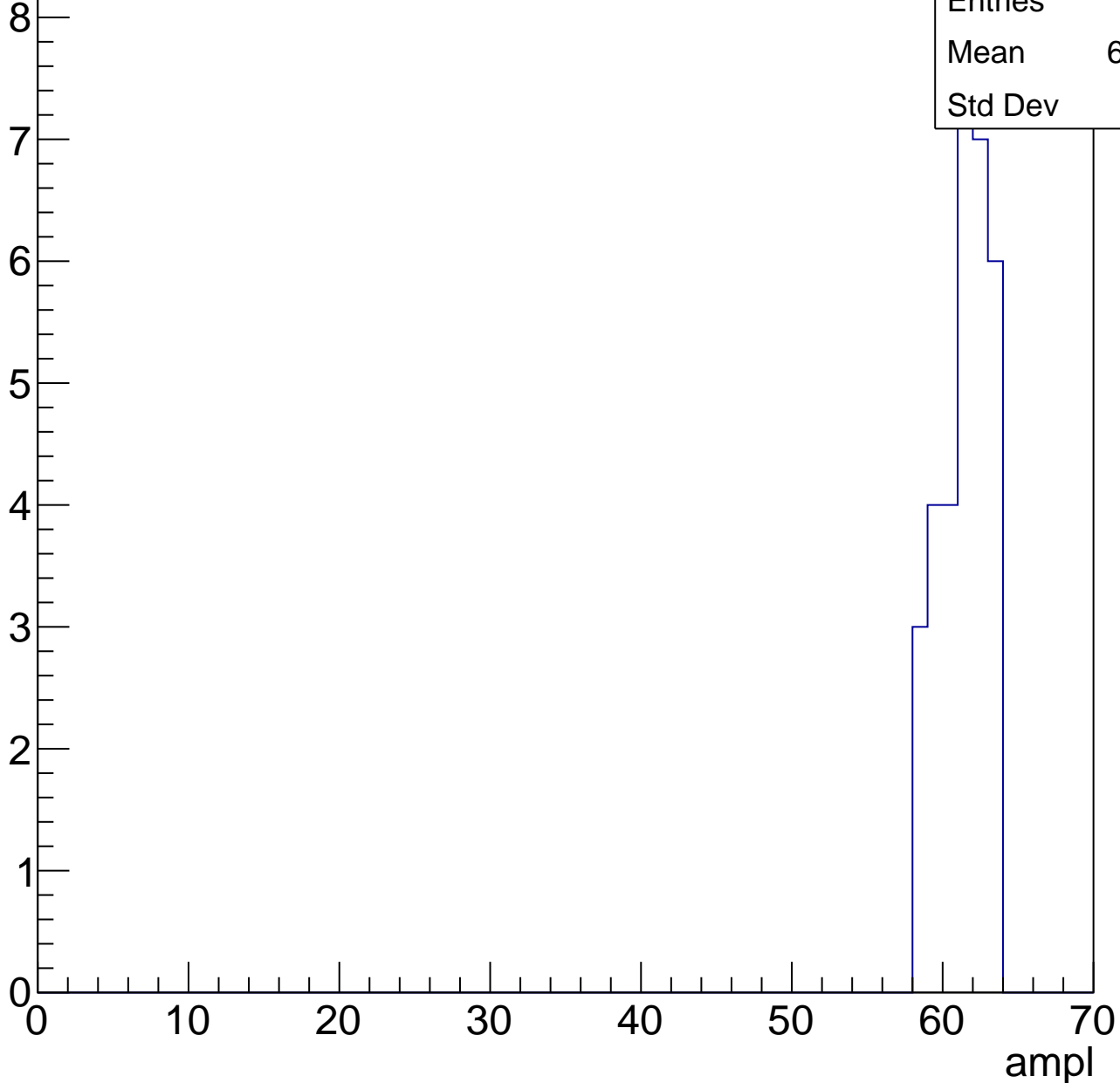


B1L103S, U17-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	60.94
Std Dev	1.56



B1L103S, U17-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch16, adc0

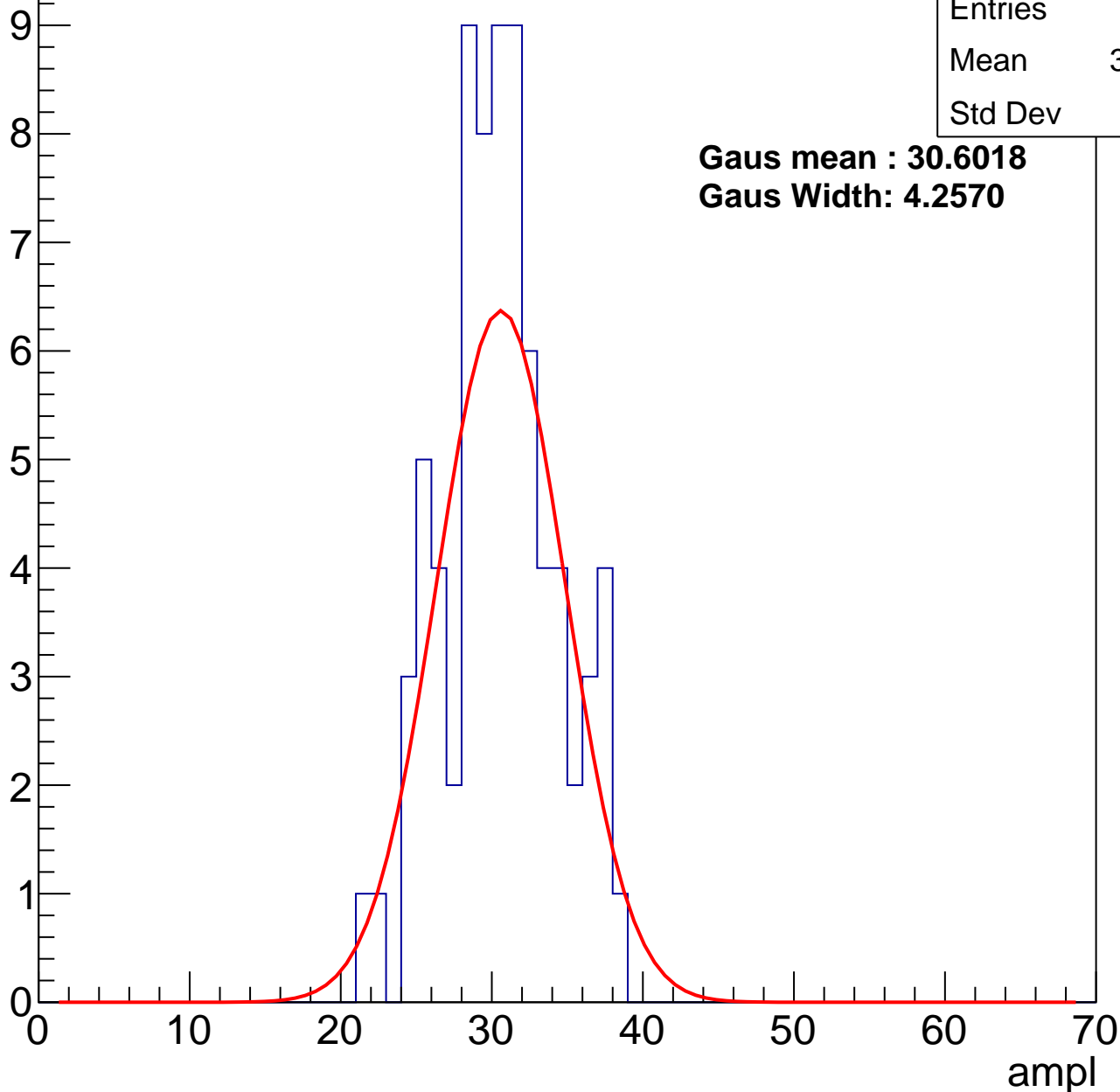
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	30.07
Std Dev	3.75

Gaus mean : 30.6018

Gaus Width: 4.2570



B1L103S, U17-ch16, adc1

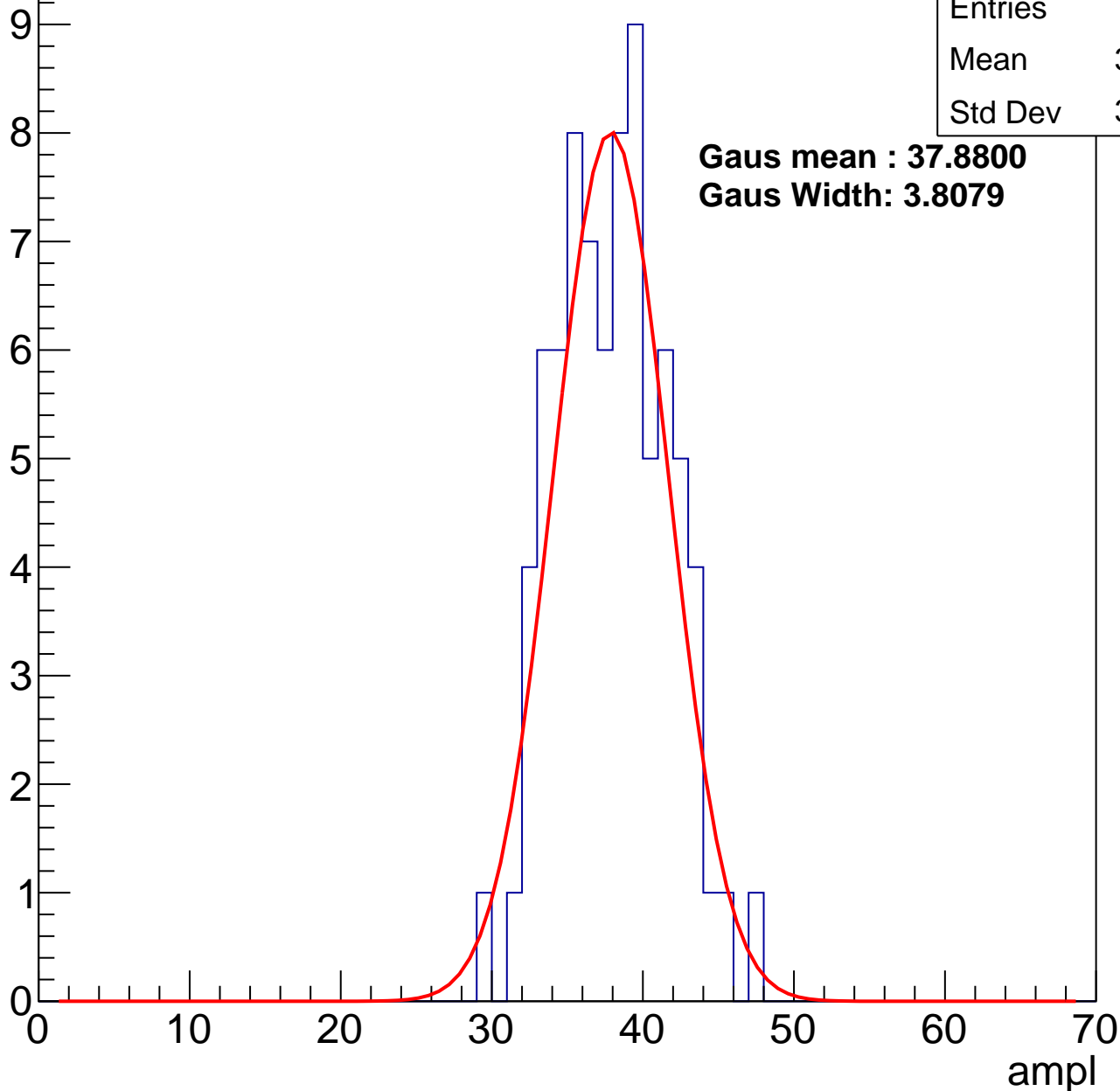
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	37.51
Std Dev	3.621

Gaus mean : 37.8800

Gaus Width: 3.8079



B1L103S, U17-ch16, adc2

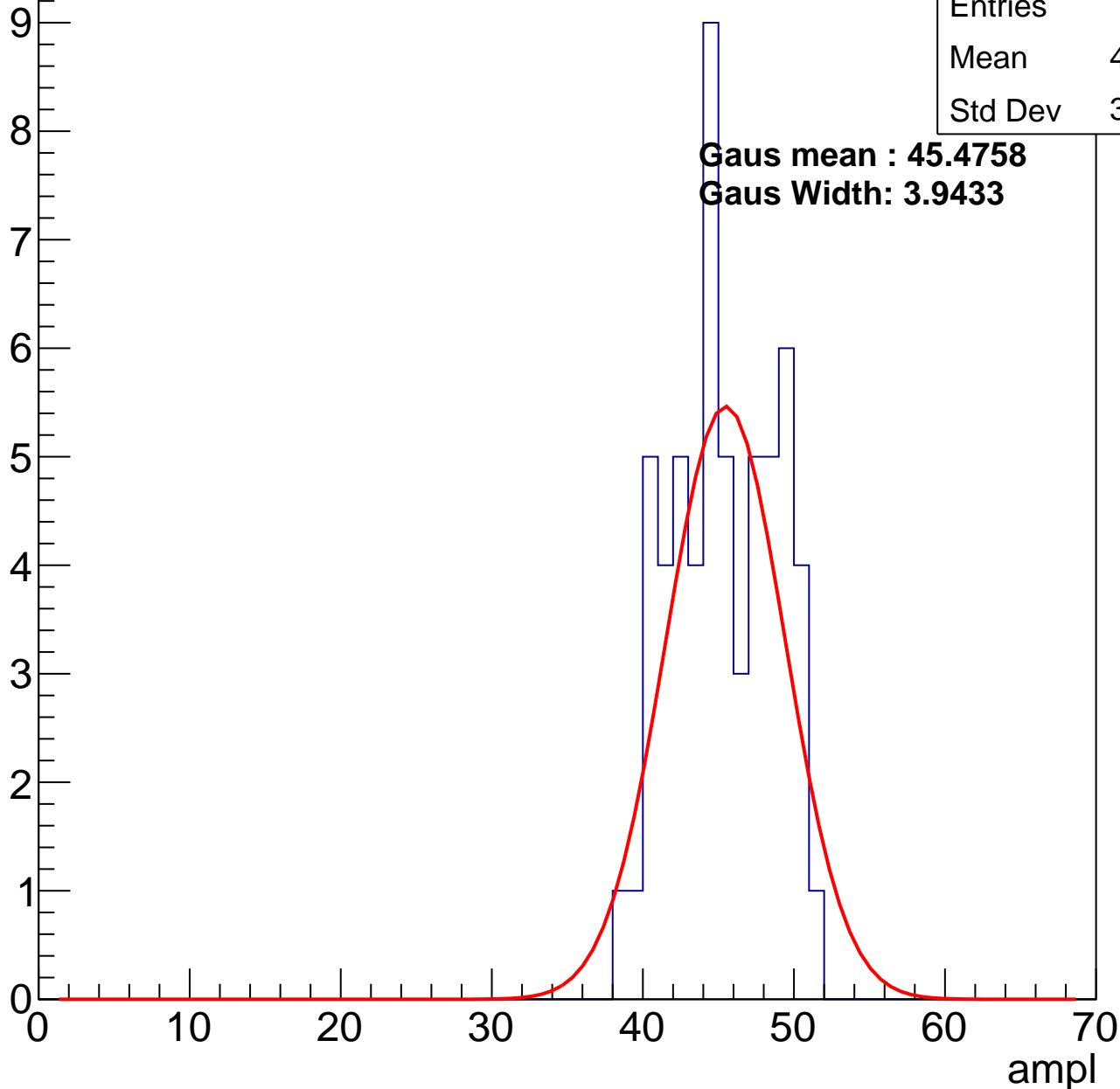
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.86
Std Dev	3.329

Gaus mean : 45.4758

Gaus Width: 3.9433

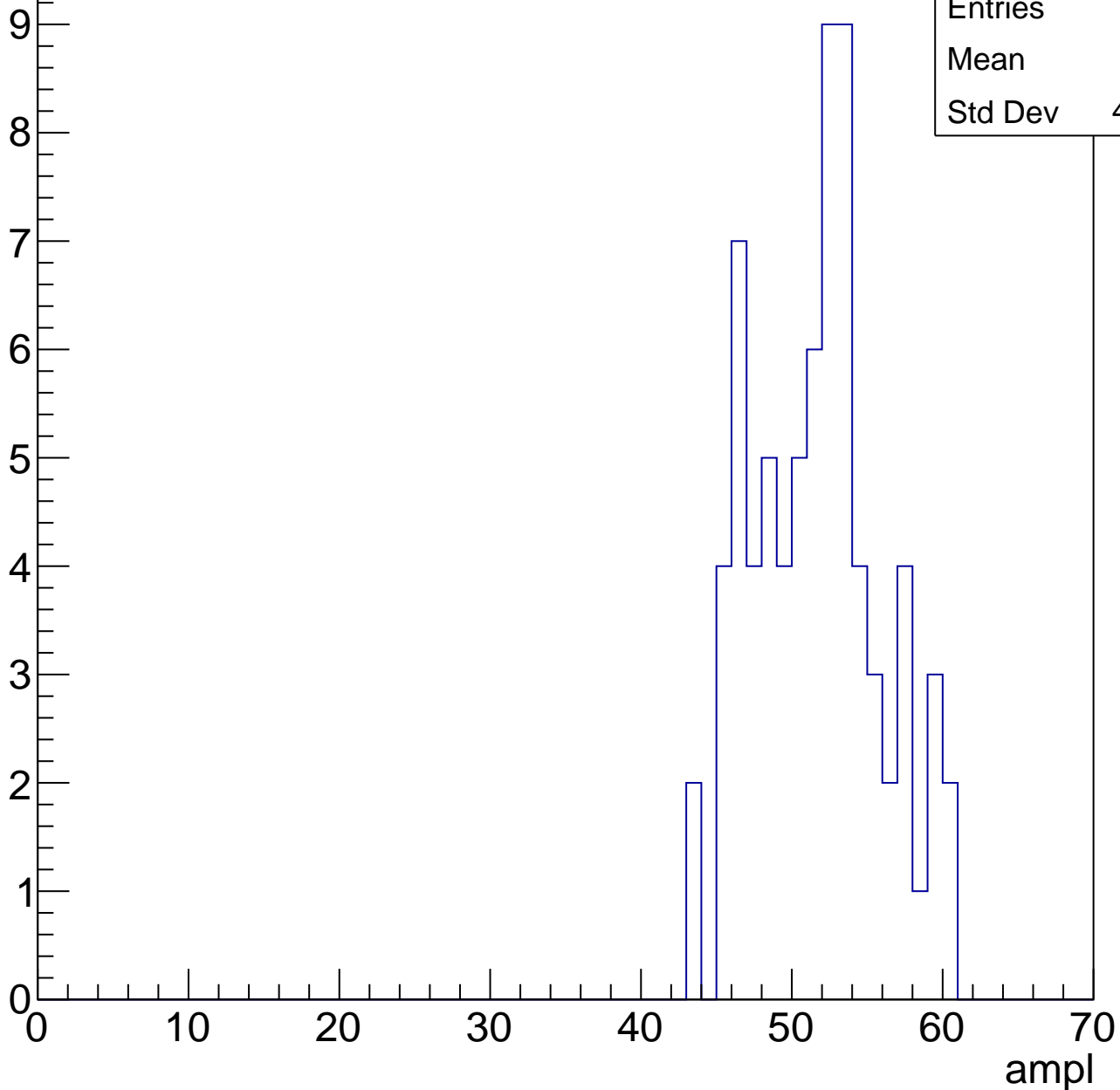


B1L103S, U17-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	51.2
Std Dev	4.191

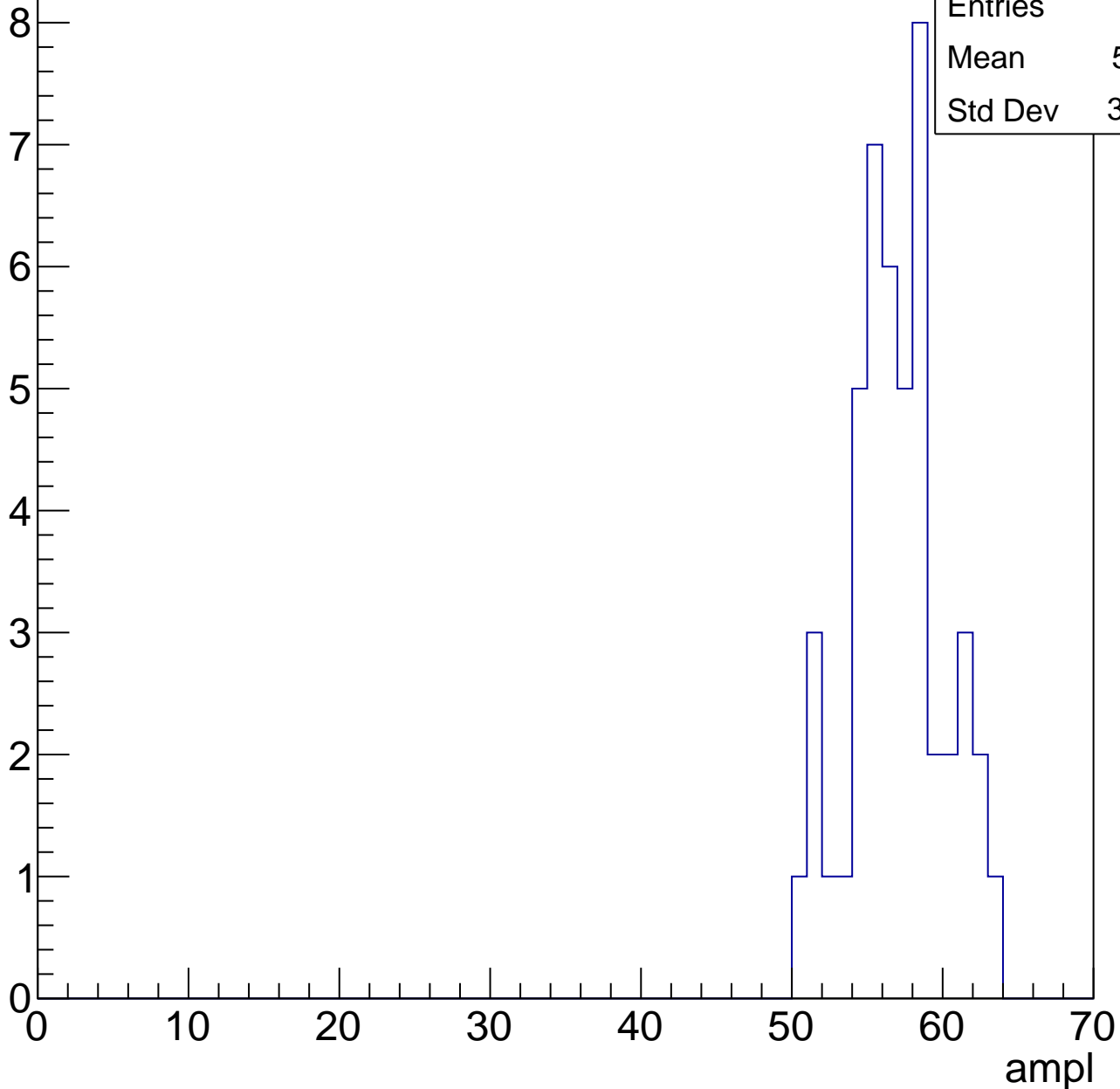


B1L103S, U17-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	56.51
Std Dev	3.038

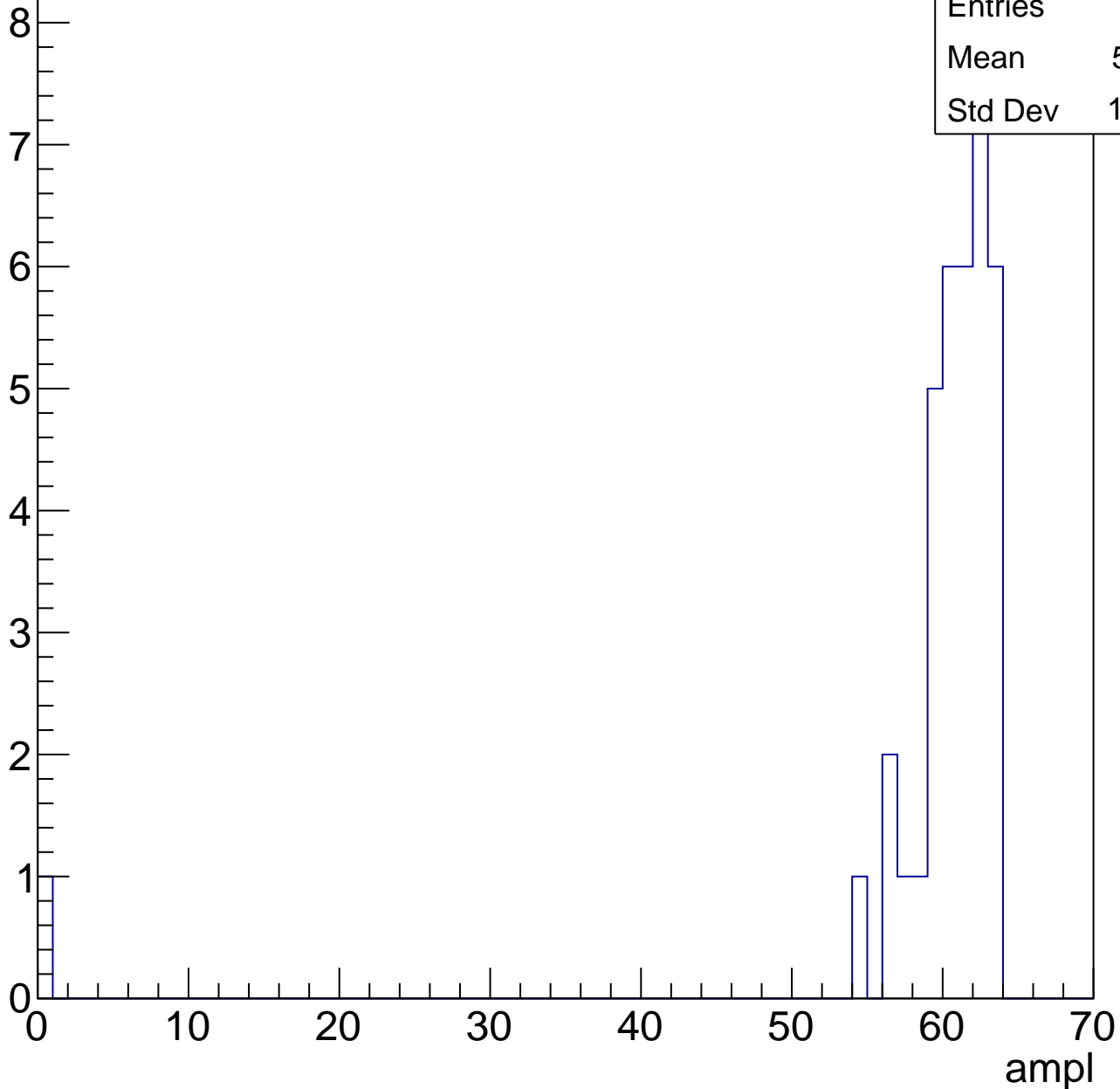


B1L103S, U17-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.81
Std Dev	10.03



B1L103S, U17-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch17, adc0

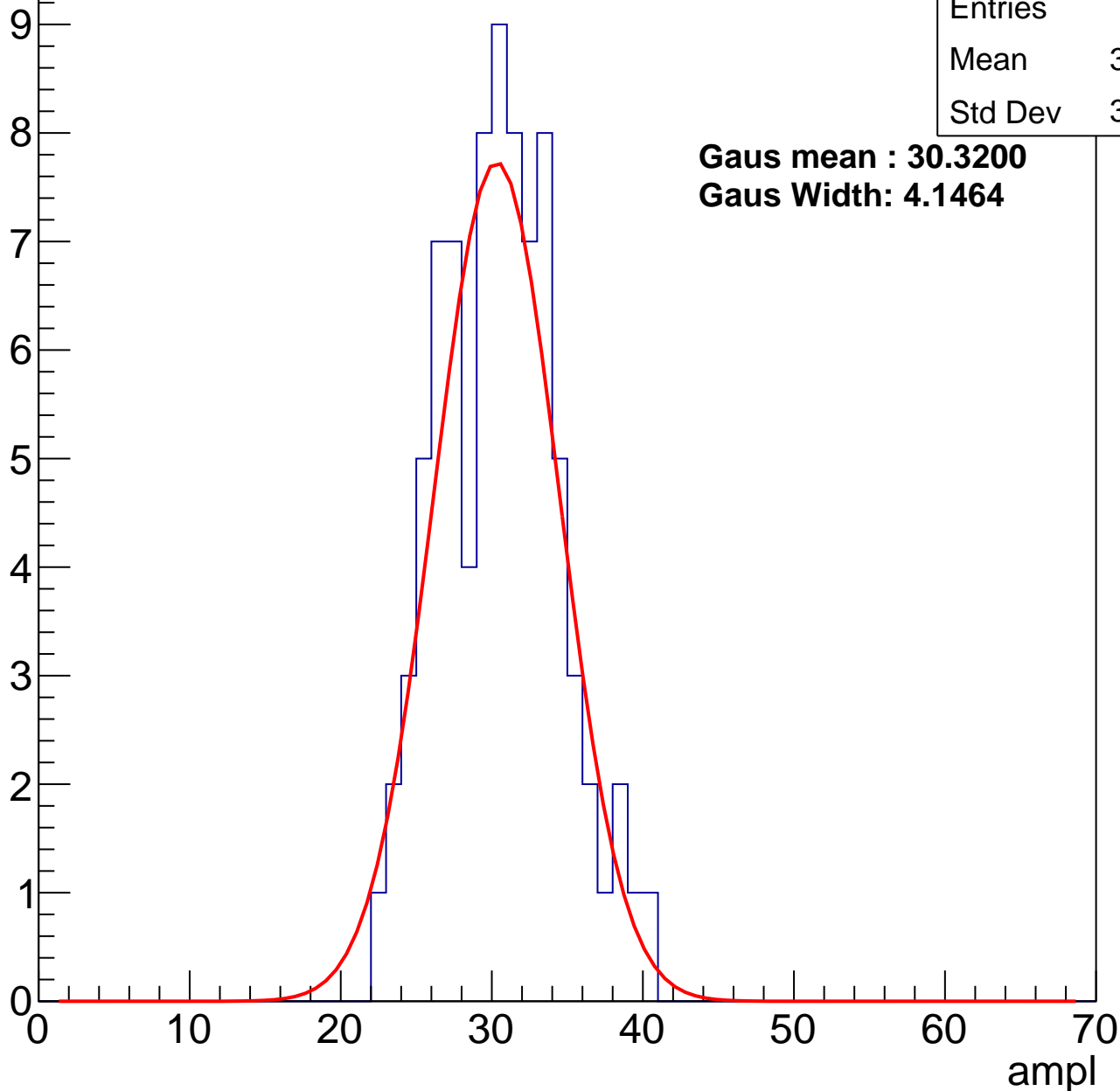
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	30.06
Std Dev	3.914

Gaus mean : 30.3200

Gaus Width: 4.1464



B1L103S, U17-ch17, adc1

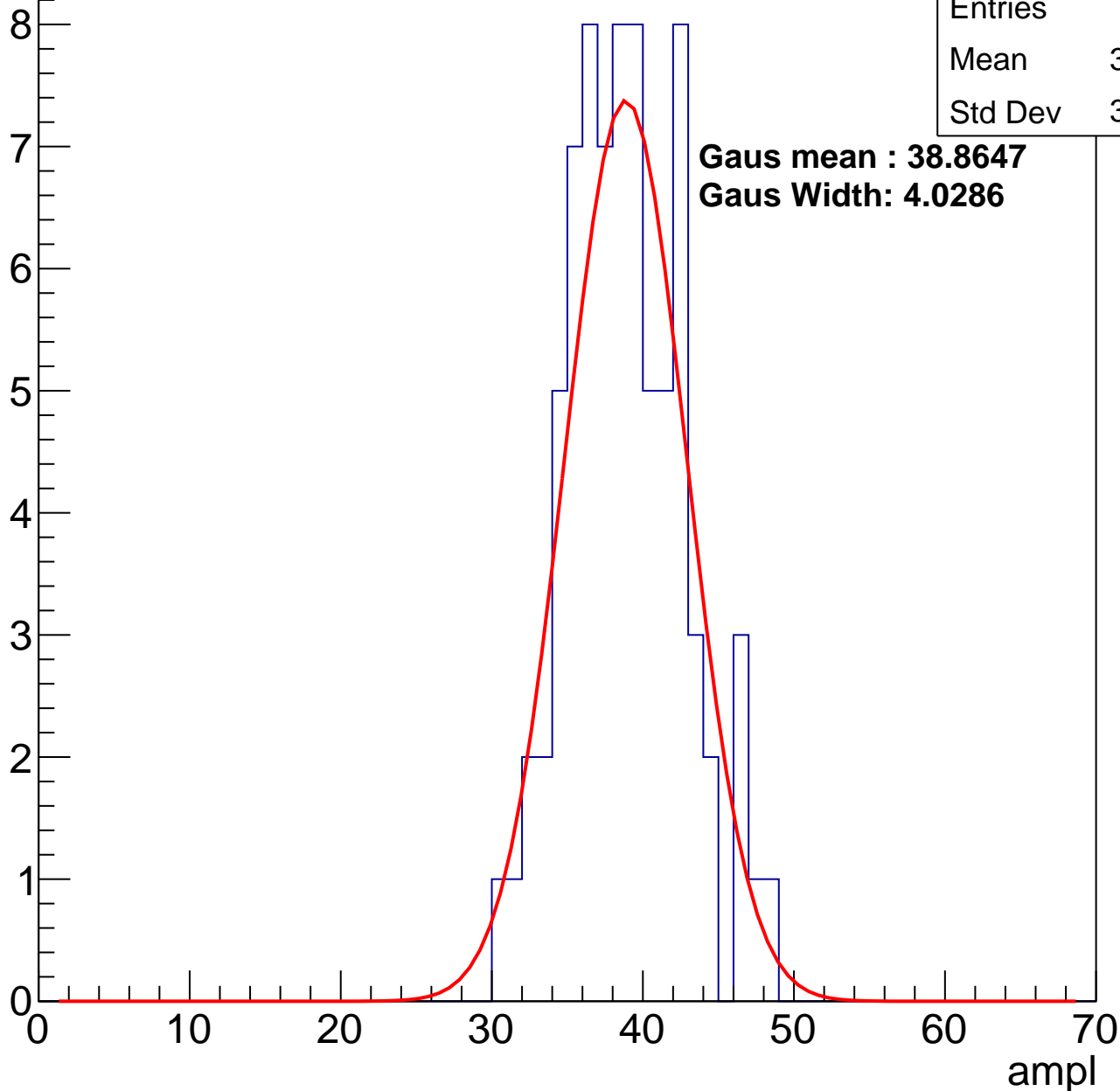
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	38.44
Std Dev	3.819

Gaus mean : 38.8647

Gaus Width: 4.0286



B1L103S, U17-ch17, adc2

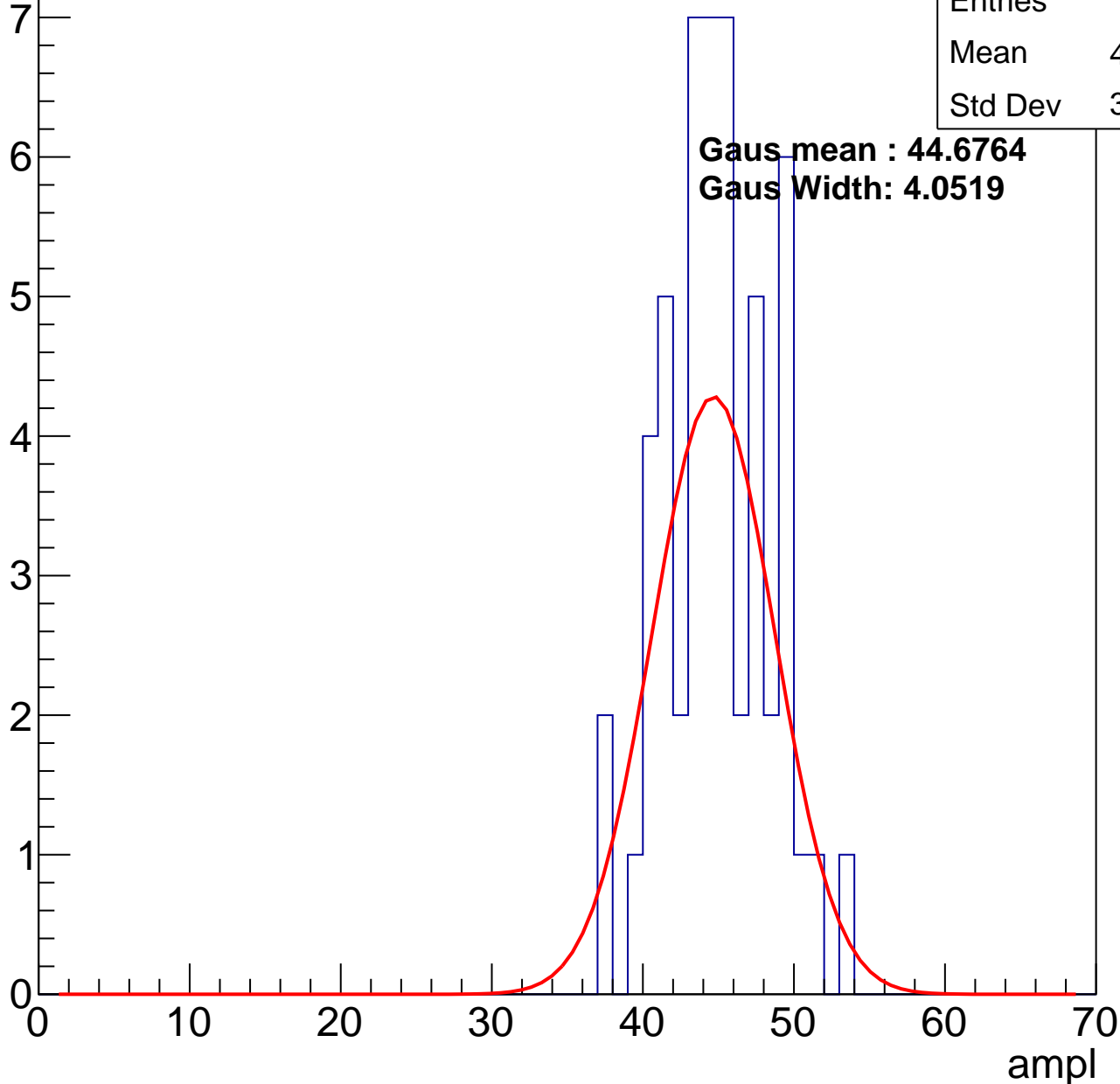
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.47
Std Dev	3.484

Gaus mean : 44.6764

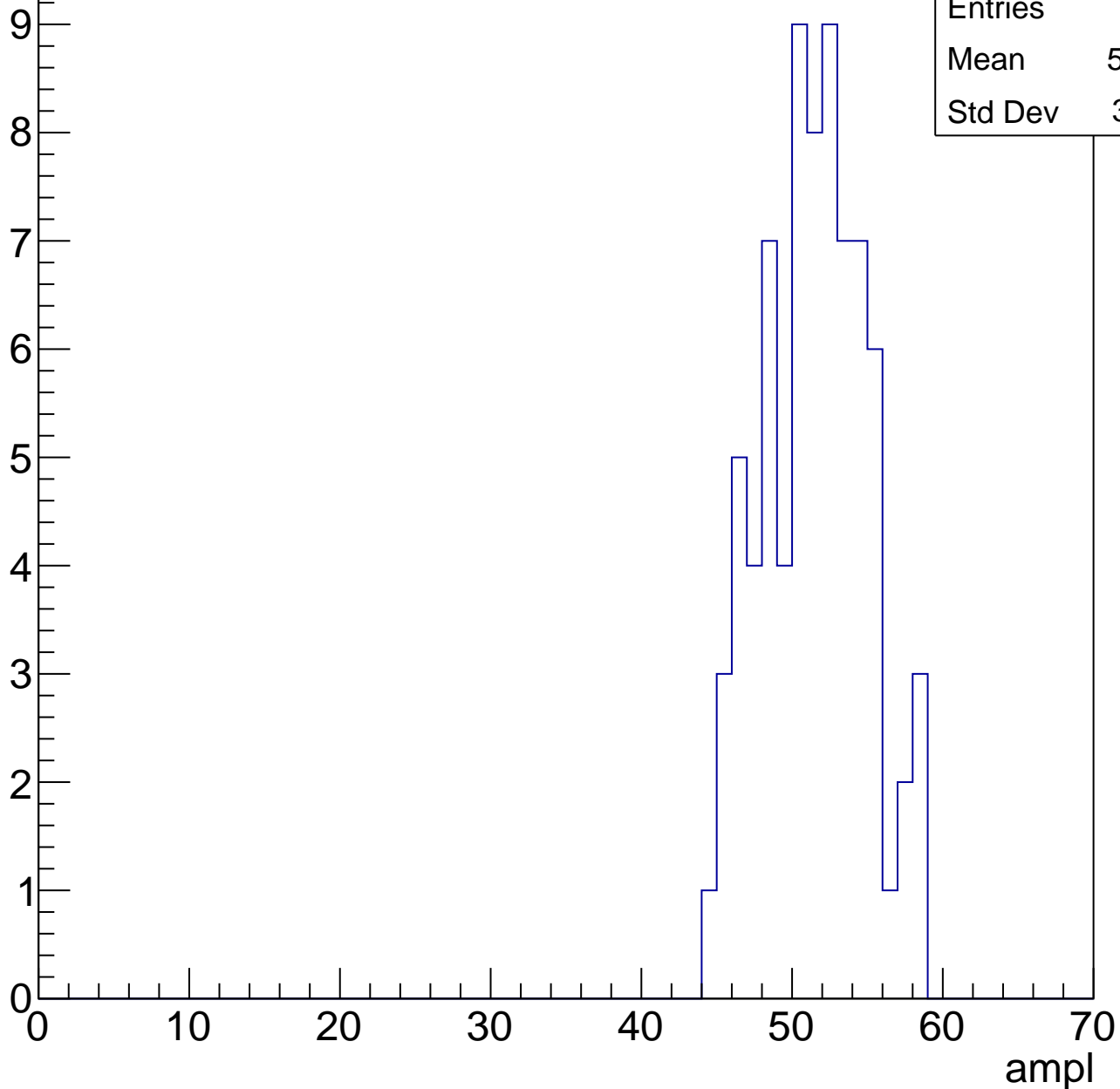
Gaus Width: 4.0519



B1L103S, U17-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

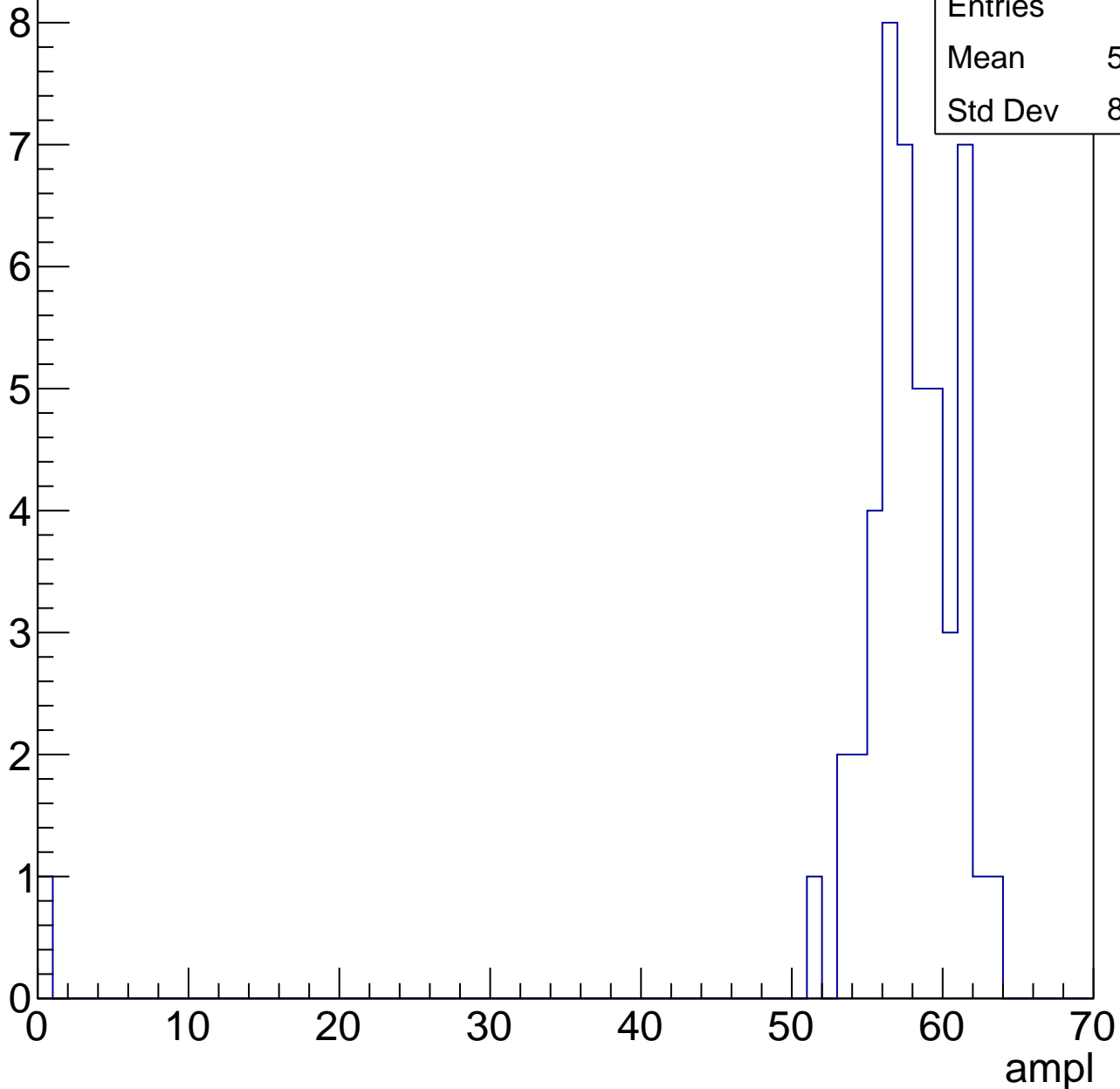


B1L103S, U17-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	56.36
Std Dev	8.709

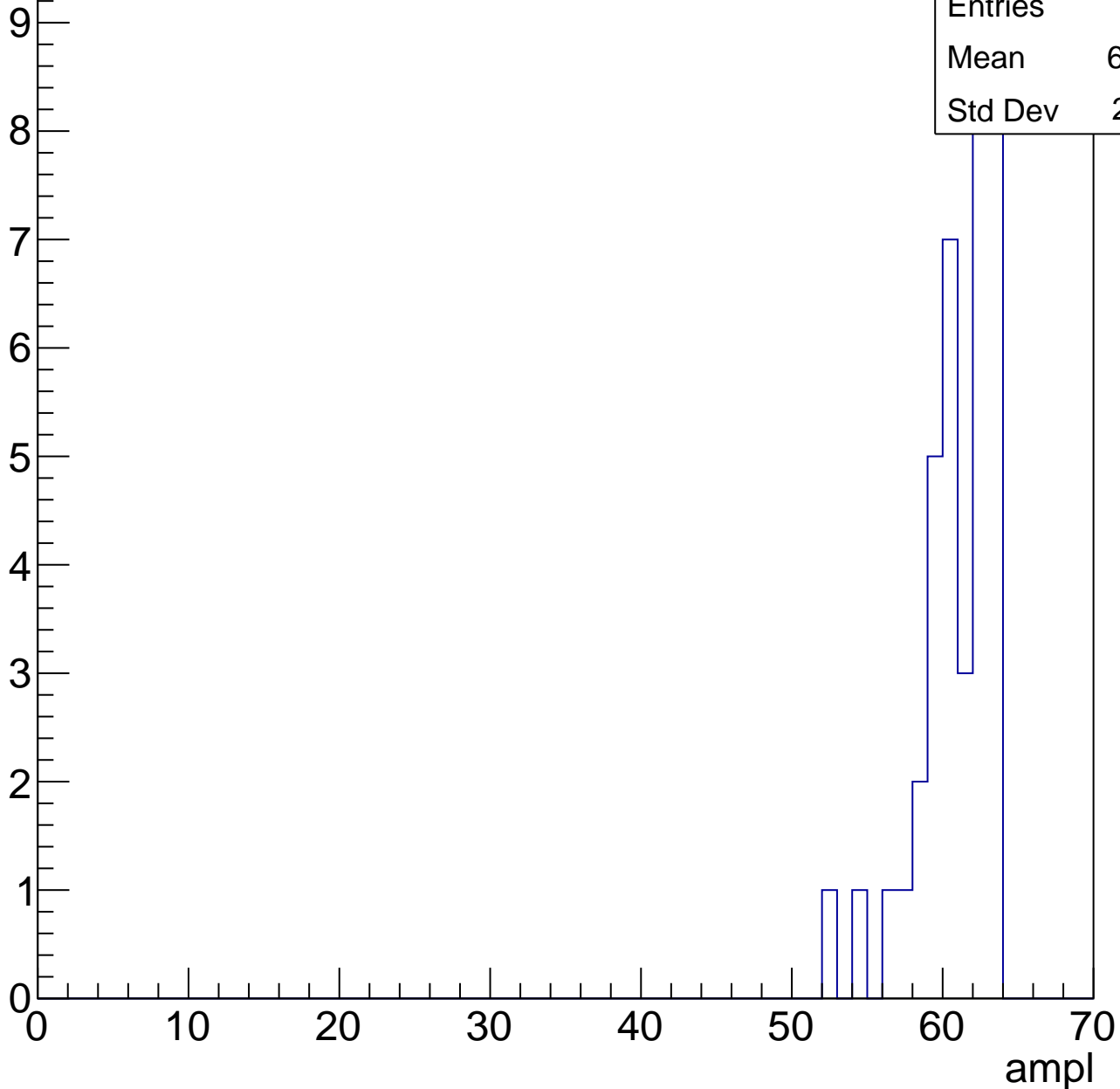


B1L103S, U17-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	60.42
Std Dev	2.551



B1L103S, U17-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch18, adc0

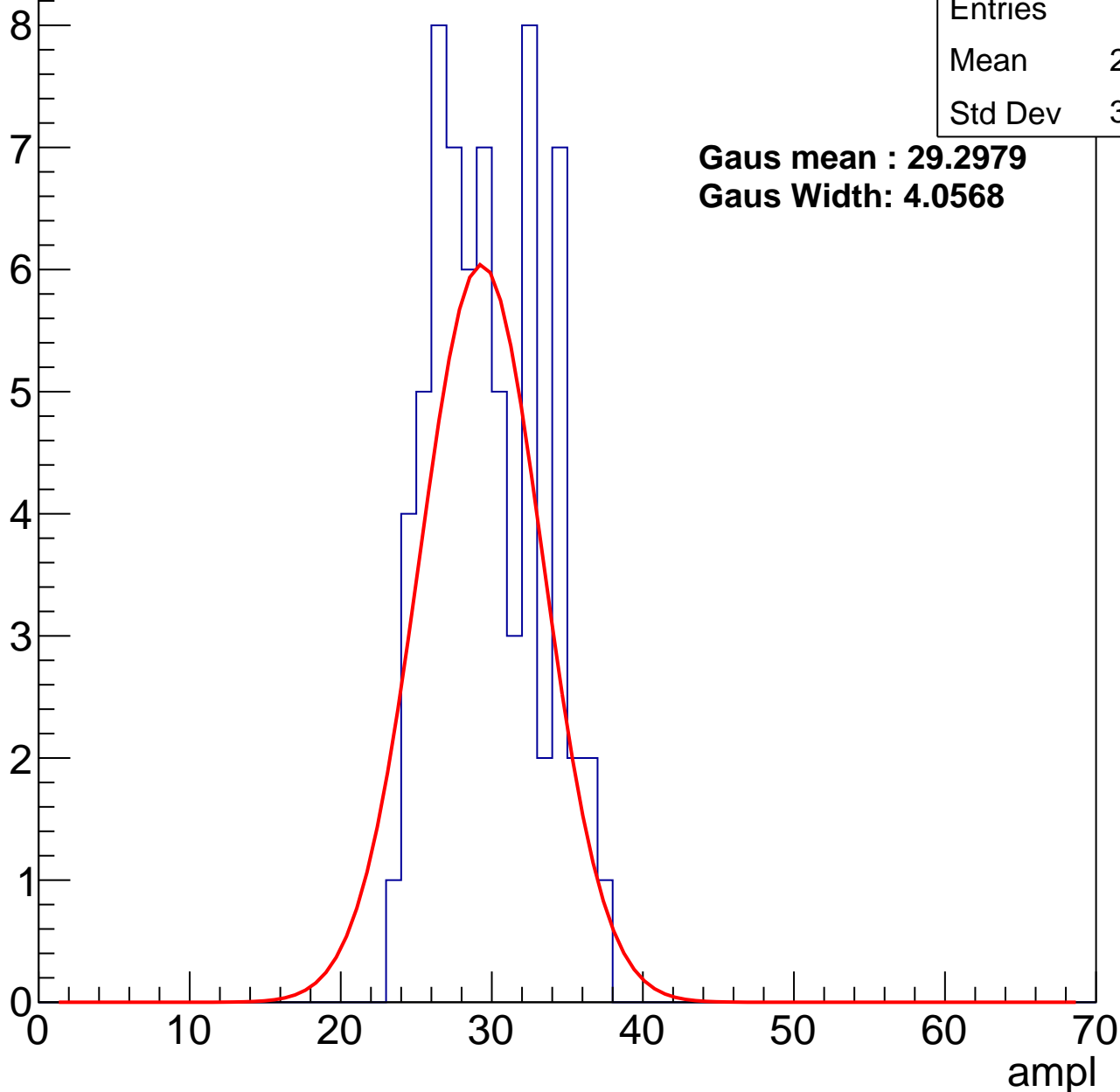
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.32
Std Dev	3.525

Gaus mean : 29.2979

Gaus Width: 4.0568



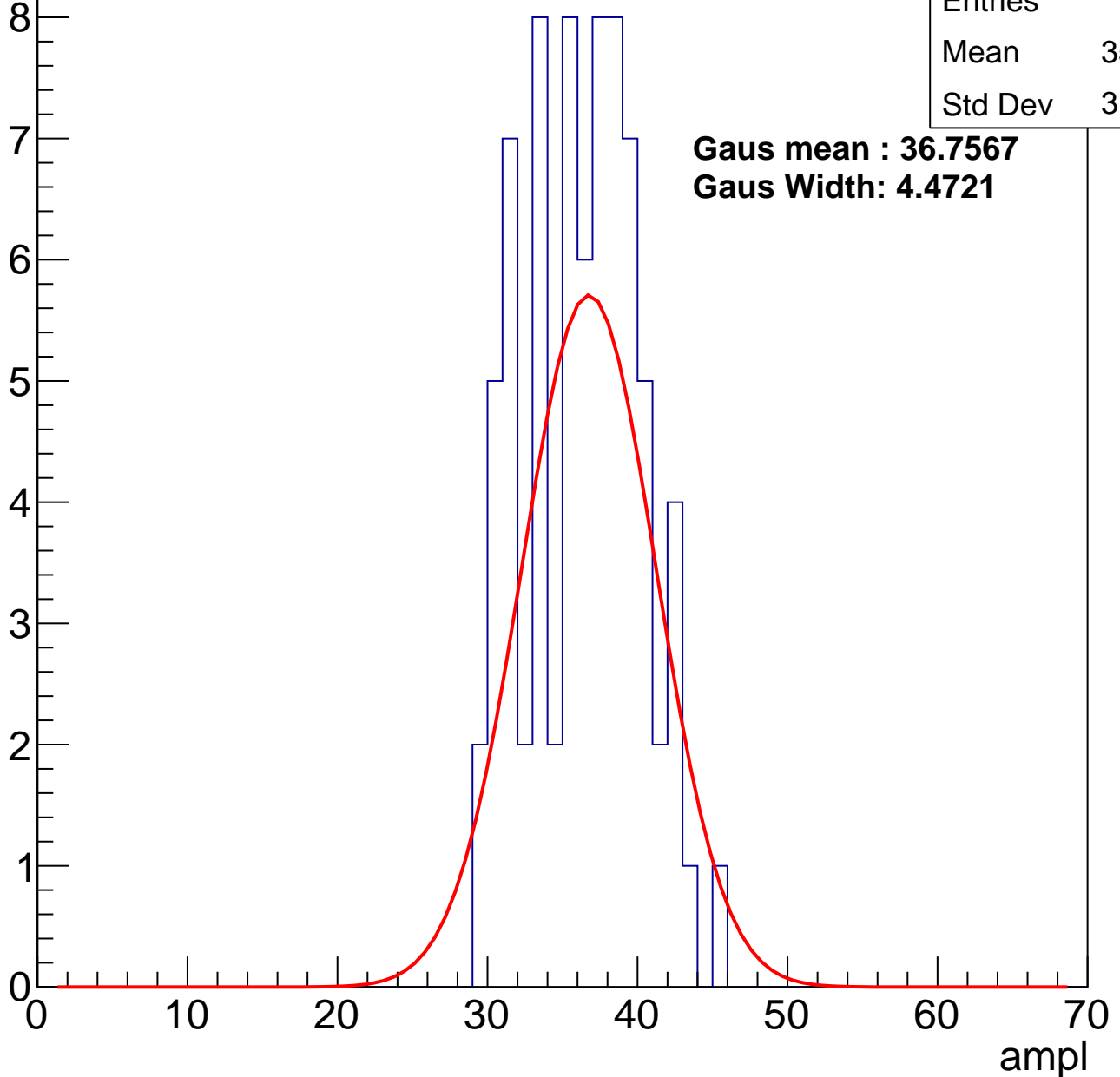
B1L103S, U17-ch18, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.89
Std Dev	3.768

Gaus mean : 36.7567
Gaus Width: 4.4721



B1L103S, U17-ch18, adc2

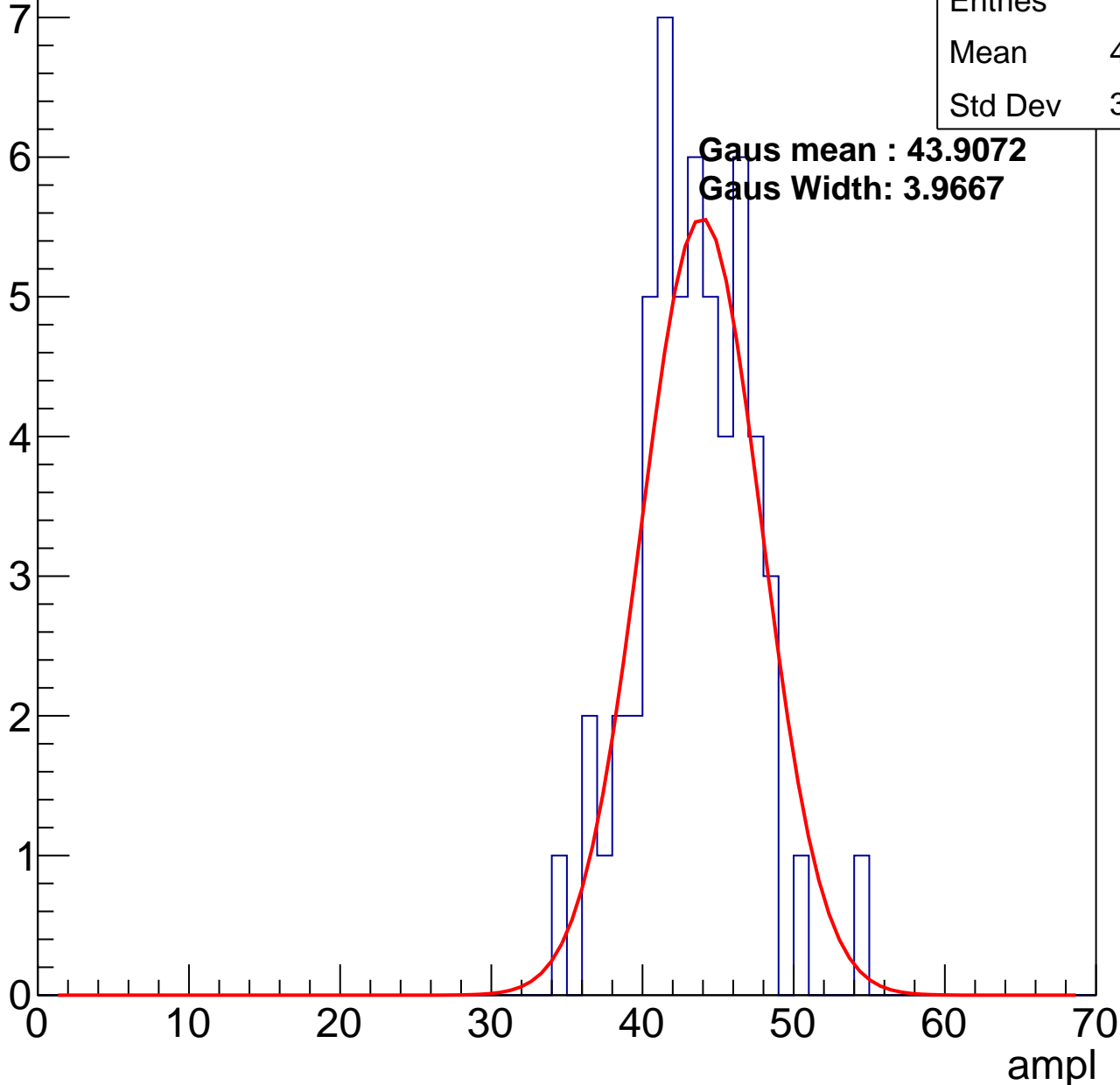
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.98
Std Dev	3.715

Gaus mean : 43.9072

Gaus Width: 3.9667

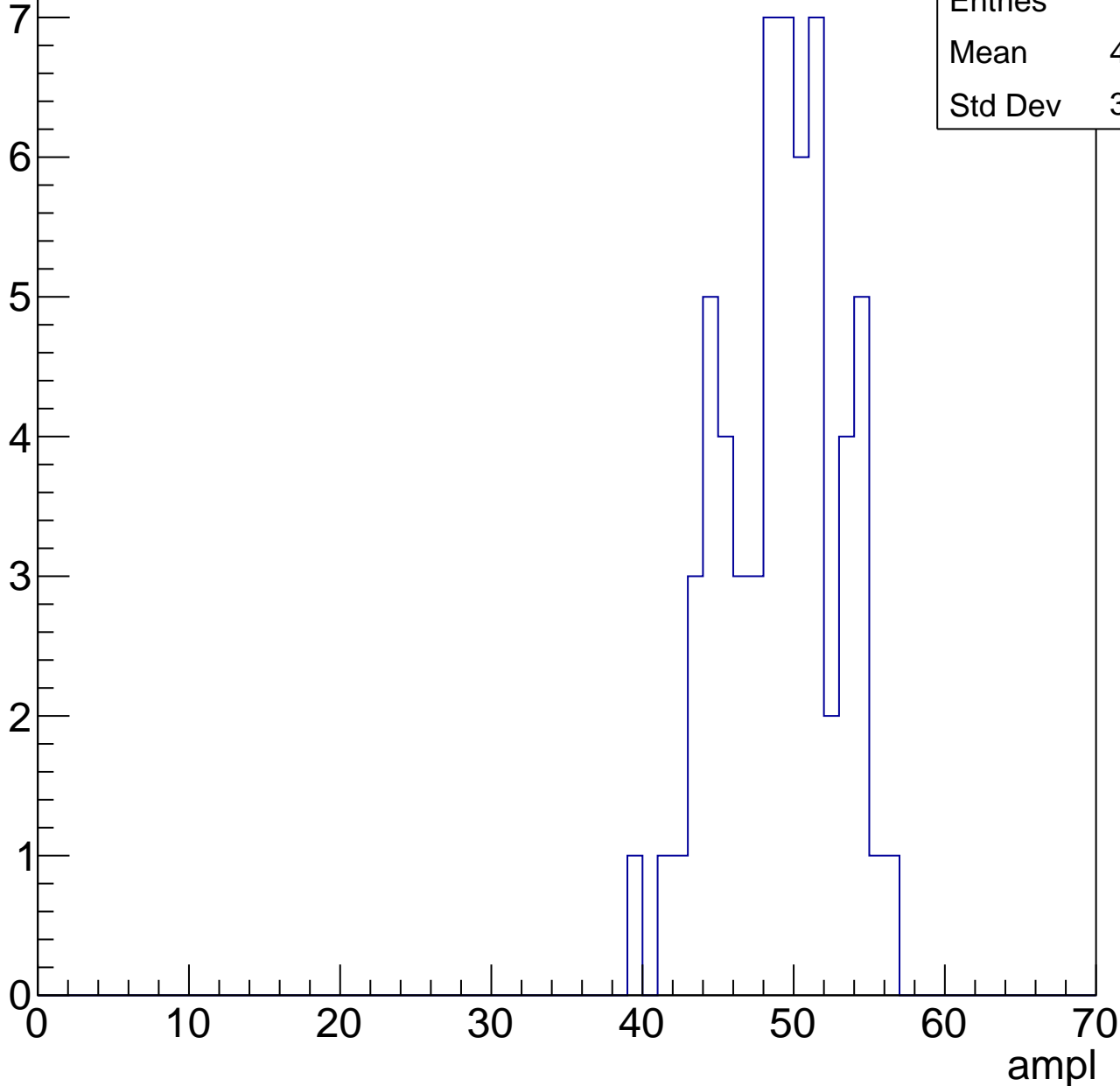


B1L103S, U17-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.57
Std Dev	3.779

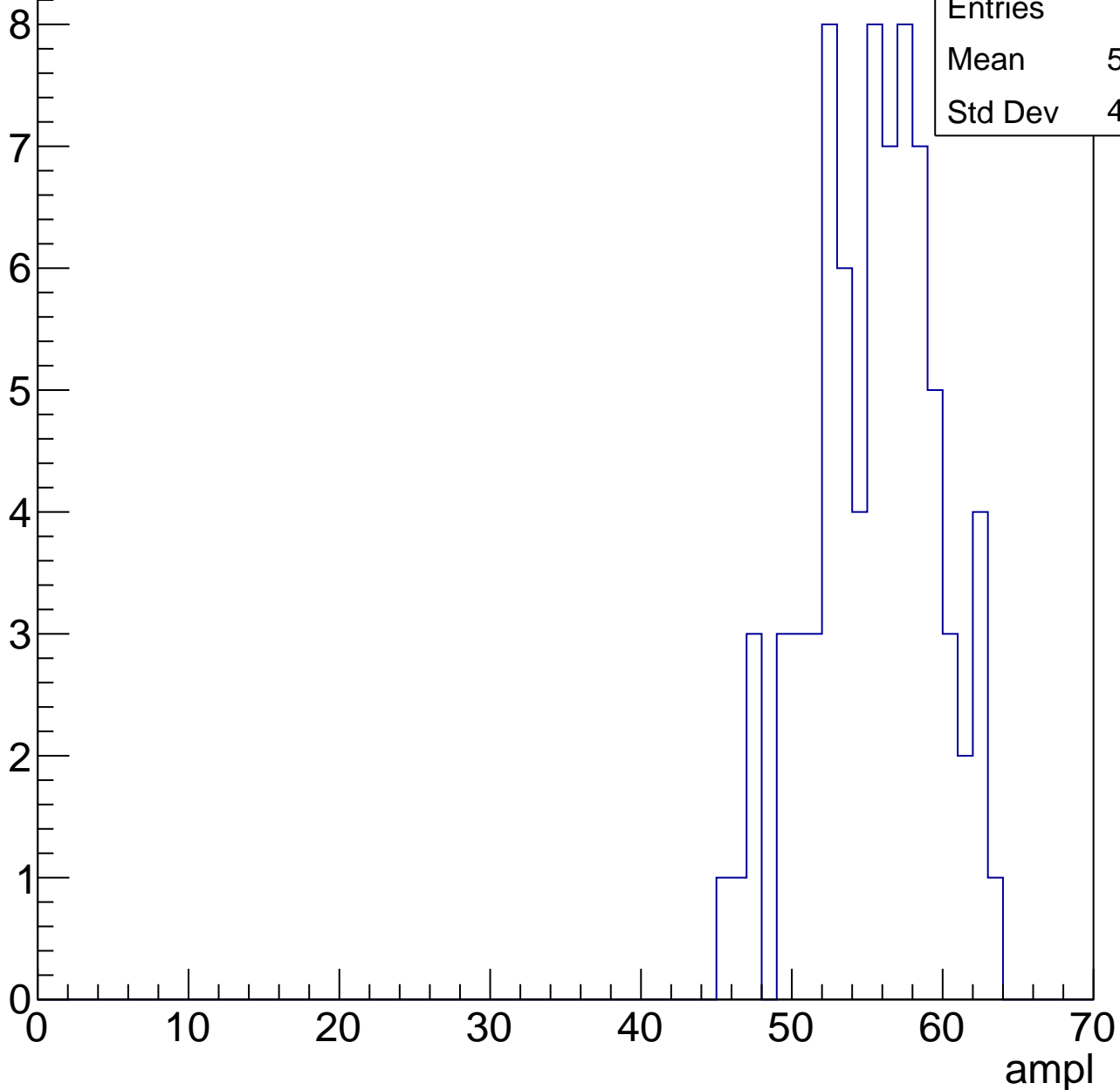


B1L103S, U17-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	54.99
Std Dev	4.098

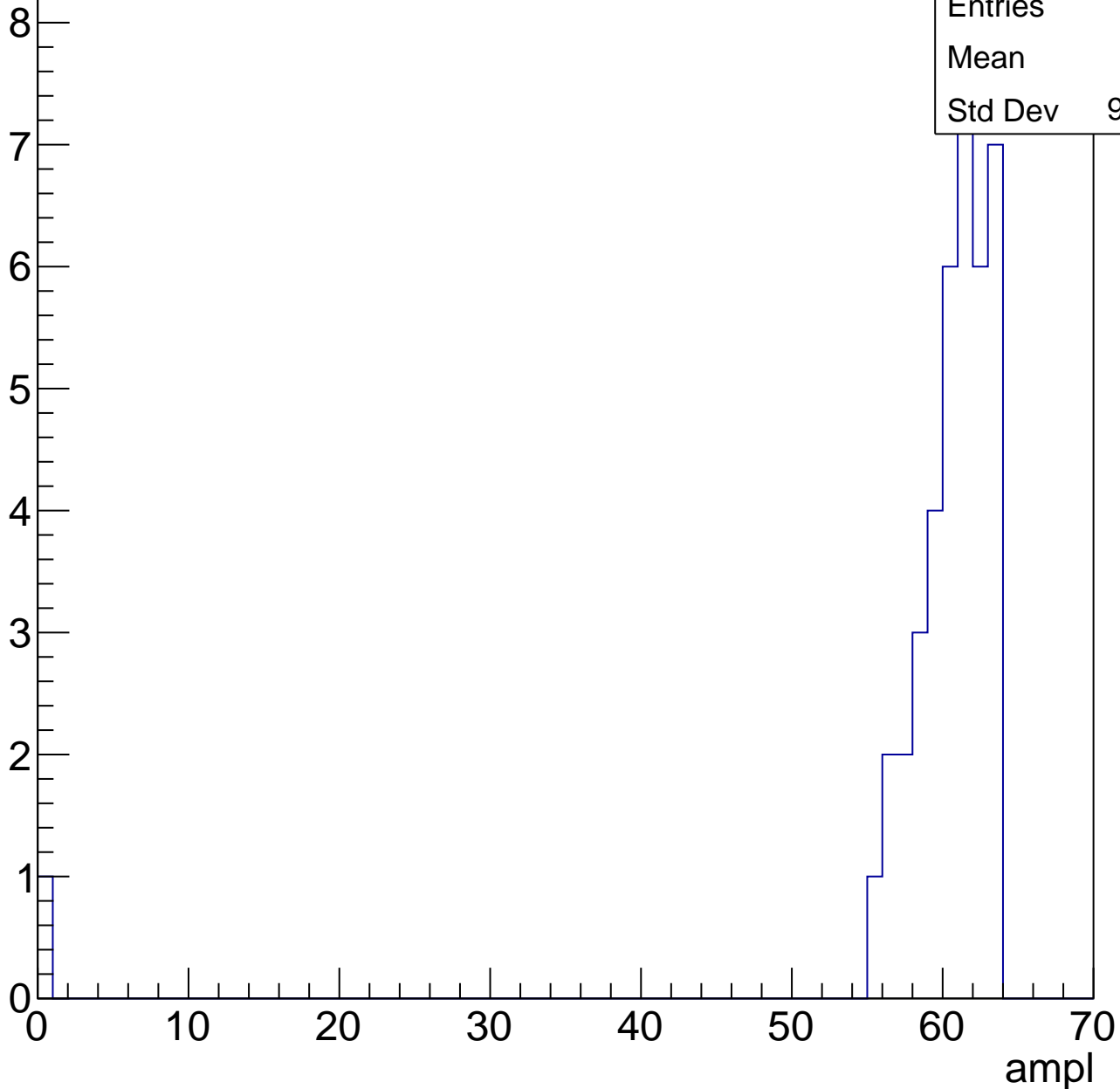


B1L103S, U17-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.8
Std Dev	9.655



B1L103S, U17-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L103S, U17-ch19, adc0

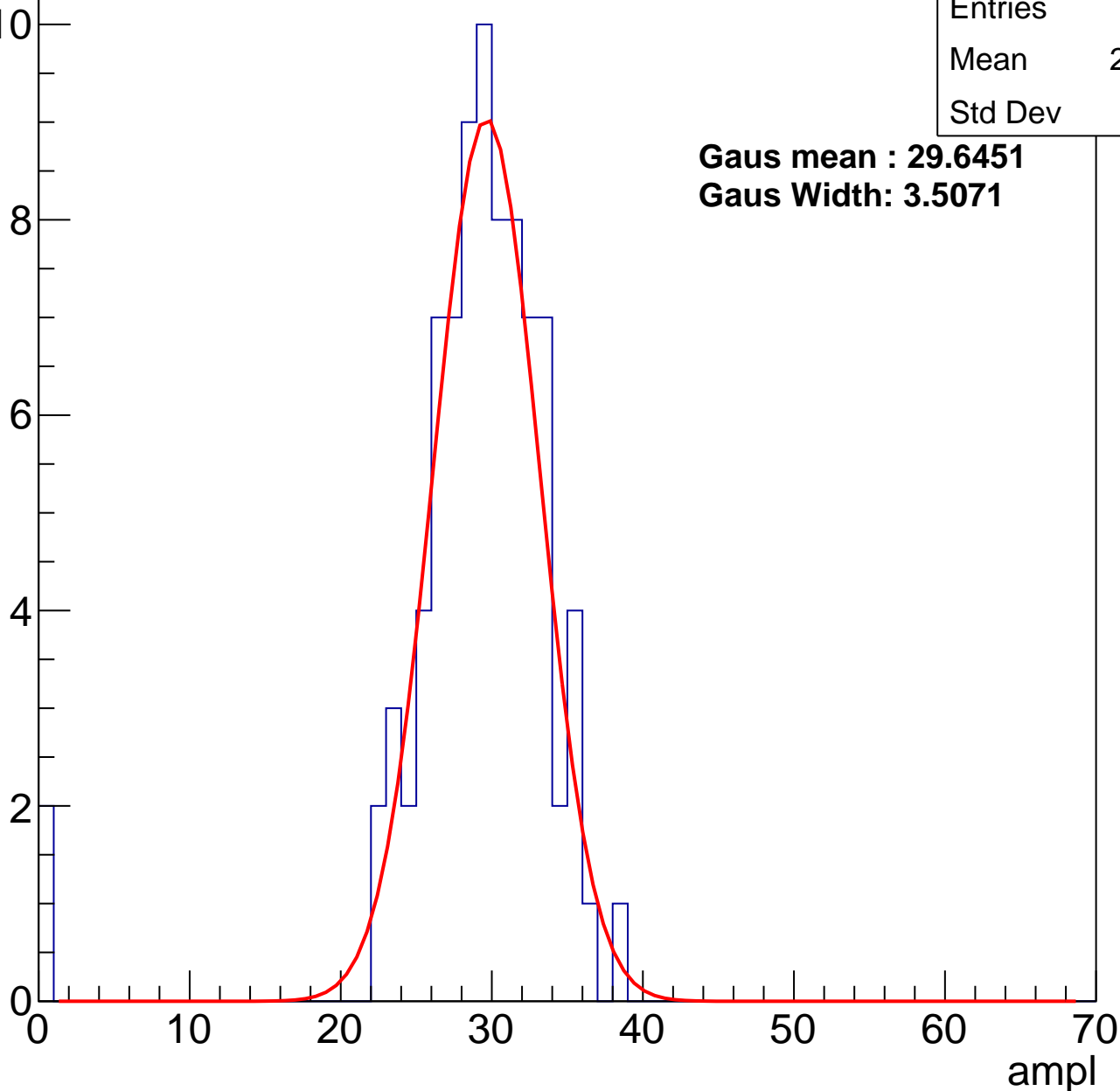
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	28.56
Std Dev	5.59

Gaus mean : 29.6451

Gaus Width: 3.5071



B1L103S, U17-ch19, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	36.66
Std Dev	3.458

Gaus mean : 36.9203

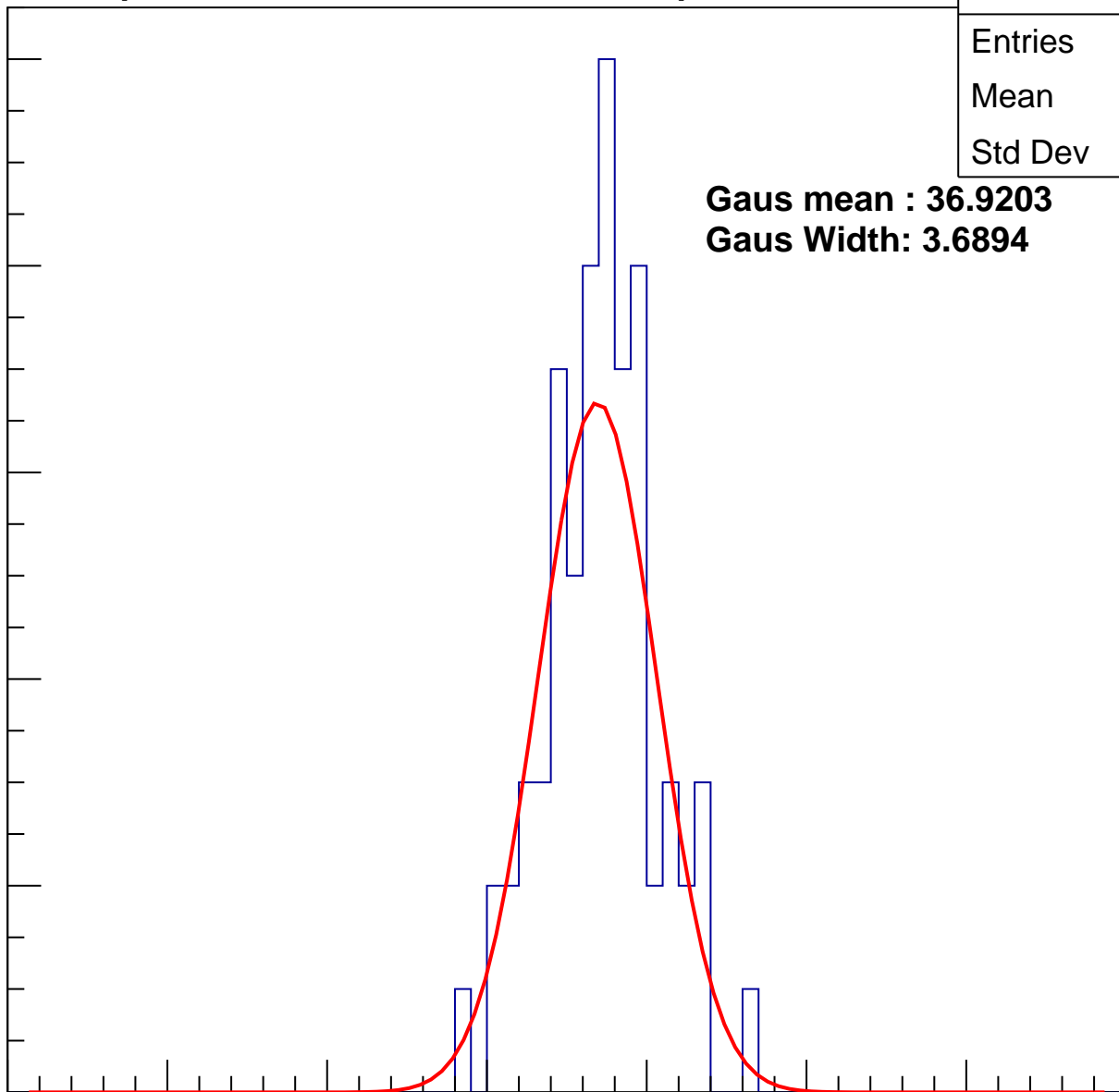
Gaus Width: 3.6894

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch19, adc2

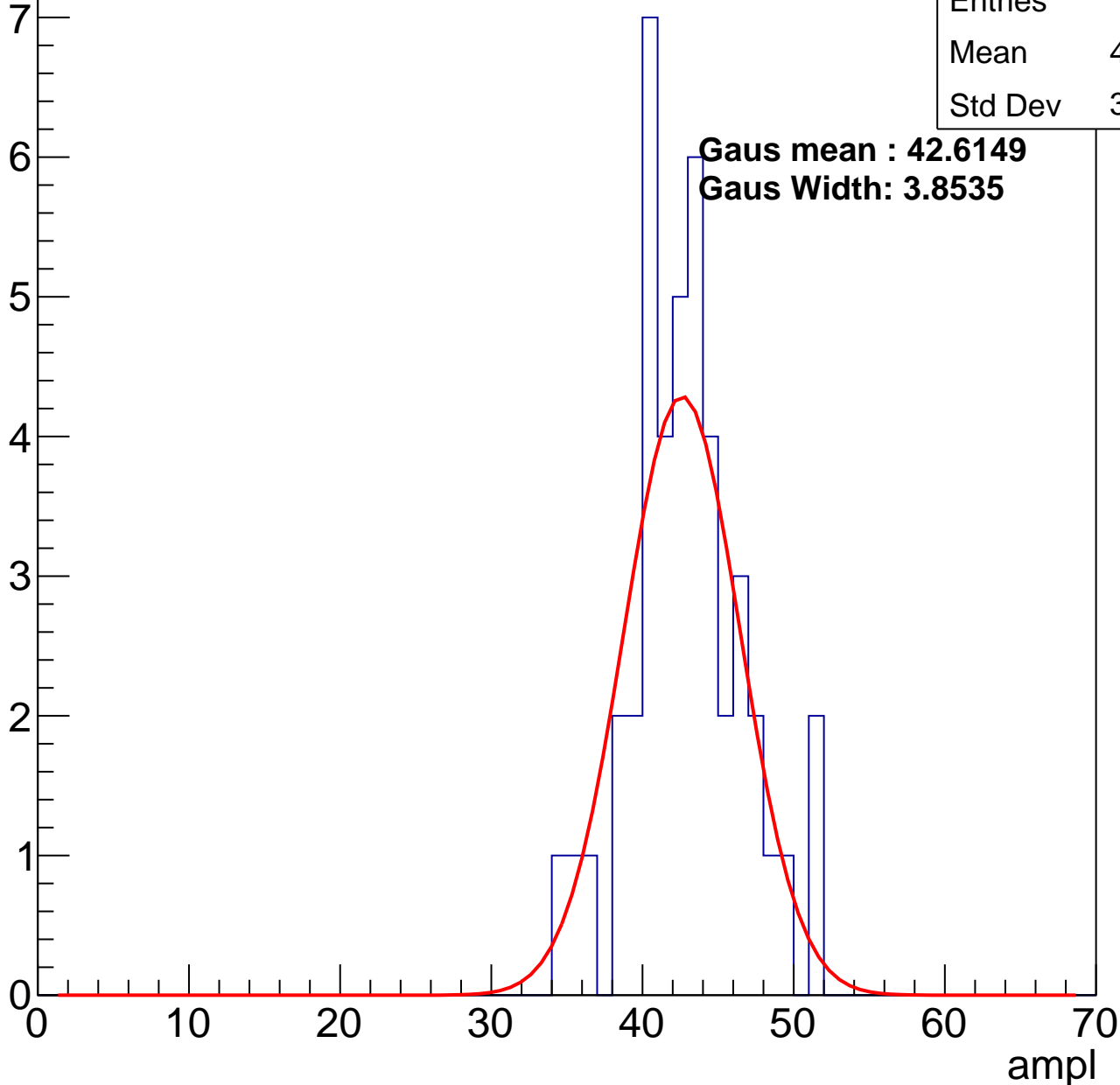
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	42.45
Std Dev	3.714

Gaus mean : 42.6149

Gaus Width: 3.8535

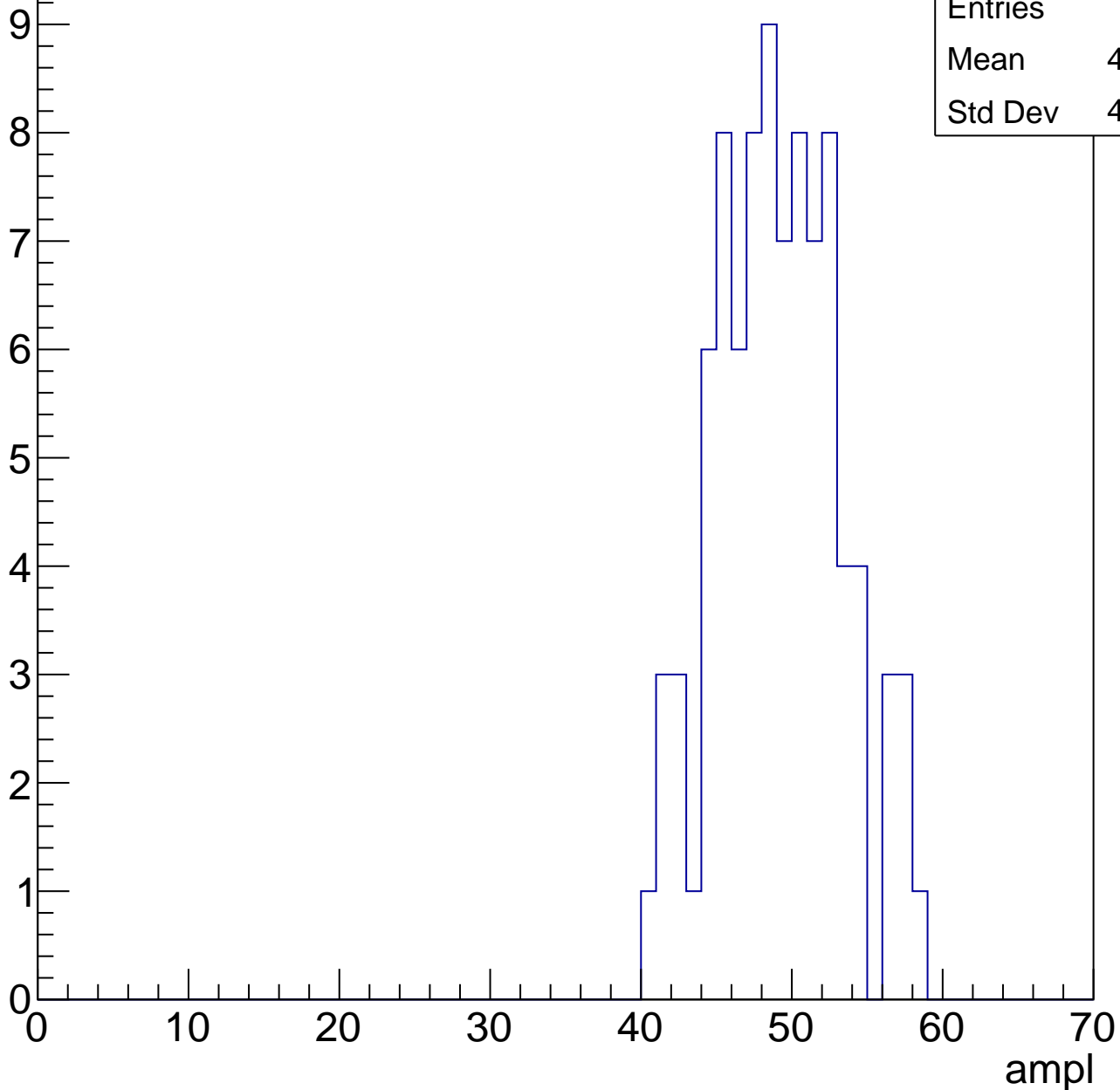


B1L103S, U17-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	48.68
Std Dev	4.098

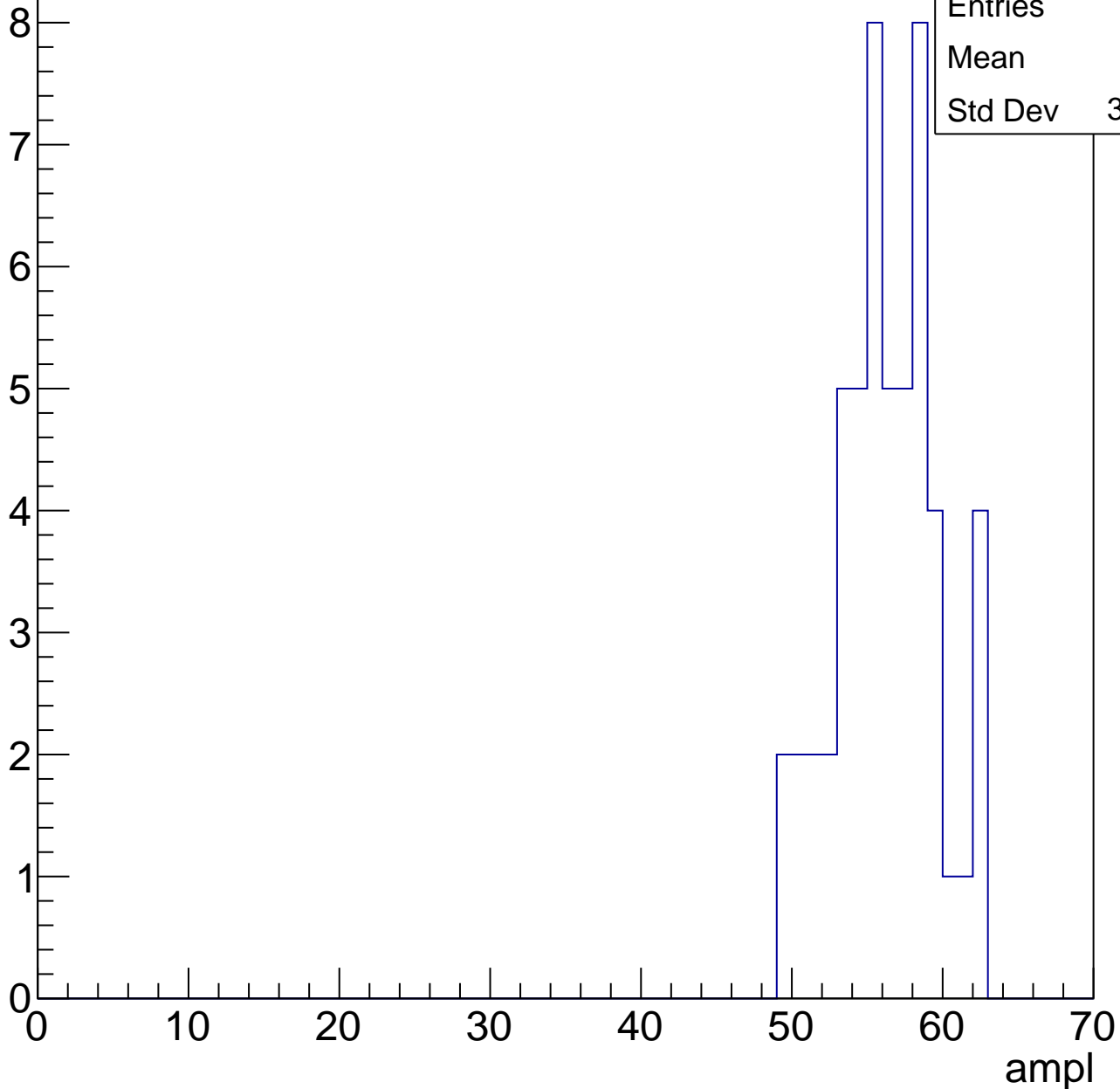


B1L103S, U17-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.8
Std Dev	3.279



B1L103S, U17-ch19, adc5

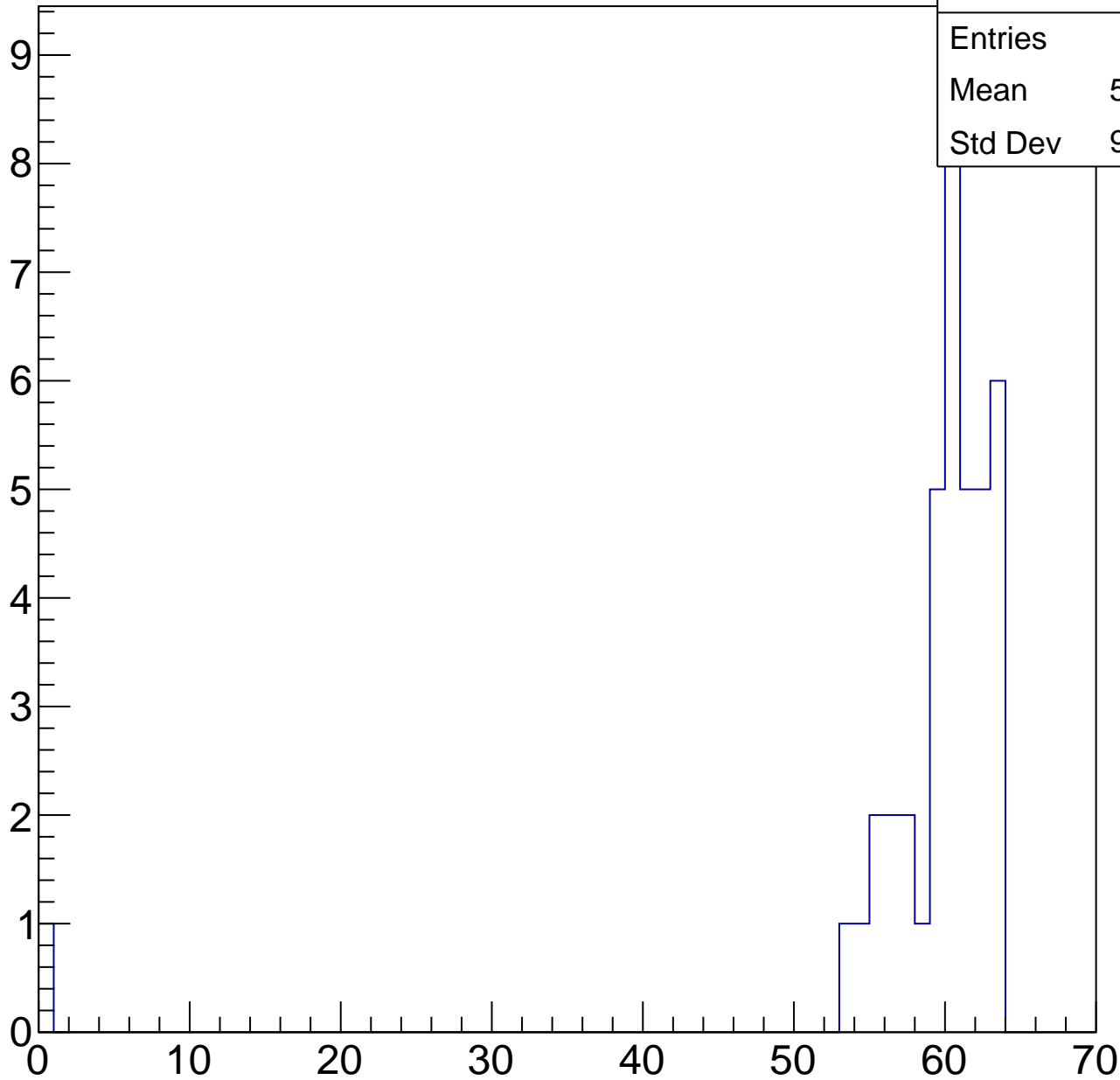
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.23
Std Dev	9.679

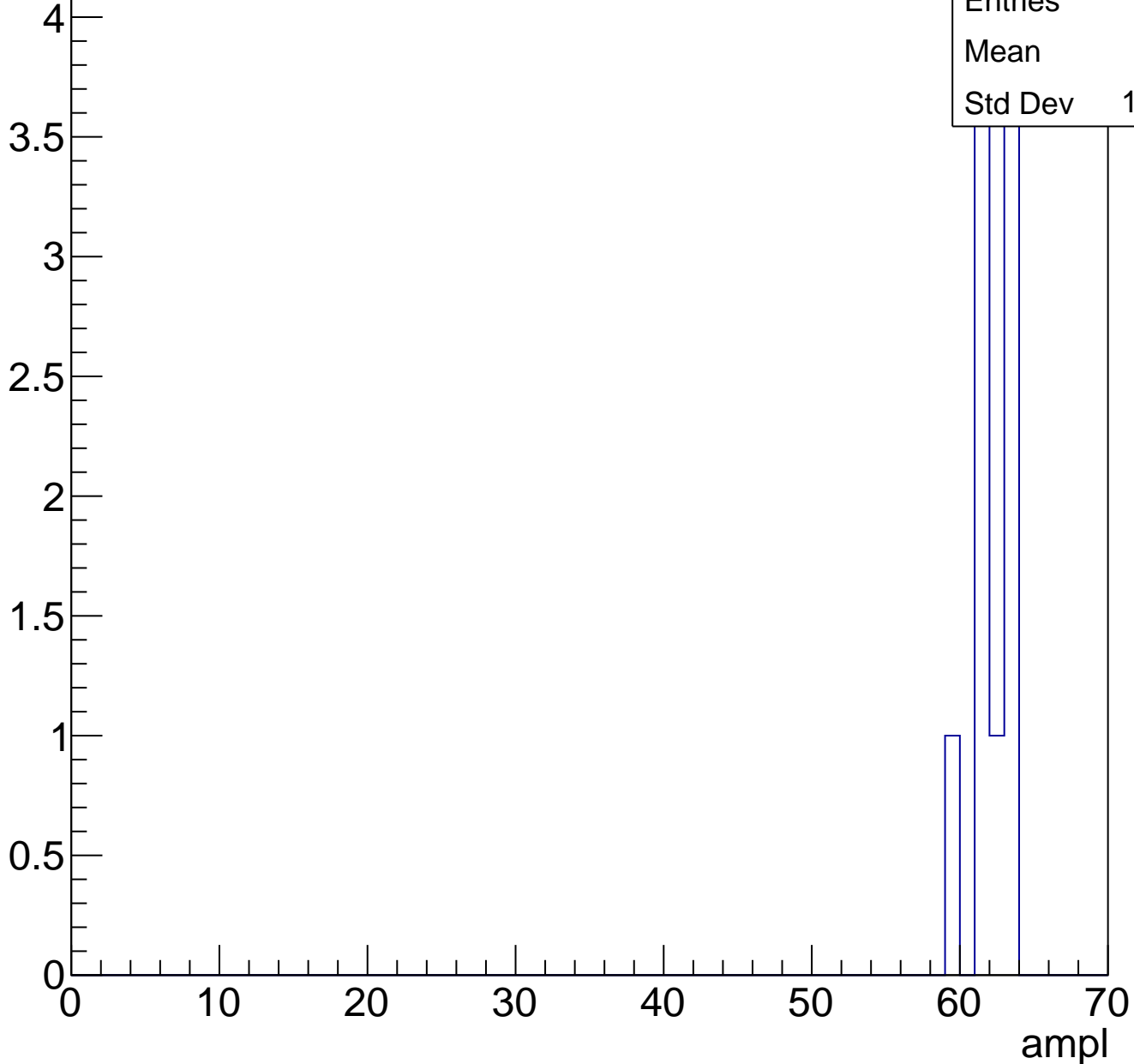
ampl



B1L103S, U17-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch20, adc0

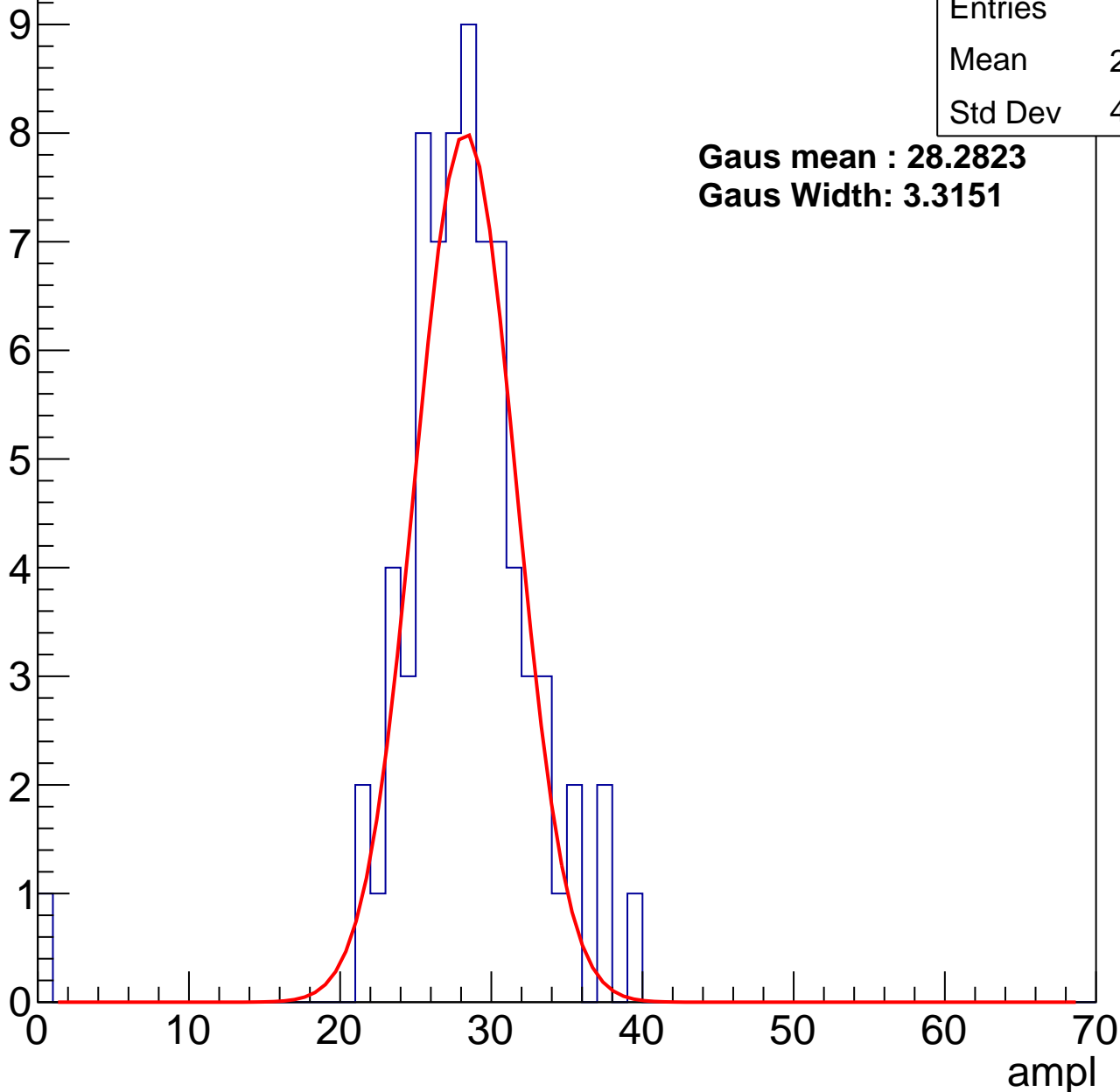
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.77
Std Dev	4.939

Gaus mean : 28.2823

Gaus Width: 3.3151



B1L103S, U17-ch20, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	35.41
Std Dev	3.254

Gaus mean : 35.6789

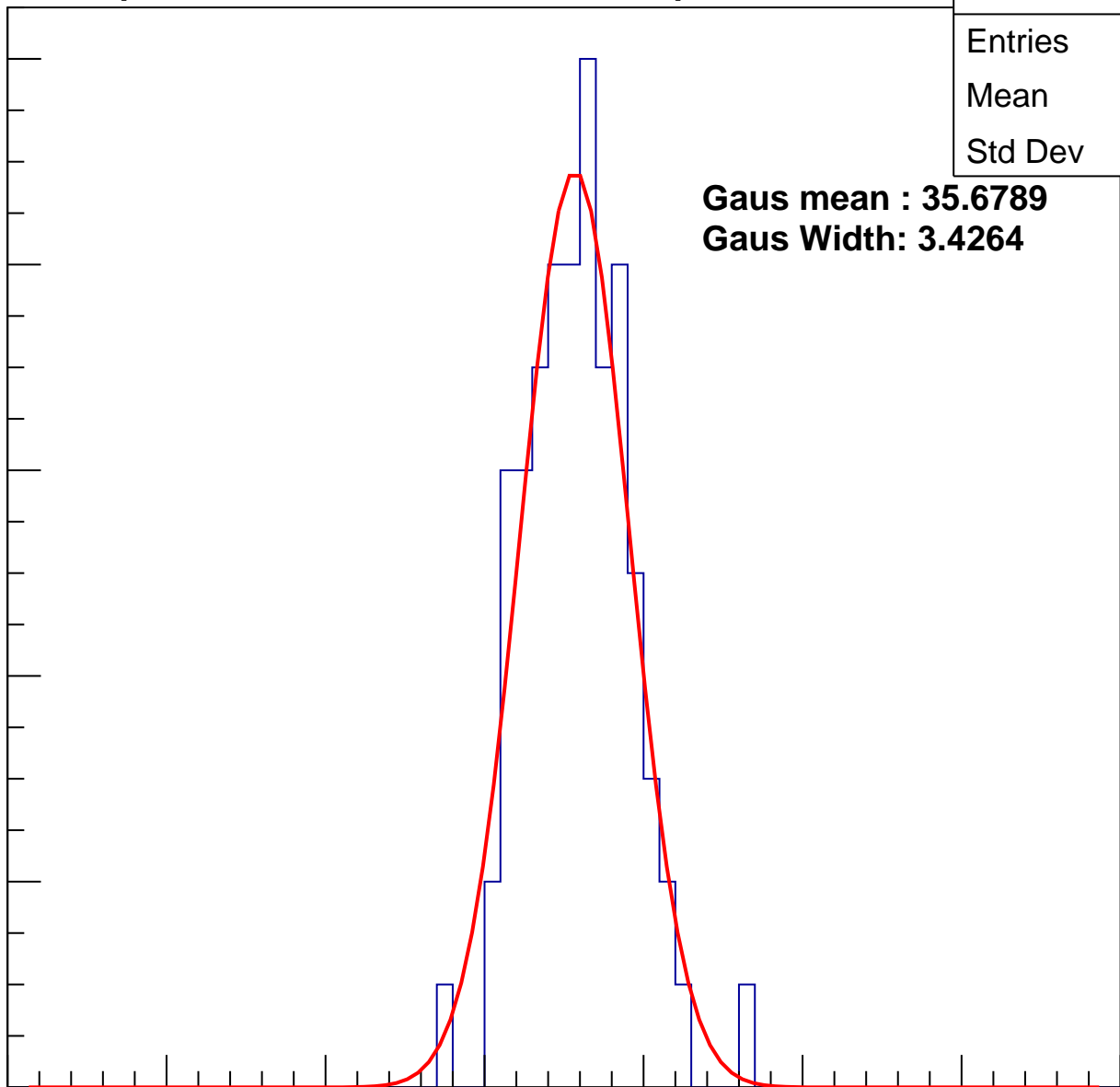
Gaus Width: 3.4264

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch20, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	43.53
Std Dev	3.529

Gaus mean : 43.5117

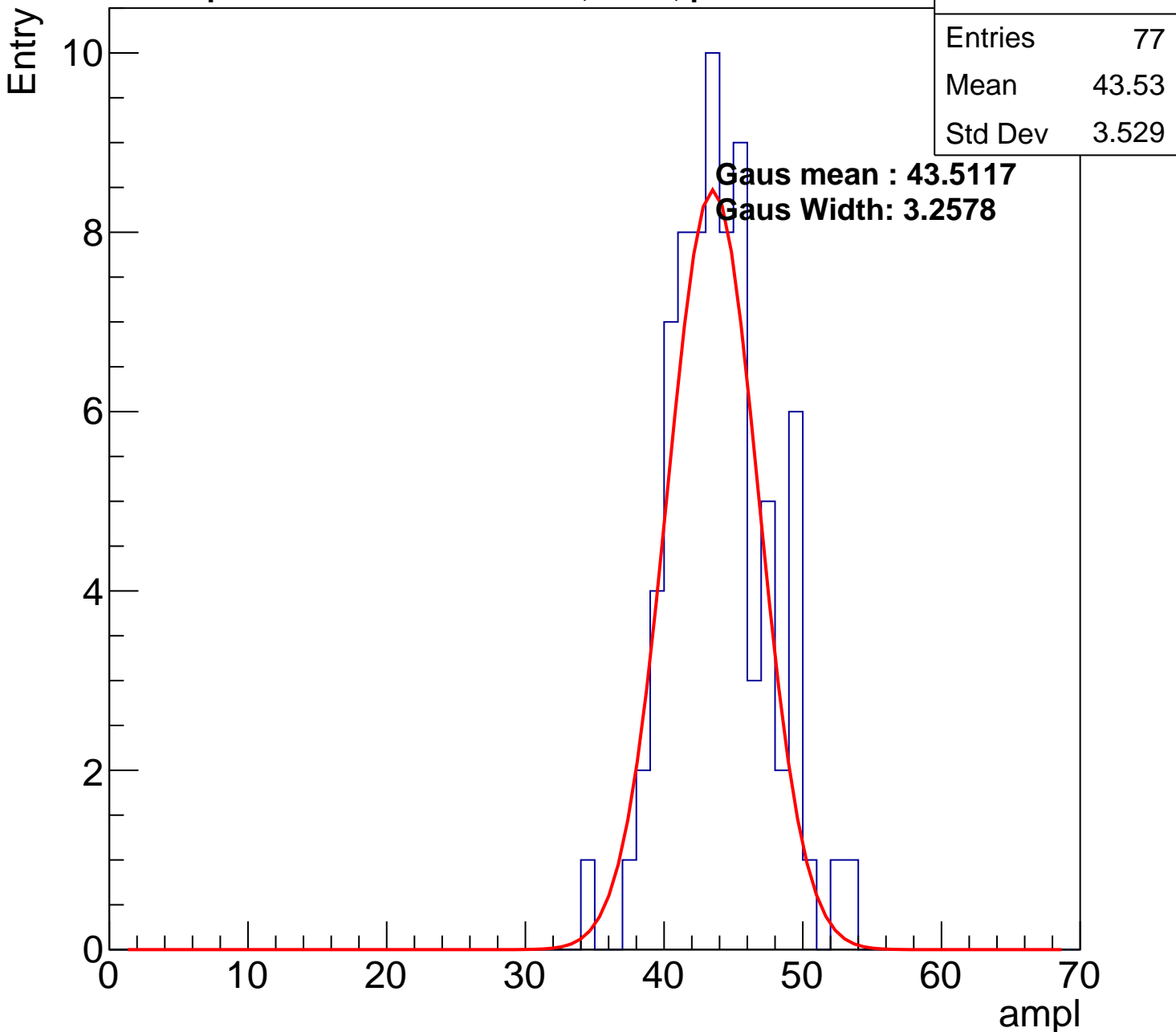
Gaus Width: 3.2578

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

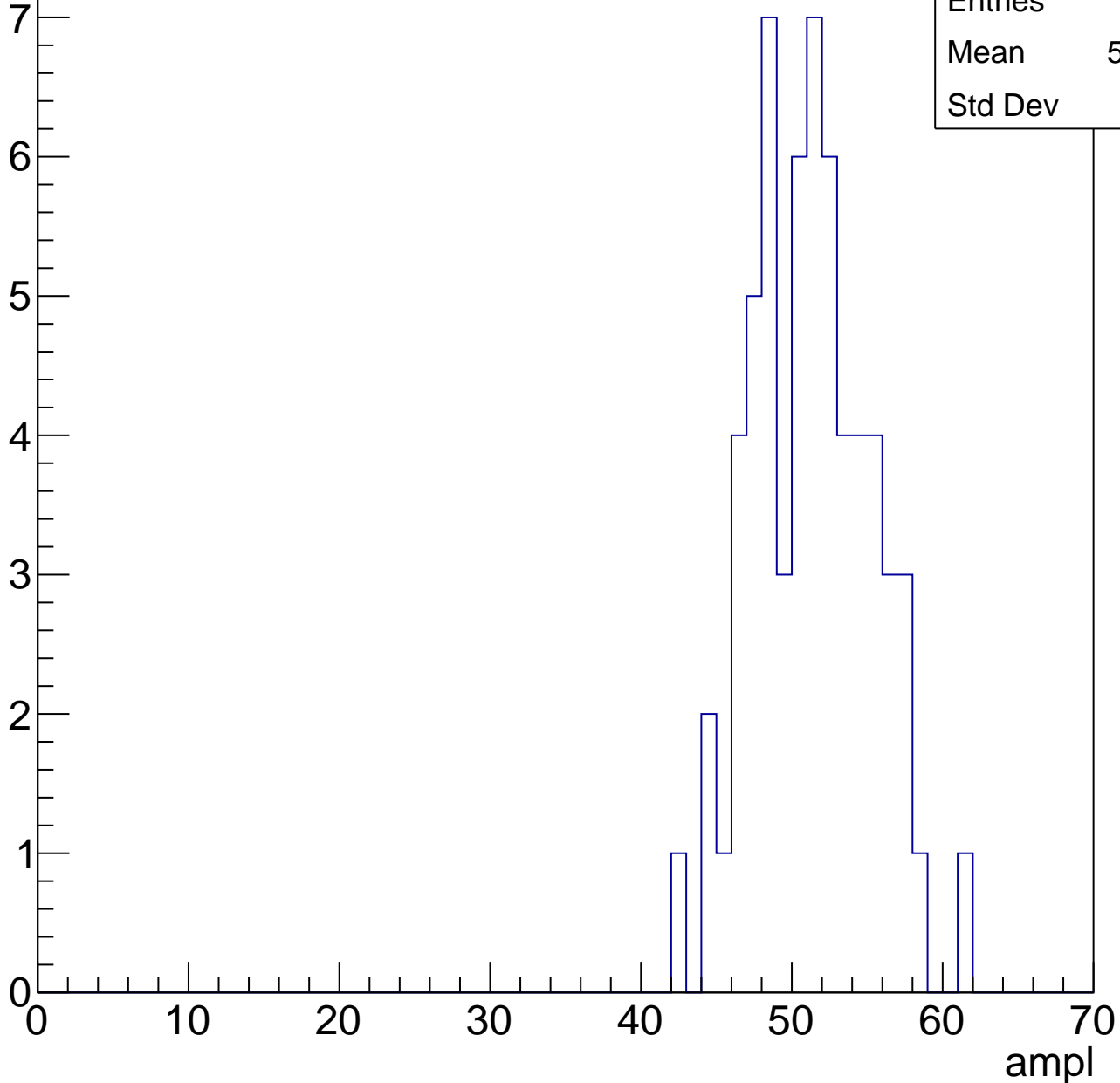


B1L103S, U17-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	50.84
Std Dev	3.87

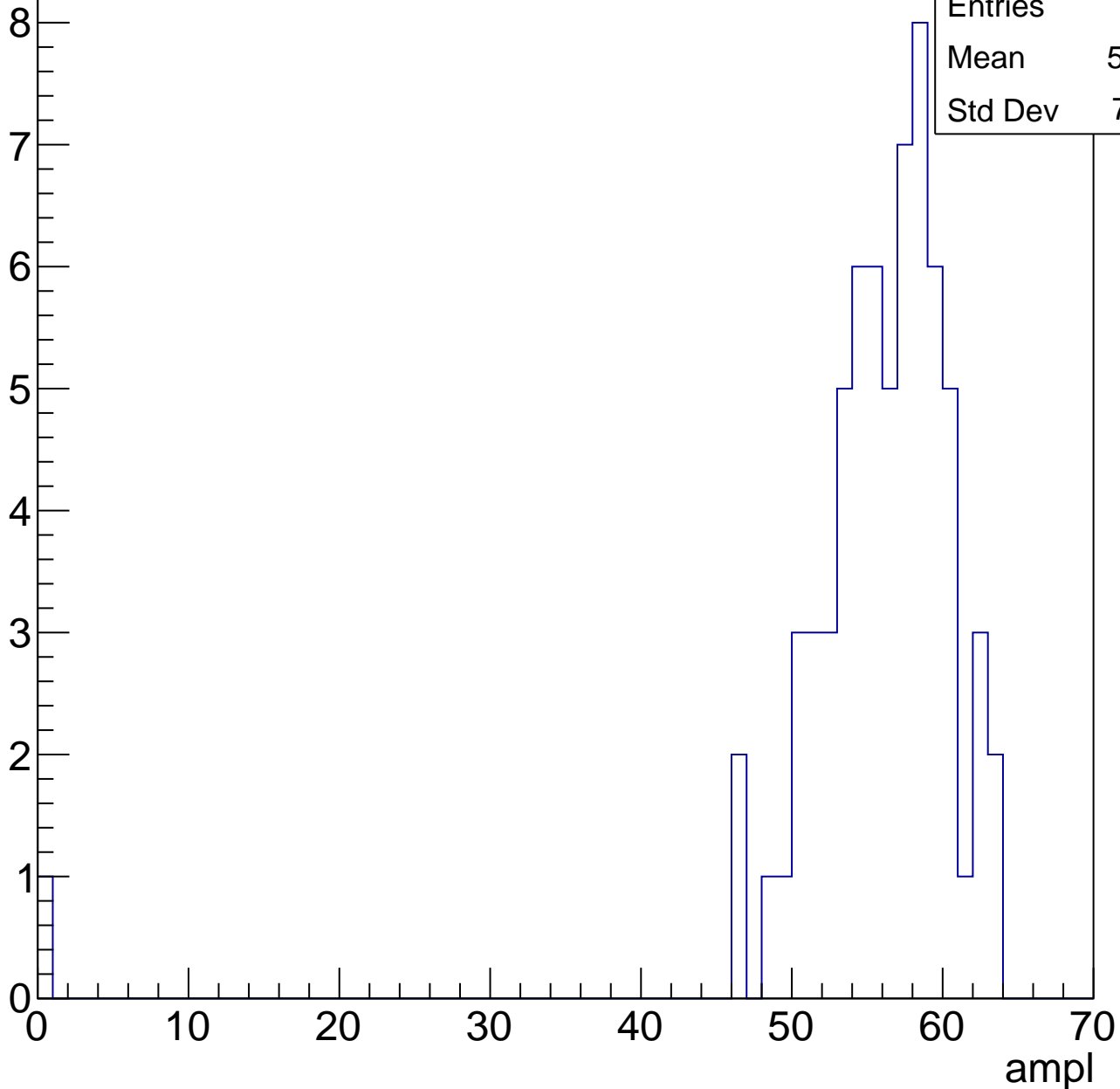


B1L103S, U17-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	54.96
Std Dev	7.751

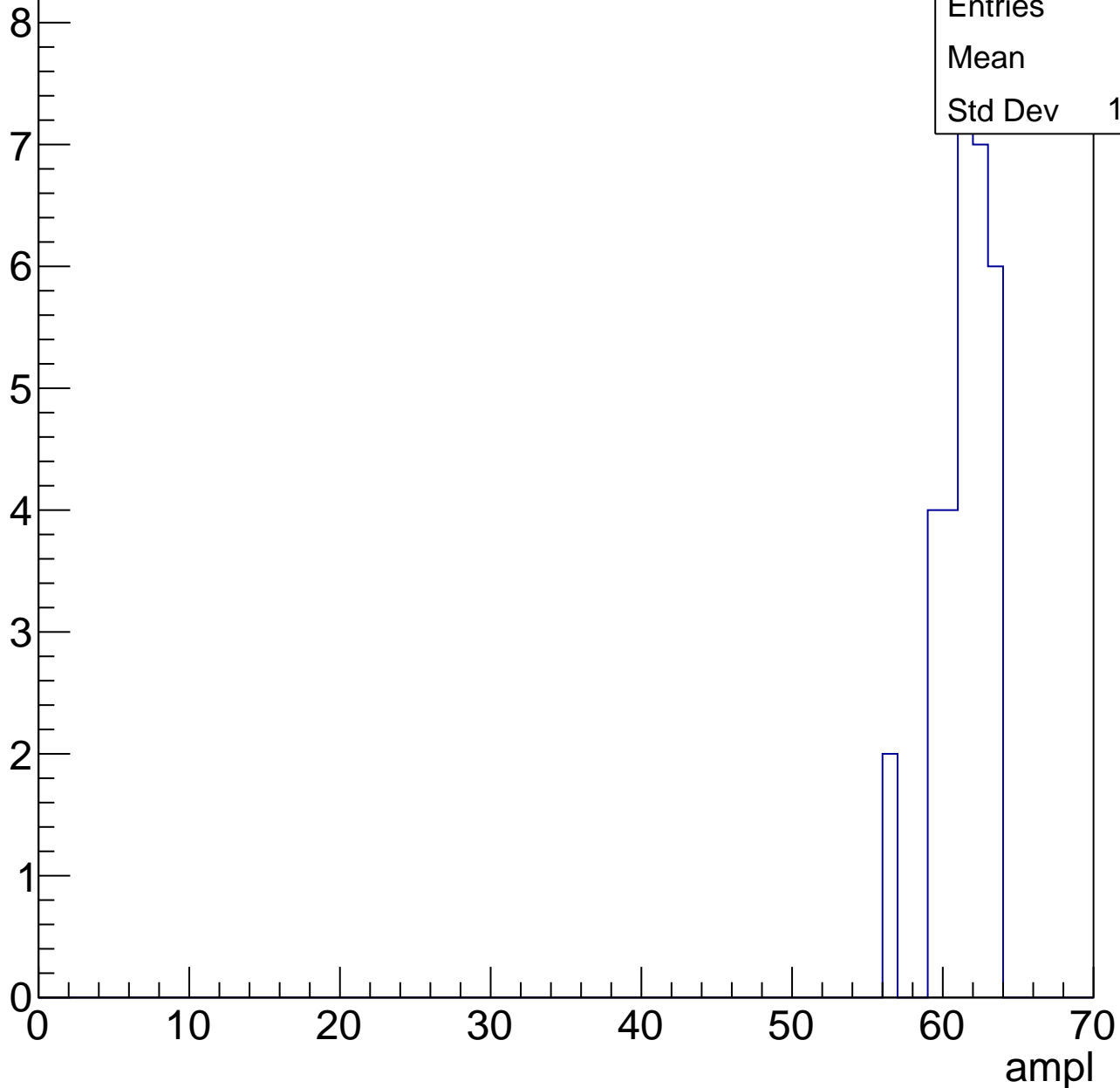


B1L103S, U17-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

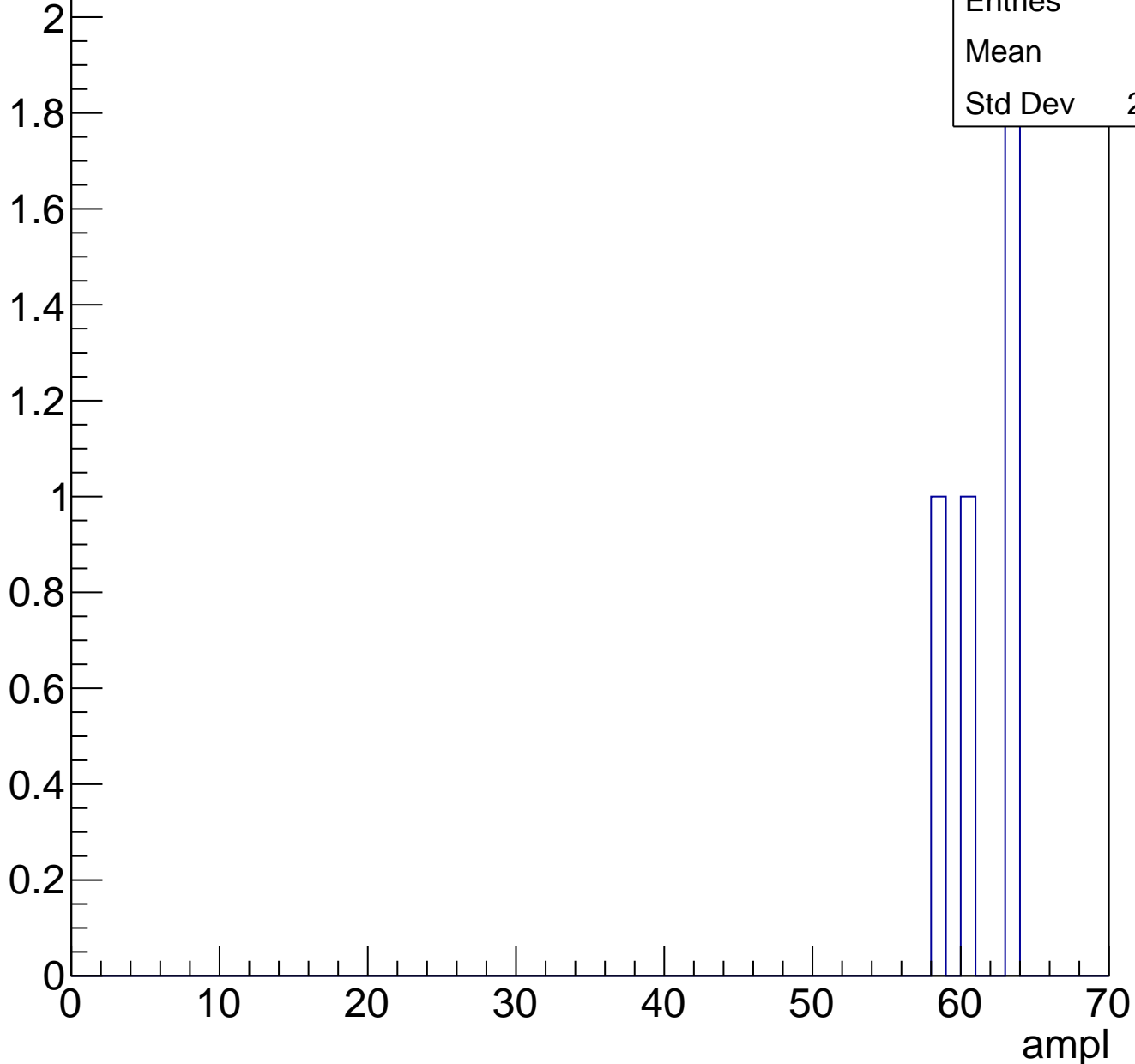
Entries	31
Mean	60.9
Std Dev	1.802



B1L103S, U17-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch21, adc0

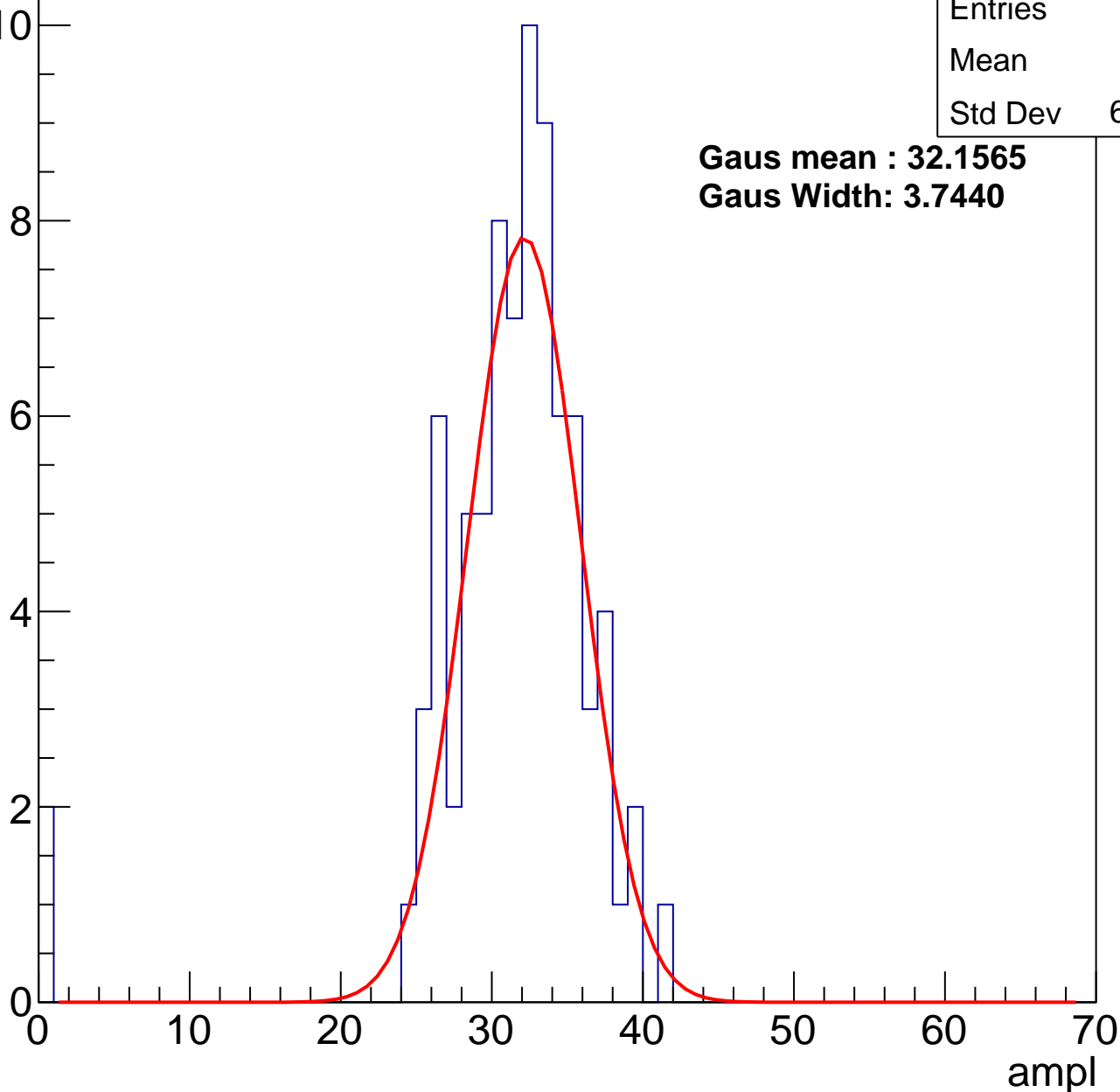
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	30.8
Std Dev	6.109

Gaus mean : 32.1565

Gaus Width: 3.7440



B1L103S, U17-ch21, adc1

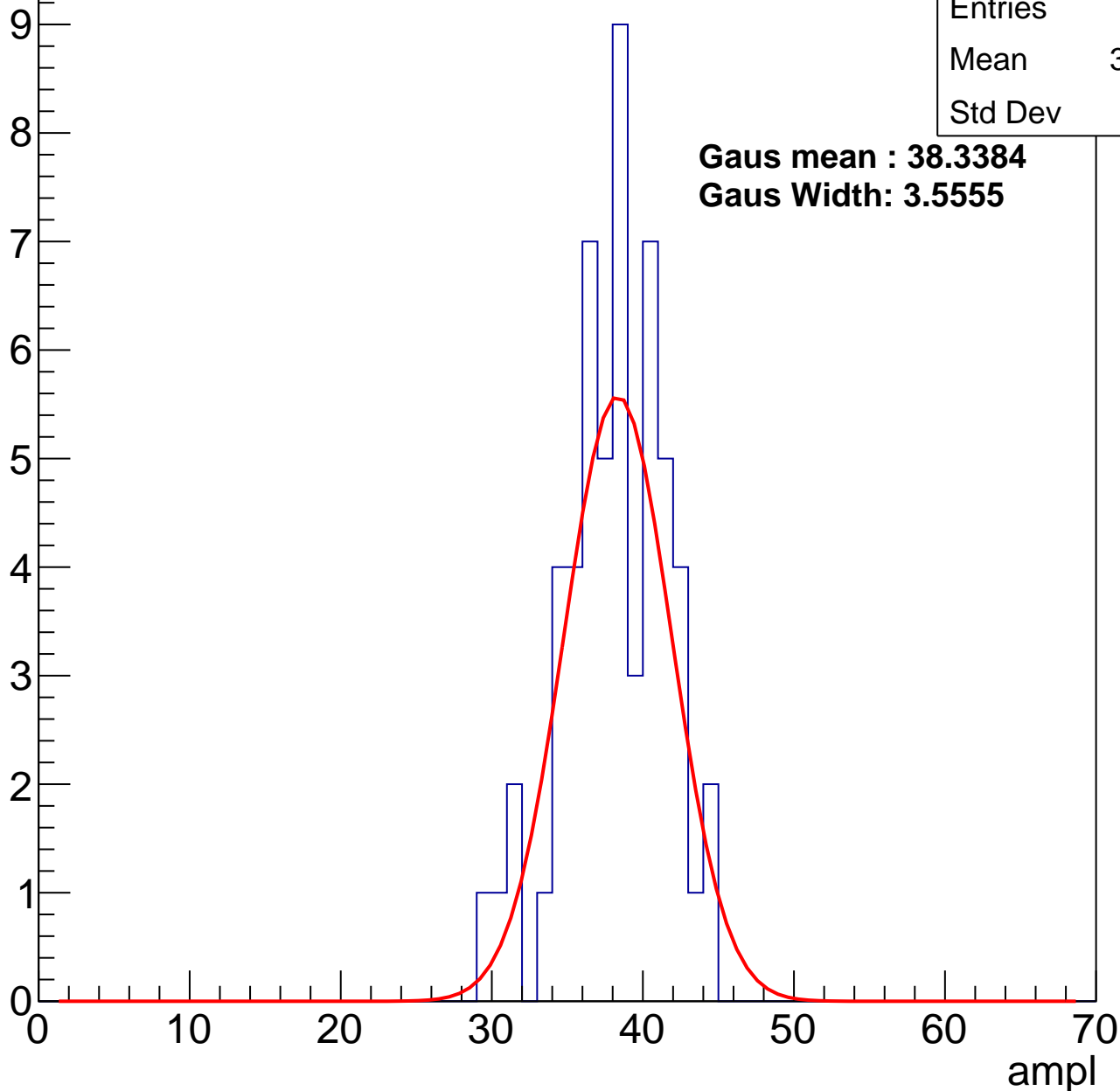
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	37.68
Std Dev	3.36

Gaus mean : 38.3384

Gaus Width: 3.5555



B1L103S, U17-ch21, adc2

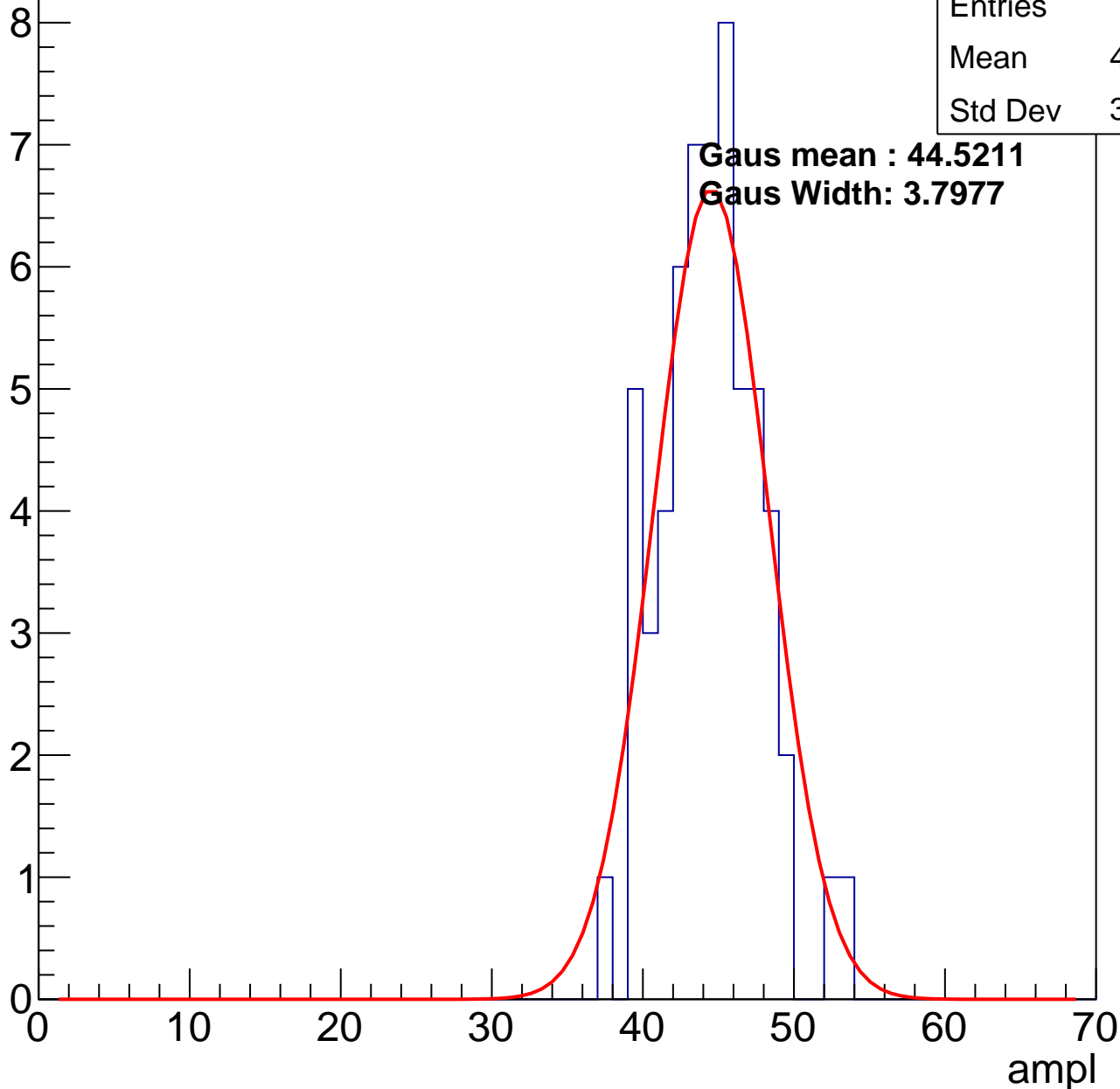
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	44.02
Std Dev	3.244

Gaus mean : 44.5211

Gaus Width: 3.7977



B1L103S, U17-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	50.85
Std Dev	3.638

Entry

10

8

6

4

2

0

0

10

20

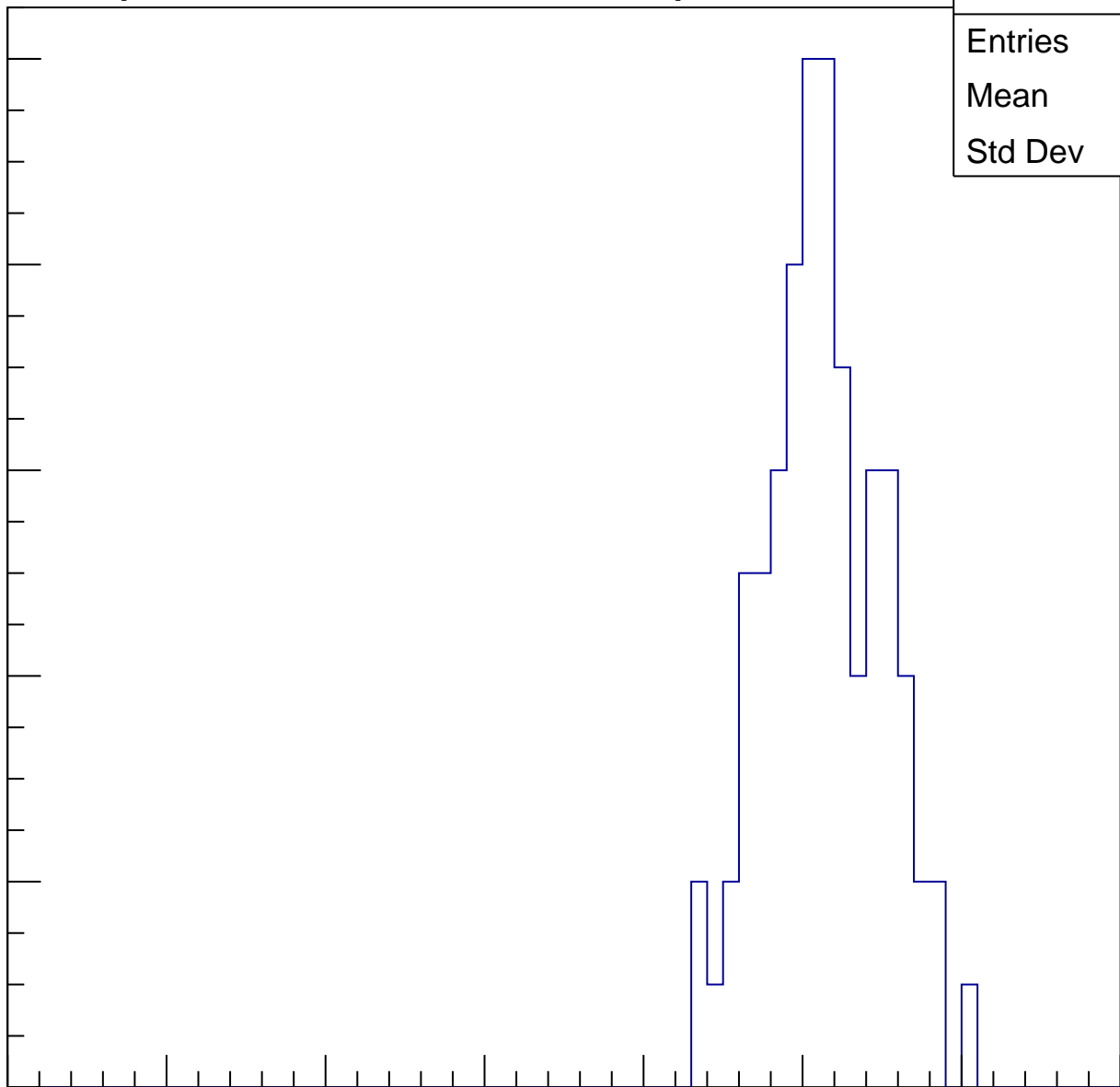
30

40

50

60

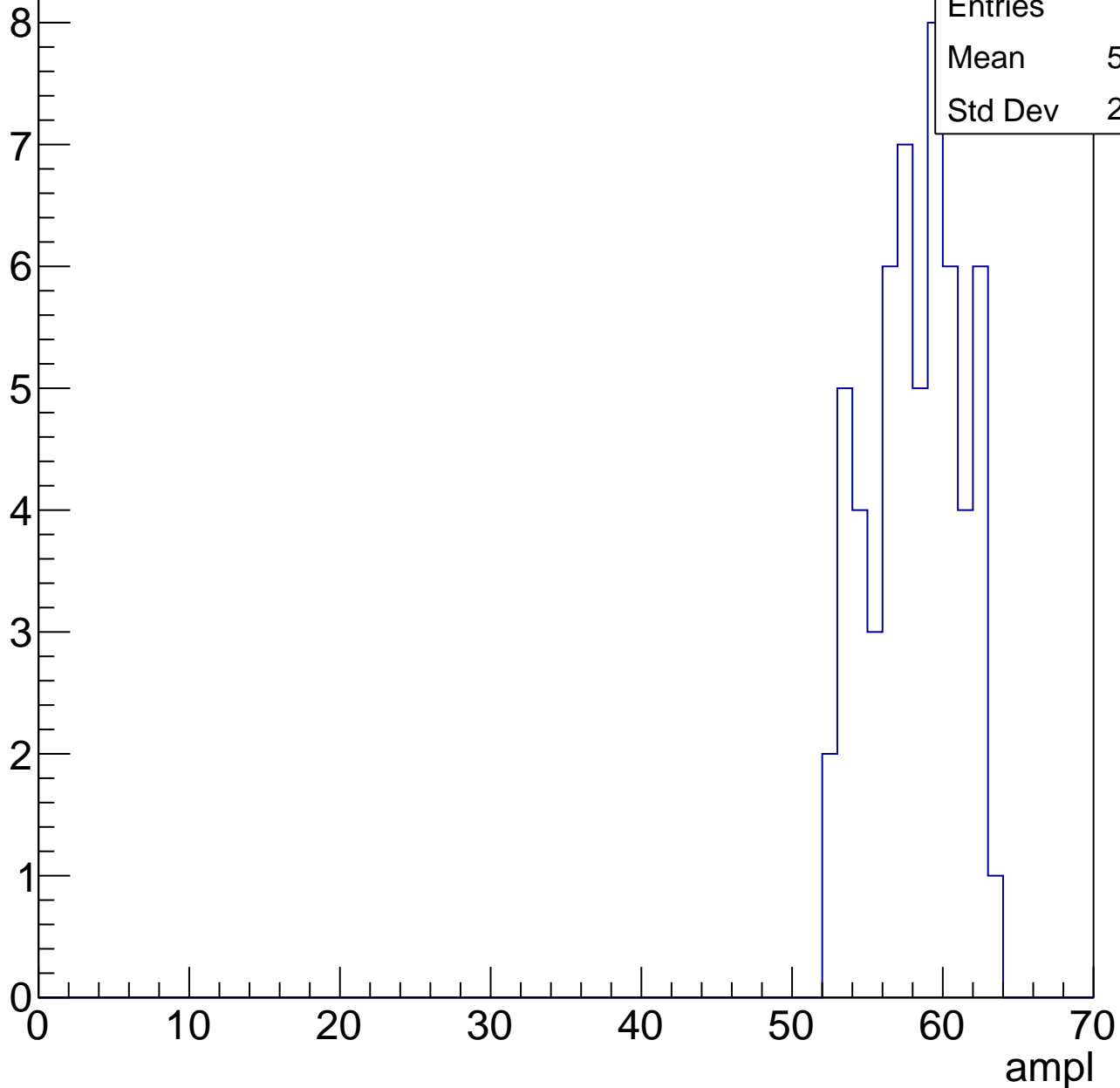
ampl



B1L103S, U17-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

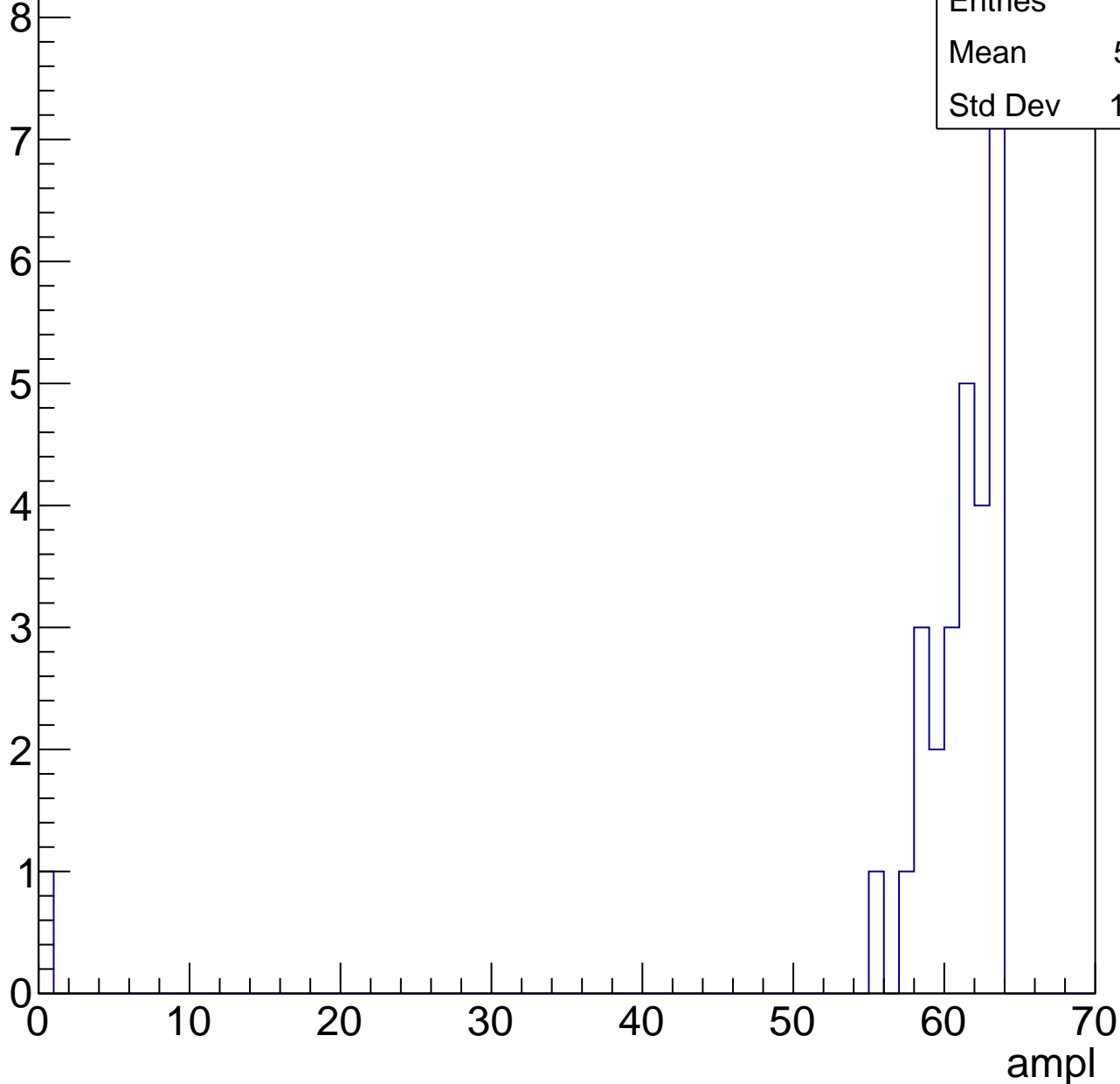


B1L103S, U17-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	58.61
Std Dev	11.47



B1L103S, U17-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

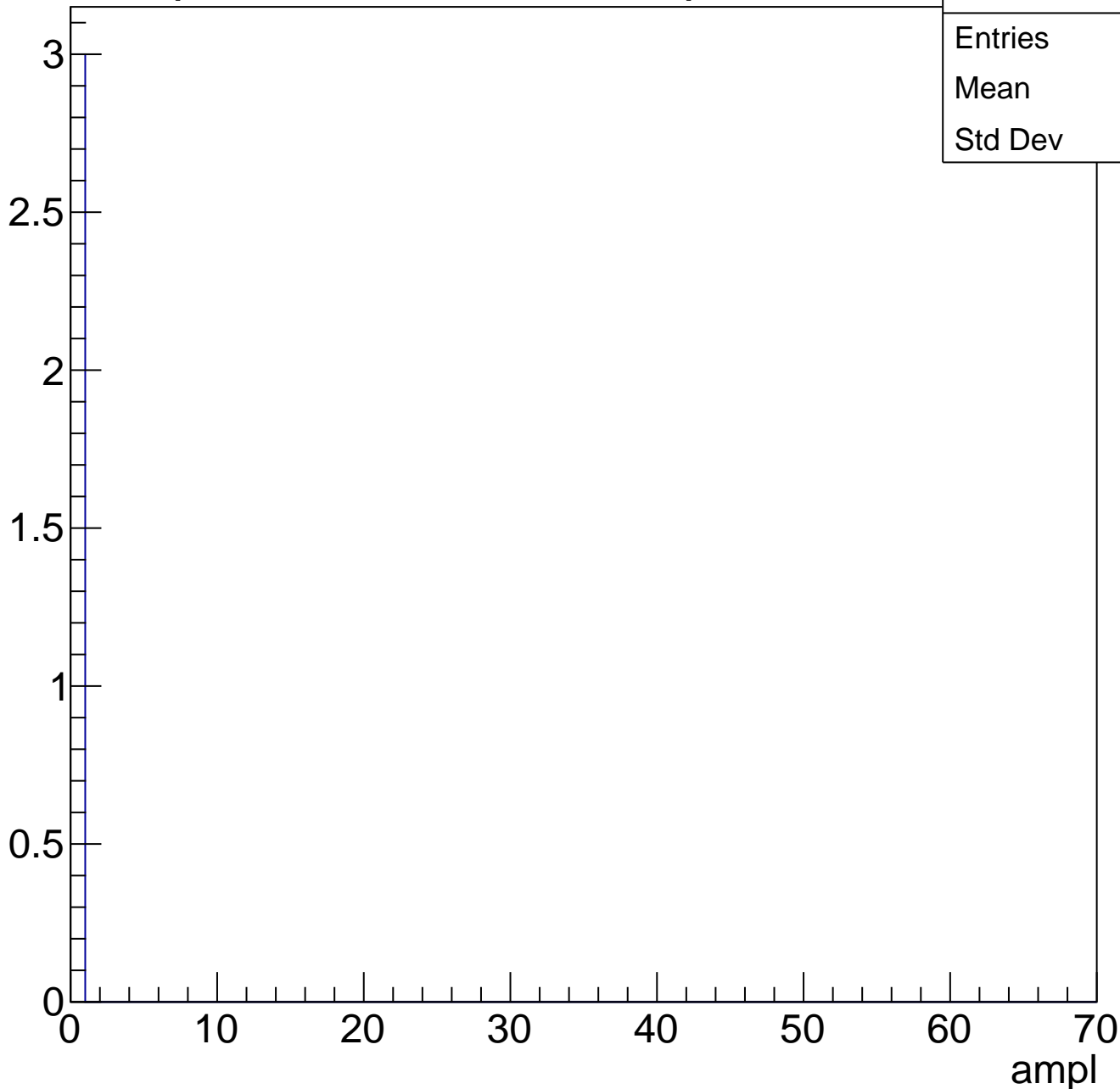
Entry



B1L103S, U17-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch22, adc0

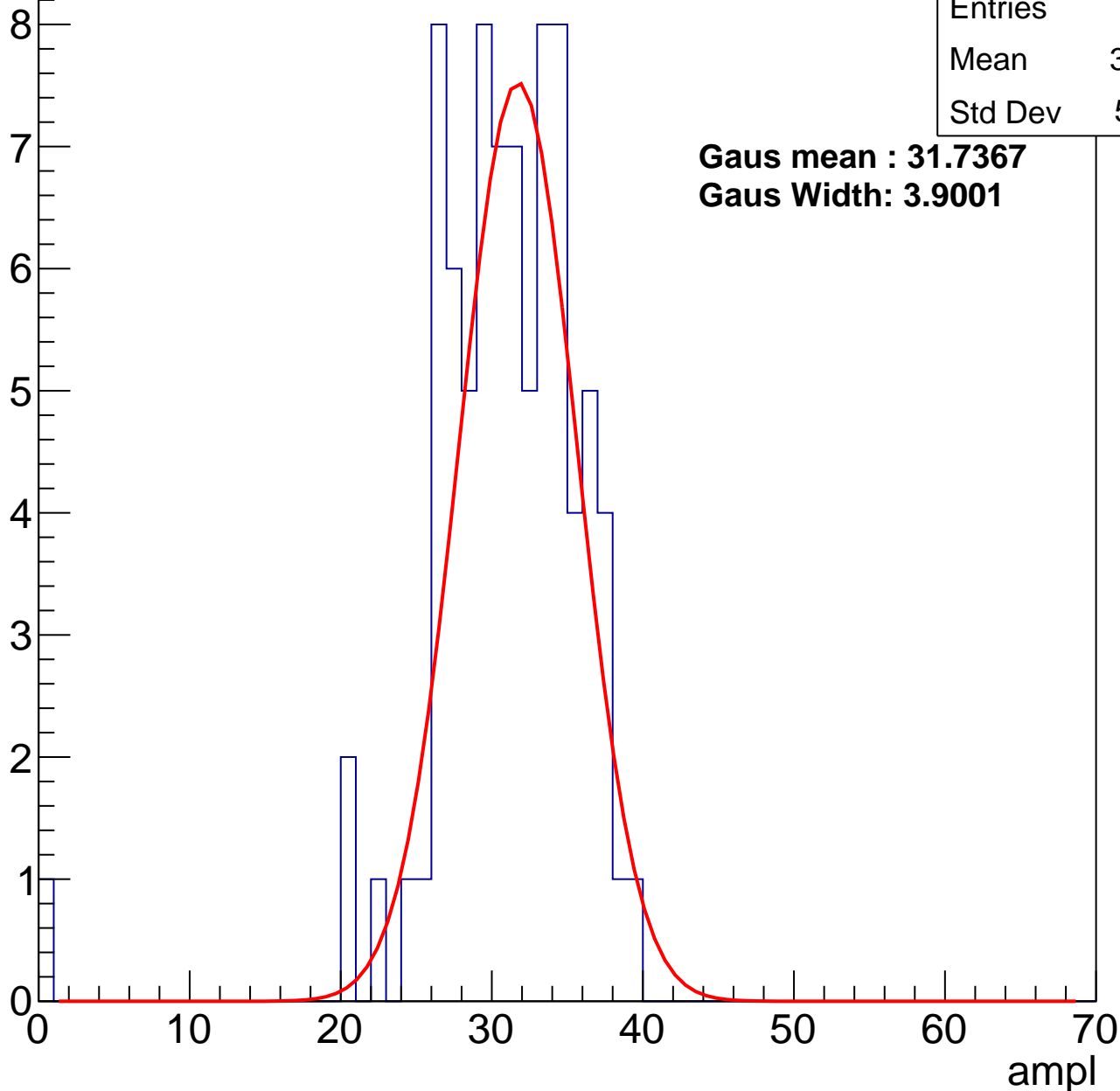
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	30.37
Std Dev	5.231

Gaus mean : 31.7367

Gaus Width: 3.9001



B1L103S, U17-ch22, adc1

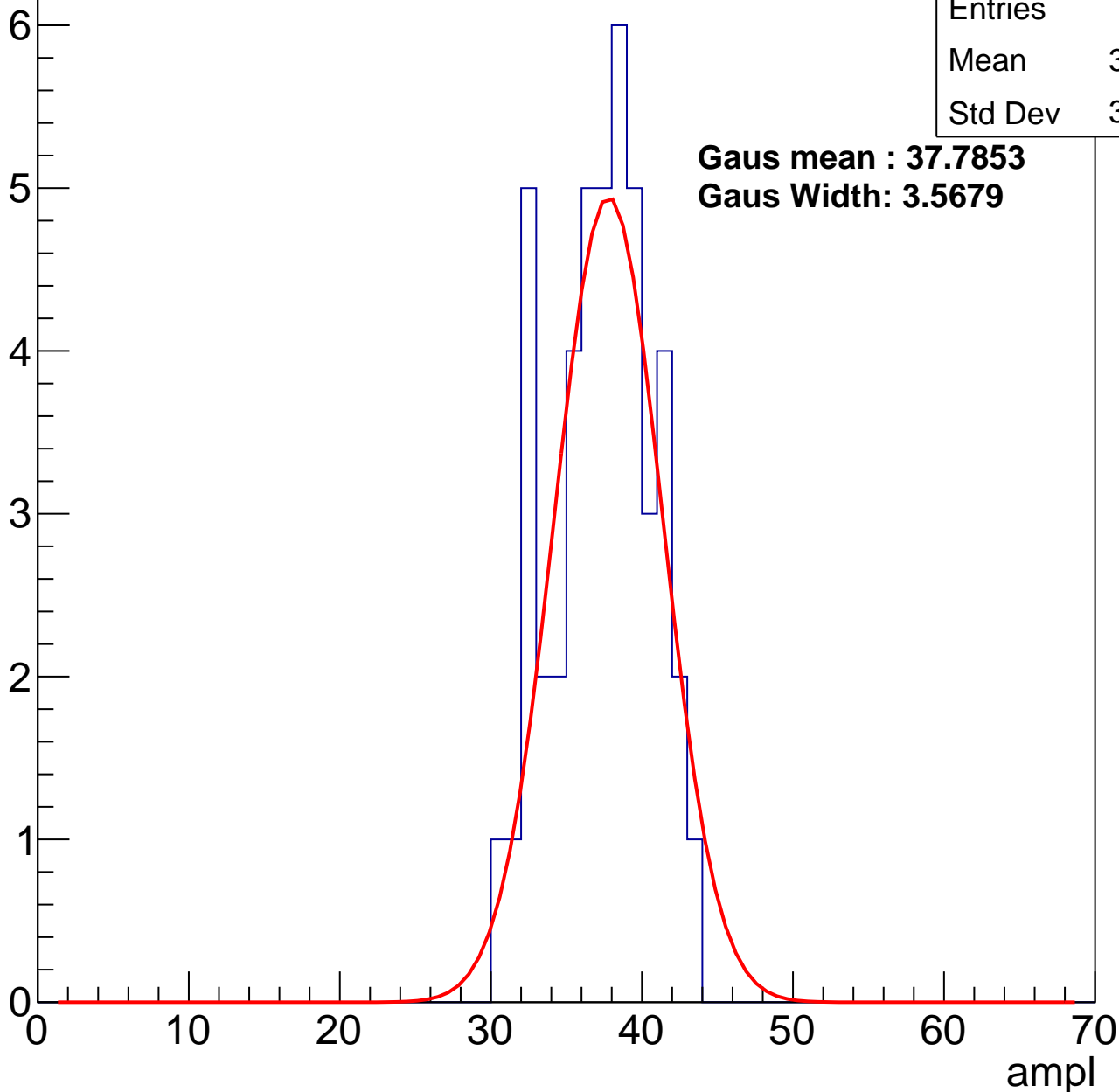
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	36.83
Std Dev	3.239

Gaus mean : 37.7853

Gaus Width: 3.5679



B1L103S, U17-ch22, adc2

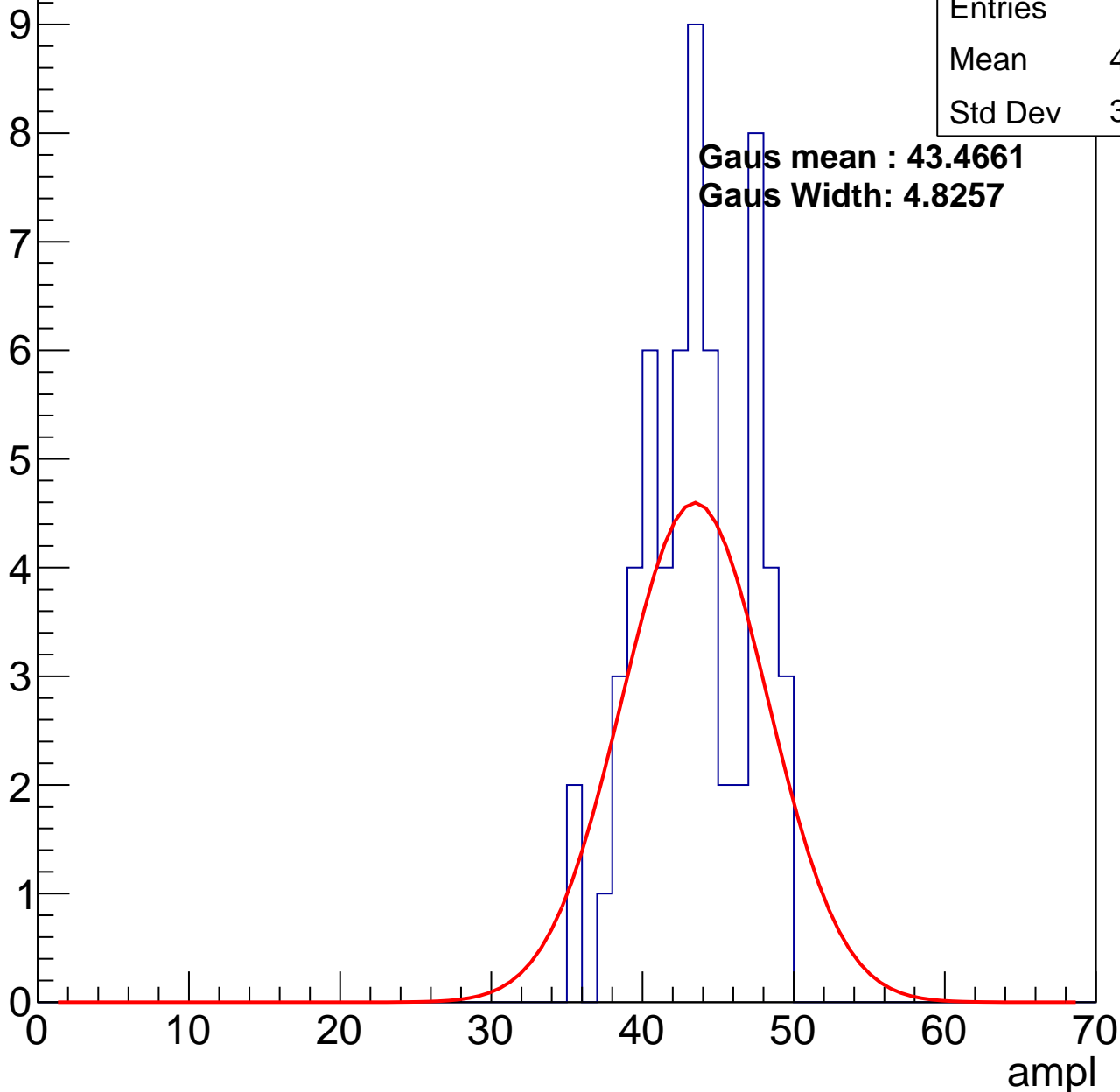
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.02
Std Dev	3.528

Gaus mean : 43.4661

Gaus Width: 4.8257

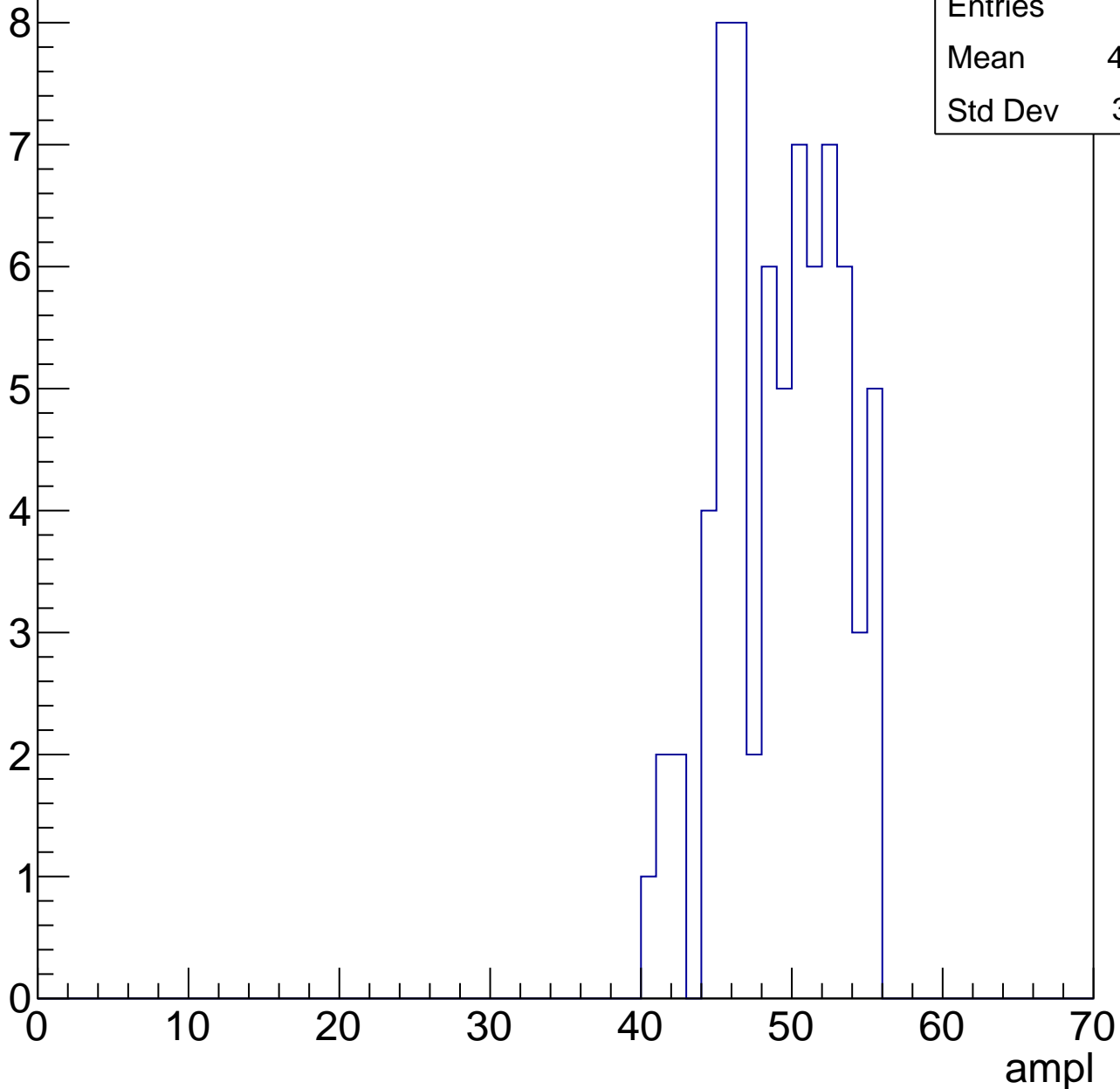


B1L103S, U17-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.78
Std Dev	3.841

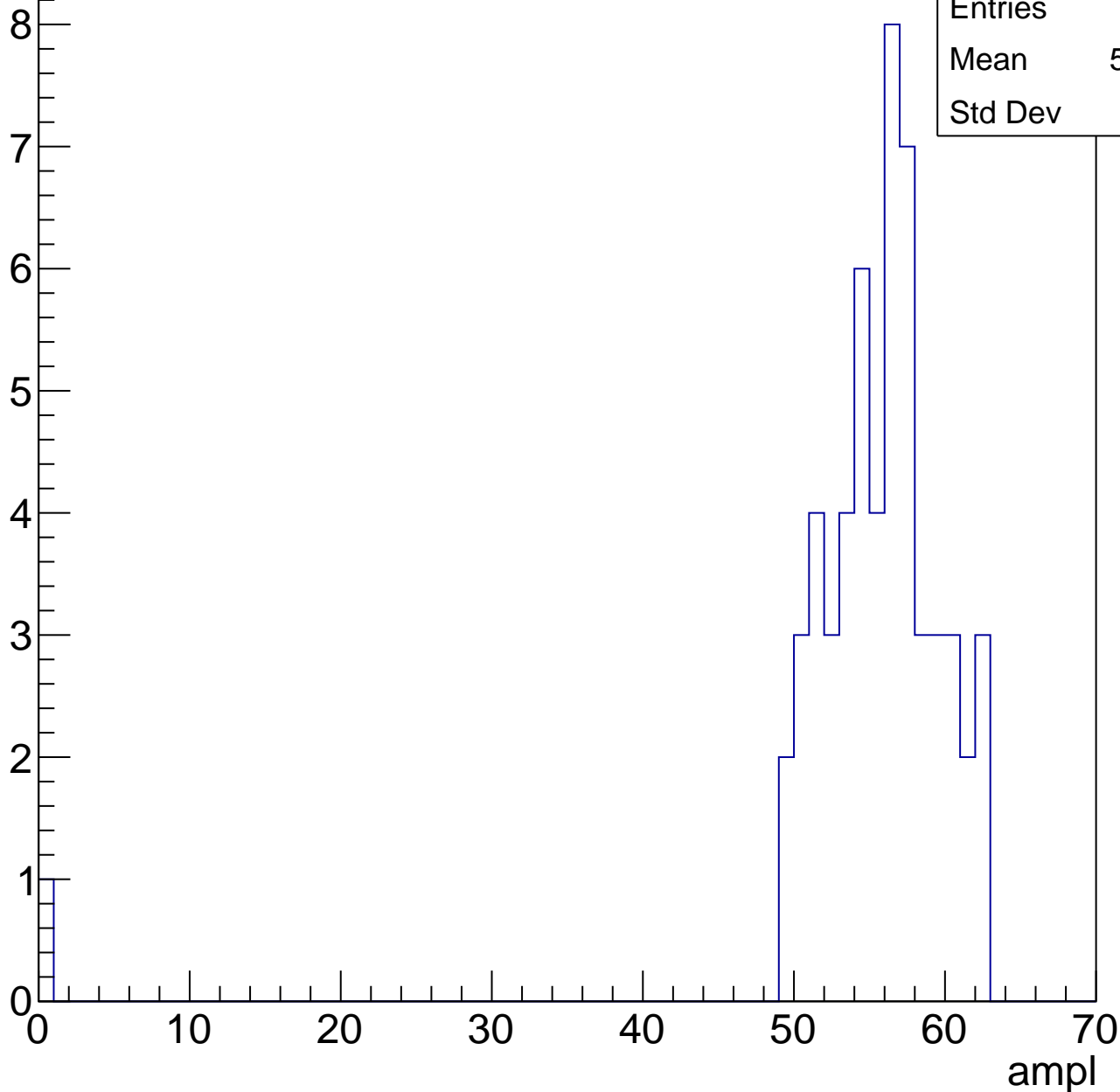


B1L103S, U17-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

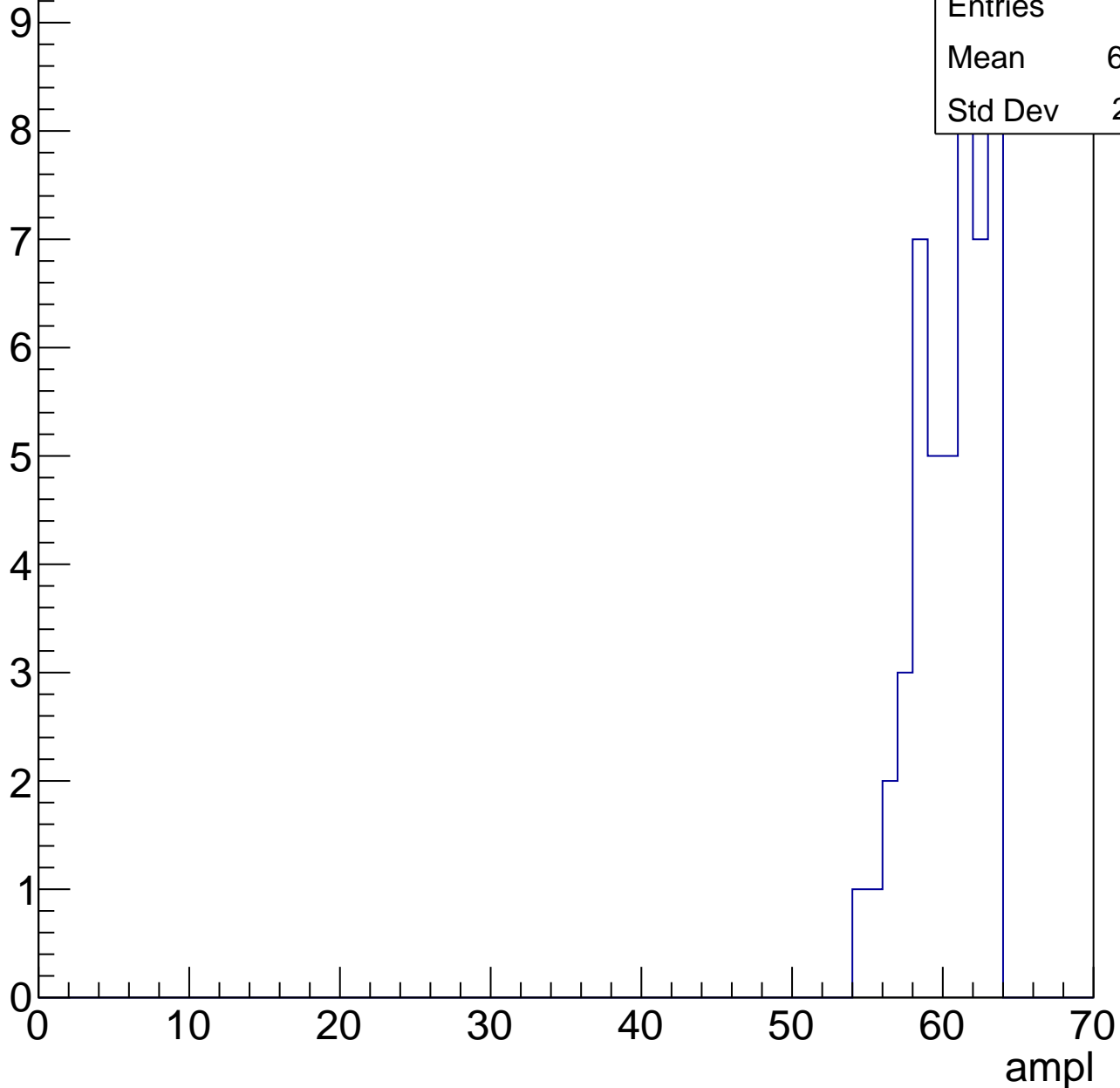
Entries	56
Mean	54.46
Std Dev	8.1



B1L103S, U17-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	48
Mean	60.04
Std Dev	2.371

B1L103S, U17-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

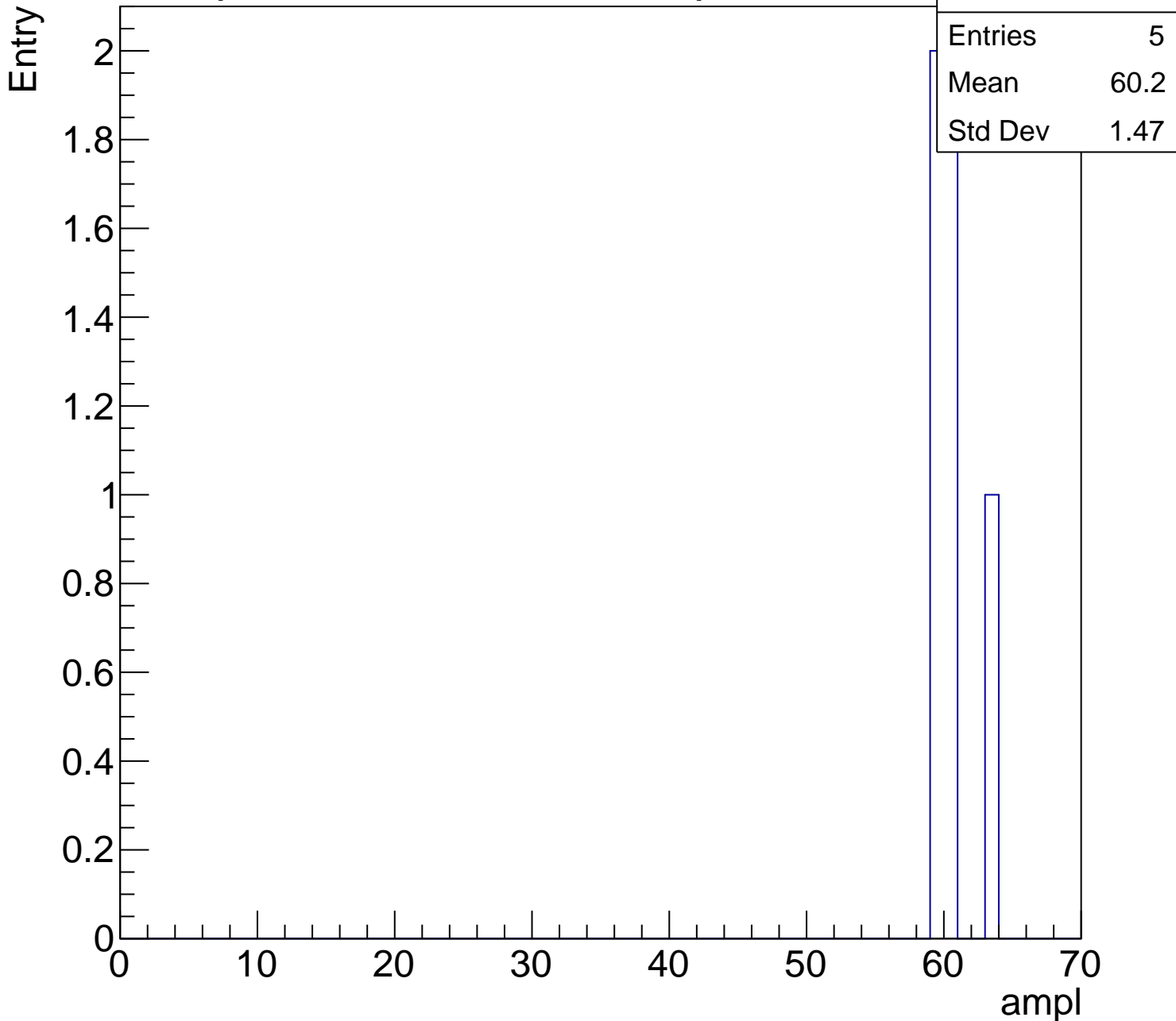
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.2
Std Dev	1.47

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch23, adc0

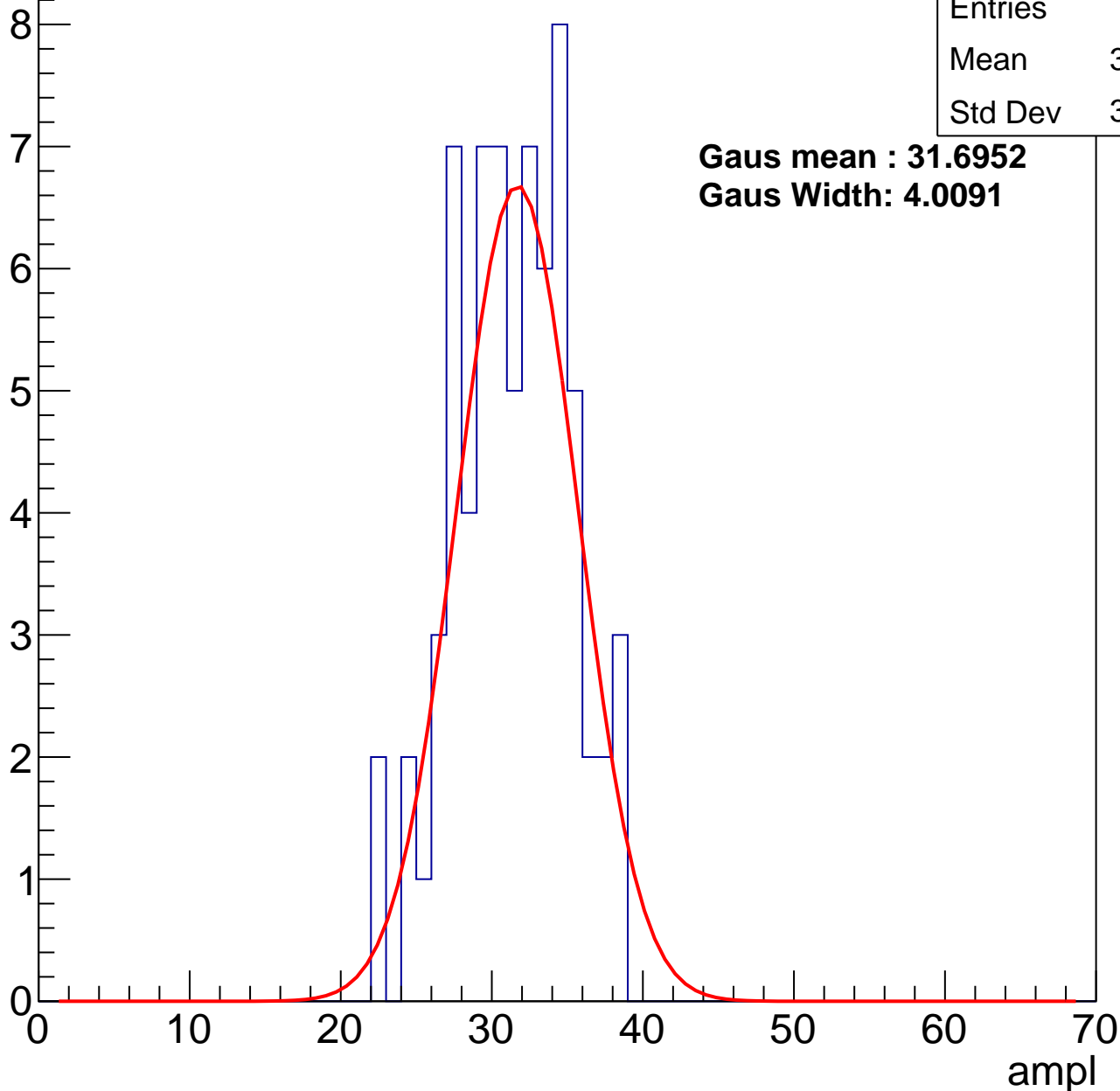
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.89
Std Dev	3.762

Gaus mean : 31.6952

Gaus Width: 4.0091



B1L103S, U17-ch23, adc1

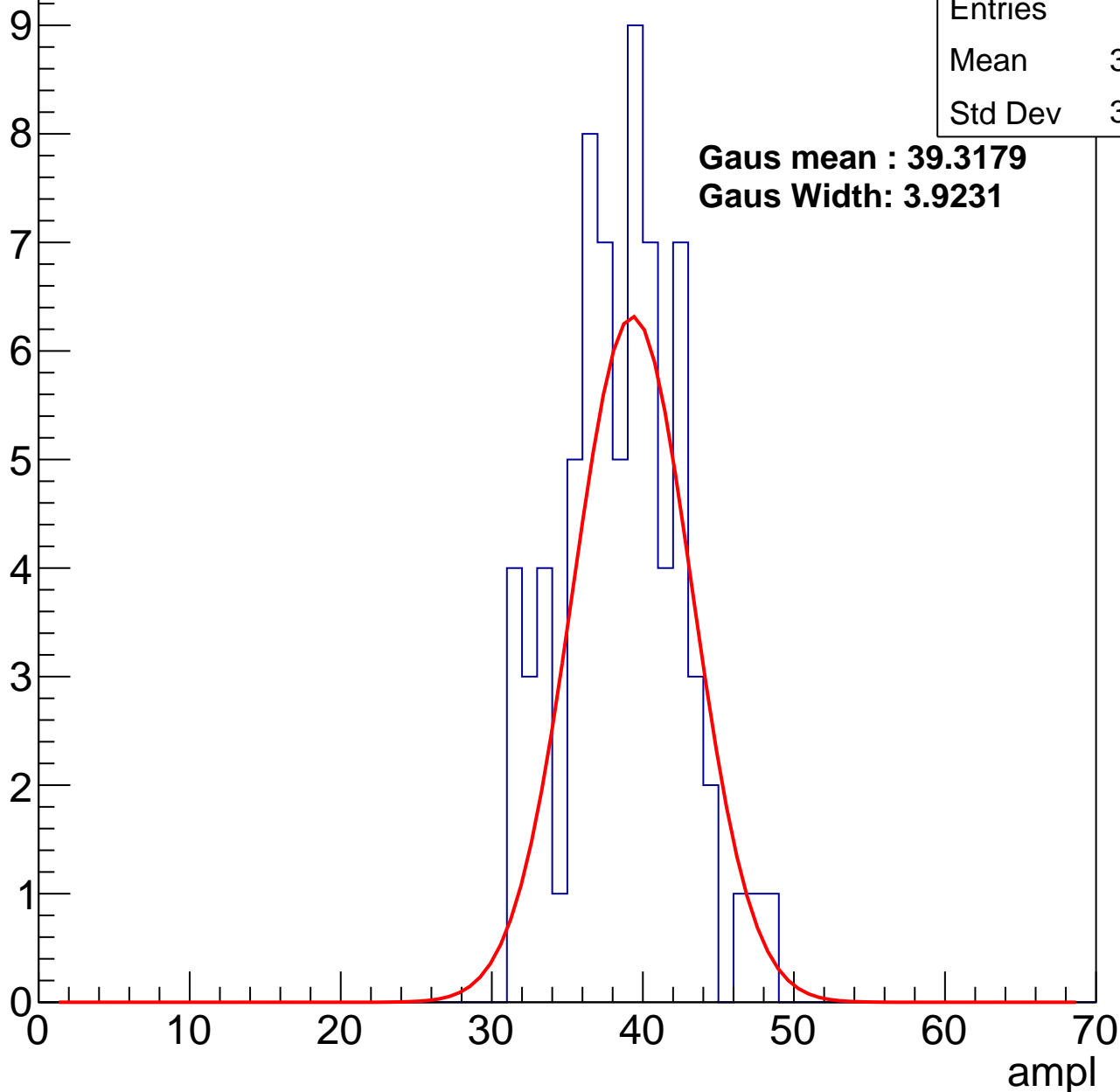
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	38.12
Std Dev	3.873

Gaus mean : 39.3179

Gaus Width: 3.9231



B1L103S, U17-ch23, adc2

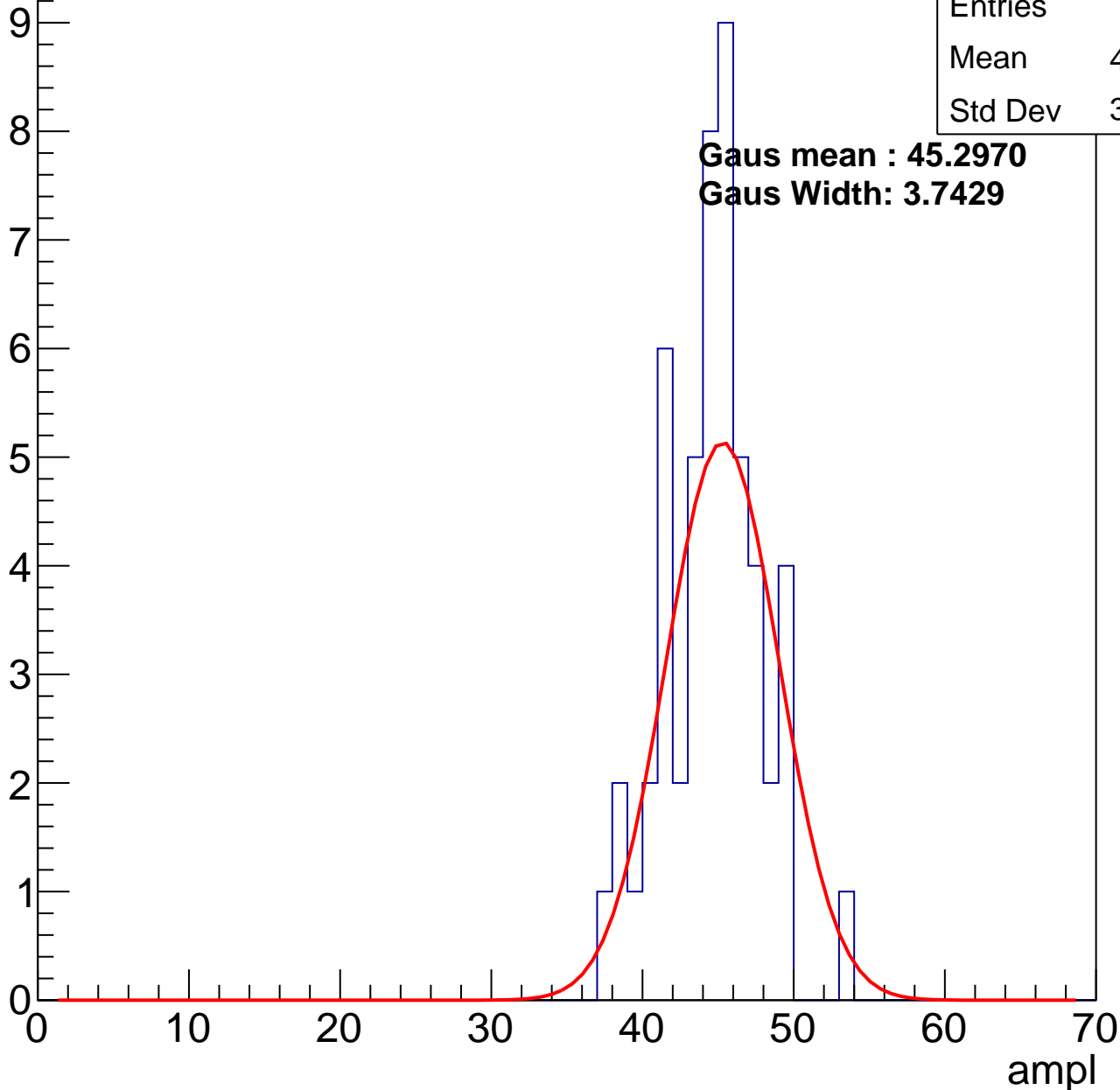
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	44.17
Std Dev	3.167

Gaus mean : 45.2970

Gaus Width: 3.7429



B1L103S, U17-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	50.71
Std Dev	3.845

Entry

10

8

6

4

2

0

0

10

20

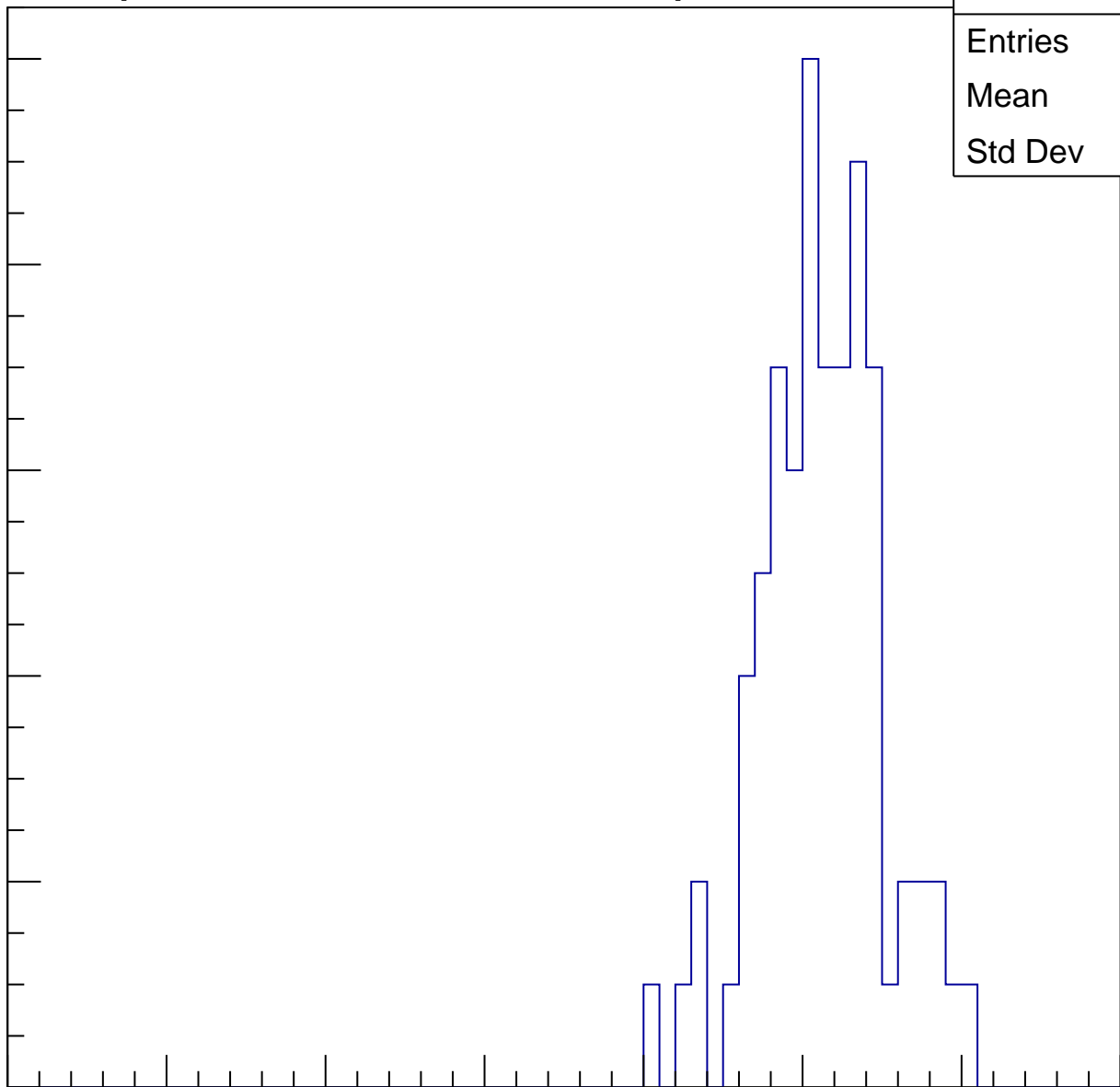
30

40

50

60

ampl

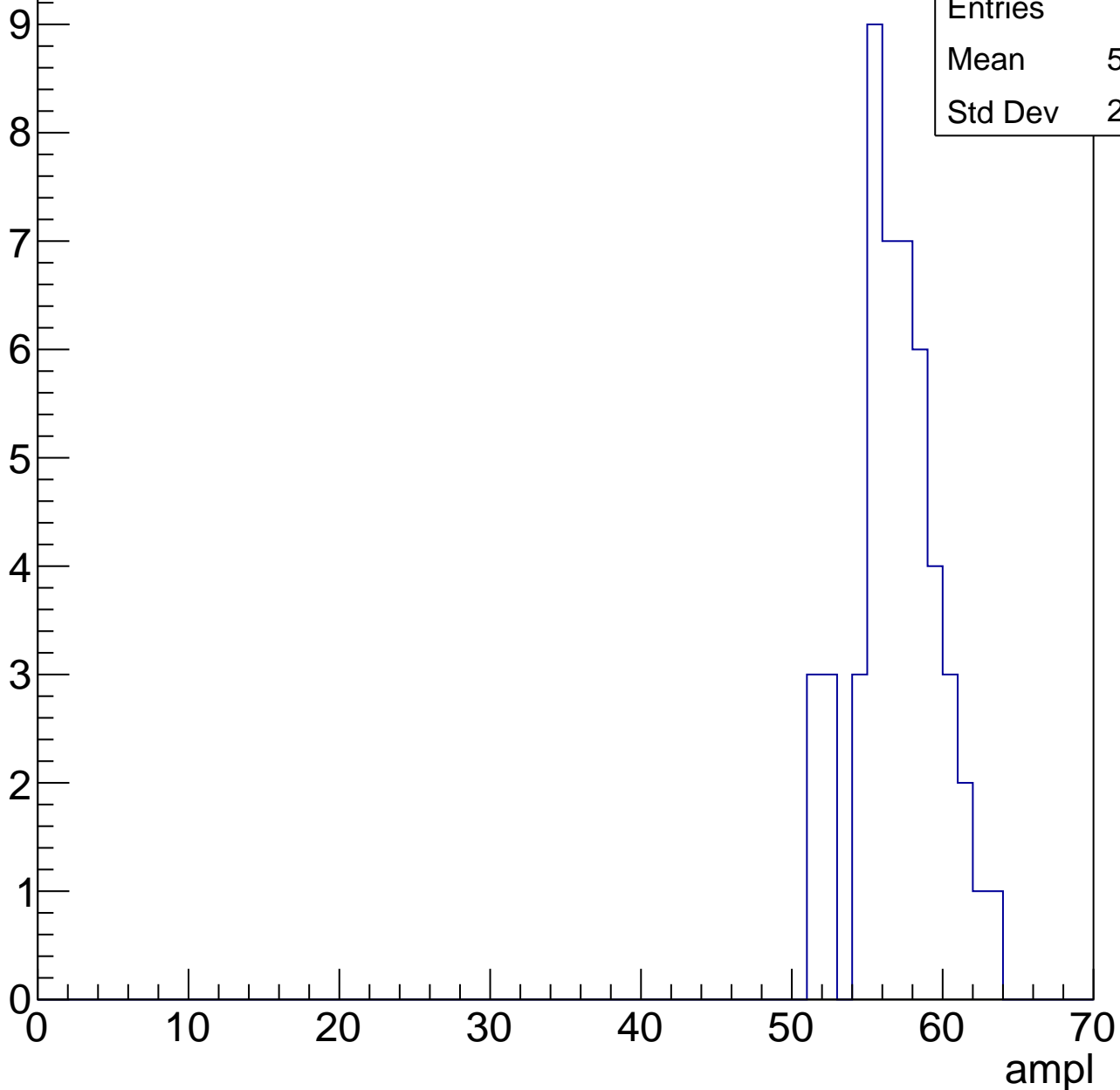


B1L103S, U17-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	56.49
Std Dev	2.793



B1L103S, U17-ch23, adc5

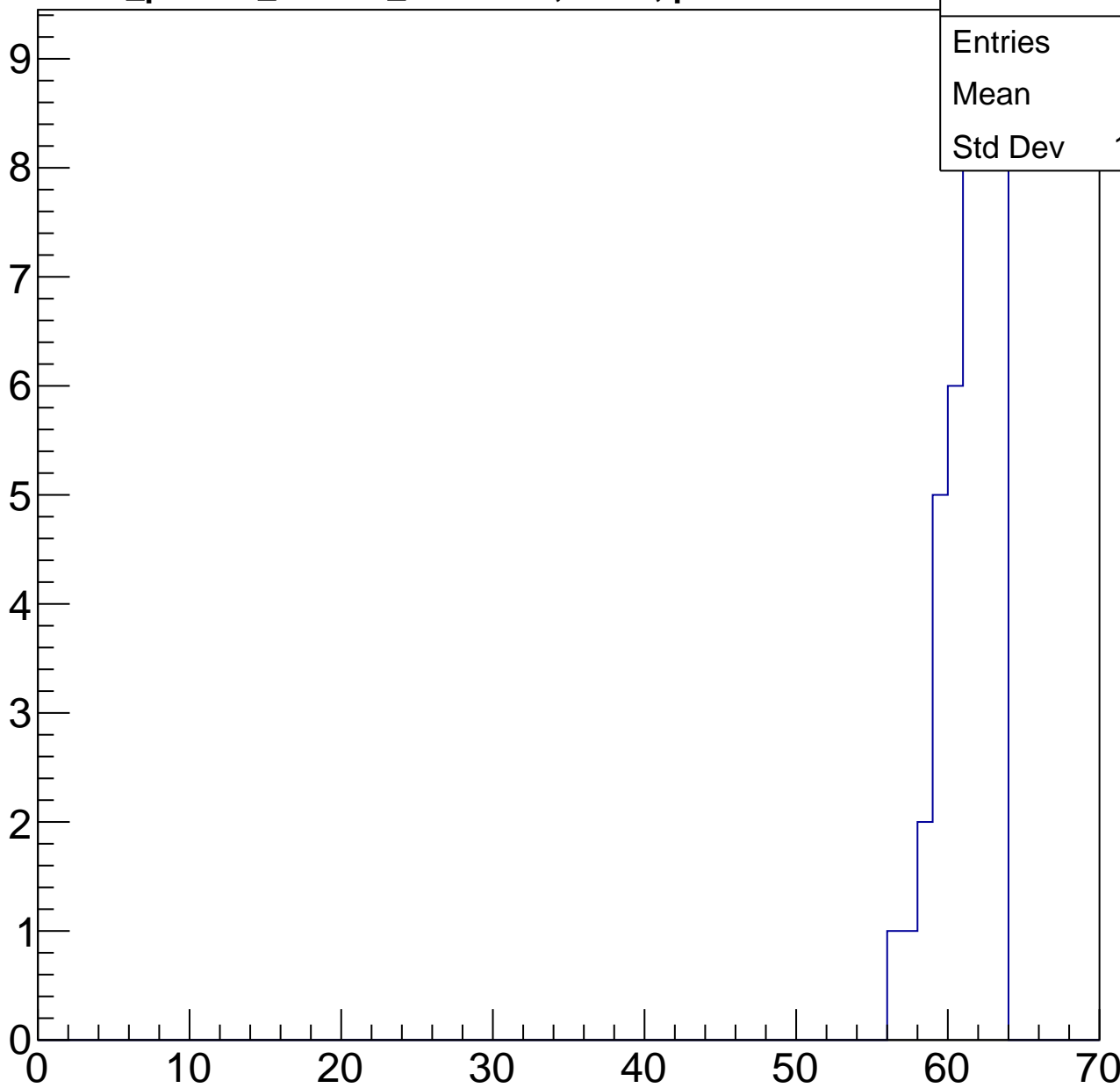
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.9
Std Dev	1.778

ampl



B1L103S, U17-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch24, adc0

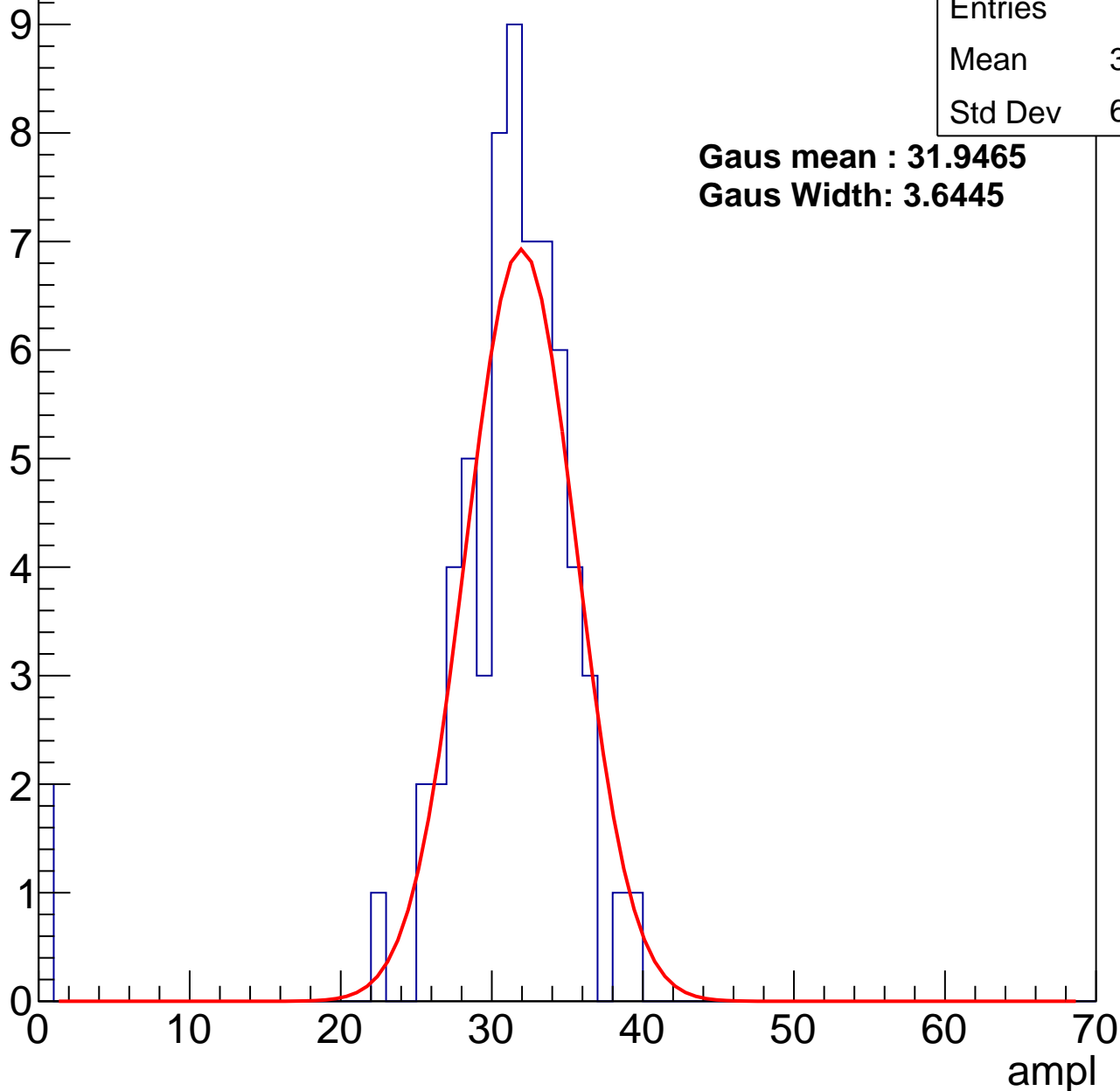
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.18
Std Dev	6.266

Gaus mean : 31.9465

Gaus Width: 3.6445



B1L103S, U17-ch24, adc1

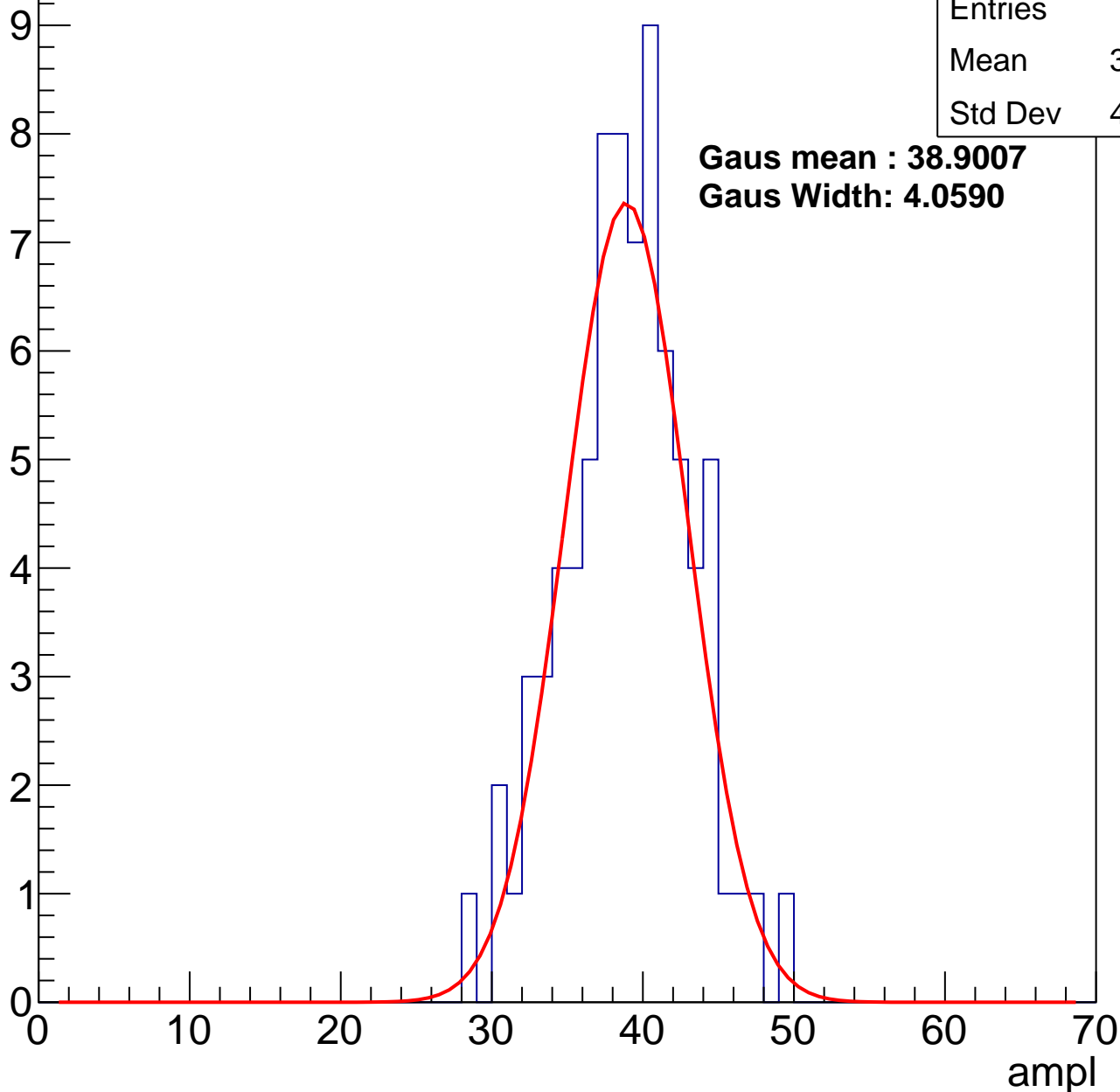
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	38.46
Std Dev	4.124

Gaus mean : 38.9007

Gaus Width: 4.0590



B1L103S, U17-ch24, adc2

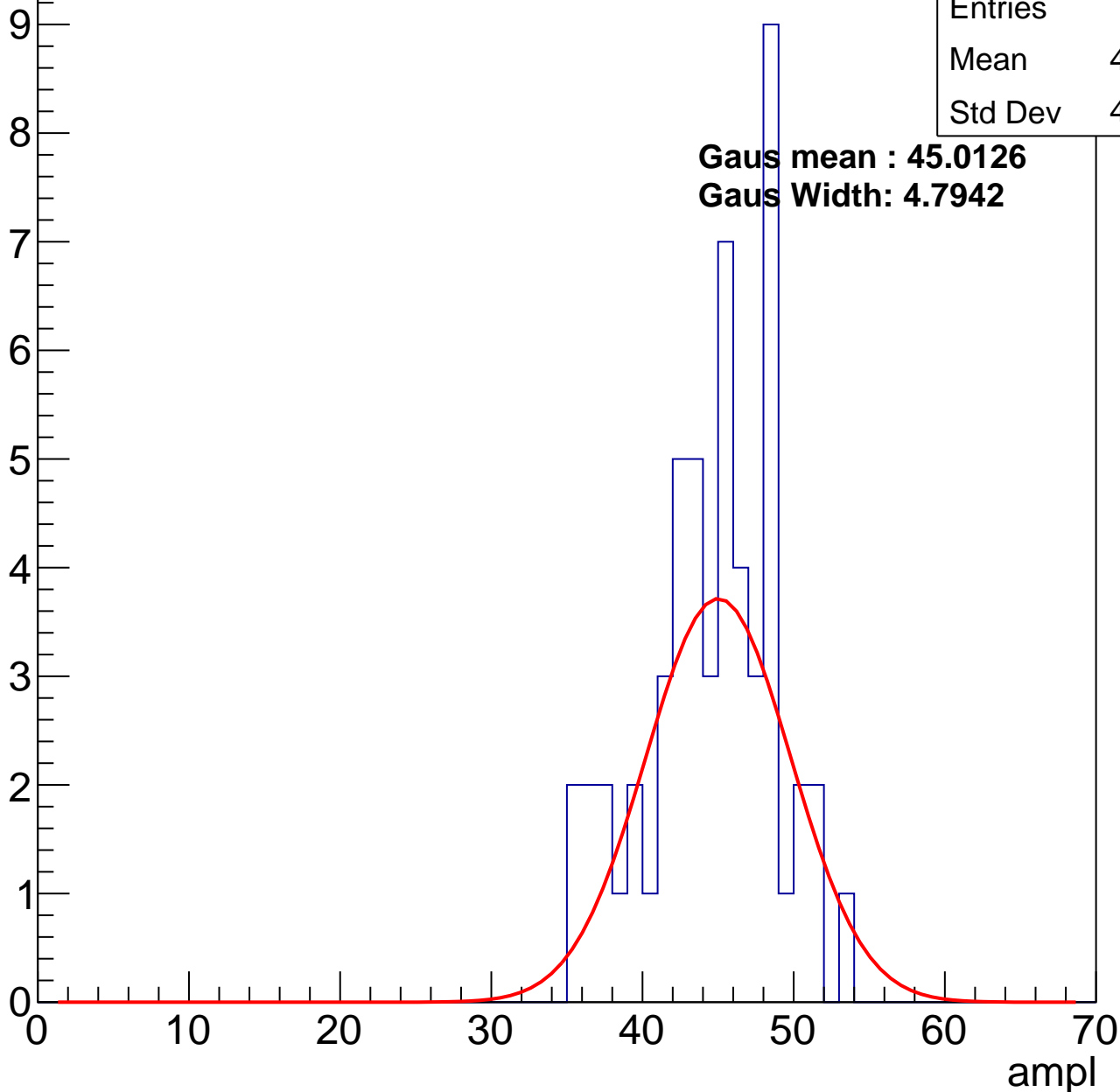
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	44.15
Std Dev	4.287

Gaus mean : 45.0126

Gaus Width: 4.7942

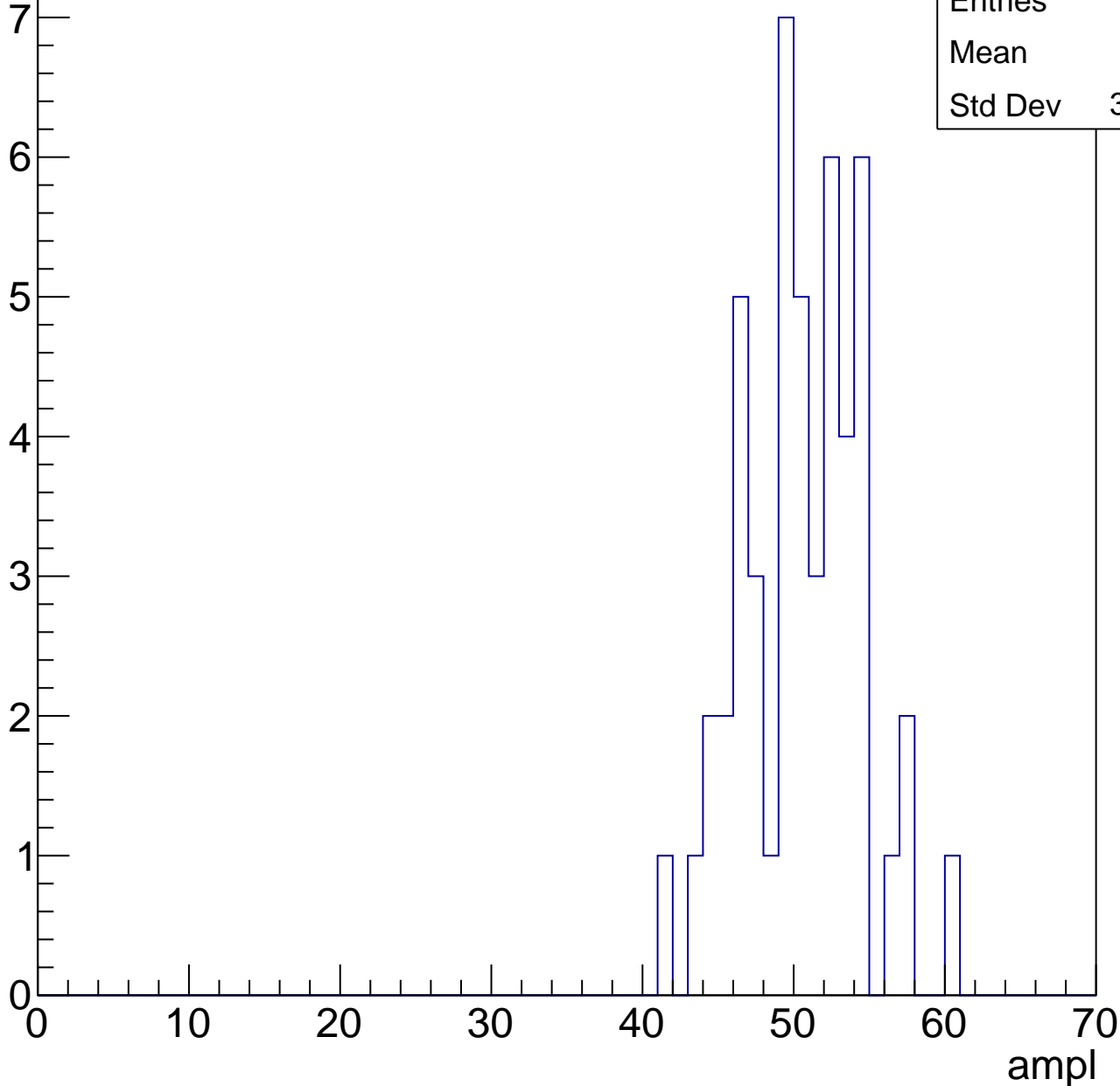


B1L103S, U17-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	50.1
Std Dev	3.905

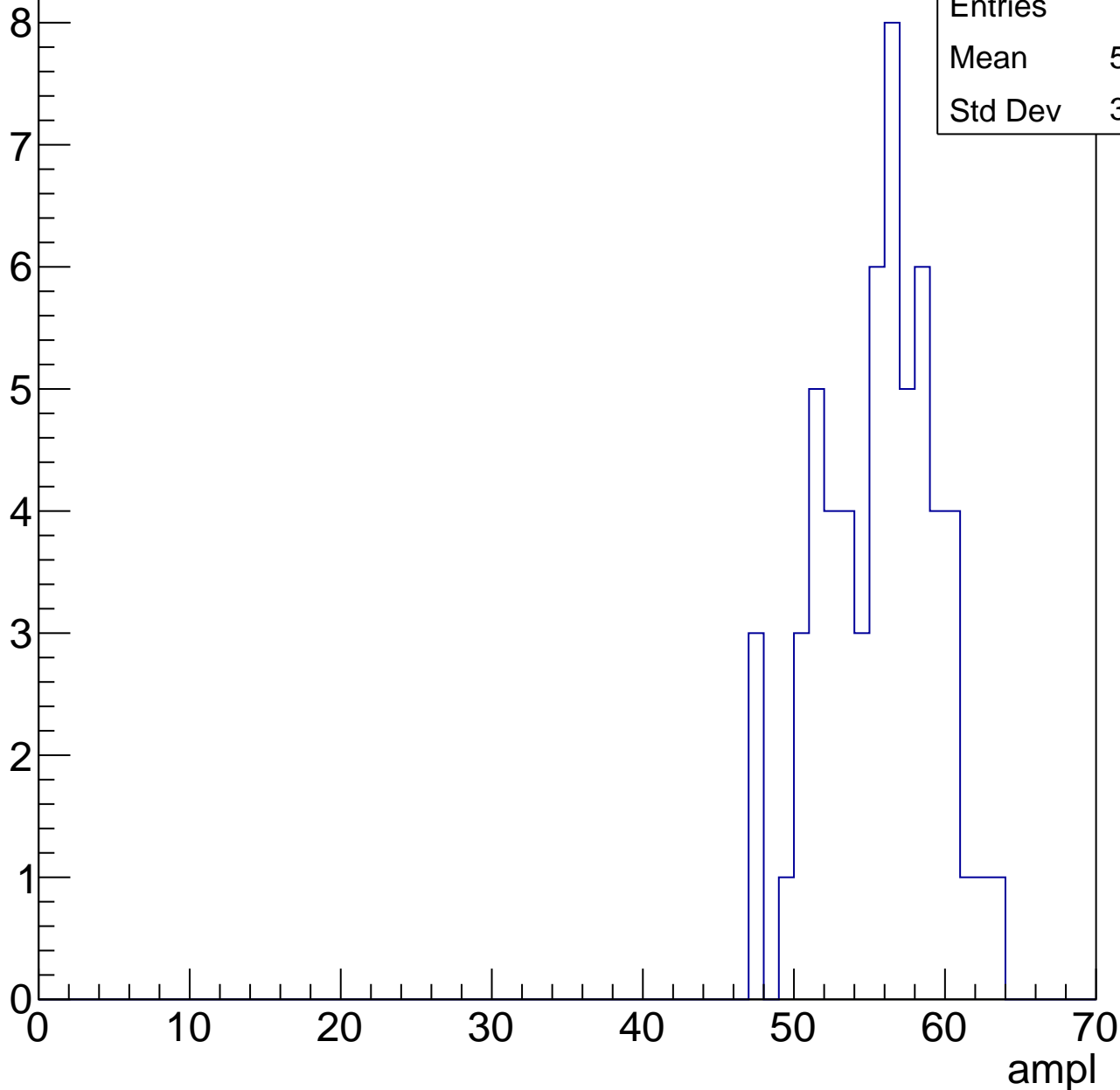


B1L103S, U17-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.08
Std Dev	3.757

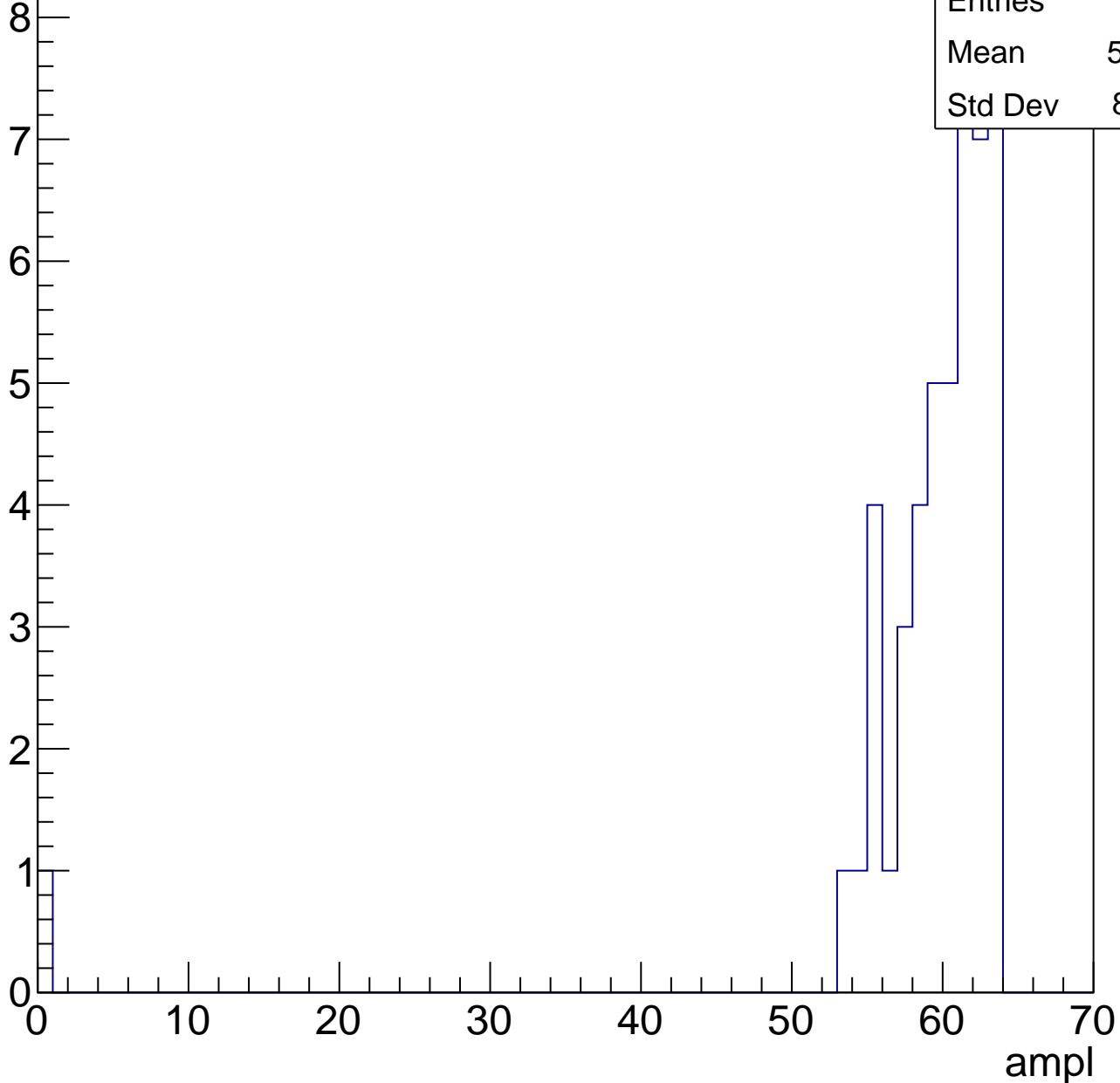


B1L103S, U17-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.48
Std Dev	8.951



B1L103S, U17-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

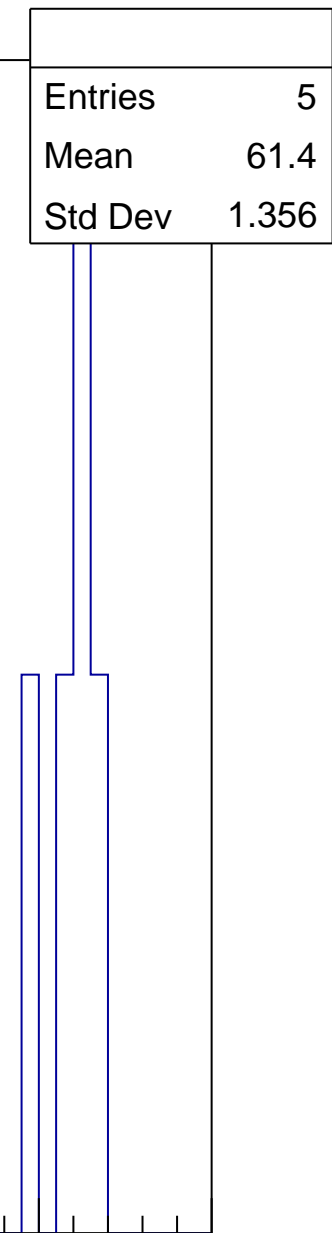
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch25, adc0

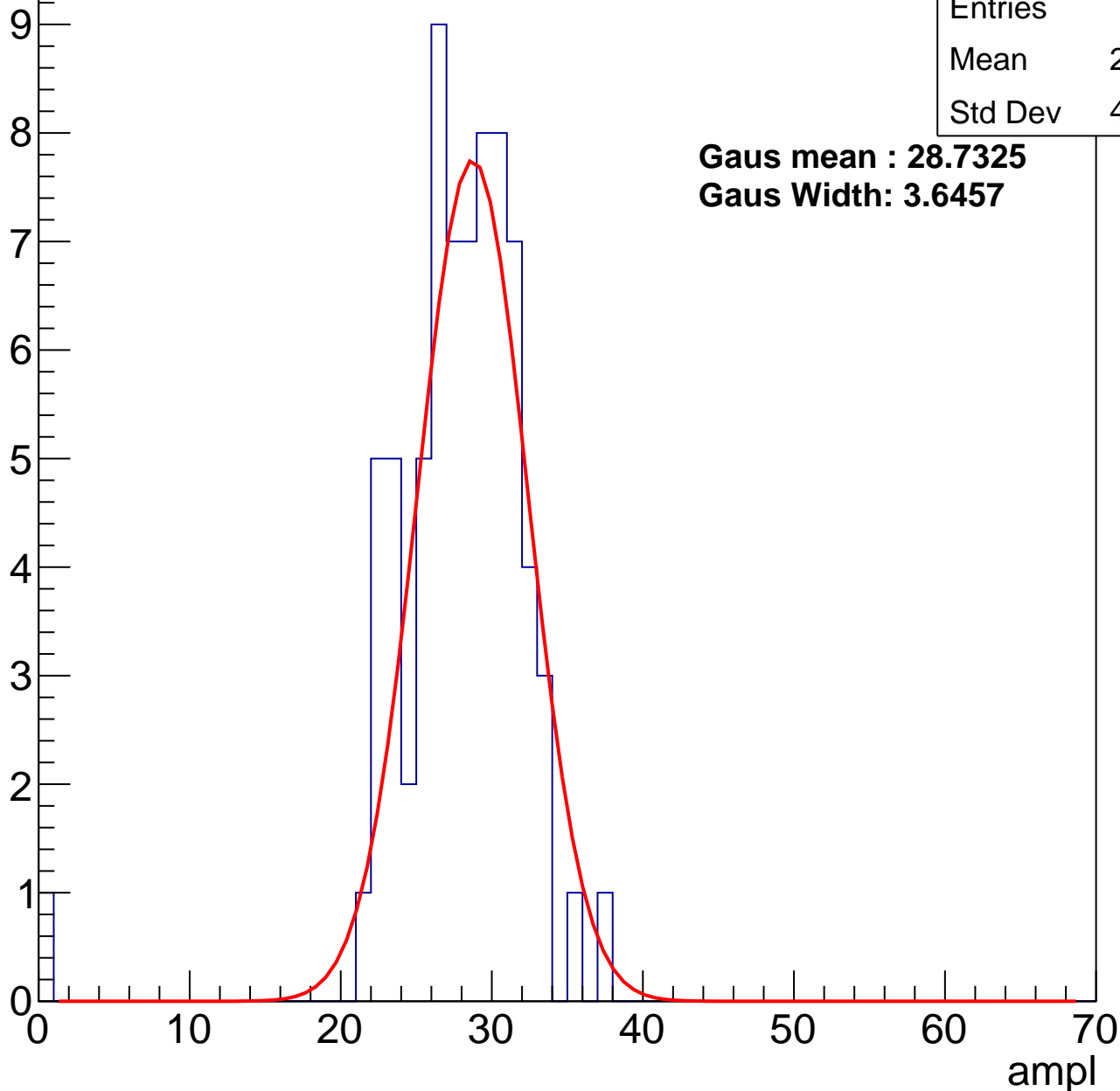
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.38
Std Dev	4.652

Gaus mean : 28.7325

Gaus Width: 3.6457



B1L103S, U17-ch25, adc1

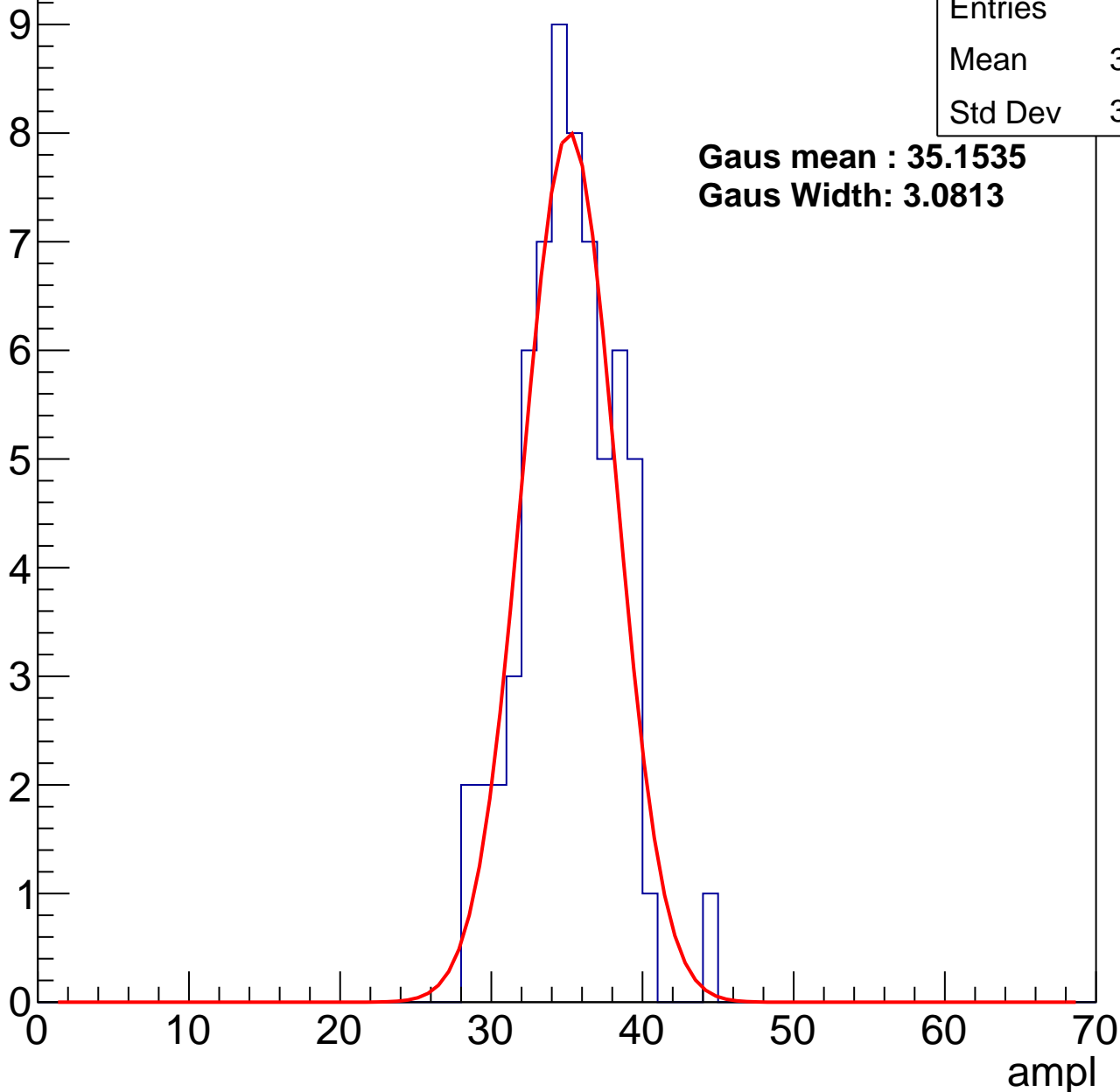
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	34.69
Std Dev	3.112

Gaus mean : 35.1535

Gaus Width: 3.0813



B1L103S, U17-ch25, adc2

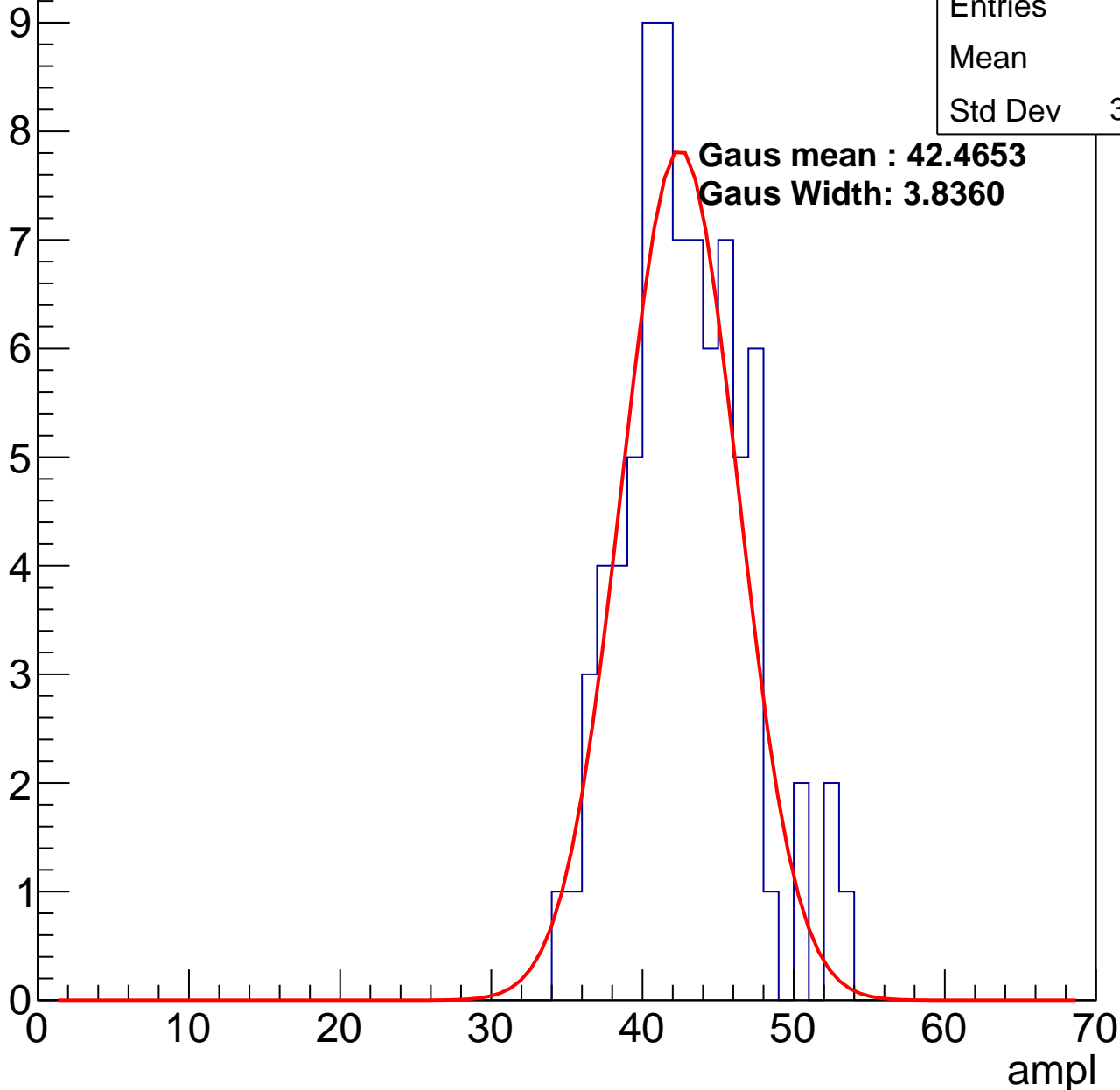
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	42.4
Std Dev	3.986

Gaus mean : 42.4653

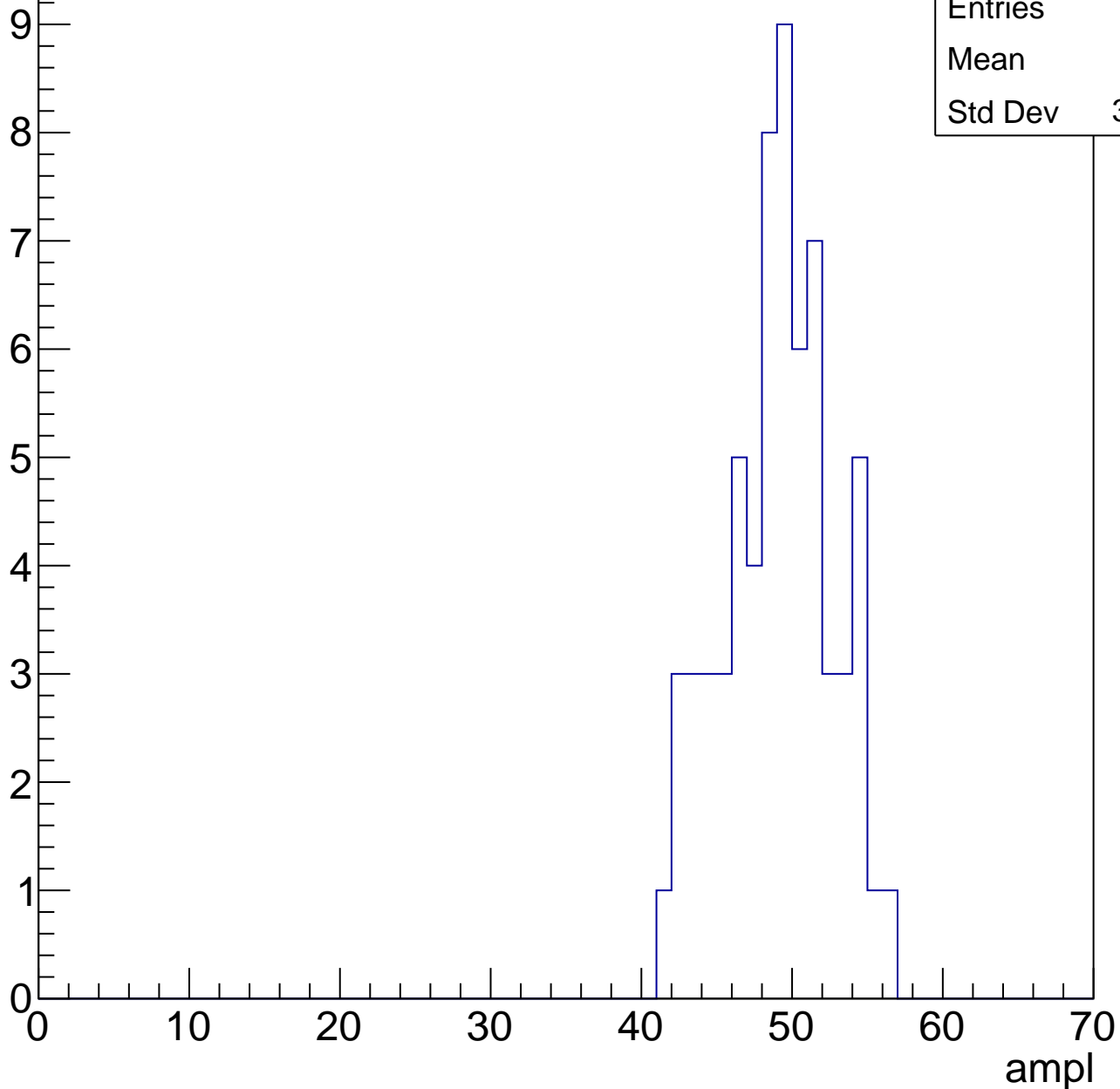
Gaus Width: 3.8360



B1L103S, U17-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

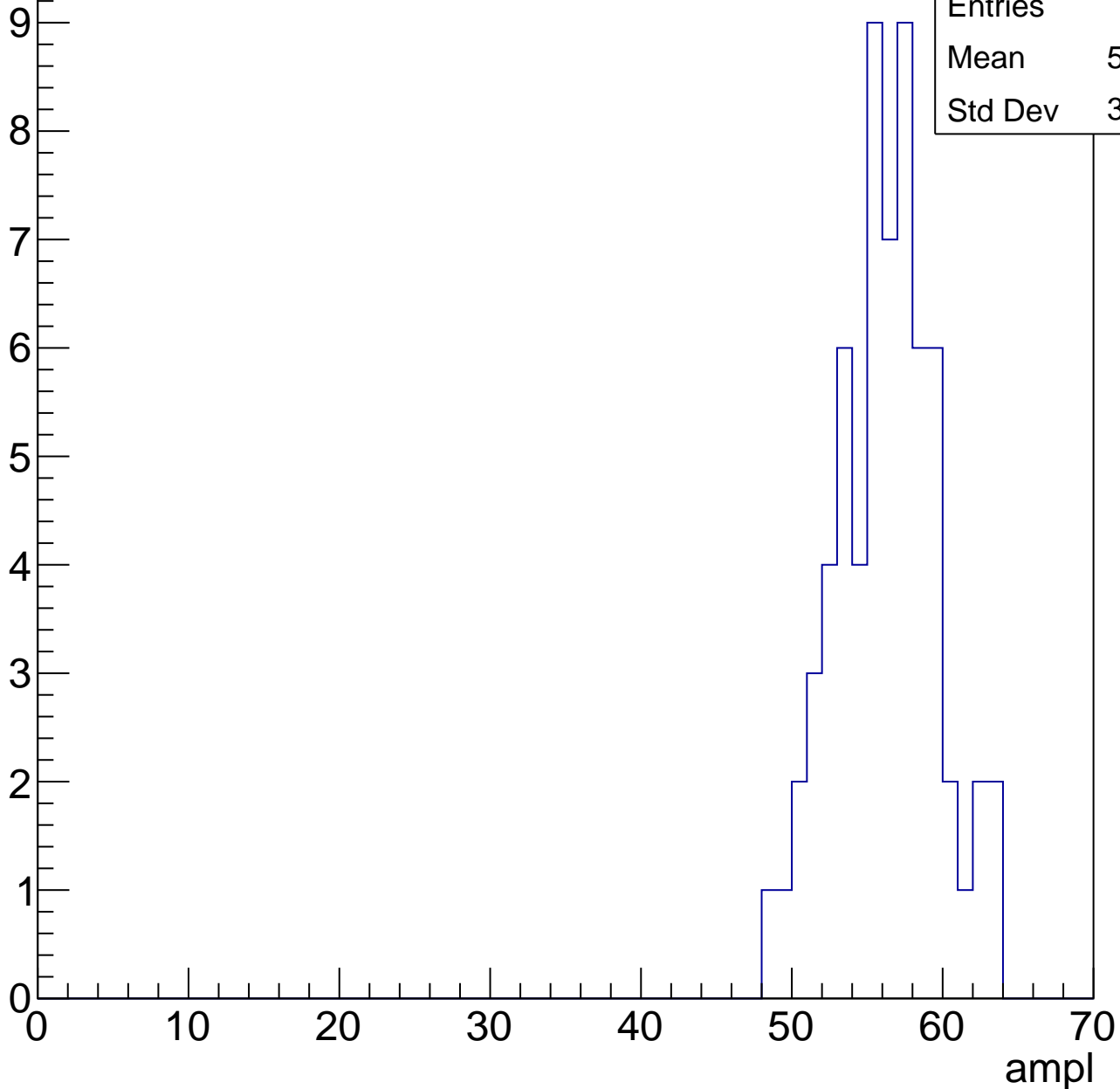


B1L103S, U17-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.77
Std Dev	3.313

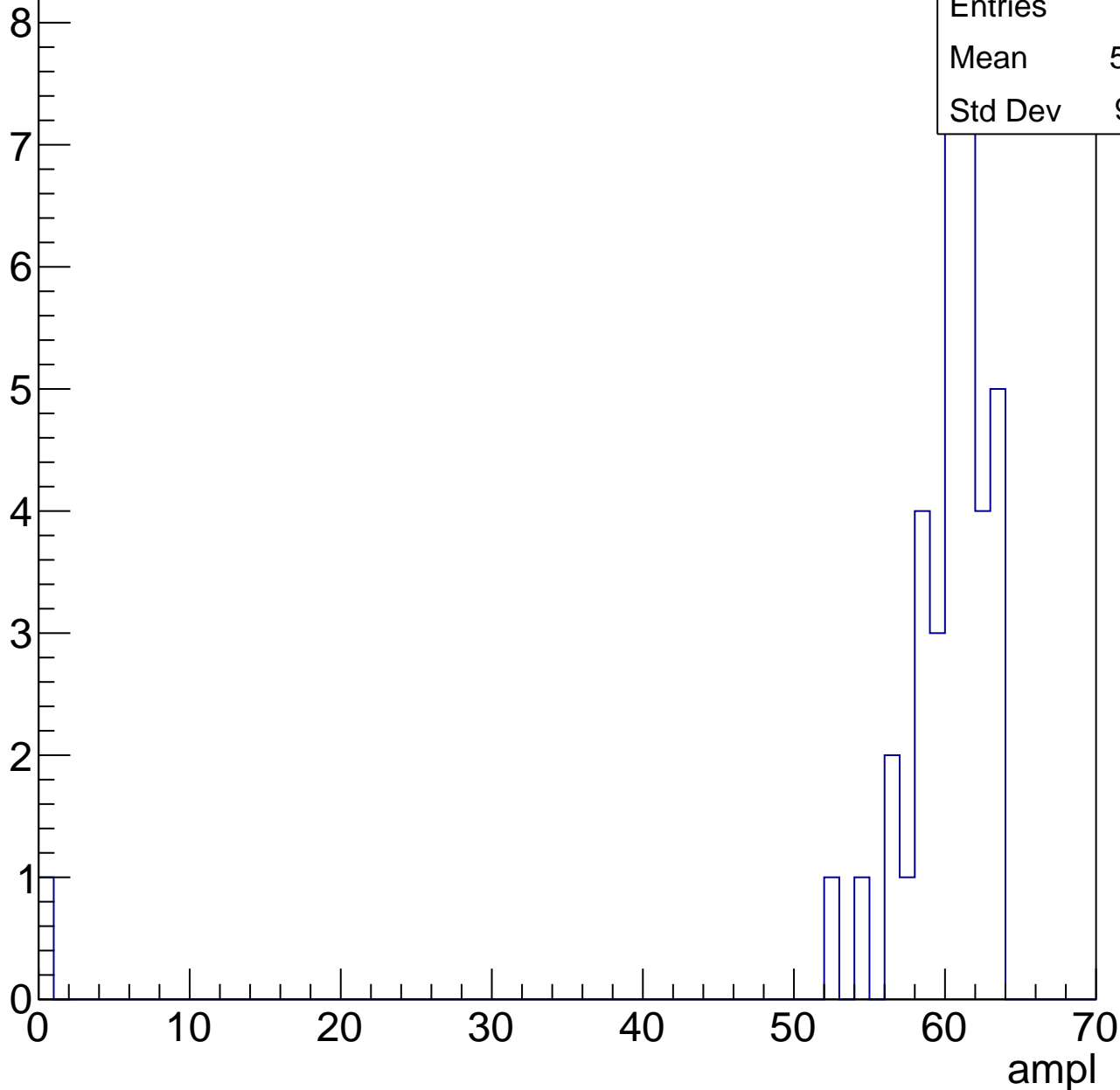


B1L103S, U17-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

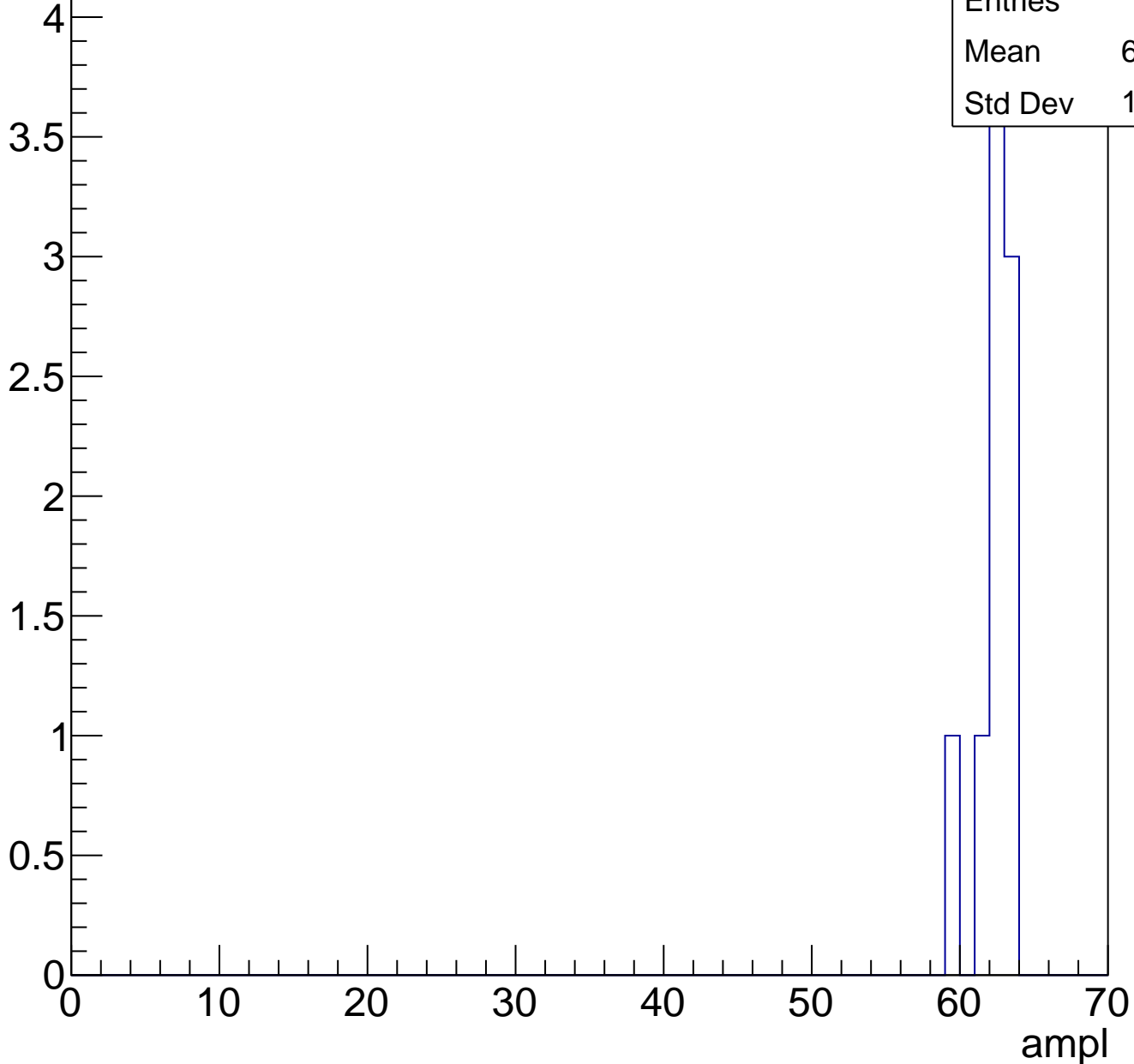
Entries	38
Mean	58.29
Std Dev	9.891



B1L103S, U17-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch26, adc0

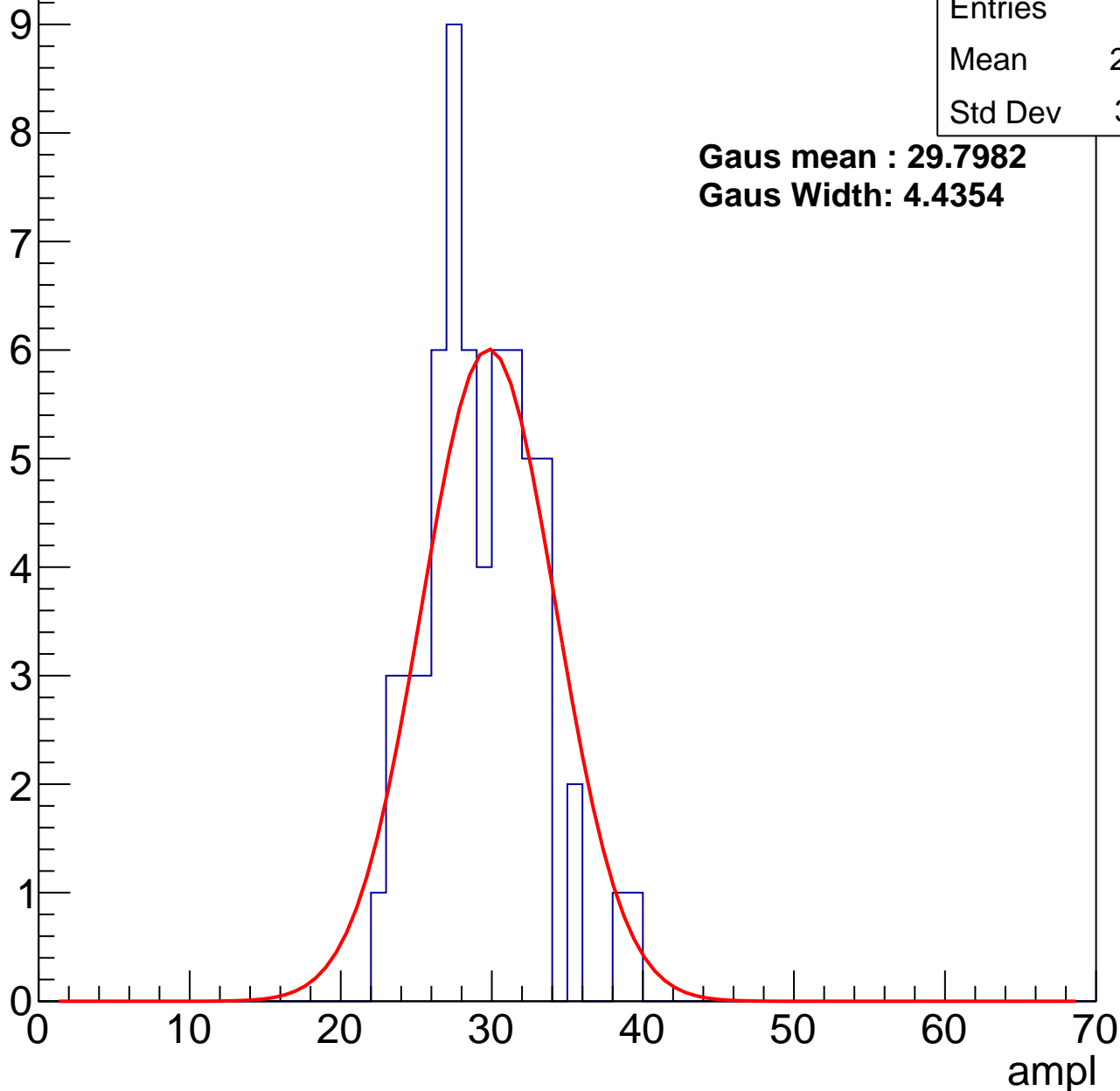
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.84
Std Dev	3.581

Gaus mean : 29.7982

Gaus Width: 4.4354



B1L103S, U17-ch26, adc1

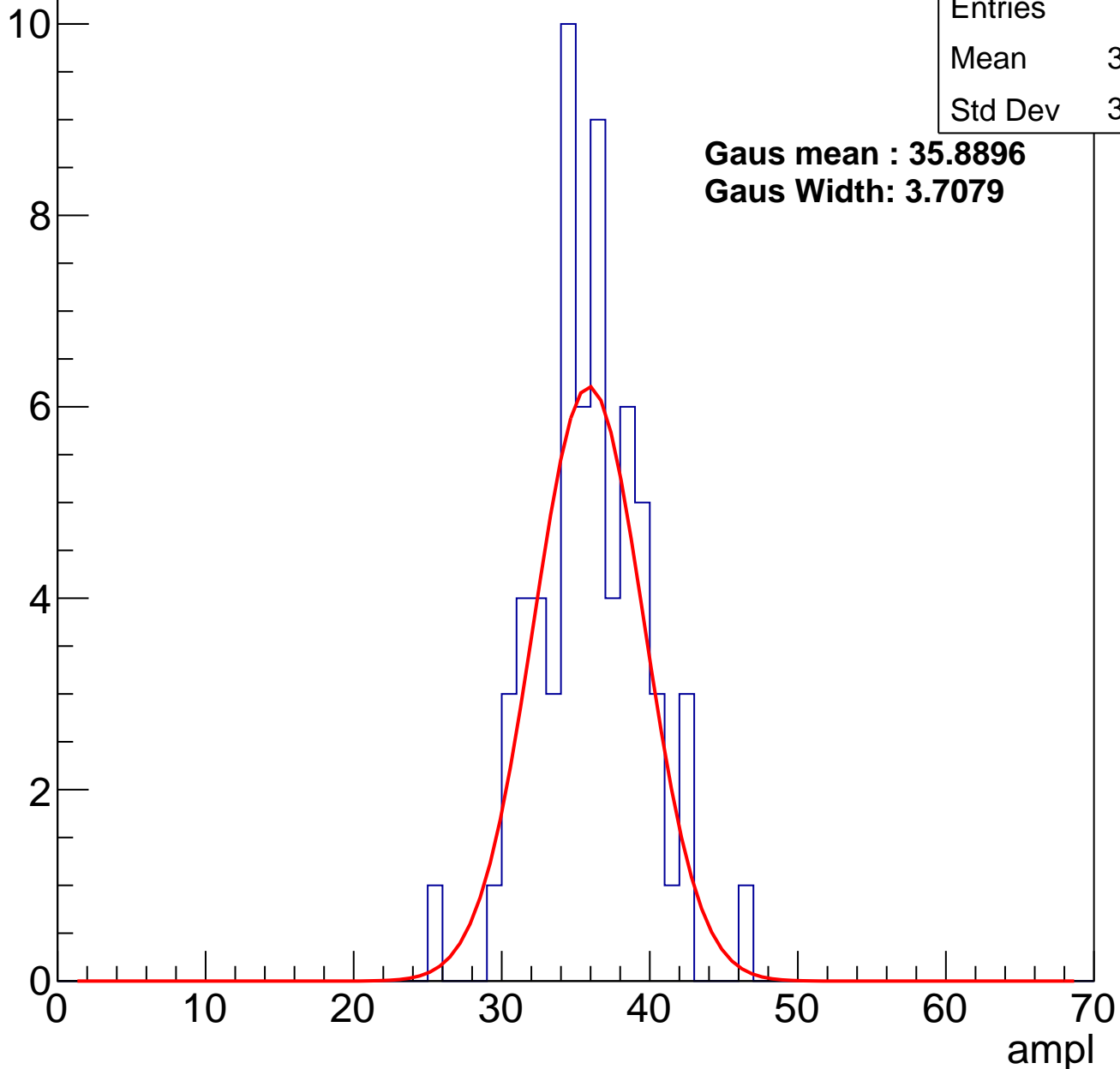
calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	35.52
Std Dev	3.653

Gaus mean : 35.8896

Gaus Width: 3.7079

Entry



B1L103S, U17-ch26, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	42.27
Std Dev	3.822

Gaus mean : 42.9506

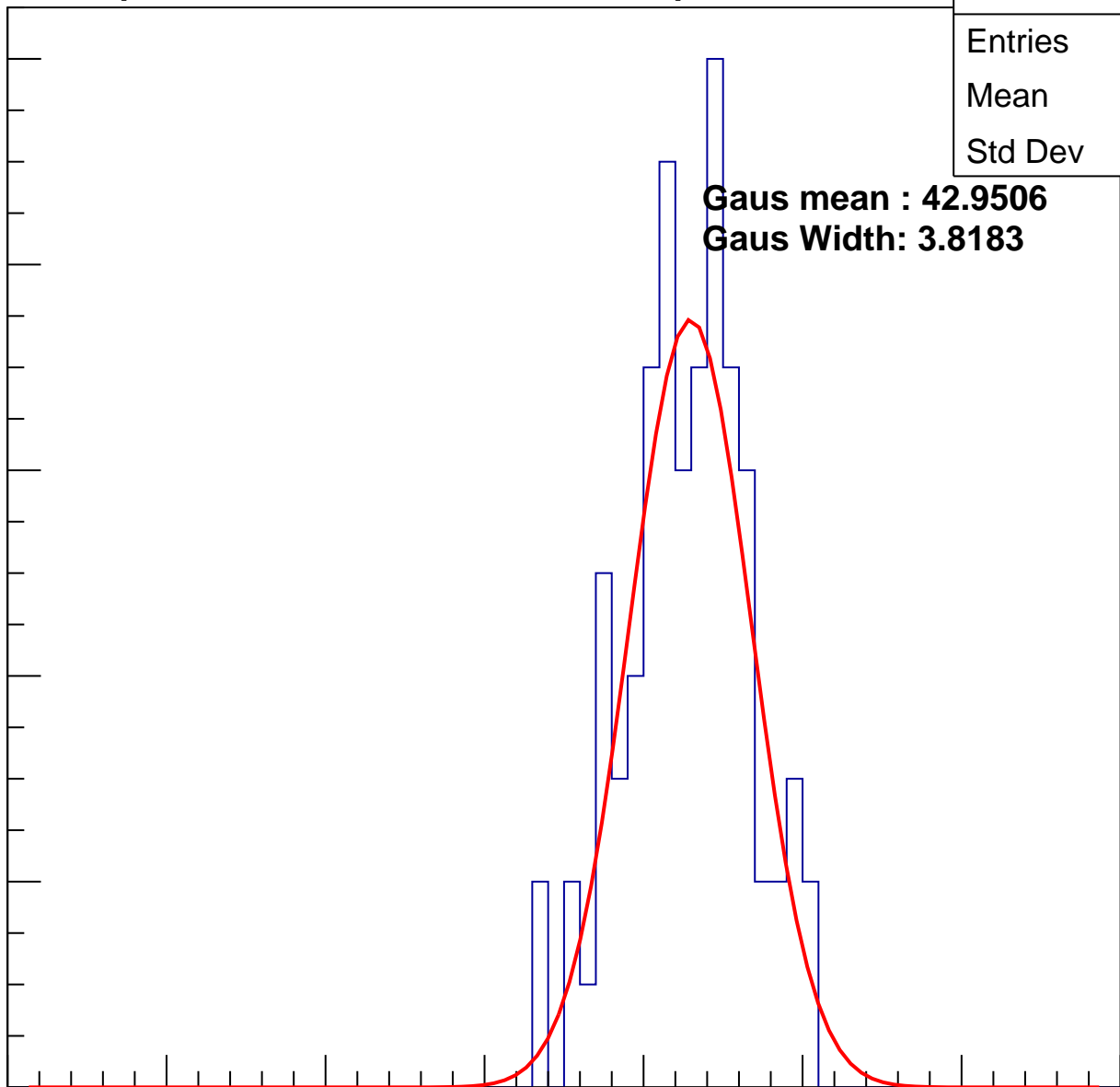
Gaus Width: 3.8183

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

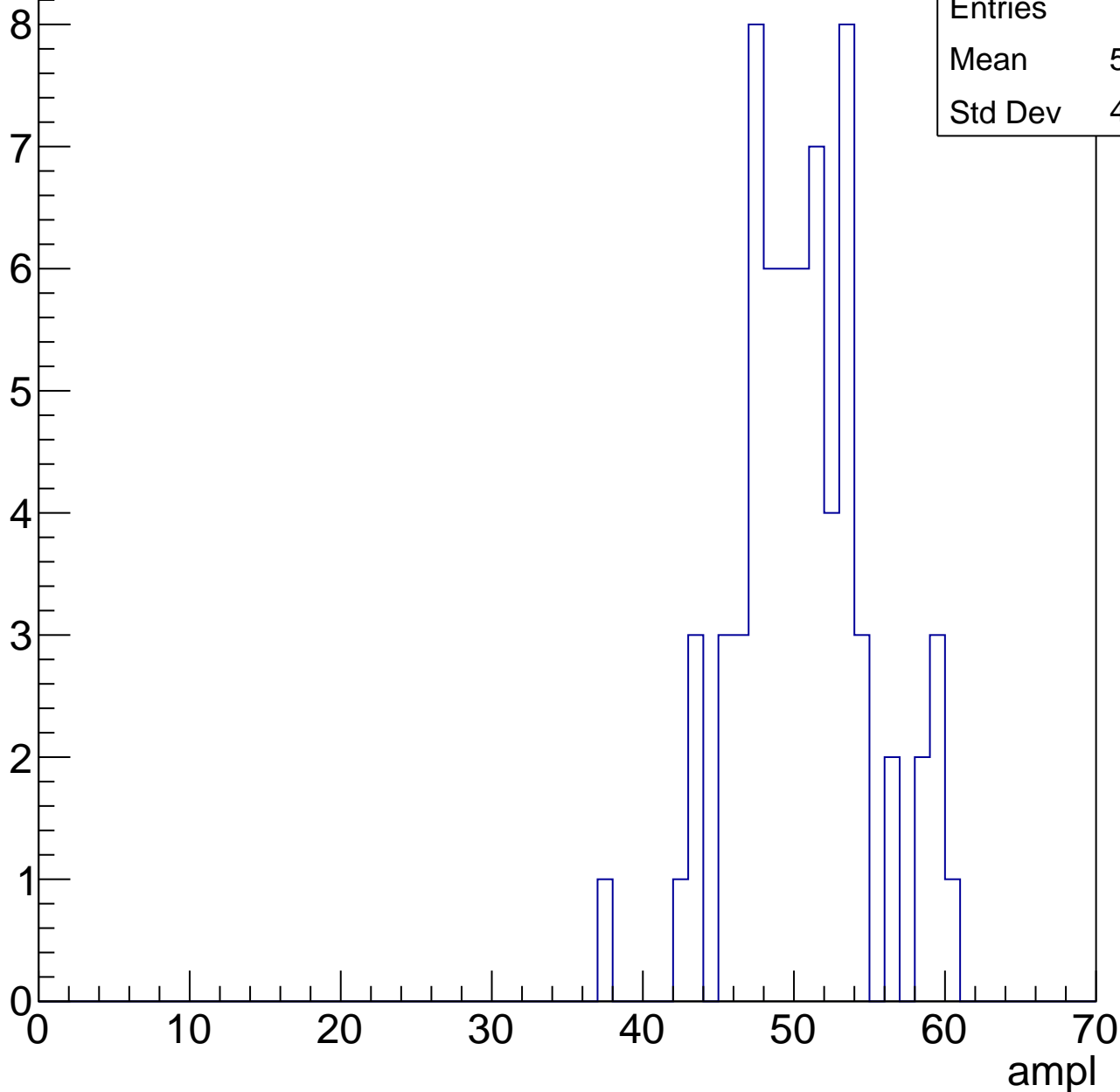


B1L103S, U17-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	50.07
Std Dev	4.389



B1L103S, U17-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 54.45

Std Dev 3.173

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

70

B1L103S, U17-ch26, adc5

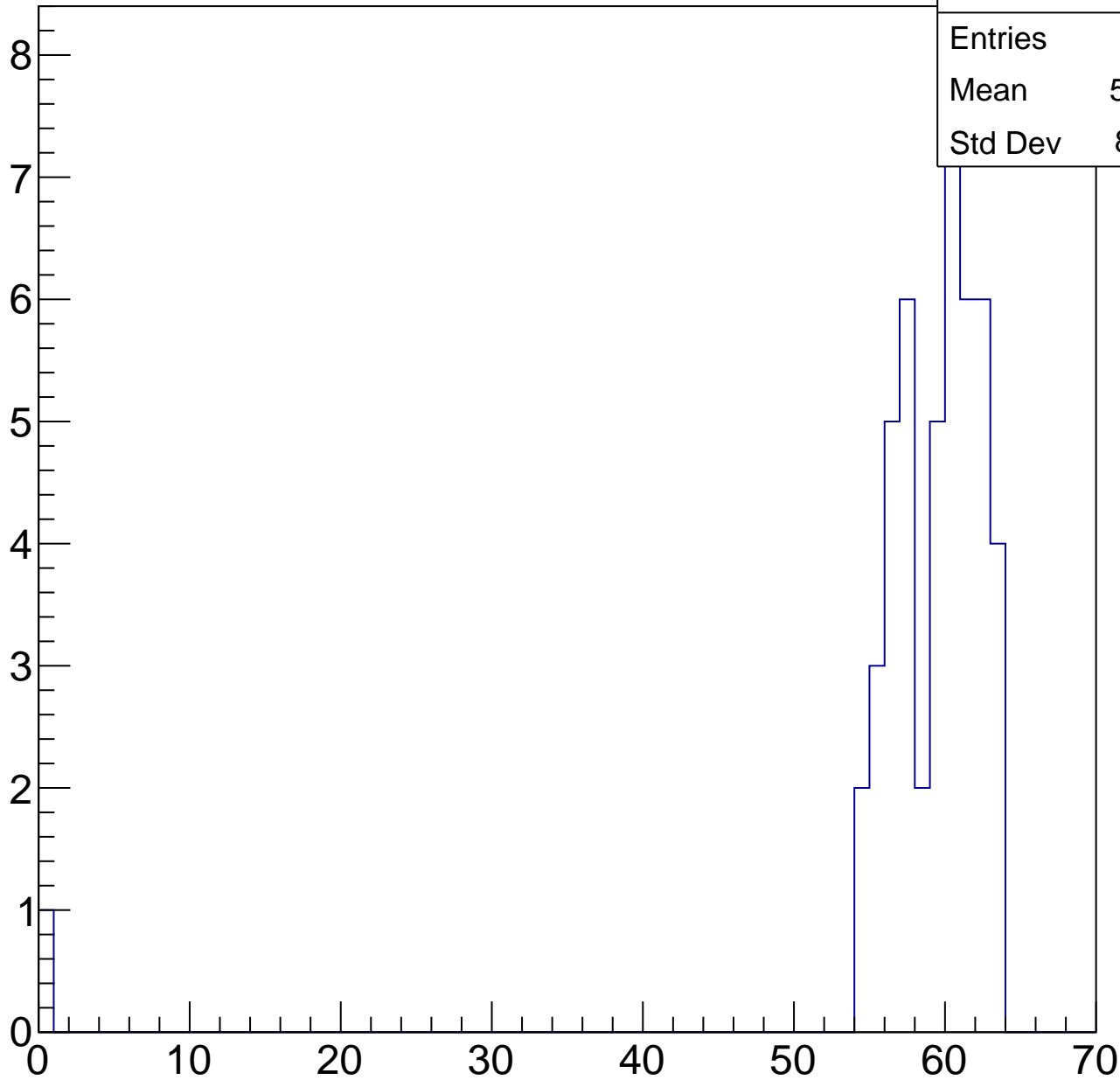
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	57.83
Std Dev	8.821

ampl

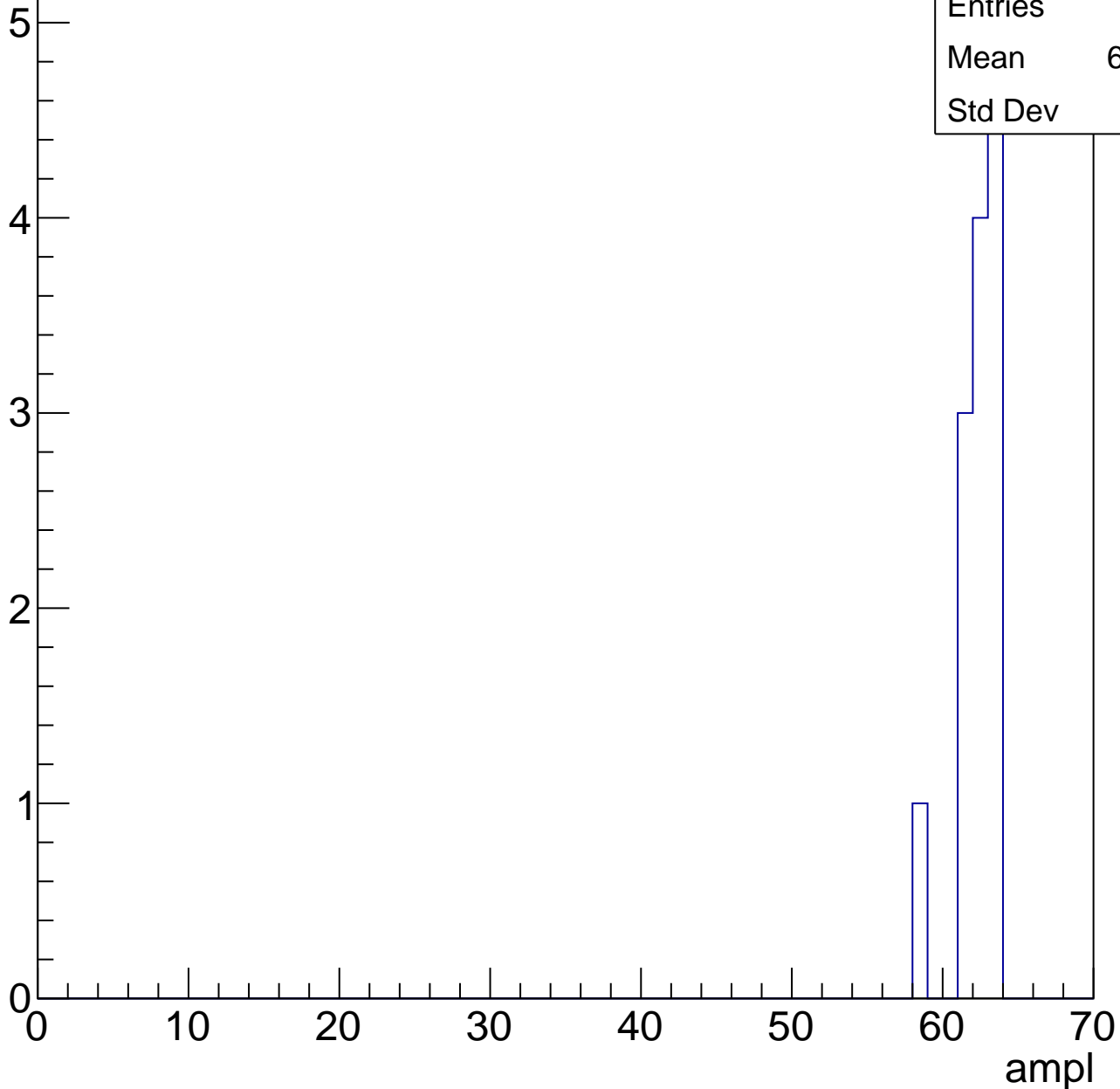


B1L103S, U17-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.85
Std Dev	1.35



B1L103S, U17-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch27, adc0

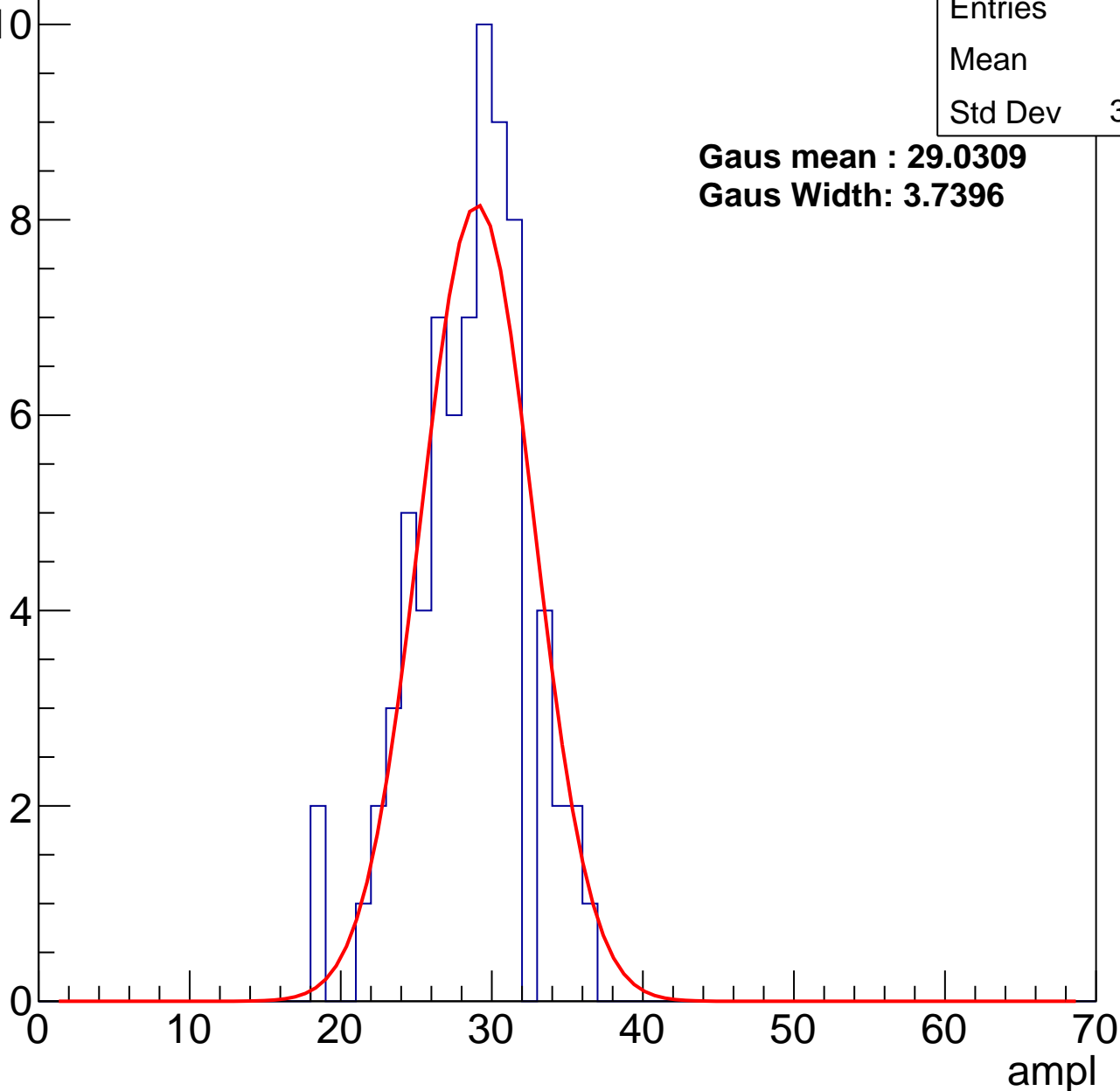
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28
Std Dev	3.697

Gaus mean : 29.0309

Gaus Width: 3.7396



B1L103S, U17-ch27, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	35.63
Std Dev	3.224

Gaus mean : 36.4664

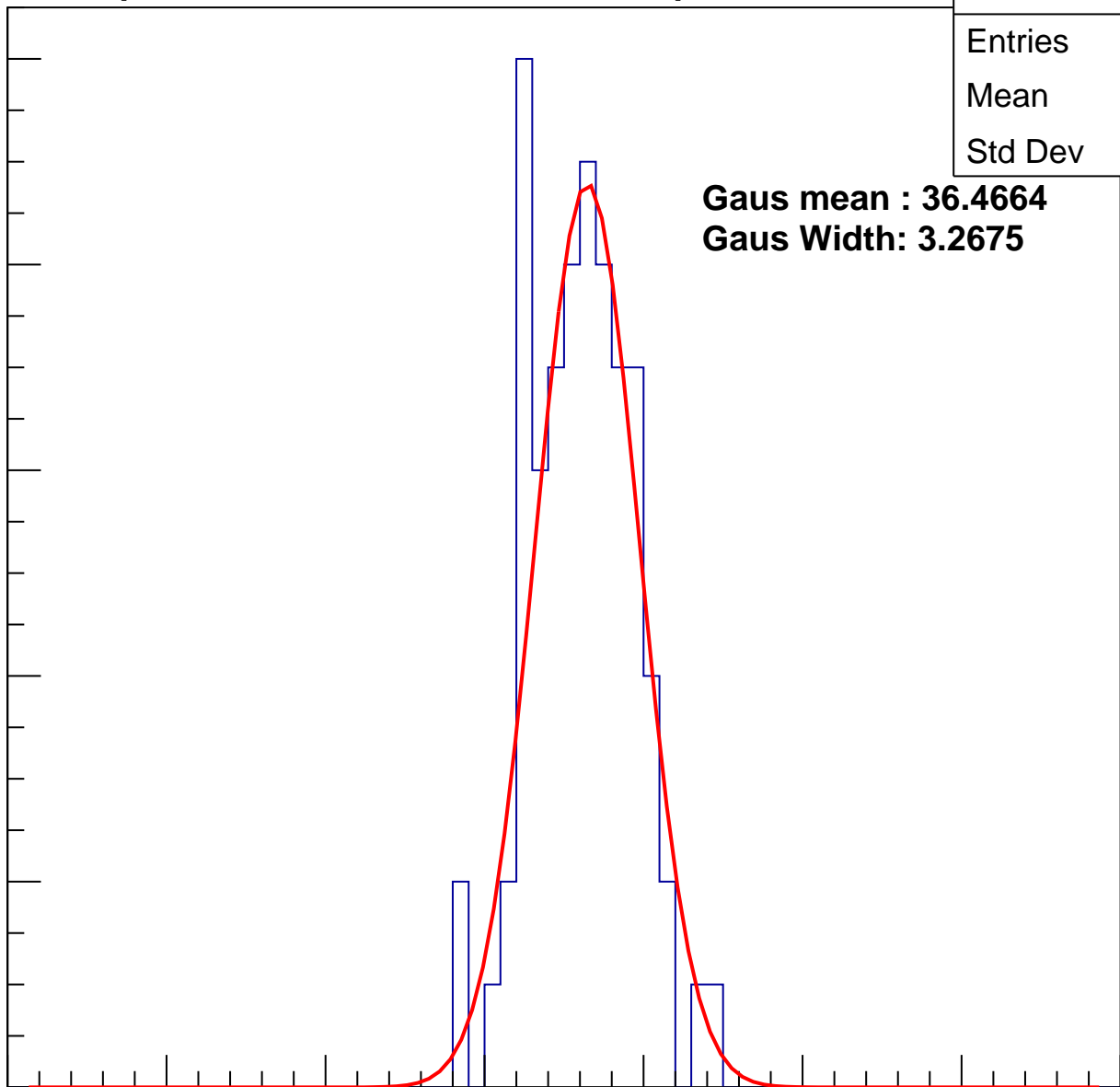
Gaus Width: 3.2675

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch27, adc2

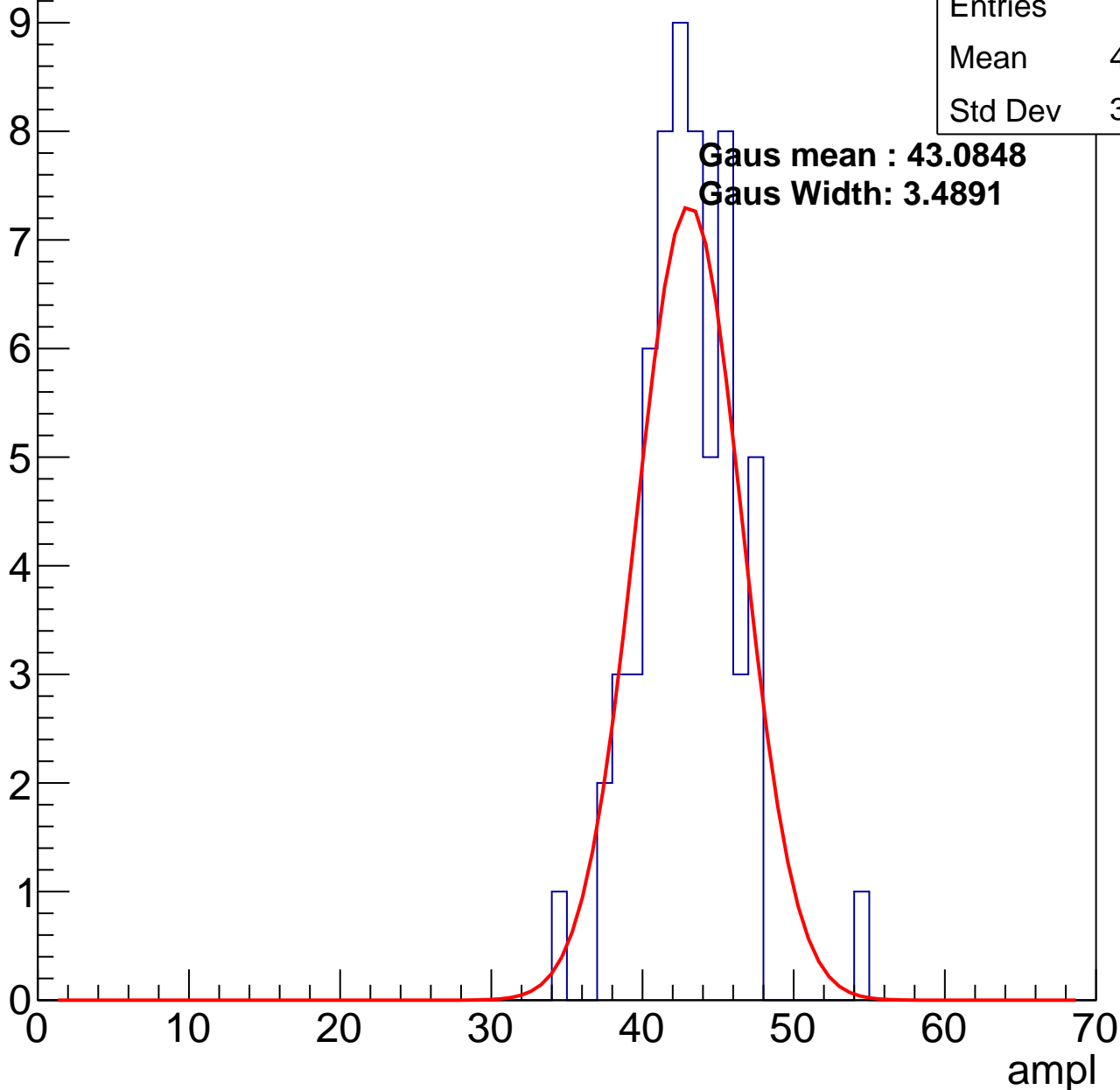
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.52
Std Dev	3.166

Gaus mean : 43.0848

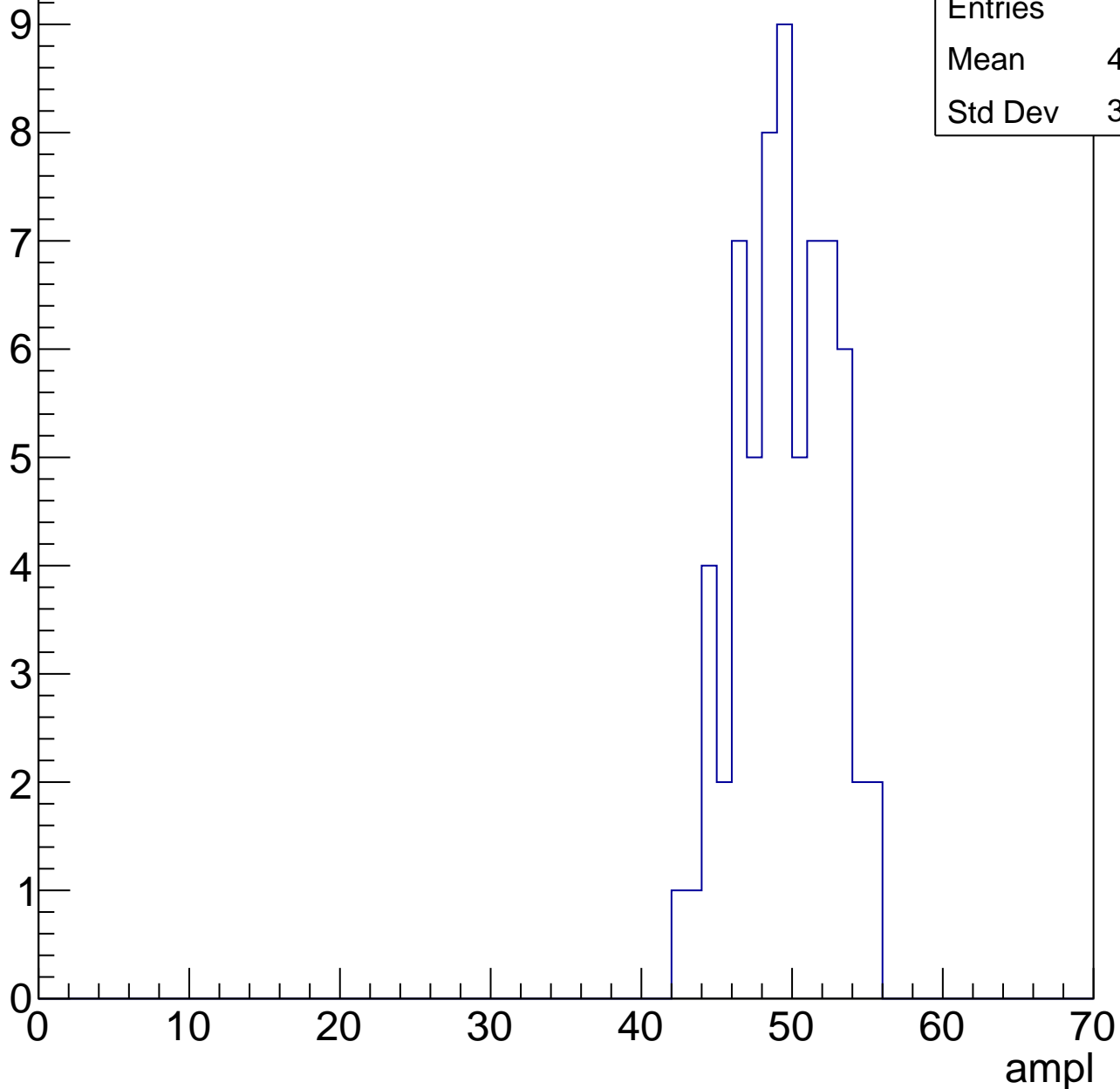
Gaus Width: 3.4891



B1L103S, U17-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



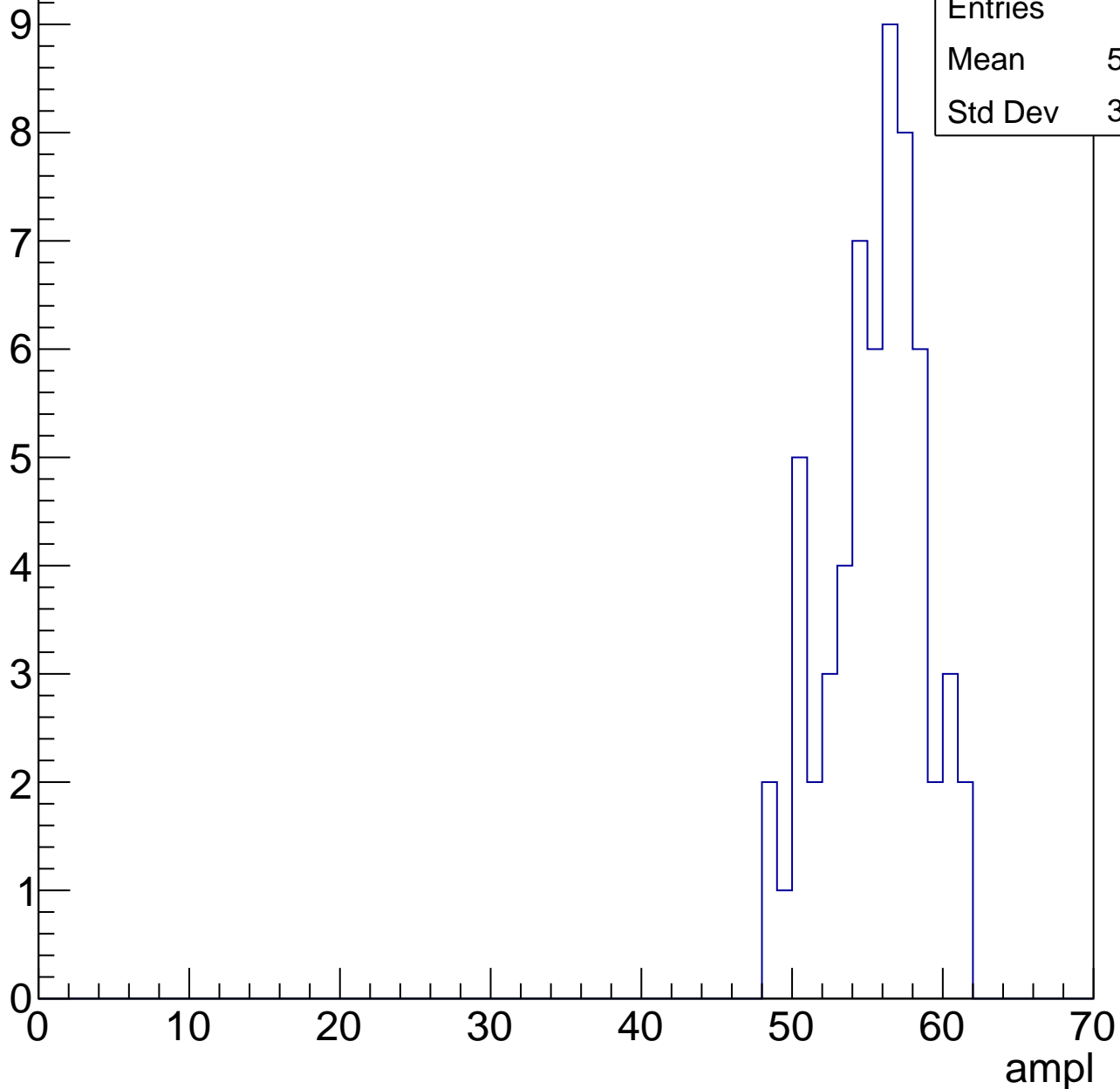
Entries	66
Mean	49.09
Std Dev	3.069

B1L103S, U17-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.02
Std Dev	3.202

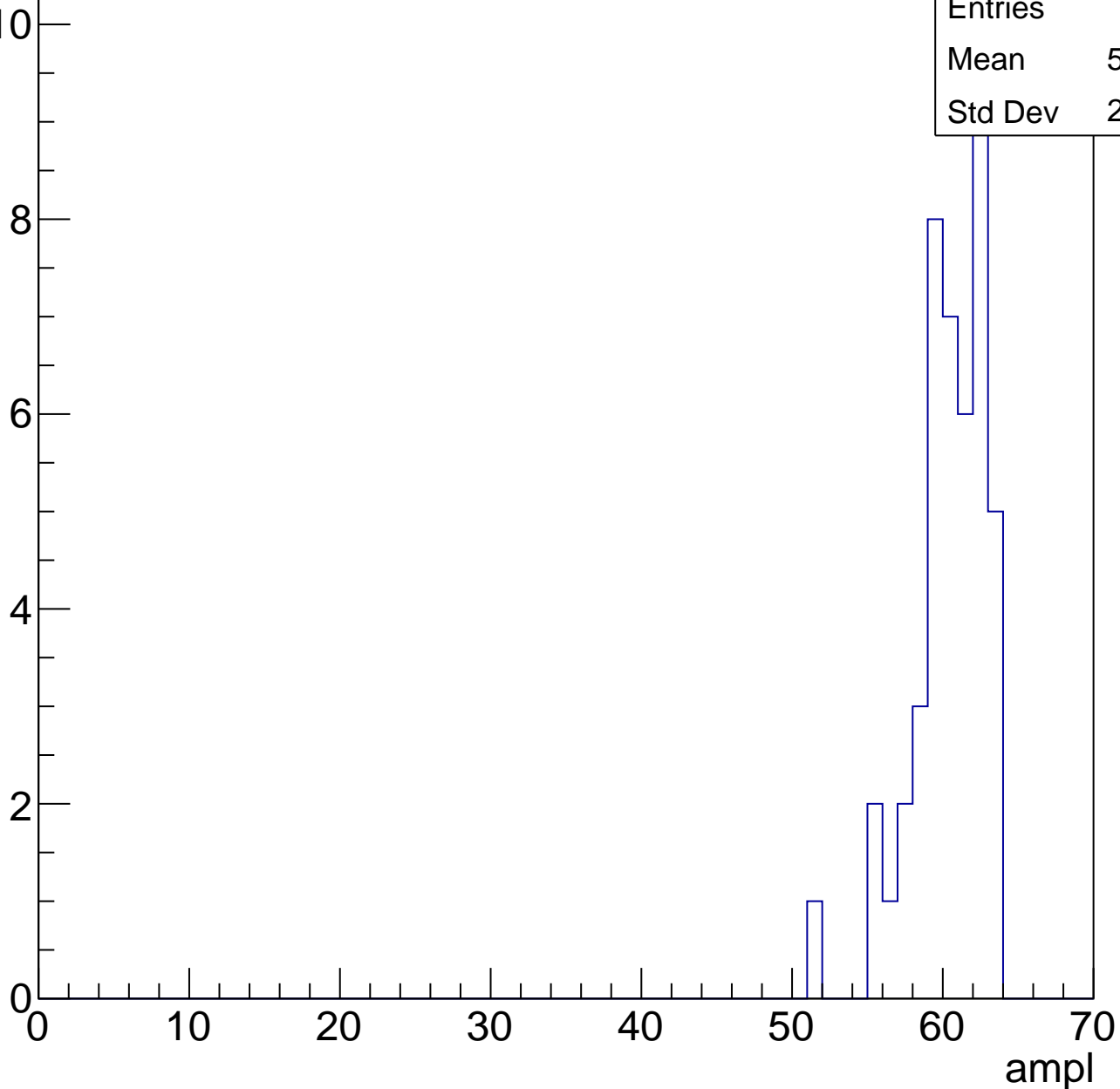


B1L103S, U17-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	59.96
Std Dev	2.476

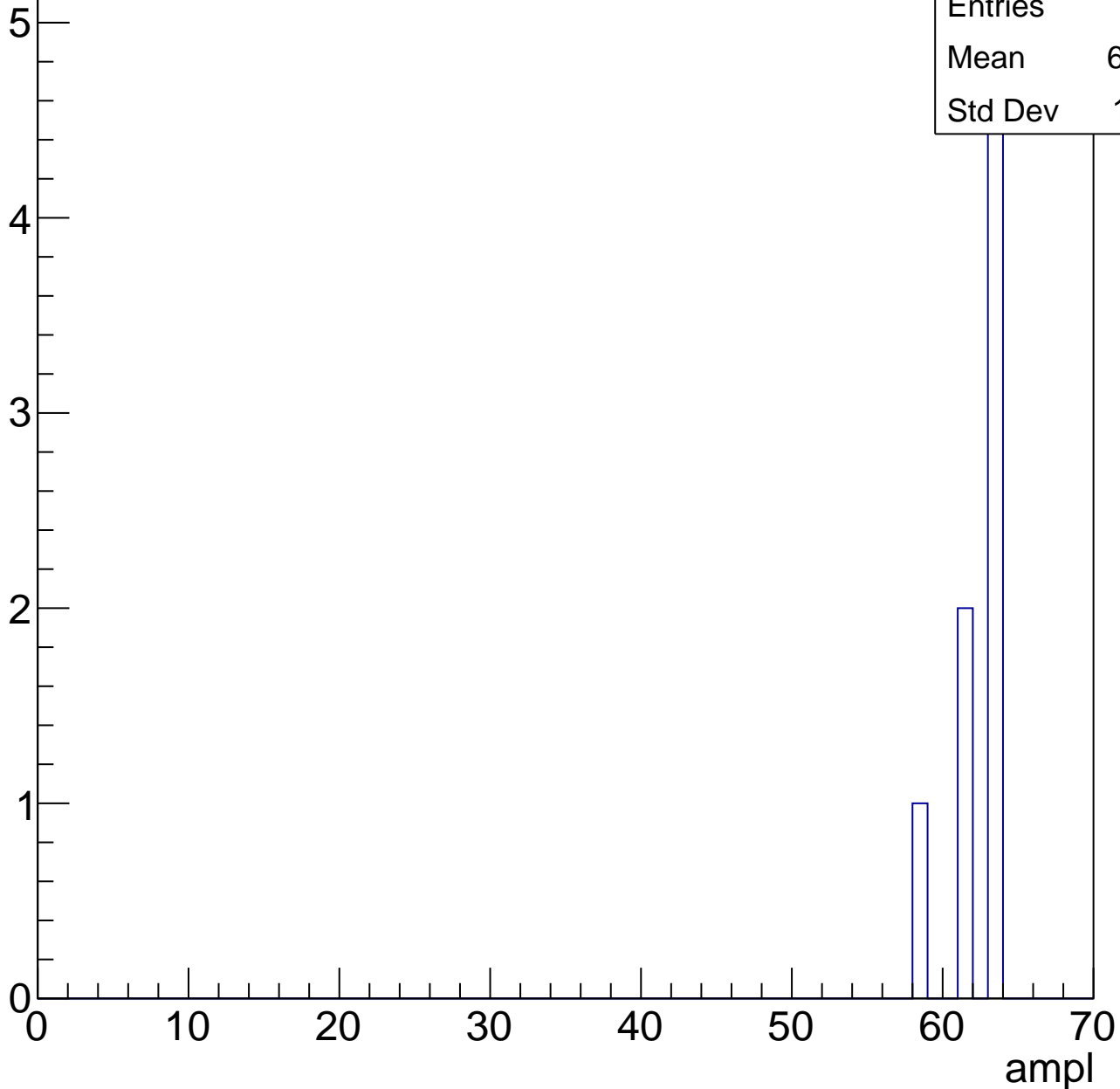


B1L103S, U17-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	61.88
Std Dev	1.691



B1L103S, U17-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch28, adc0

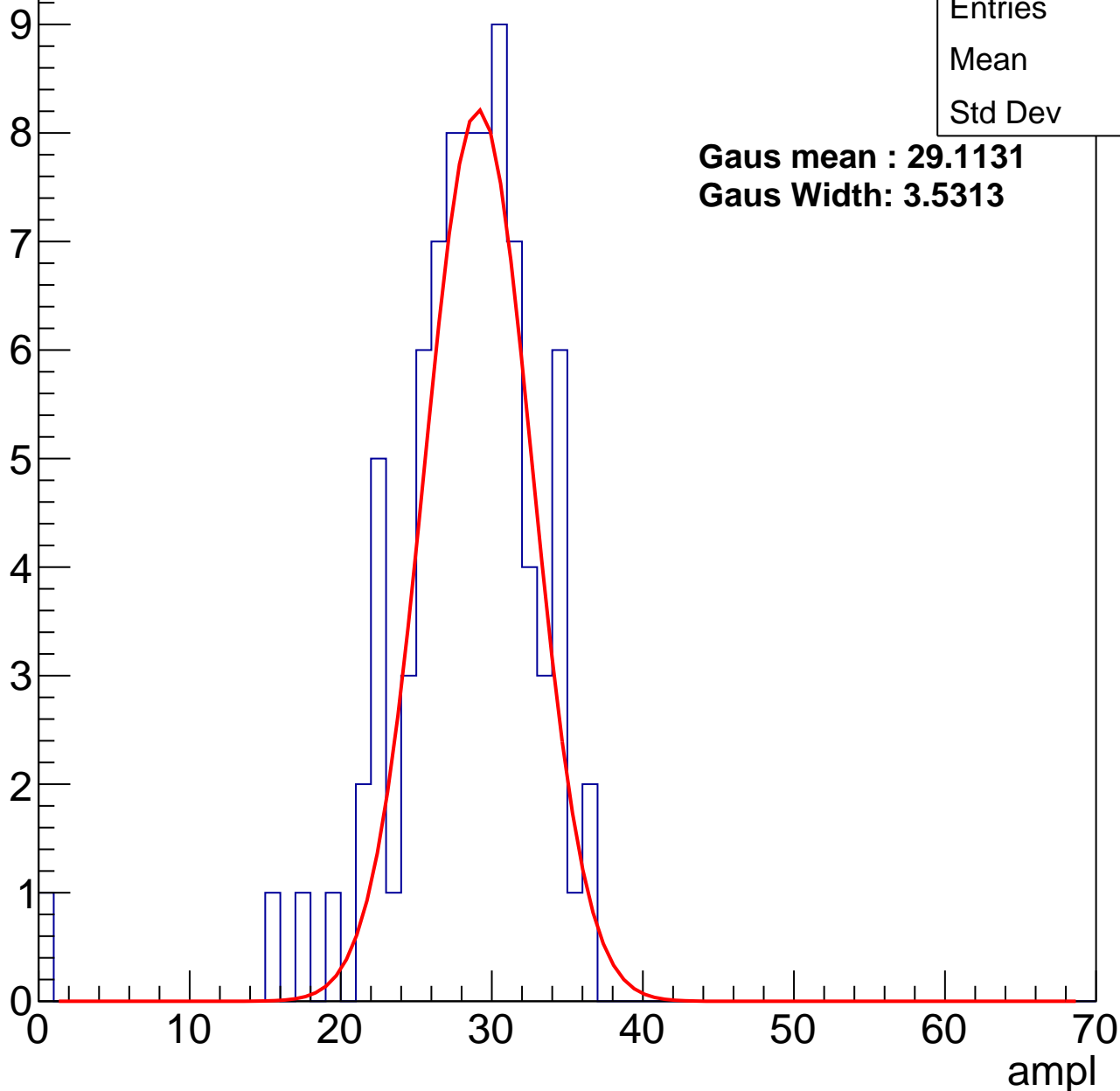
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	27.7
Std Dev	5.15

Gaus mean : 29.1131

Gaus Width: 3.5313



B1L103S, U17-ch28, adc1

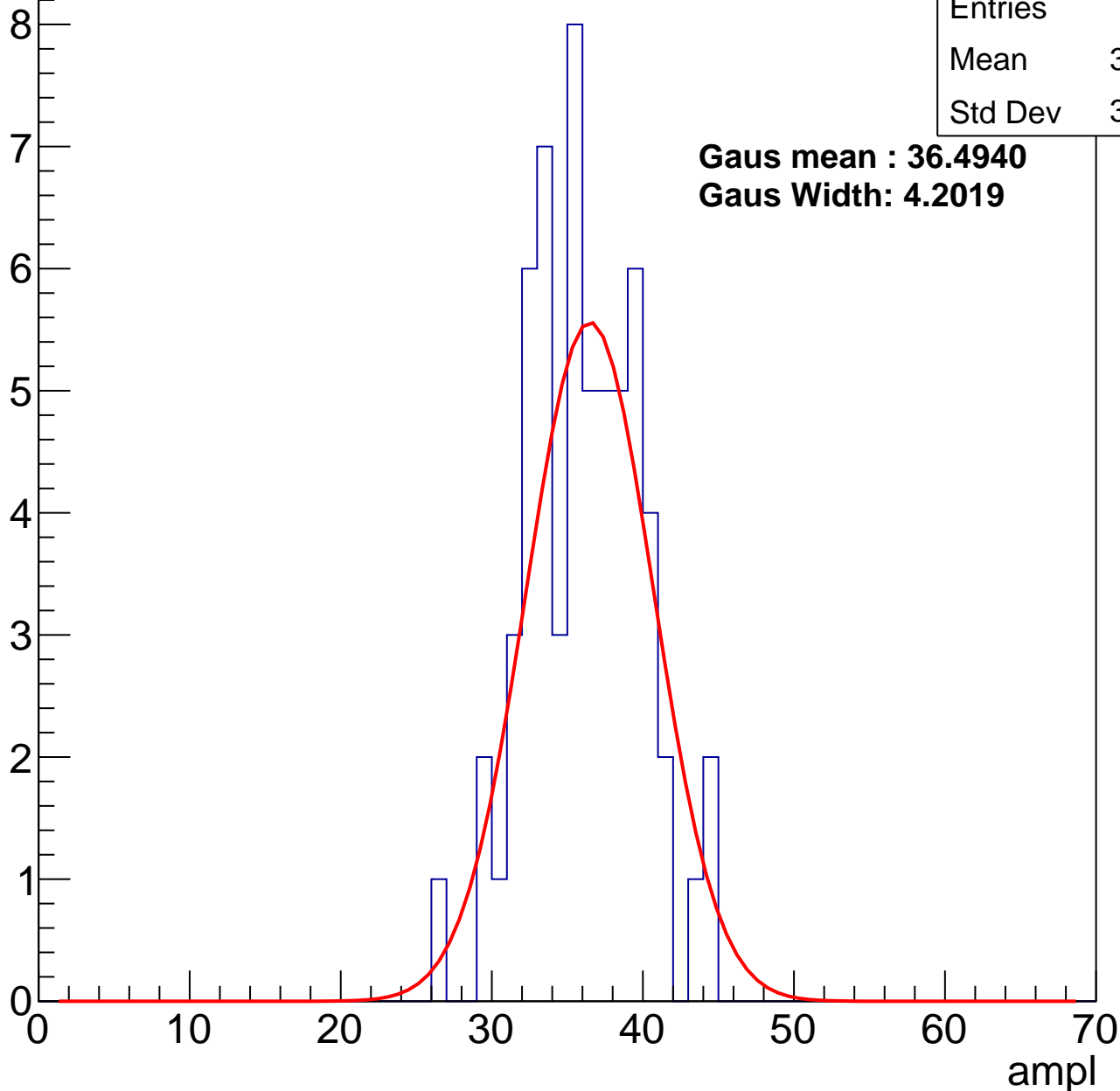
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.64
Std Dev	3.733

Gaus mean : 36.4940

Gaus Width: 4.2019

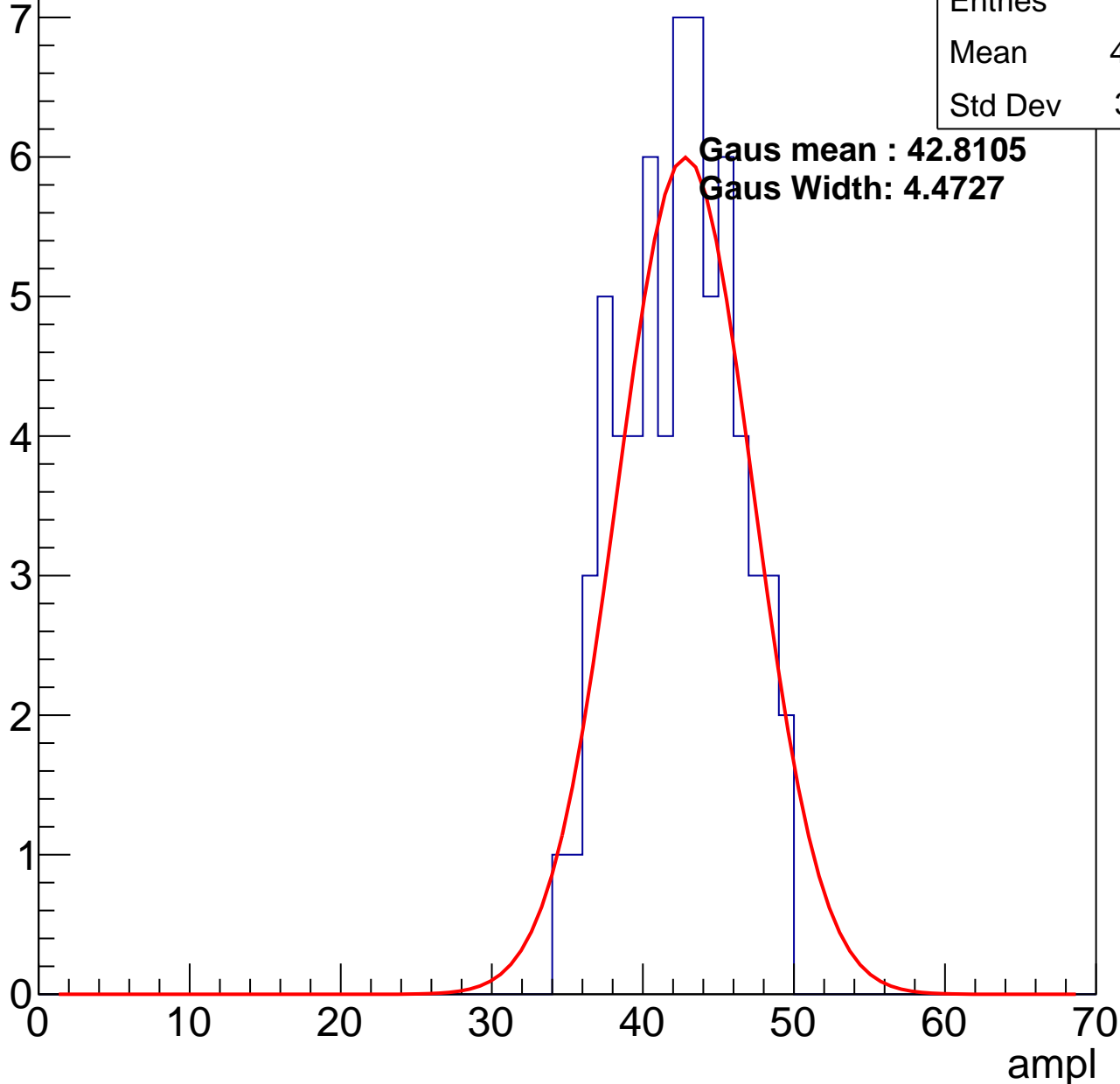


B1L103S, U17-ch28, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.94
Std Dev	3.721

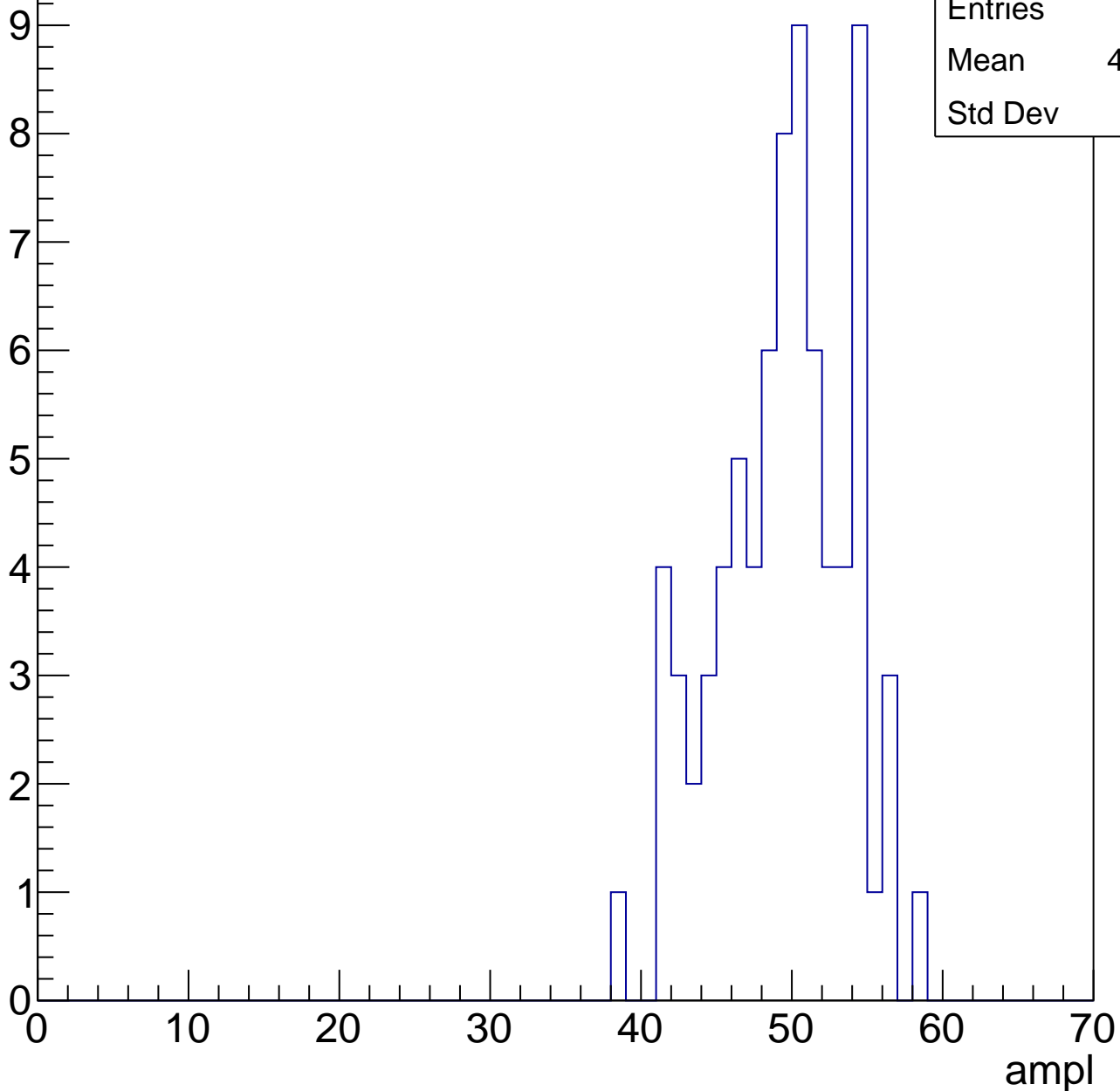


B1L103S, U17-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

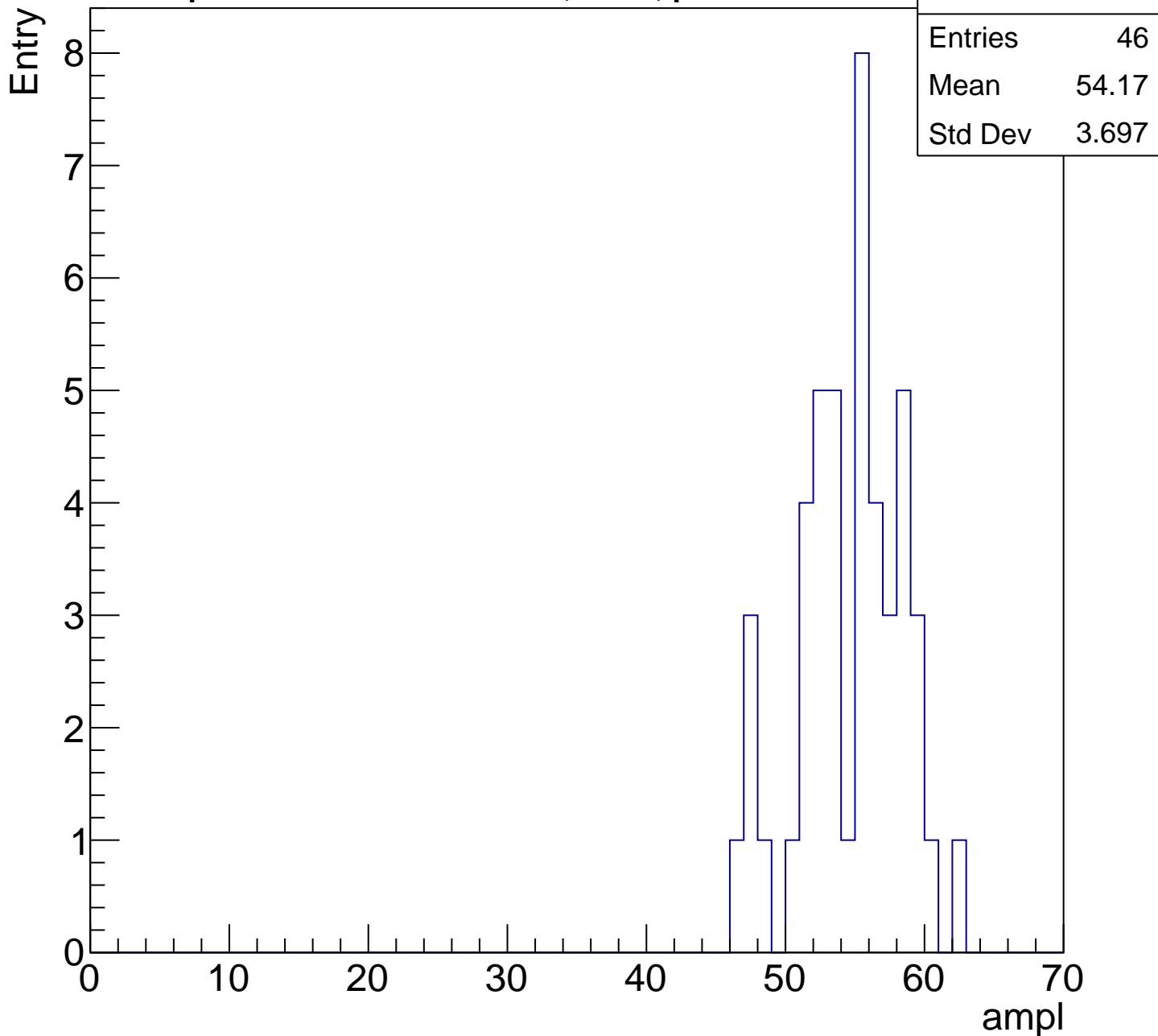
Entry

Entries	77
Mean	48.92
Std Dev	4.3



B1L103S, U17-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

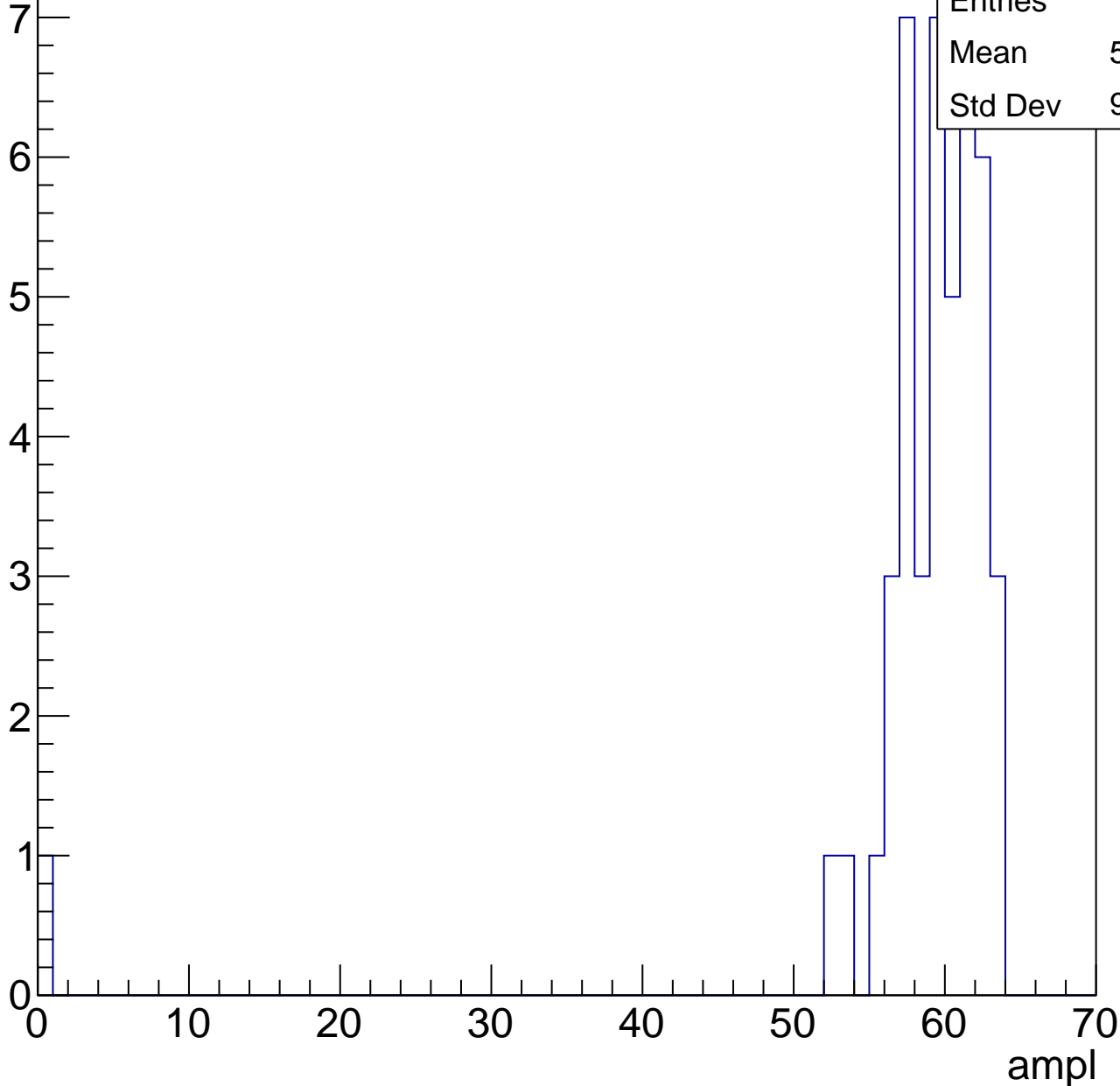


B1L103S, U17-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	57.82
Std Dev	9.083

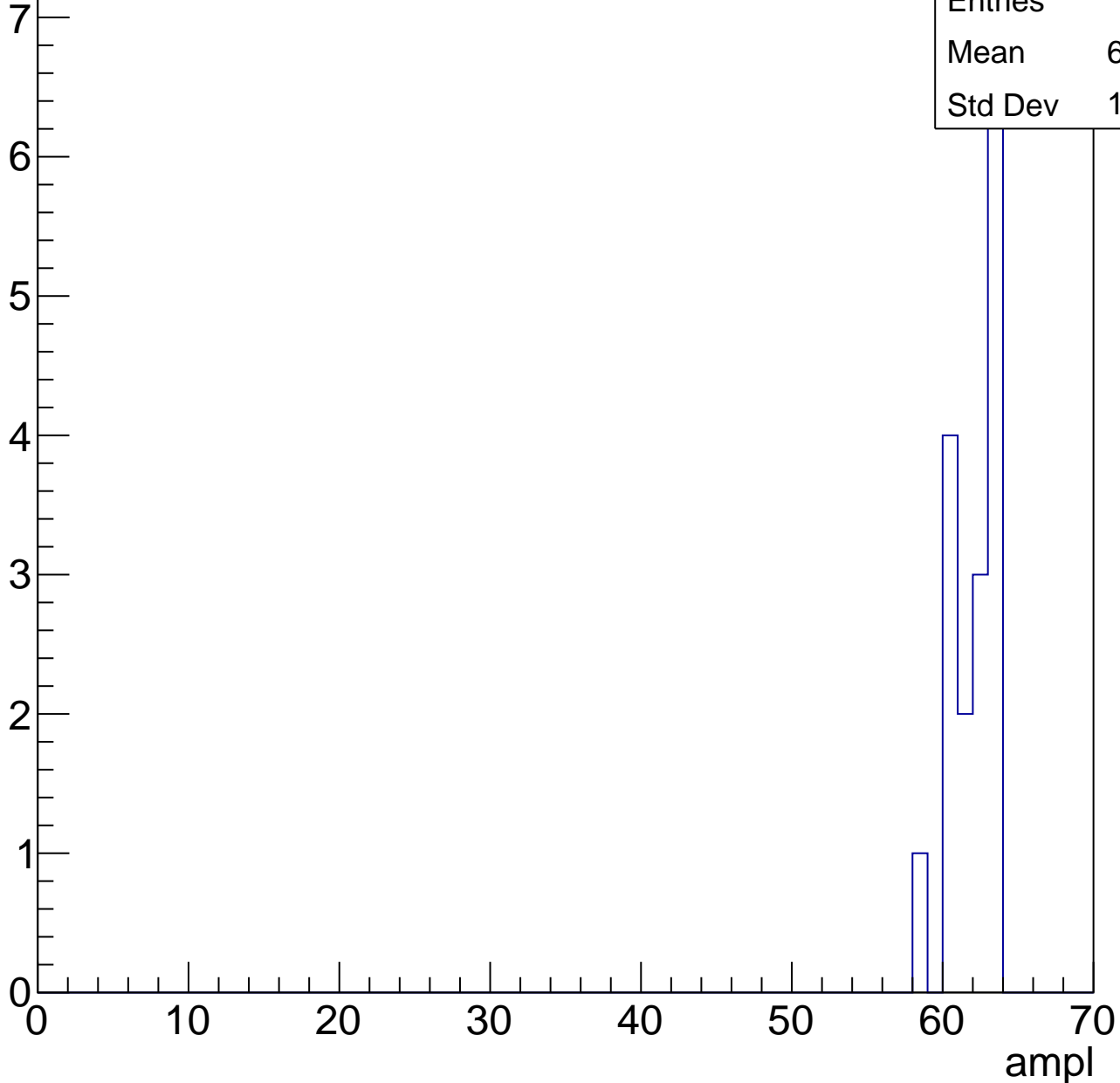


B1L103S, U17-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.59
Std Dev	1.497



B1L103S, U17-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch29, adc0

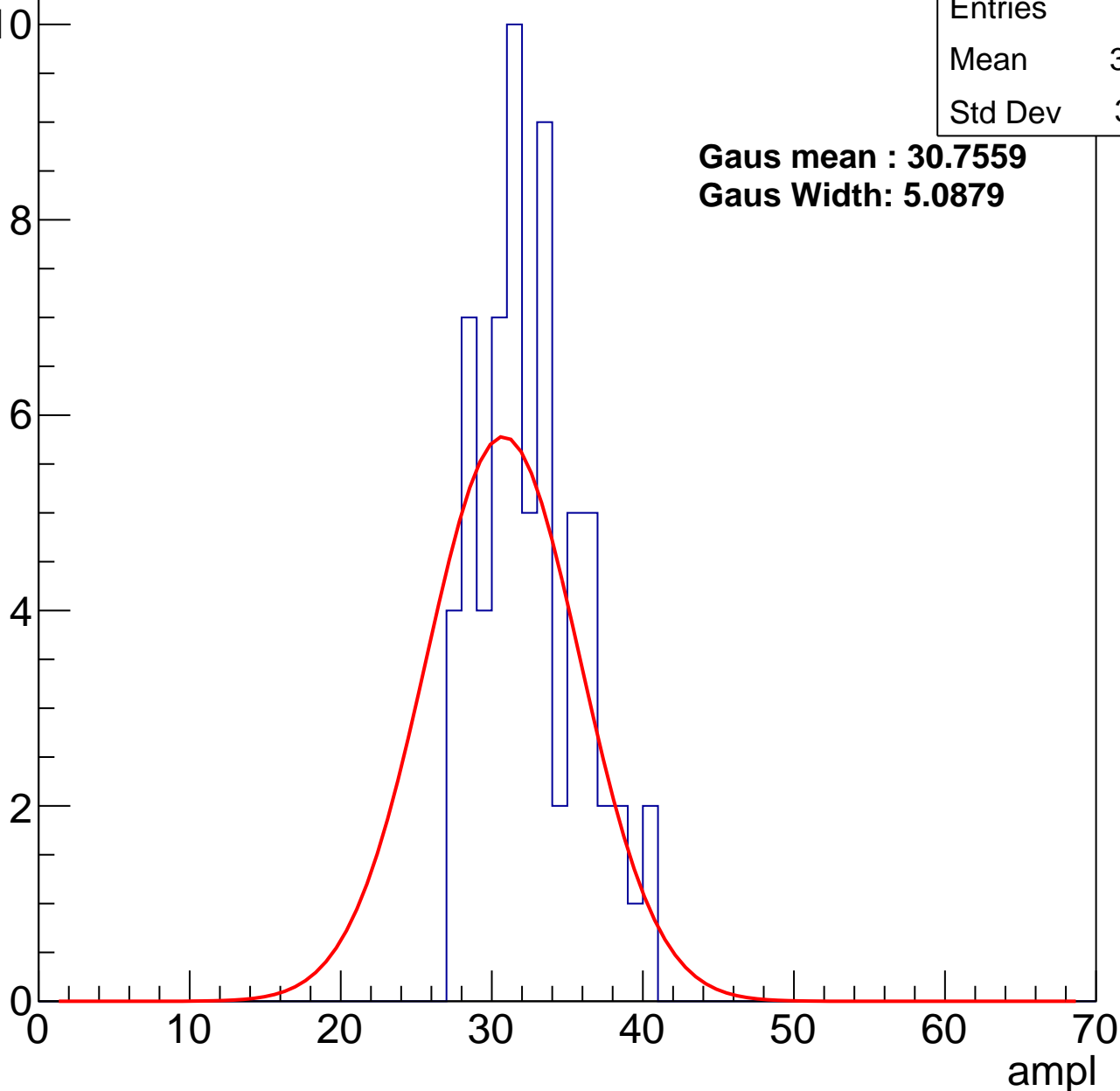
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	32.14
Std Dev	3.341

Gaus mean : 30.7559

Gaus Width: 5.0879



B1L103S, U17-ch29, adc1

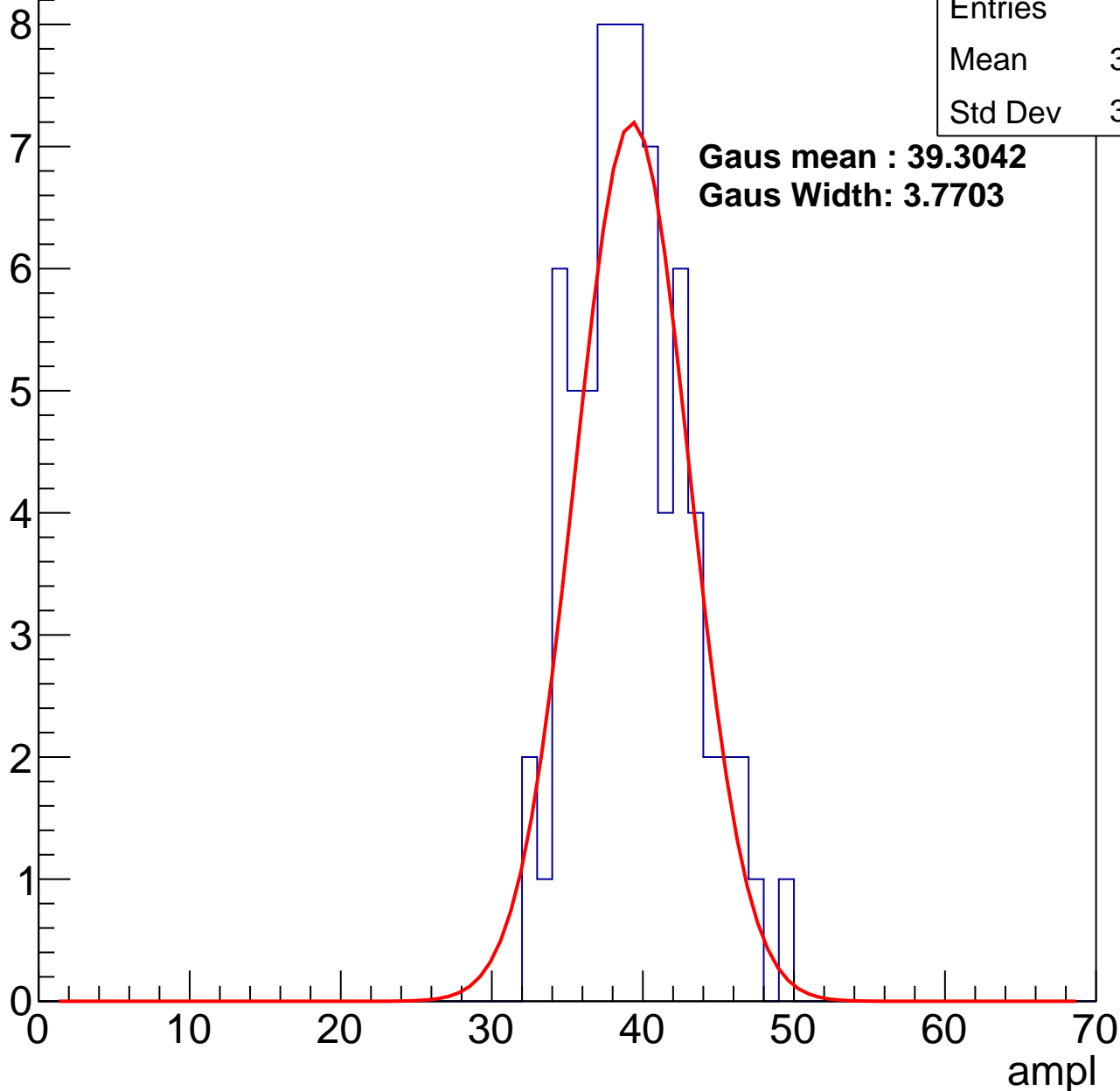
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	38.92
Std Dev	3.677

Gaus mean : 39.3042

Gaus Width: 3.7703



B1L103S, U17-ch29, adc2

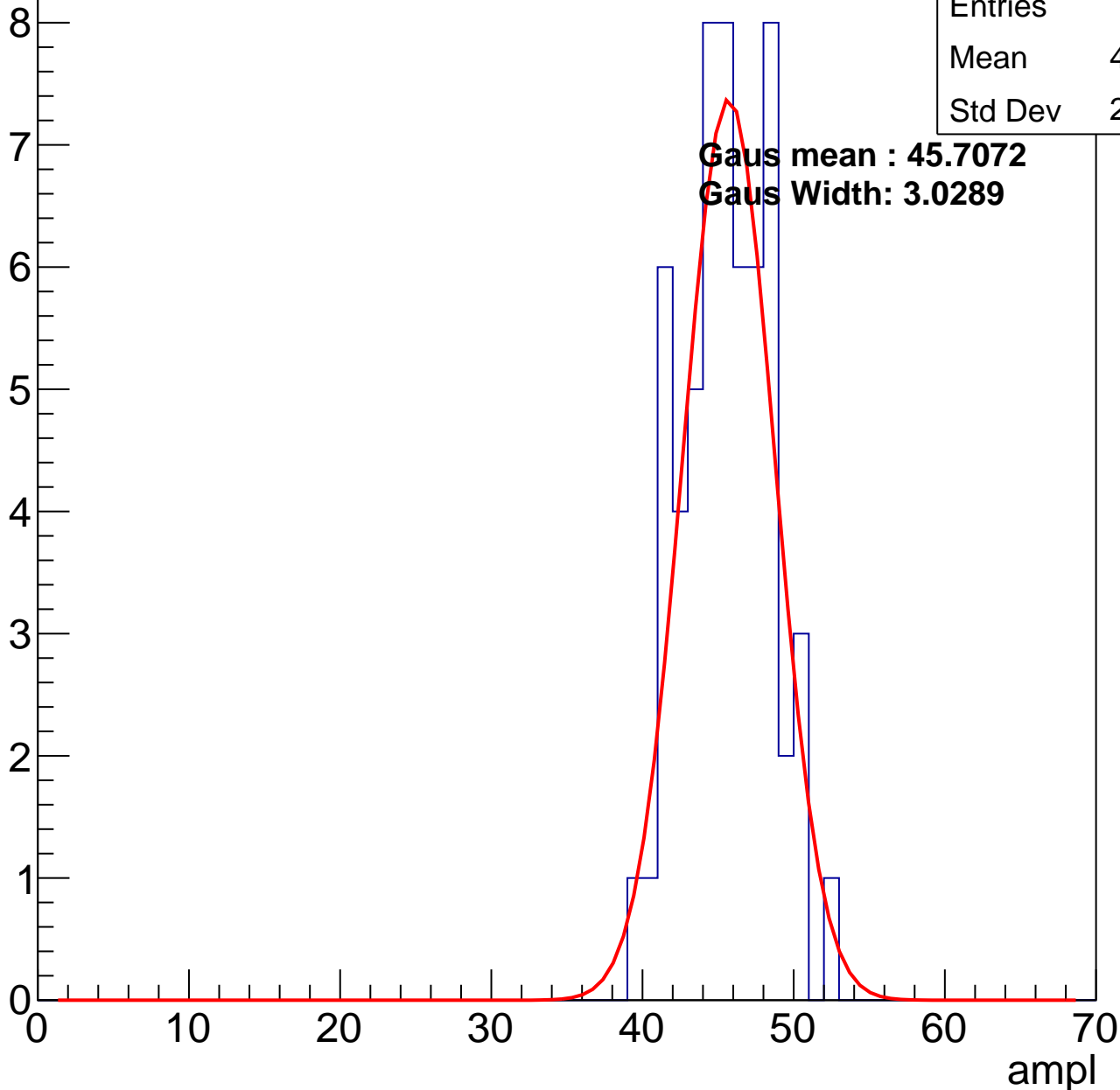
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	45.12
Std Dev	2.847

Gaus mean : 45.7072

Gaus Width: 3.0289

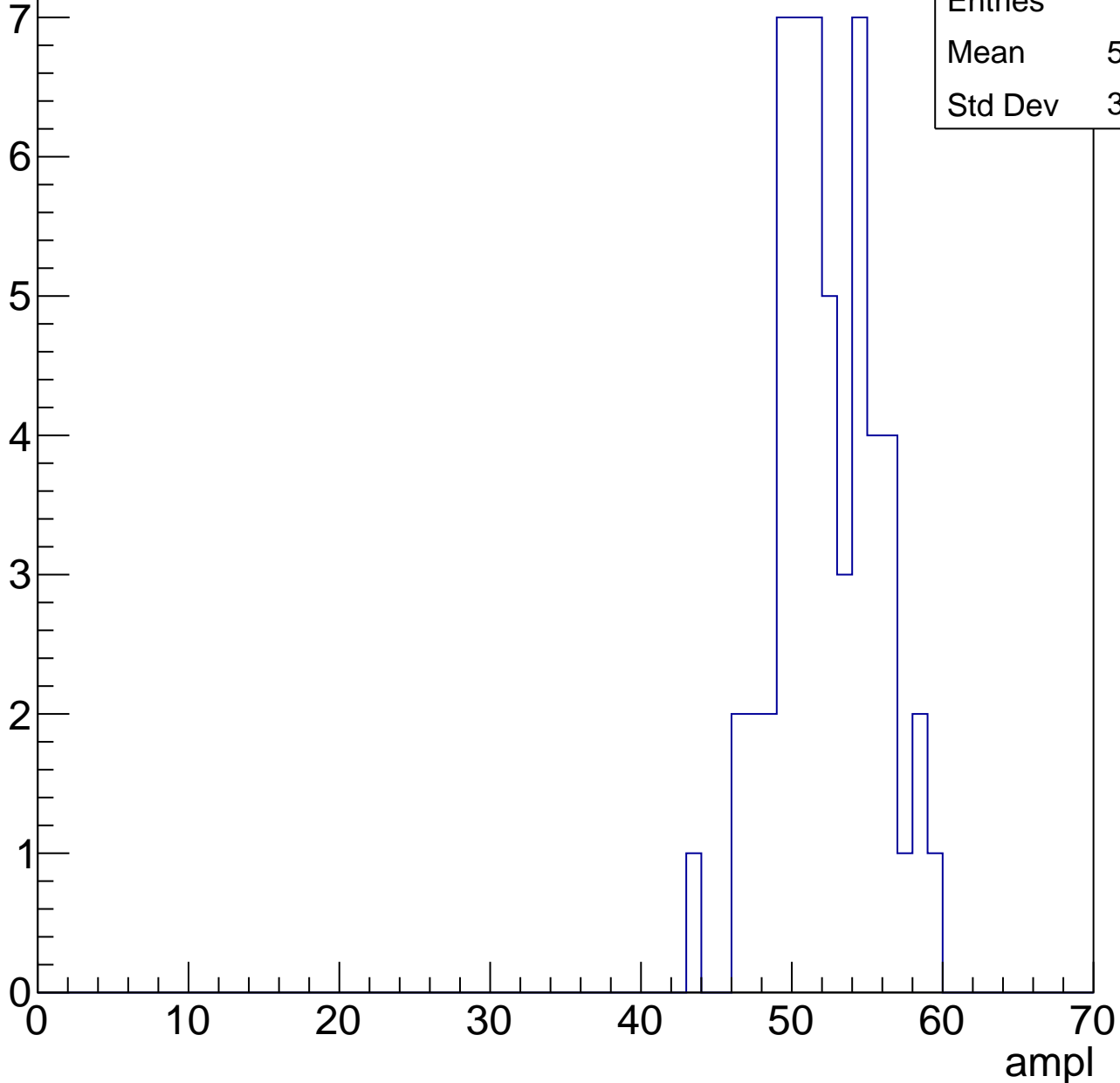


B1L103S, U17-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	51.78
Std Dev	3.323

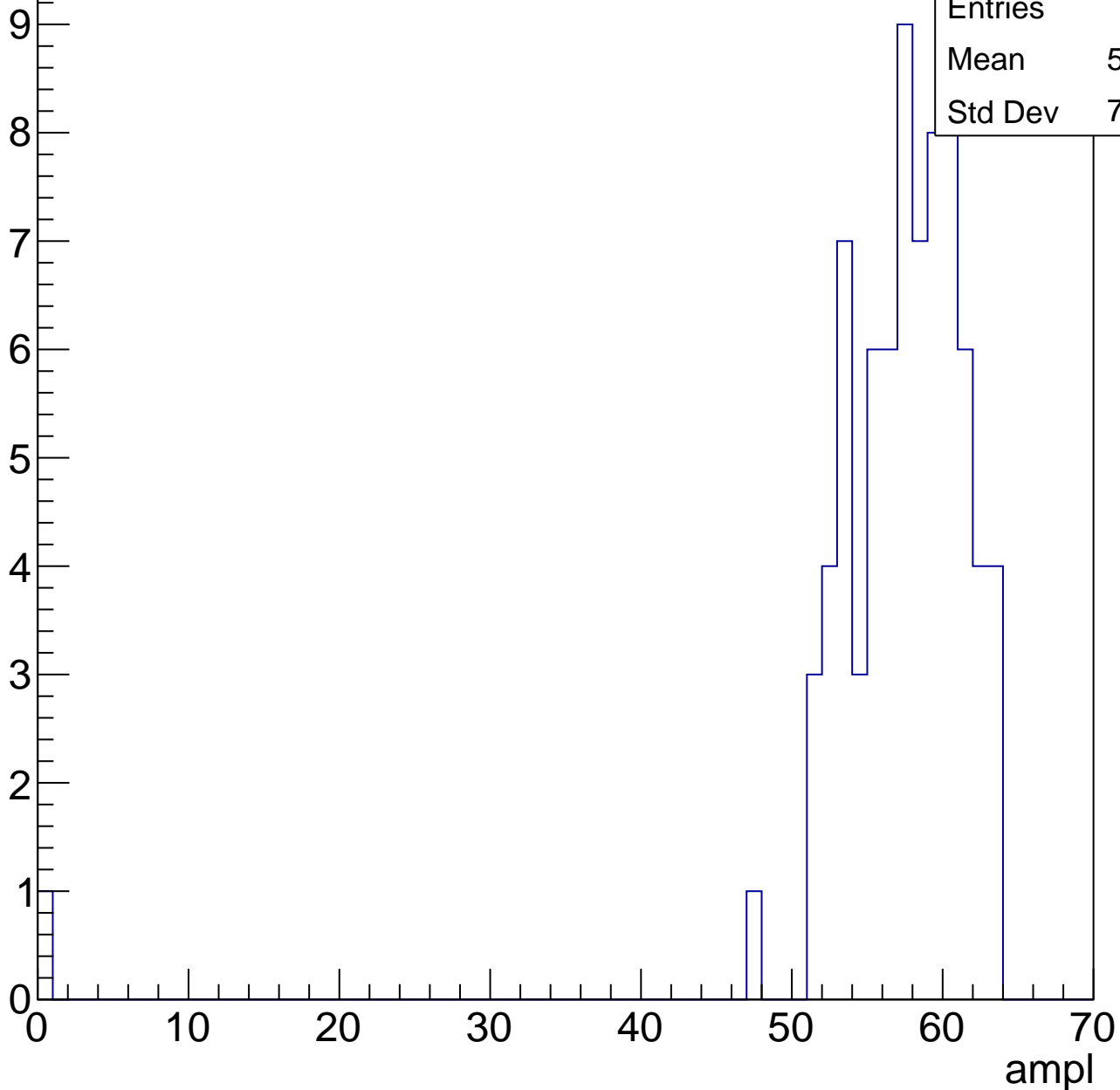


B1L103S, U17-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	56.46
Std Dev	7.306

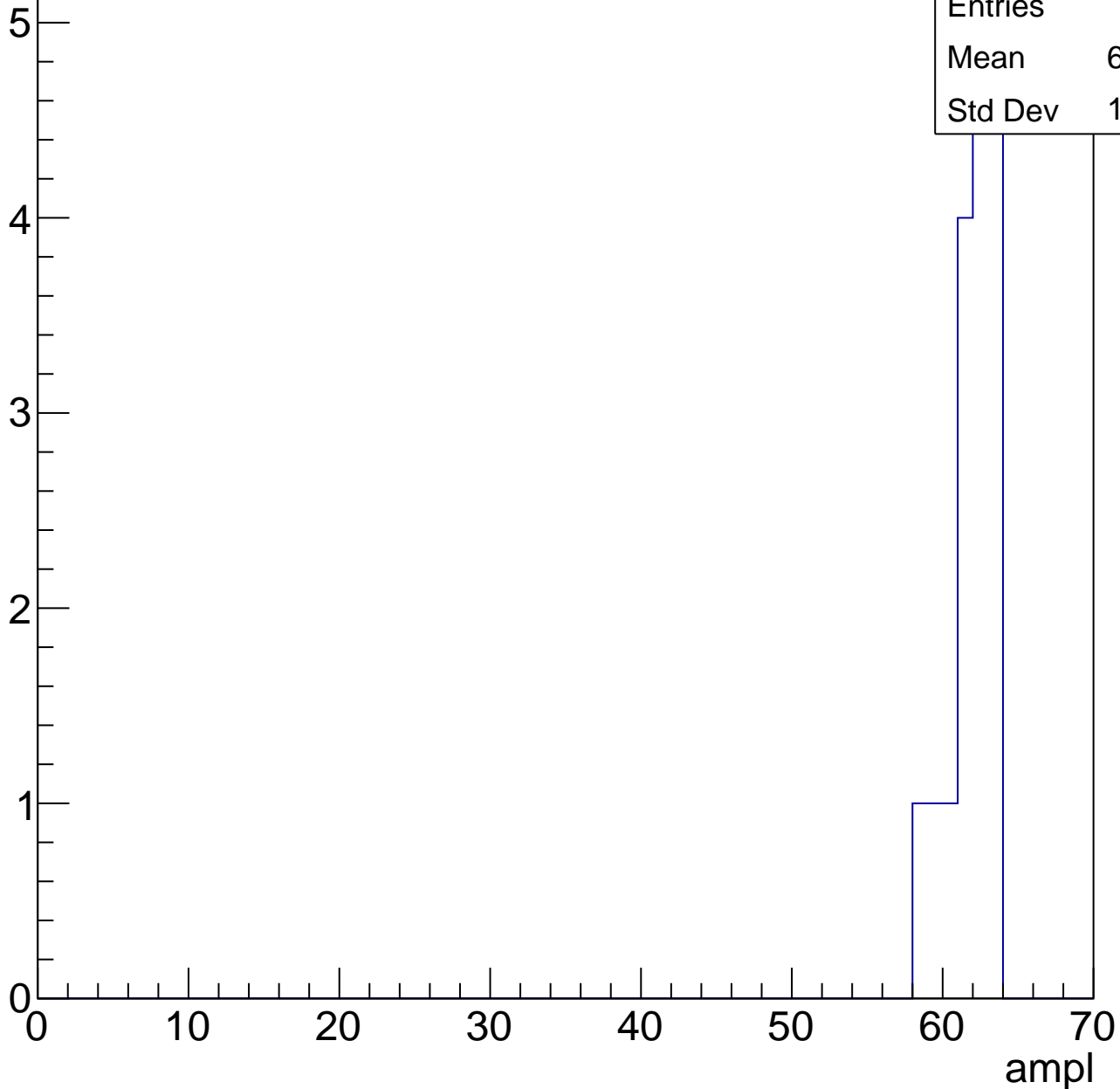


B1L103S, U17-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.53
Std Dev	1.419



B1L103S, U17-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch30, adc0

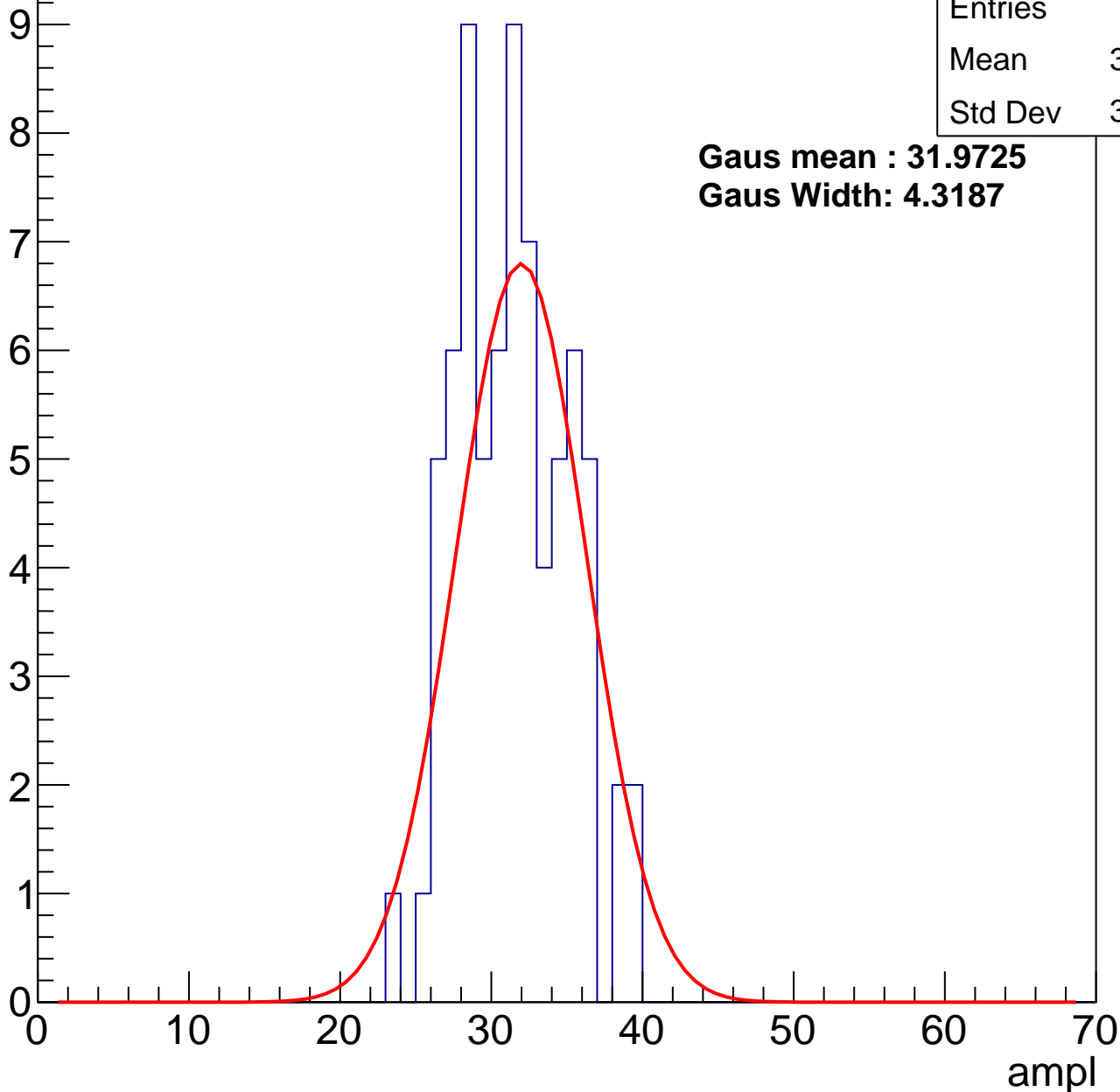
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	31.04
Std Dev	3.594

Gaus mean : 31.9725

Gaus Width: 4.3187



B1L103S, U17-ch30, adc1

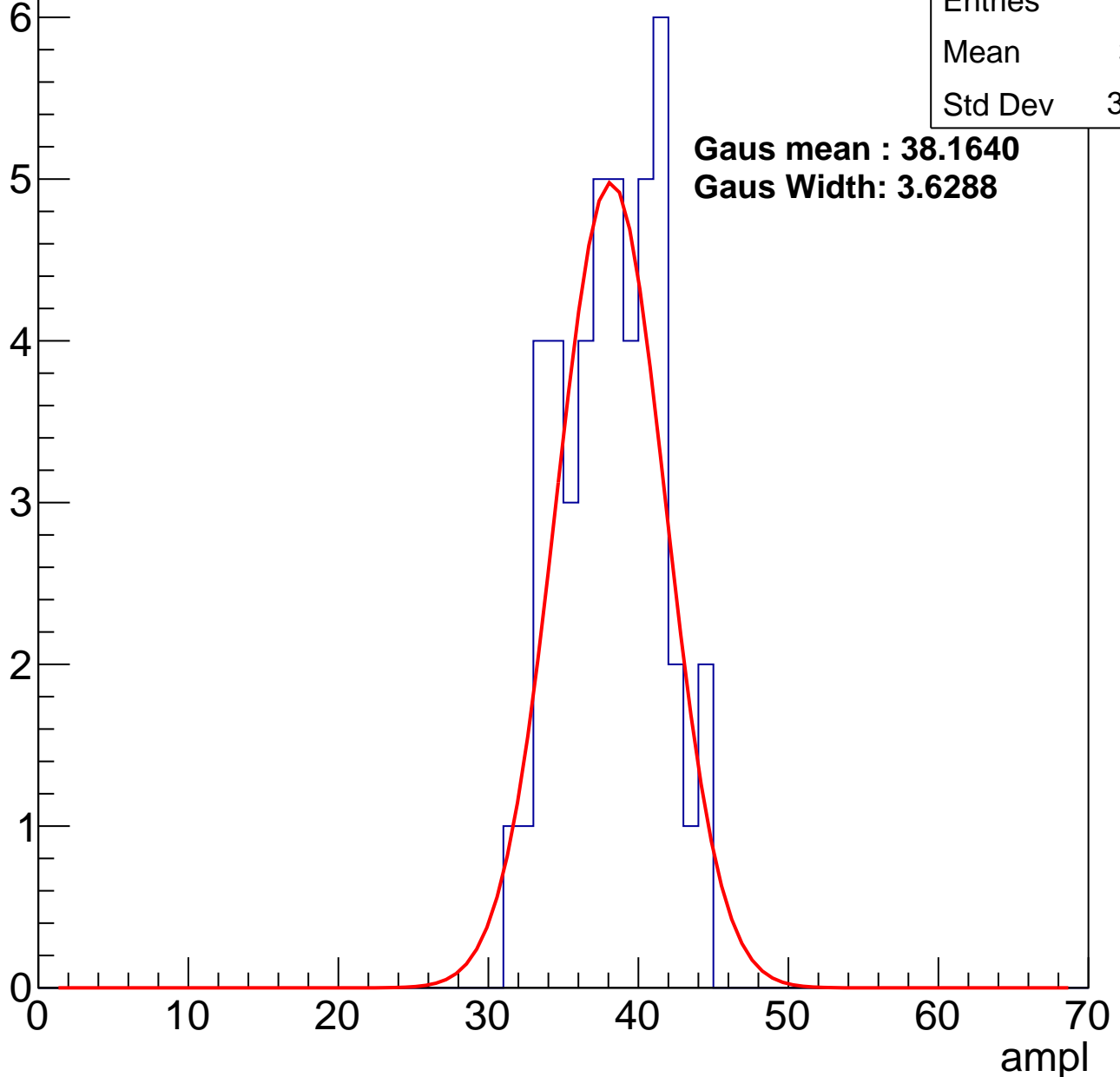
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	37.7
Std Dev	3.261

Gaus mean : 38.1640

Gaus Width: 3.6288

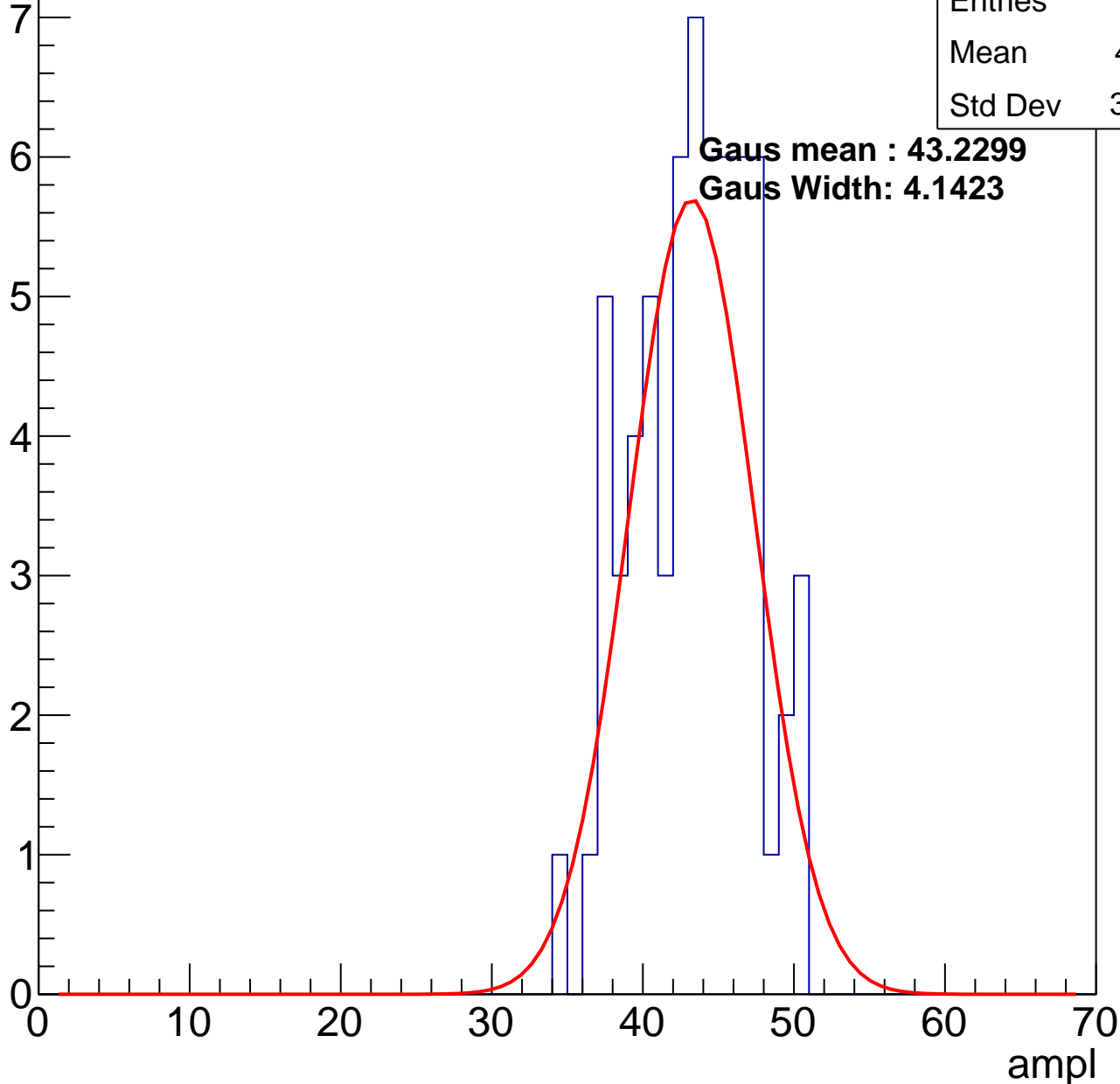


B1L103S, U17-ch30, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.91
Std Dev	3.794

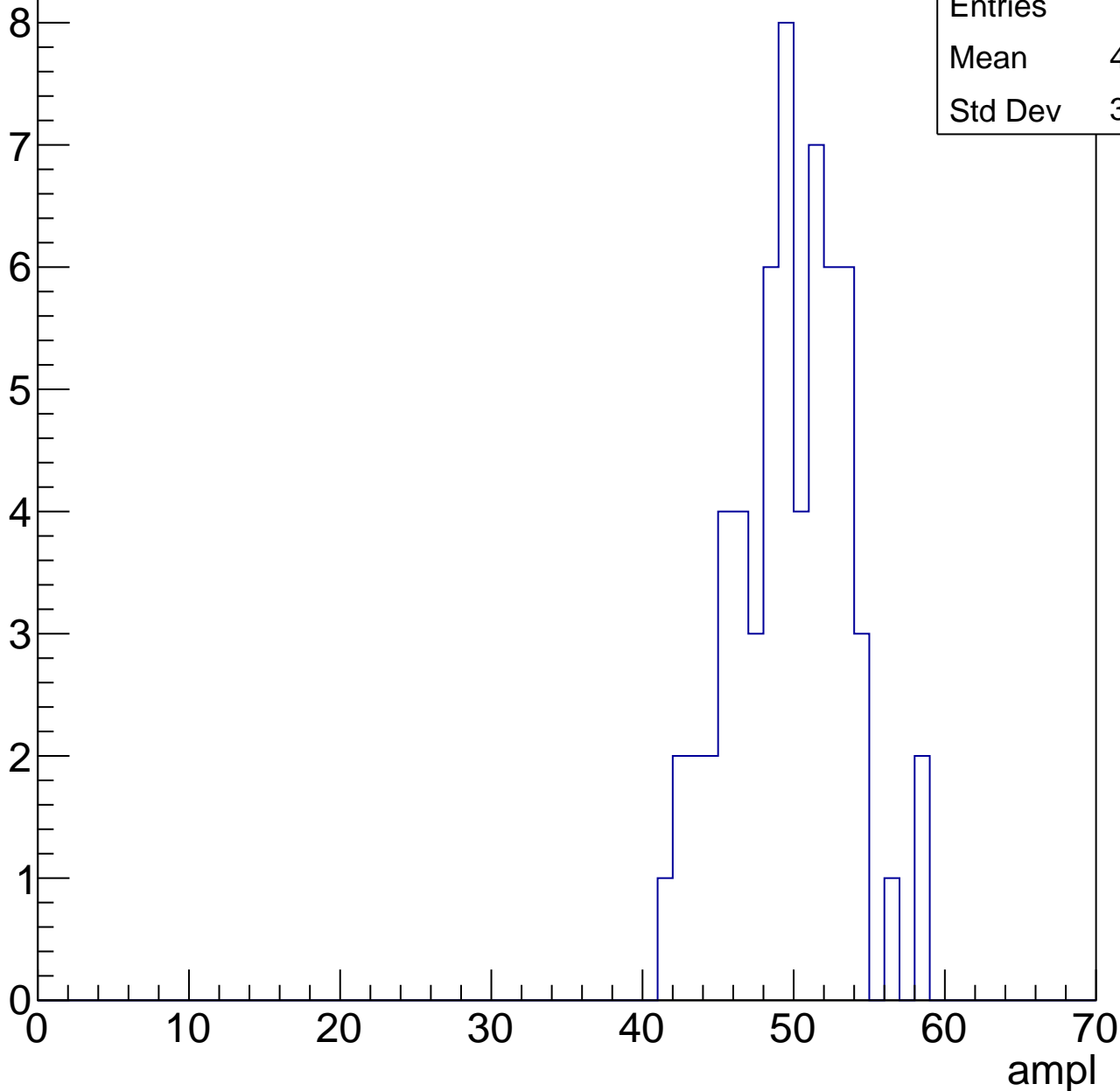


B1L103S, U17-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.26
Std Dev	3.767

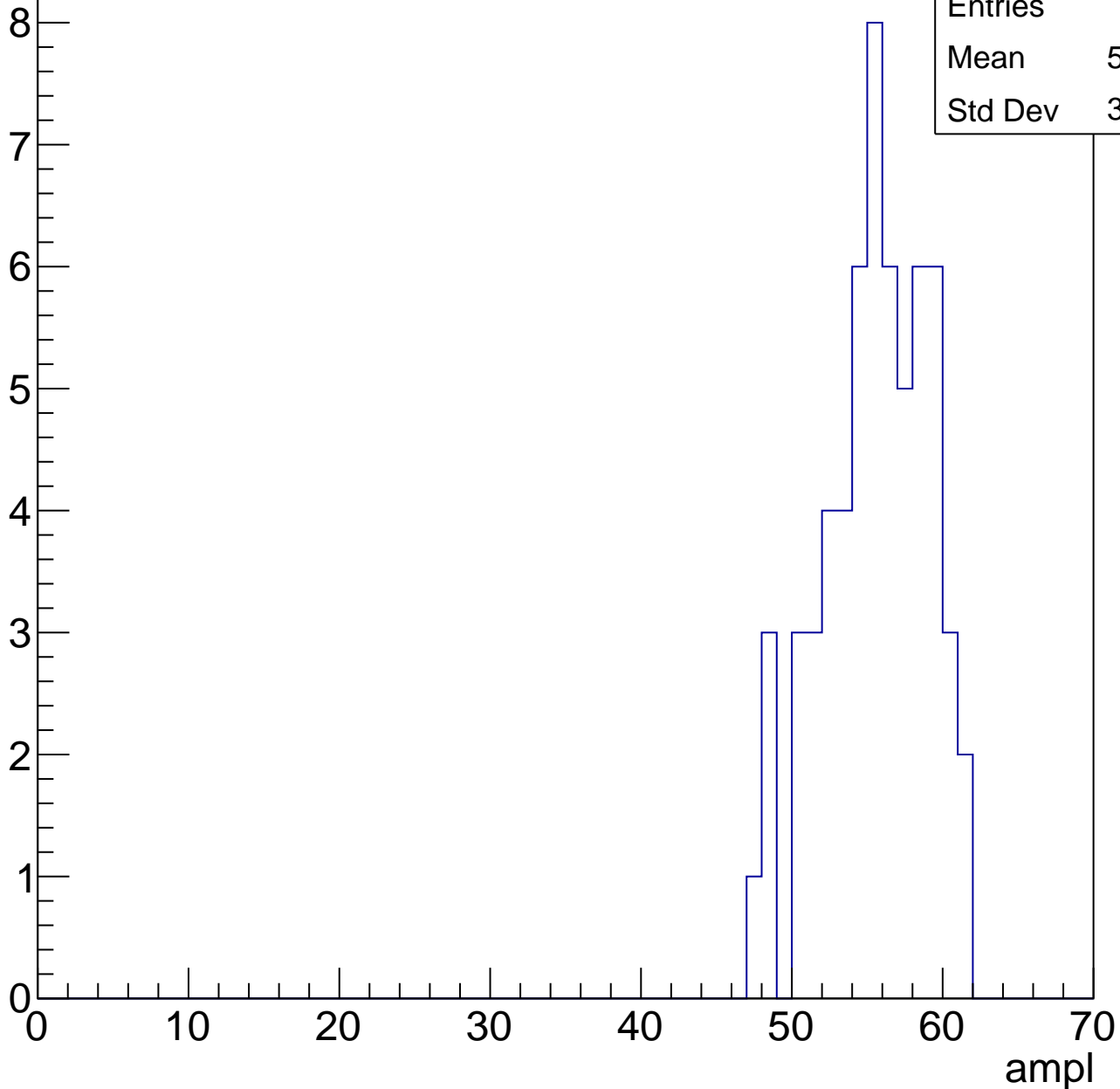


B1L103S, U17-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.05
Std Dev	3.452

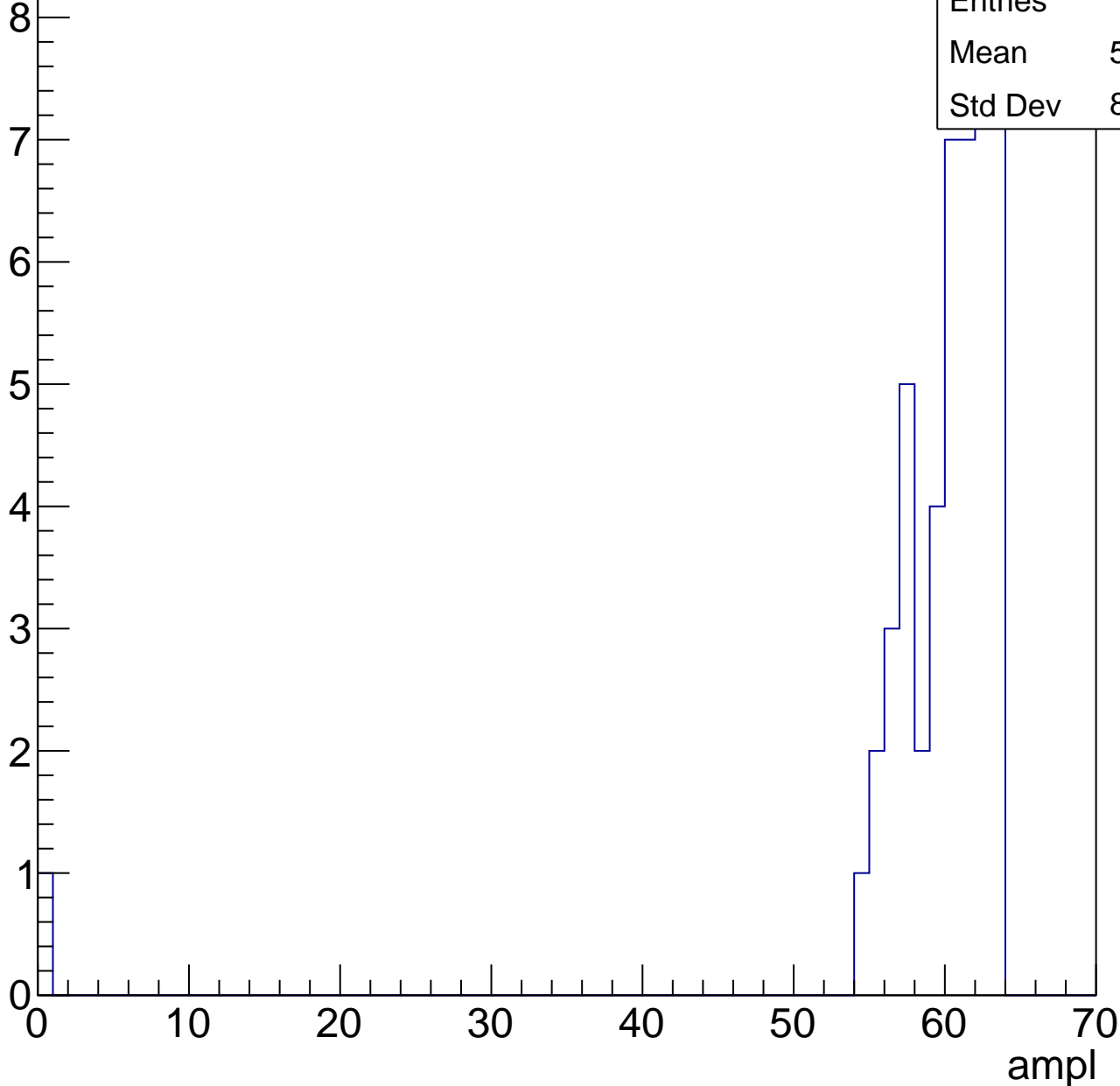


B1L103S, U17-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.67
Std Dev	8.917



B1L103S, U17-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch31, adc0

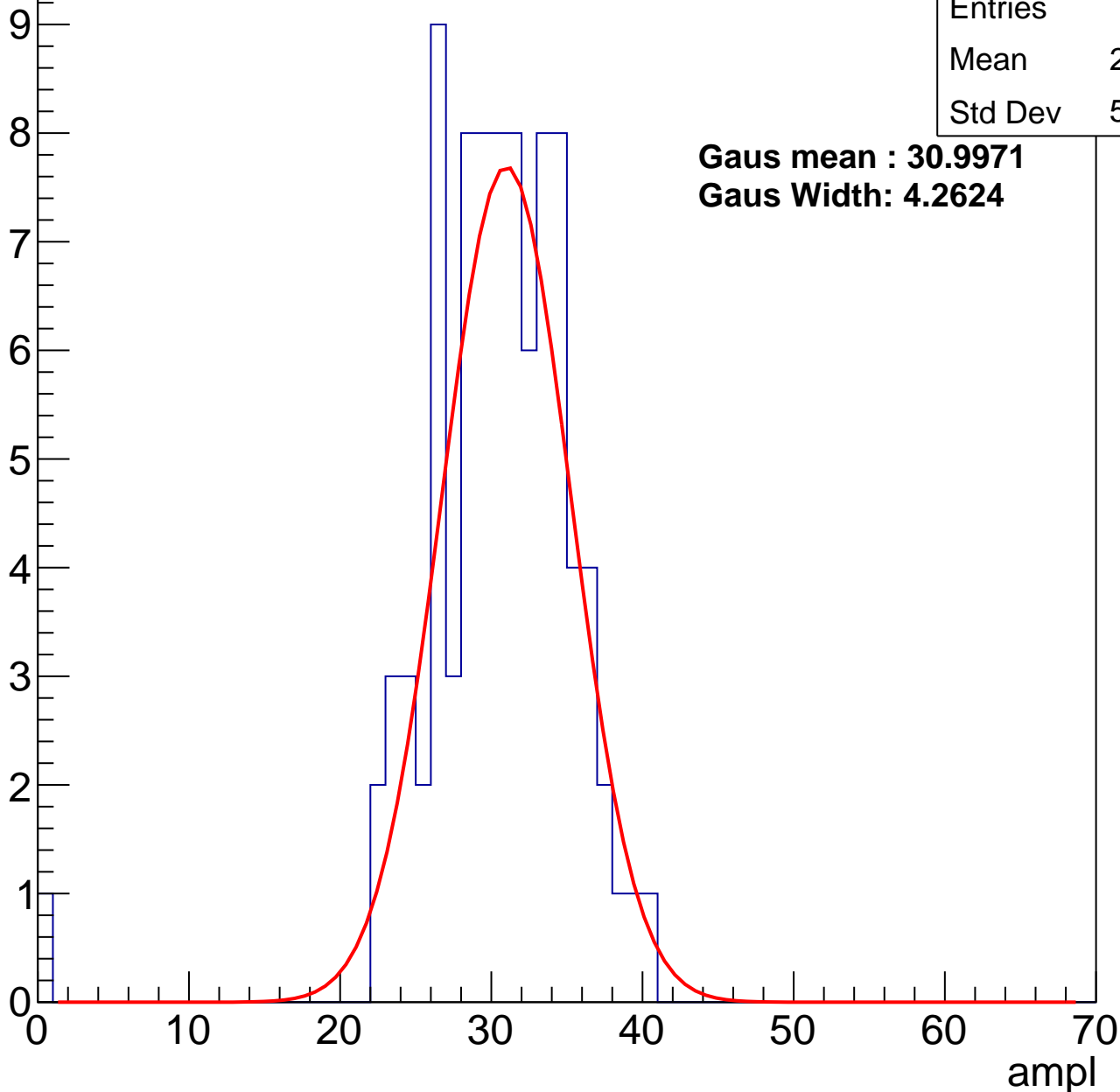
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	29.97
Std Dev	5.124

Gaus mean : 30.9971

Gaus Width: 4.2624



B1L103S, U17-ch31, adc1

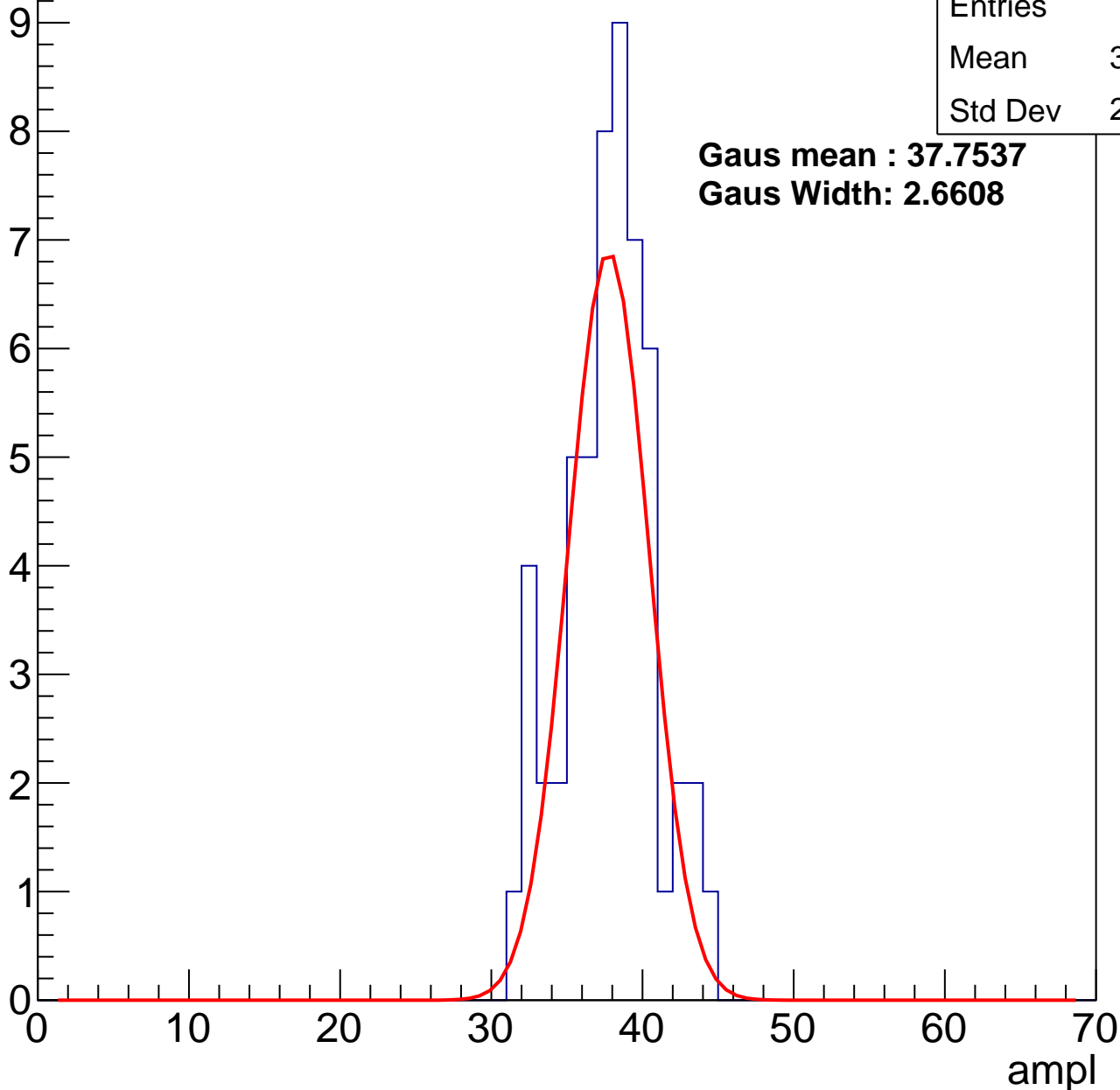
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	37.35
Std Dev	2.962

Gaus mean : 37.7537

Gaus Width: 2.6608



B1L103S, U17-ch31, adc2

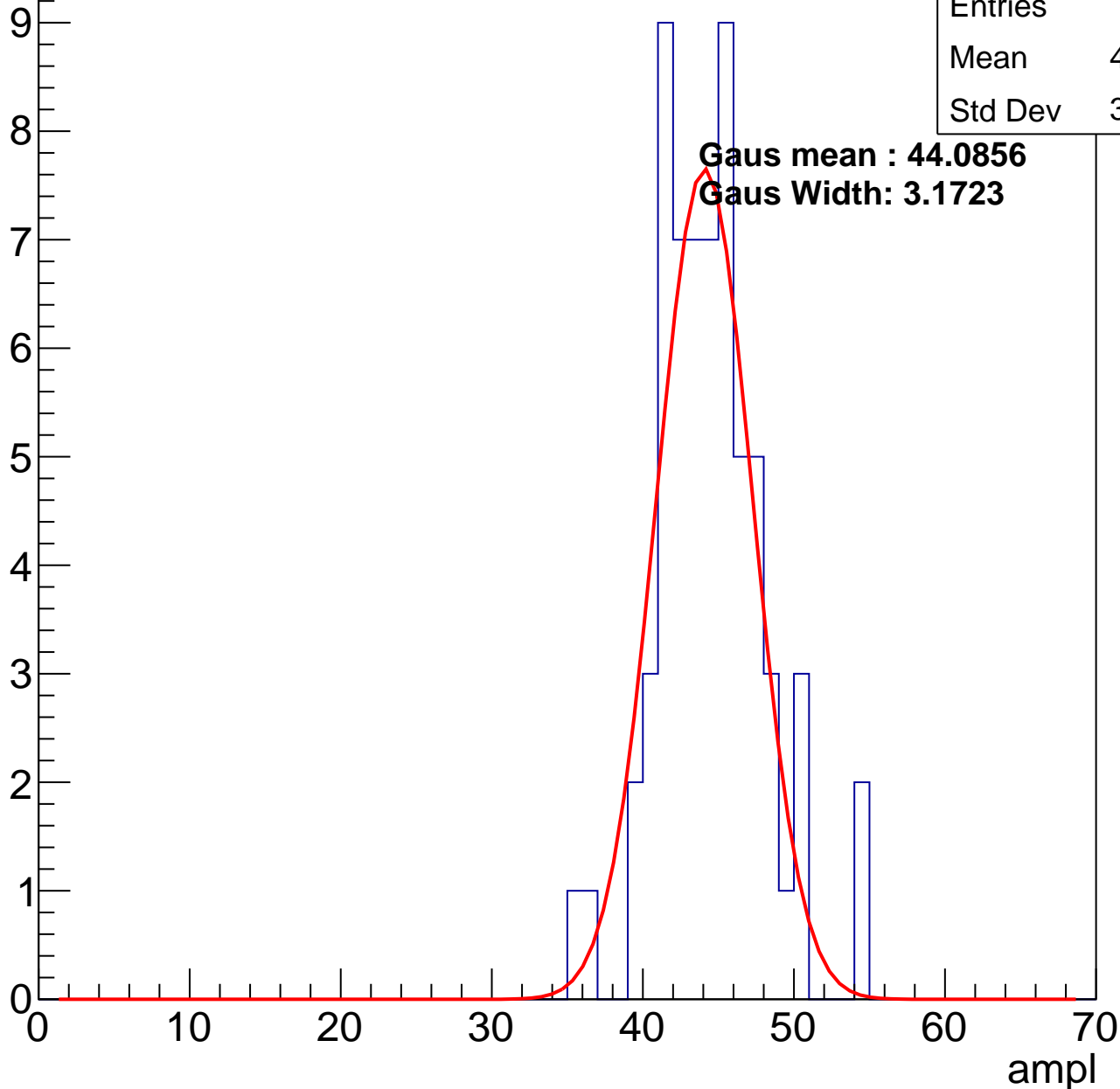
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.03
Std Dev	3.539

Gaus mean : 44.0856

Gaus Width: 3.1723

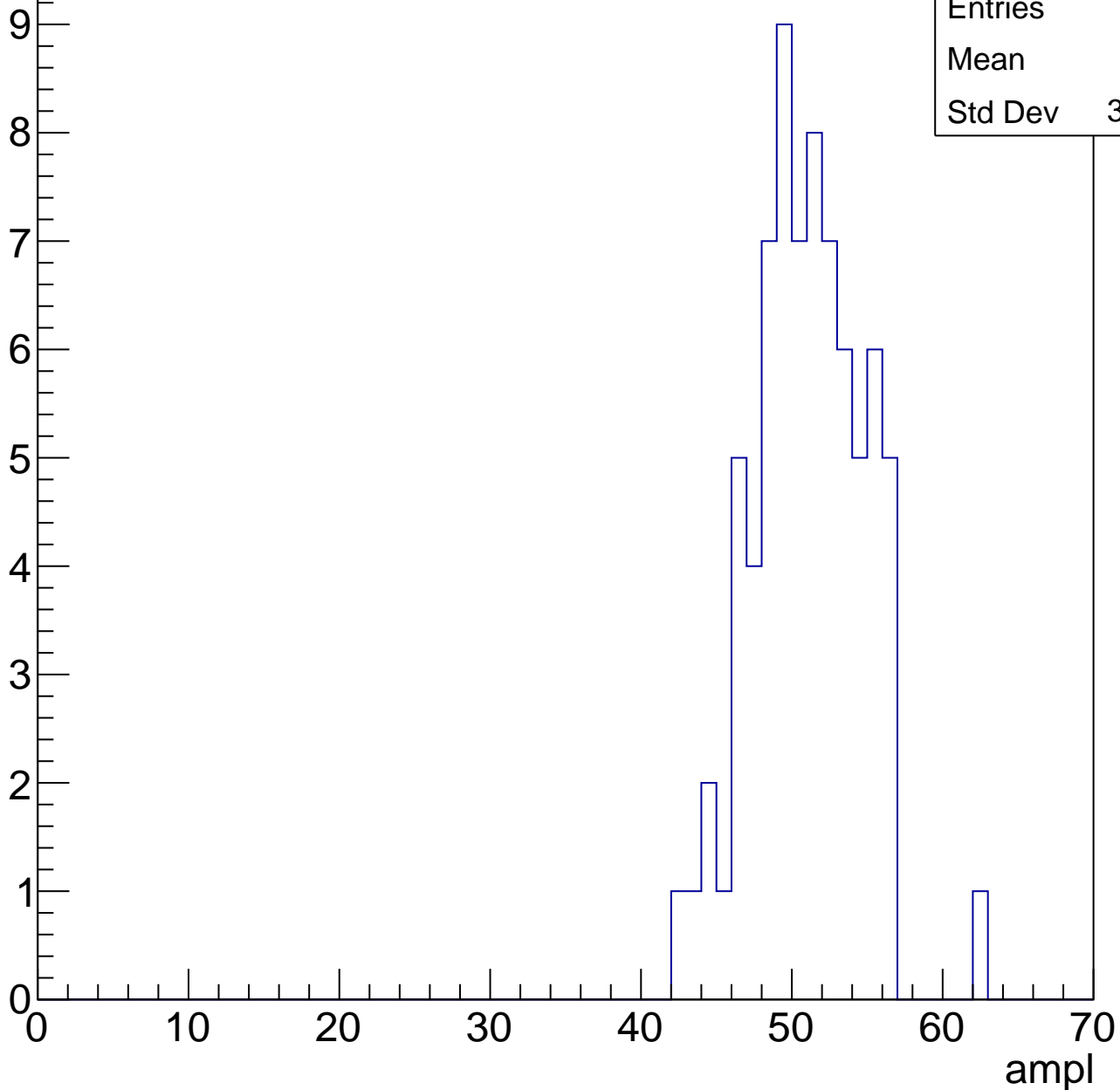


B1L103S, U17-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	50.6
Std Dev	3.615

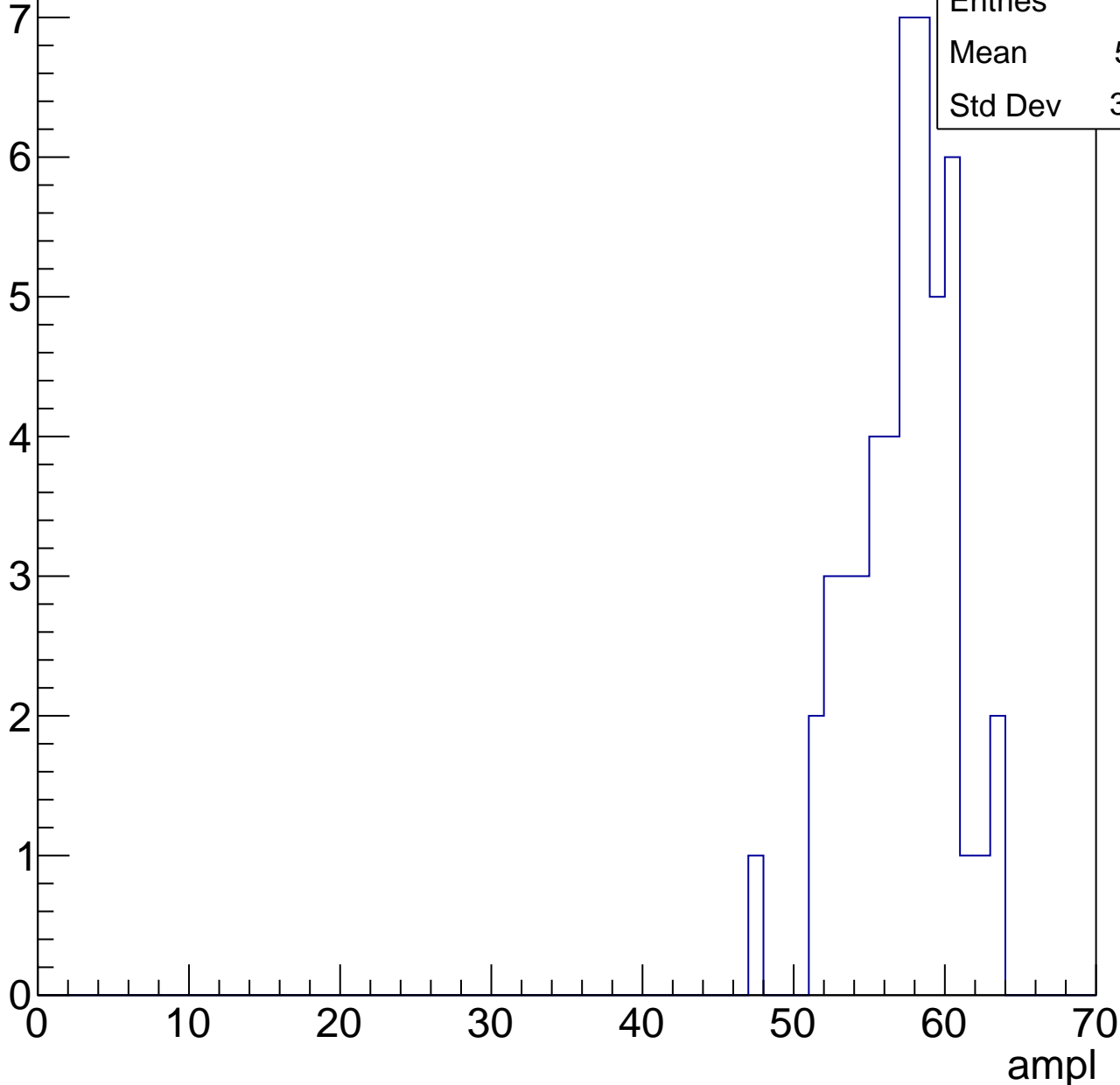


B1L103S, U17-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	56.71
Std Dev	3.295

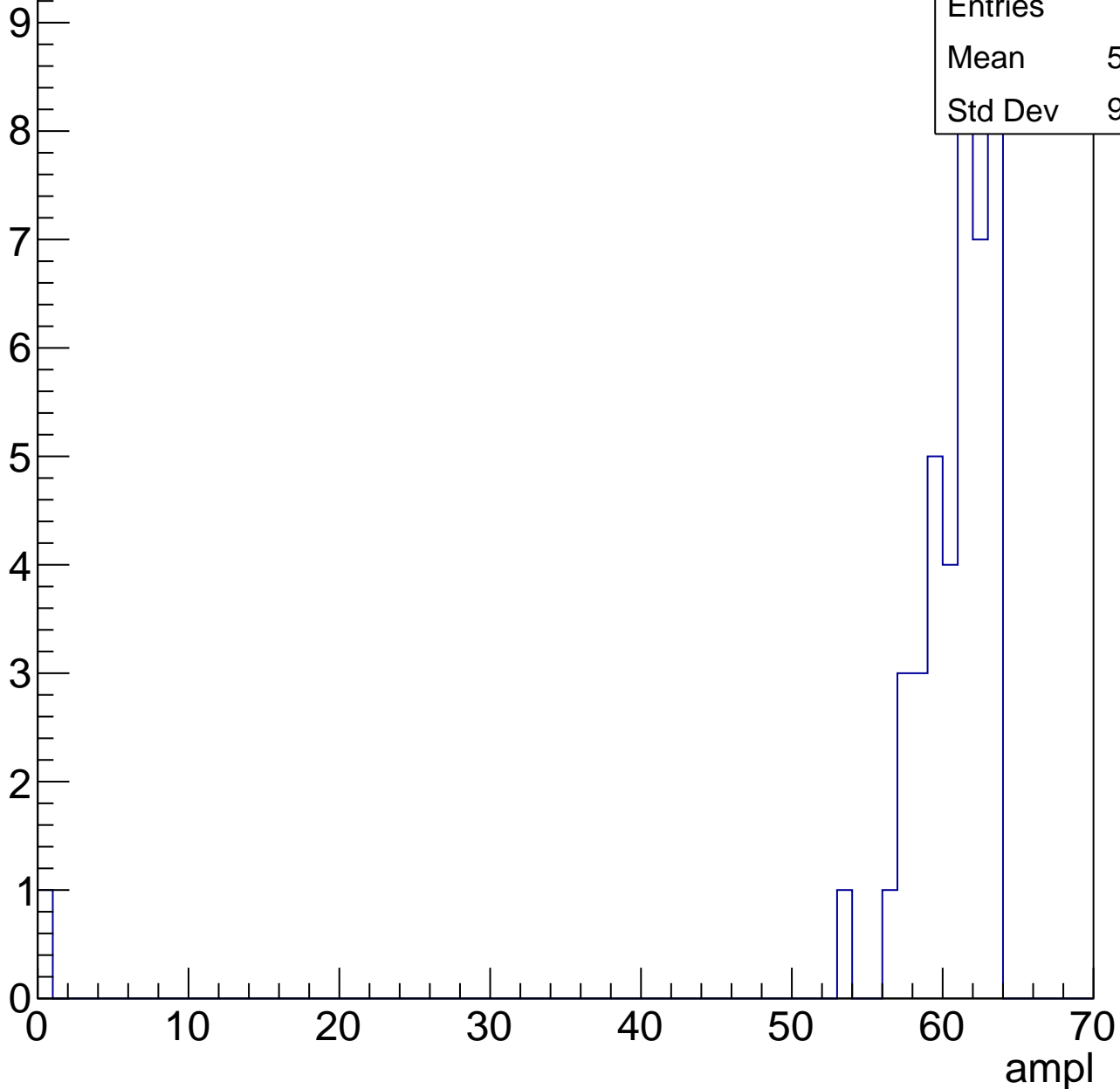


B1L103S, U17-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.95
Std Dev	9.477



B1L103S, U17-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U17-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch32, adc0

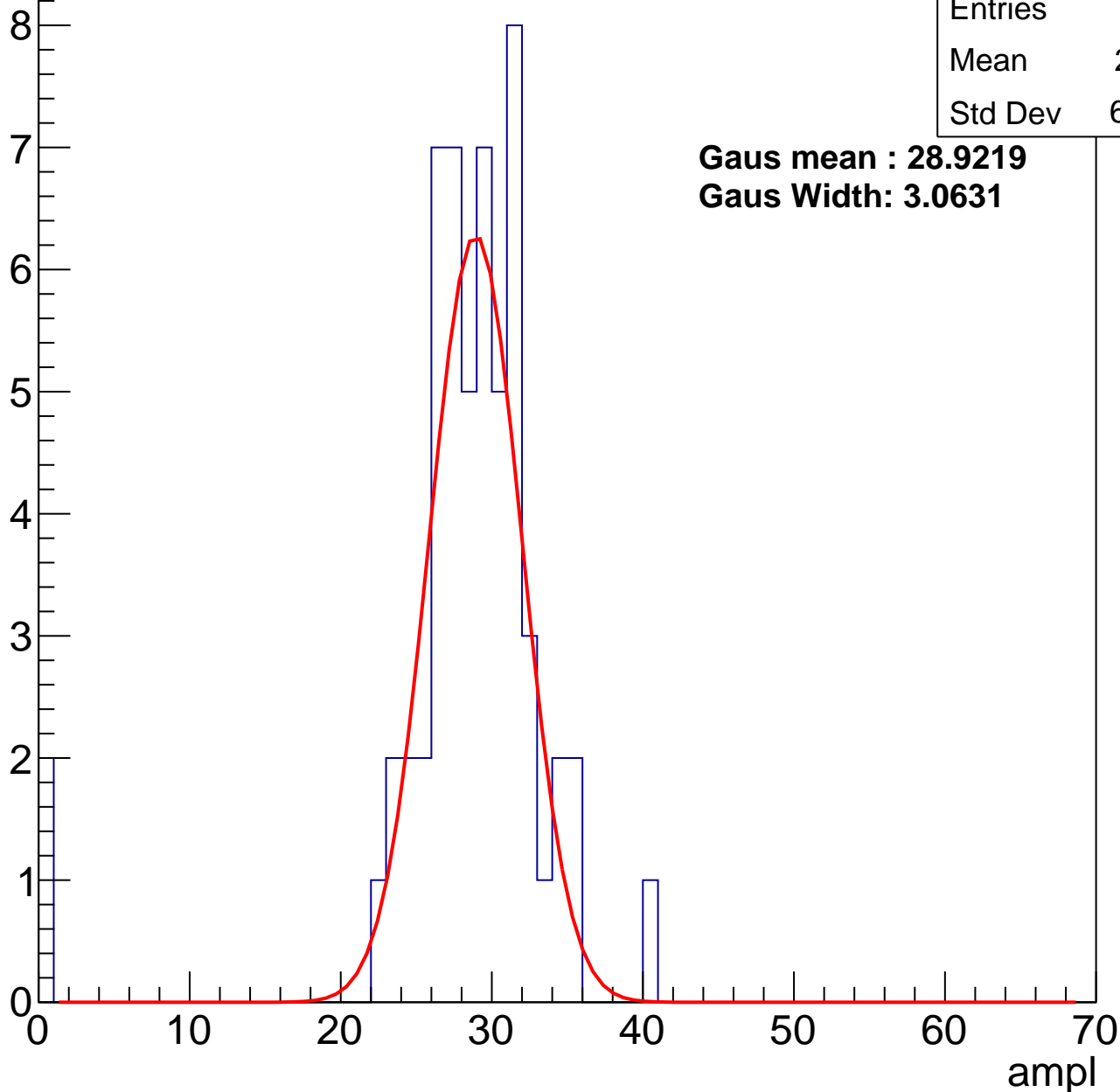
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	27.81
Std Dev	6.245

Gaus mean : 28.9219

Gaus Width: 3.0631



B1L103S, U17-ch32, adc1

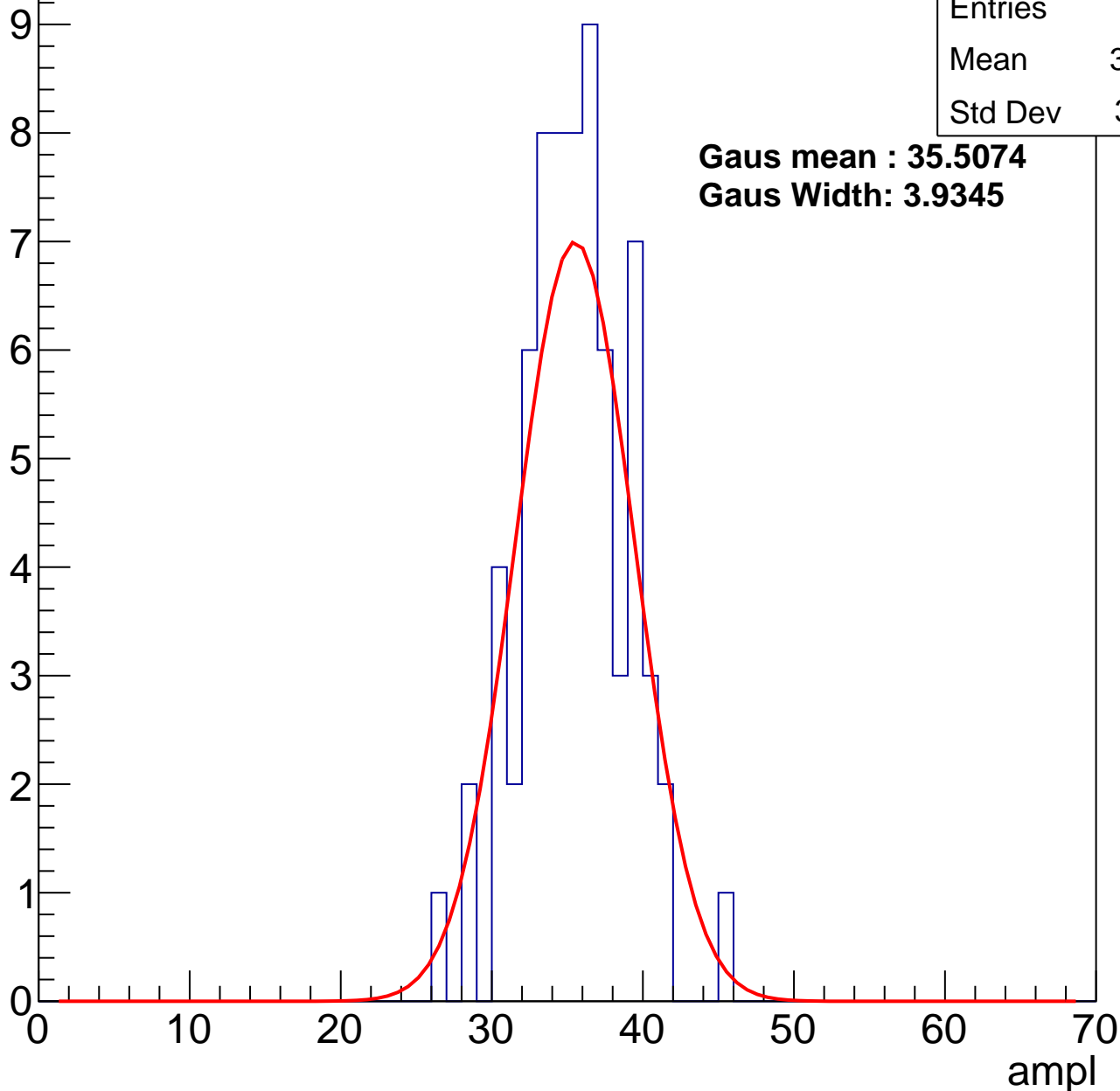
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.03
Std Dev	3.431

Gaus mean : 35.5074

Gaus Width: 3.9345



B1L103S, U17-ch32, adc2

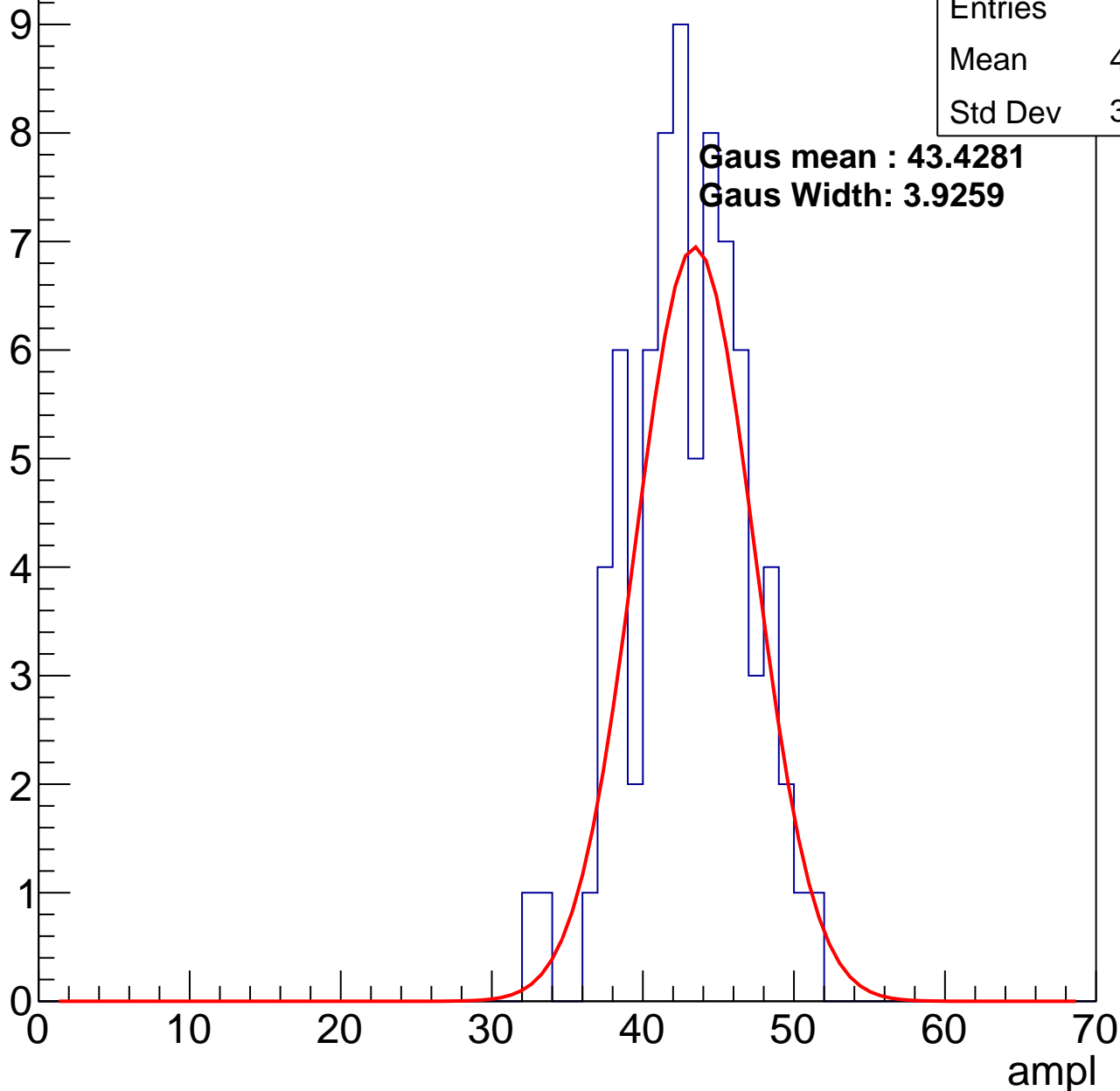
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.55
Std Dev	3.827

Gaus mean : 43.4281

Gaus Width: 3.9259

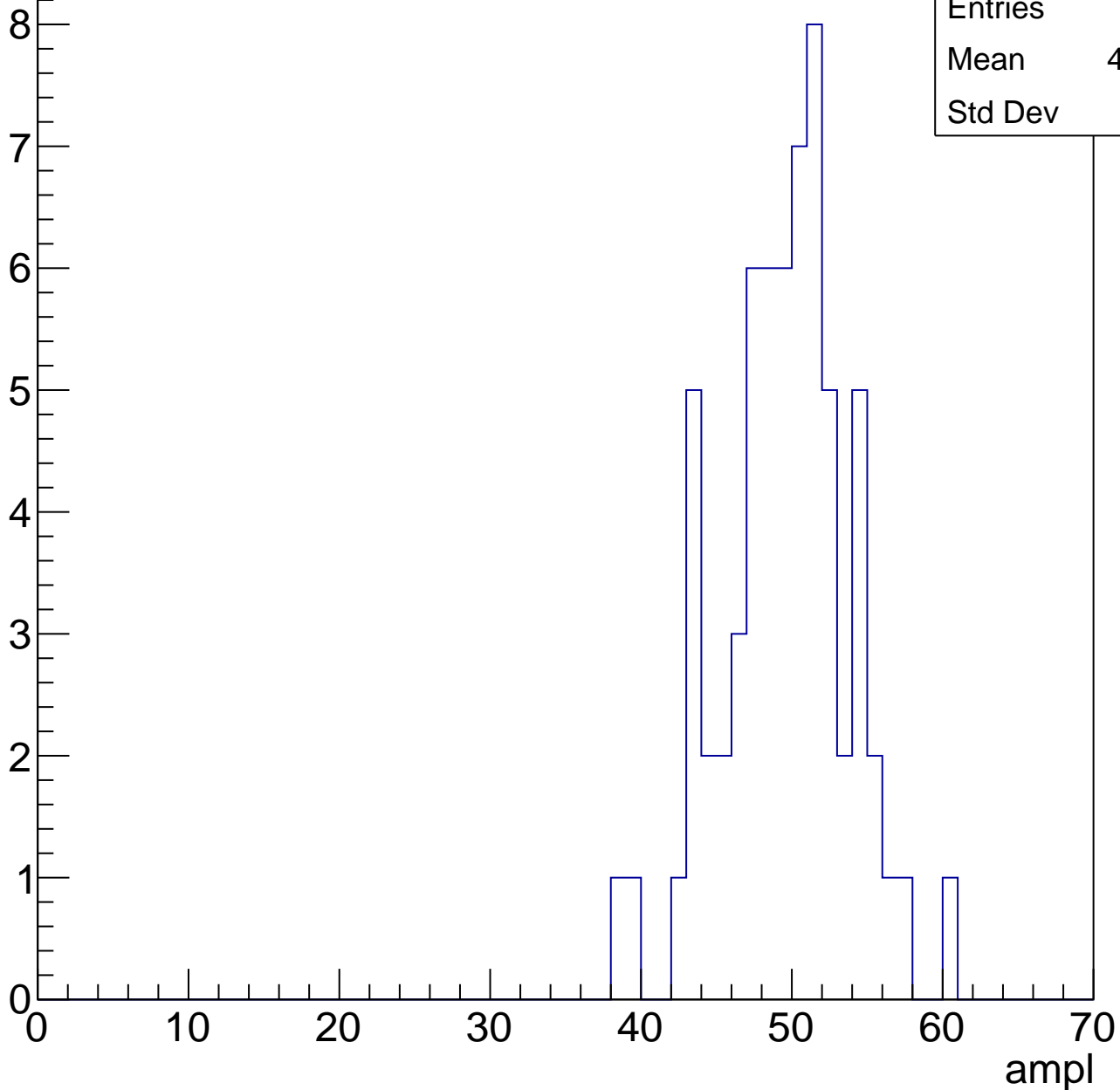


B1L103S, U17-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.09
Std Dev	4.18

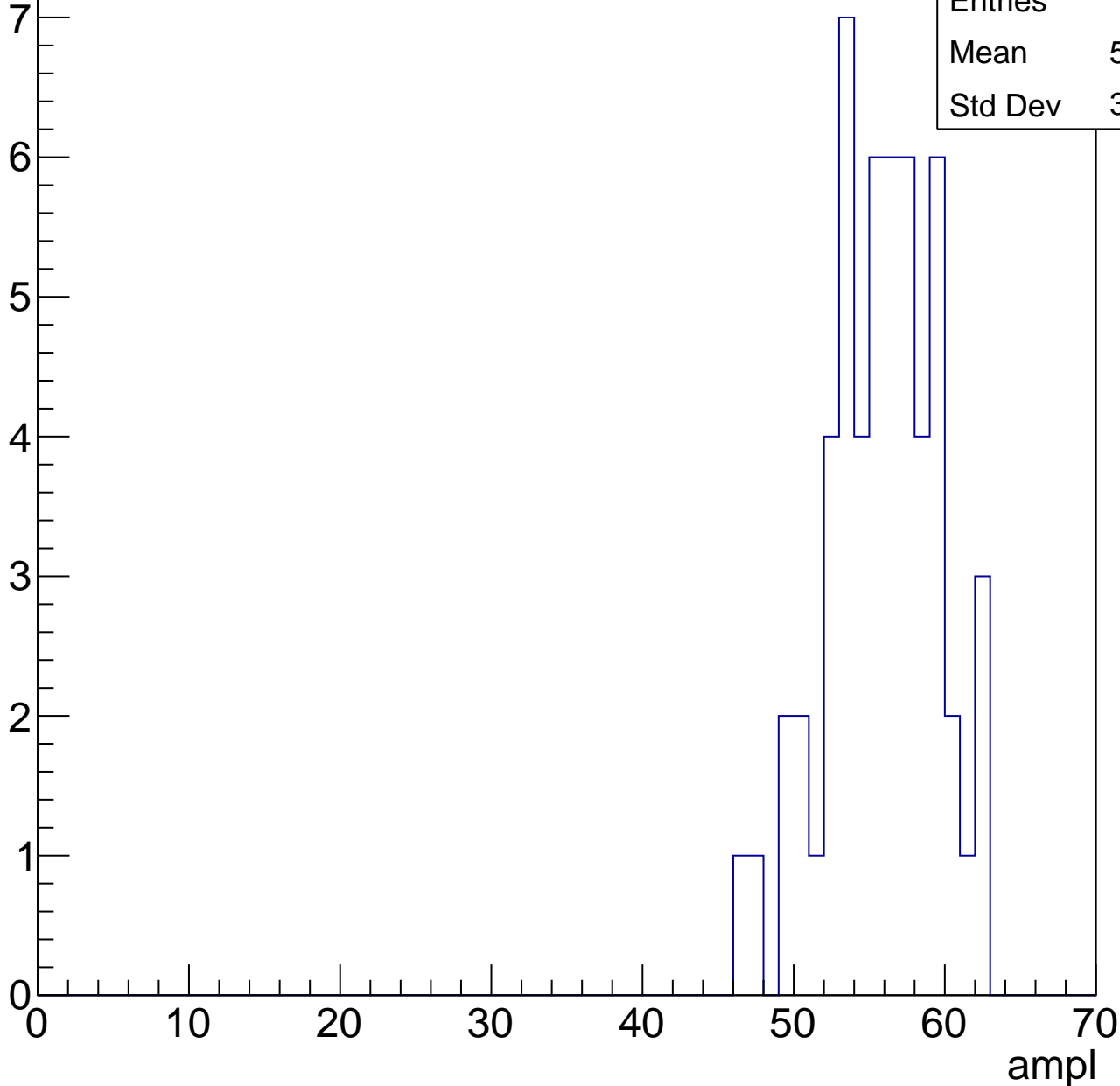


B1L103S, U17-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.32
Std Dev	3.636



B1L103S, U17-ch32, adc5

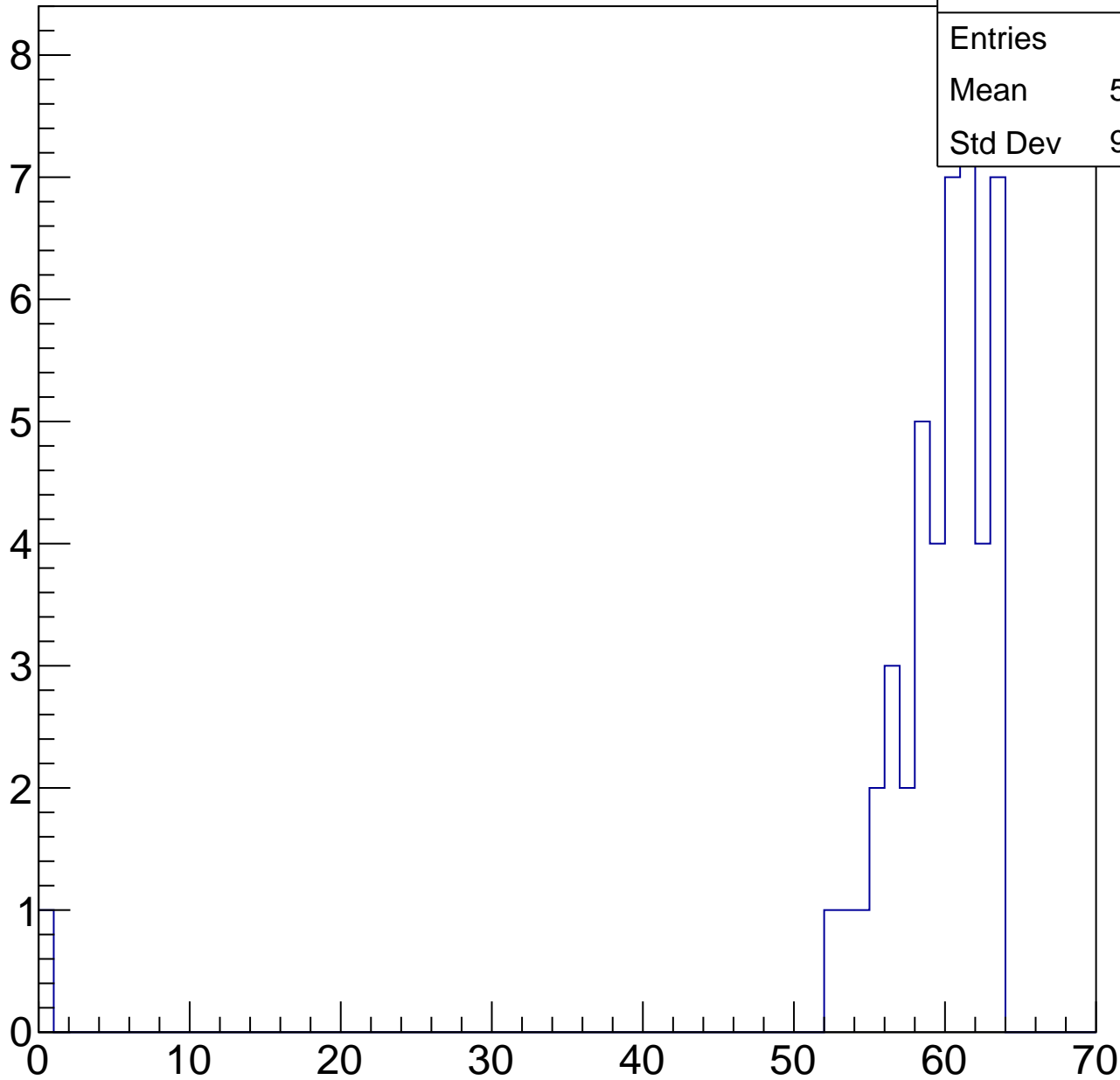
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.13
Std Dev	9.107

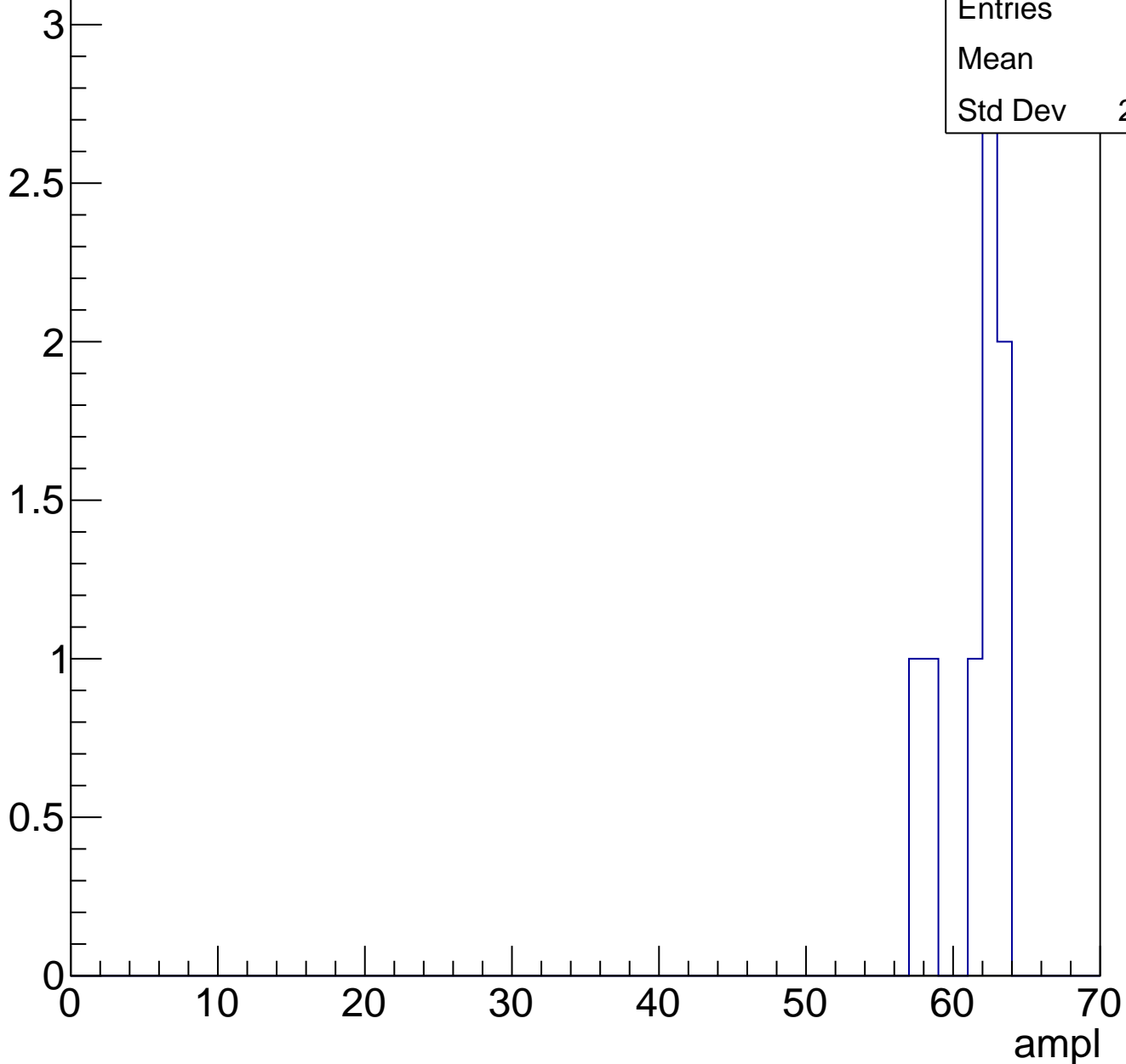
ampl



B1L103S, U17-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch33, adc0

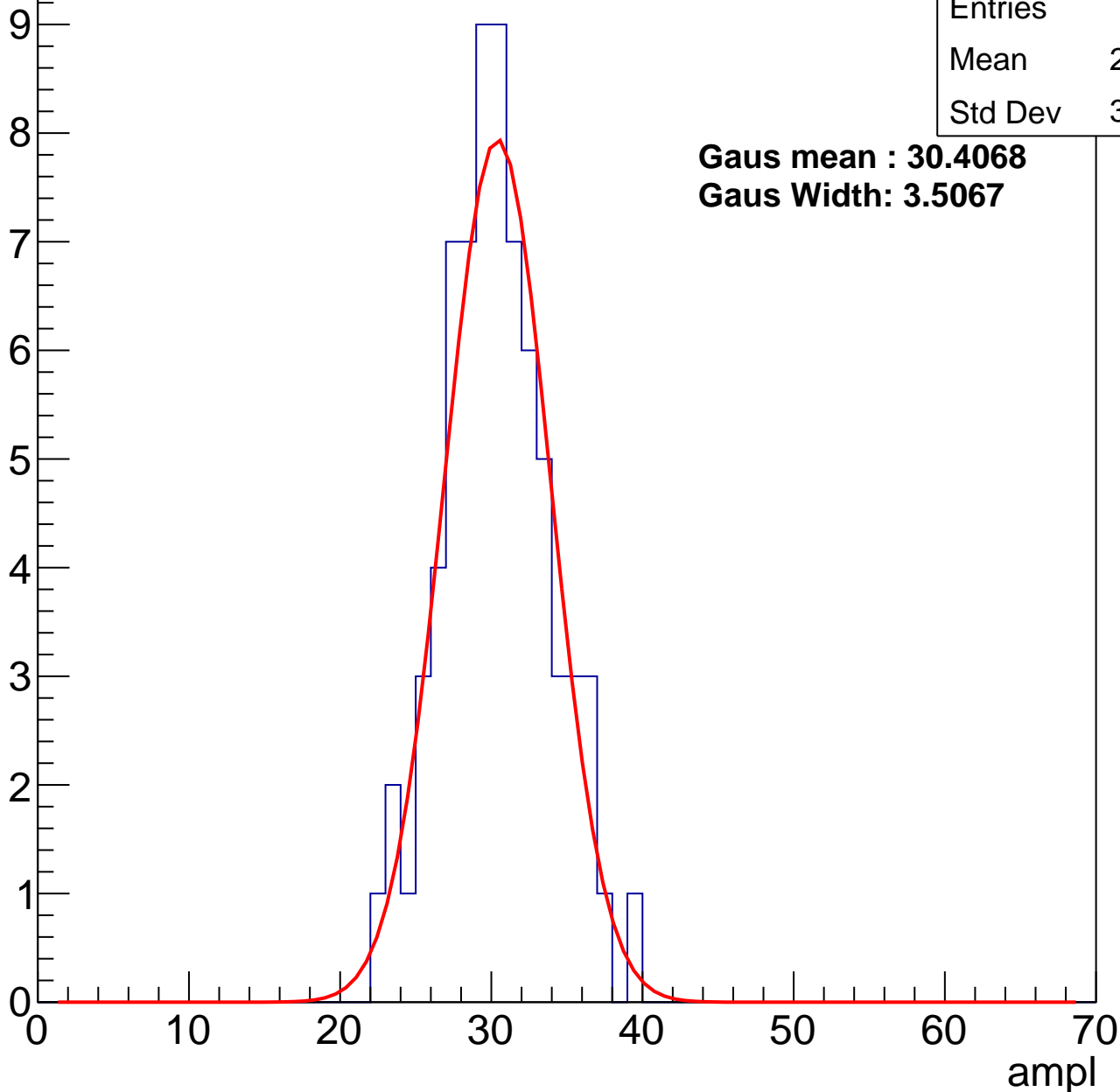
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.89
Std Dev	3.482

Gaus mean : 30.4068

Gaus Width: 3.5067



B1L103S, U17-ch33, adc1

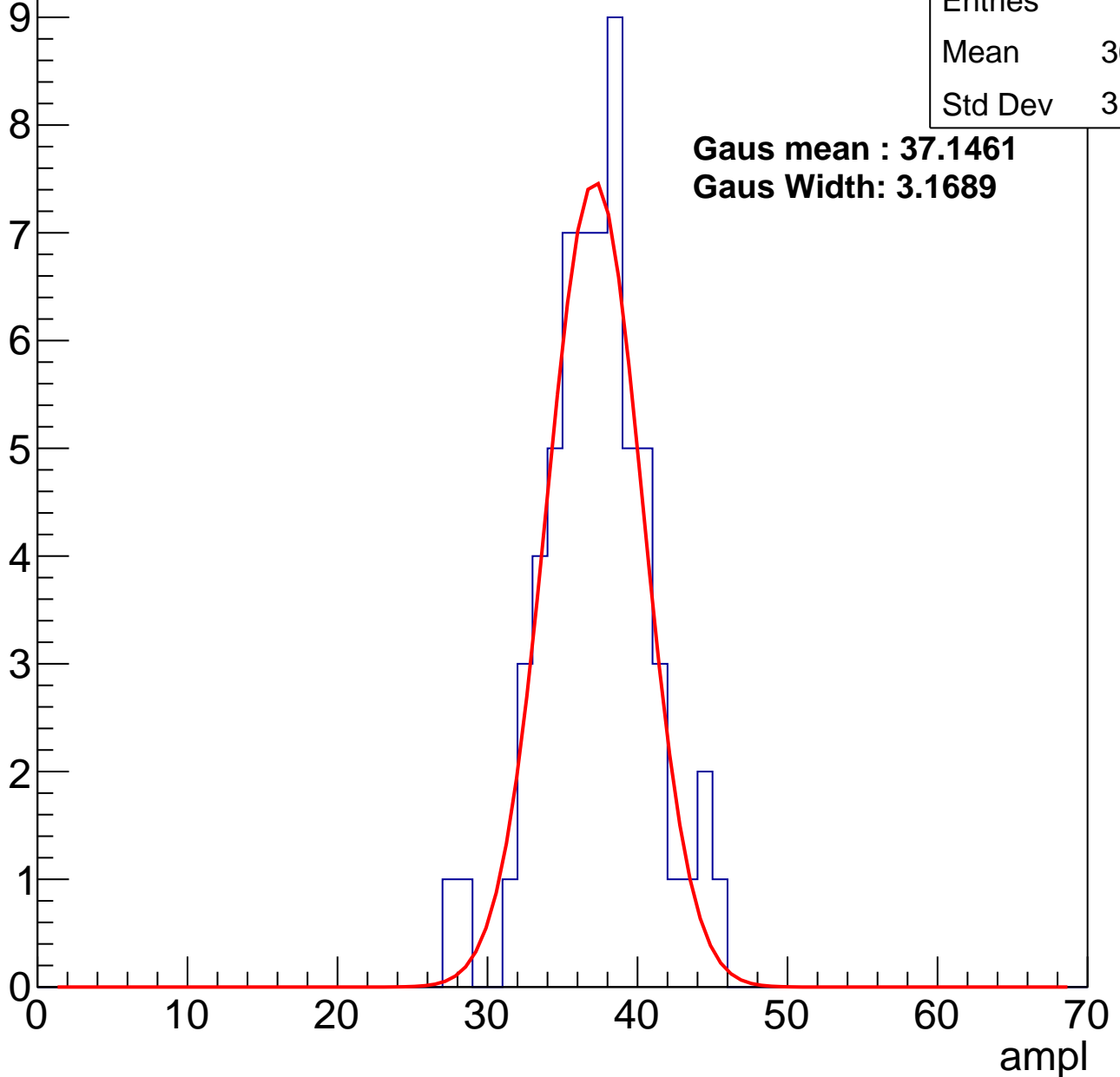
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	36.79
Std Dev	3.515

Gaus mean : 37.1461

Gaus Width: 3.1689



B1L103S, U17-ch33, adc2

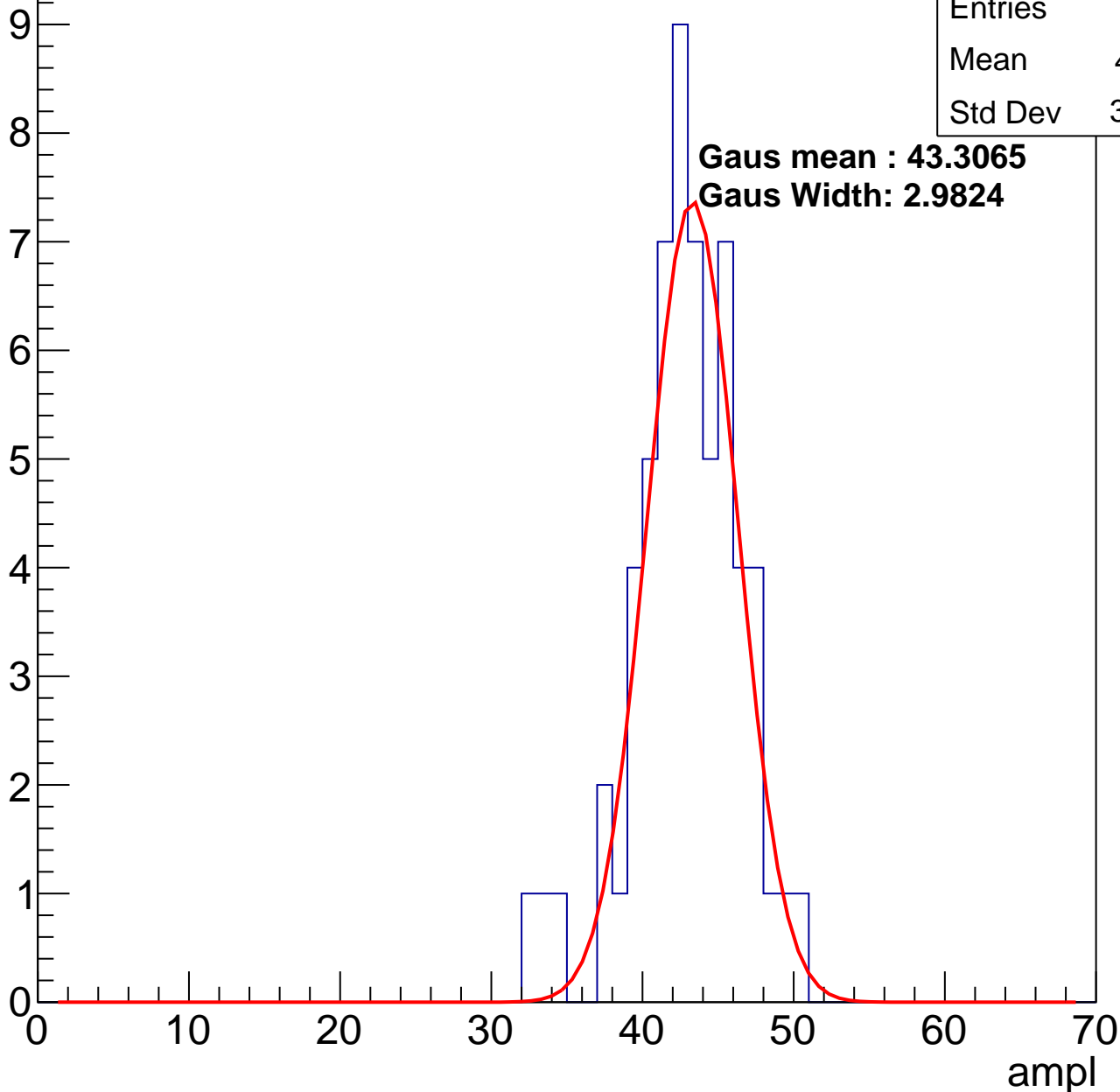
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.41
Std Dev	3.545

Gaus mean : 43.3065

Gaus Width: 2.9824

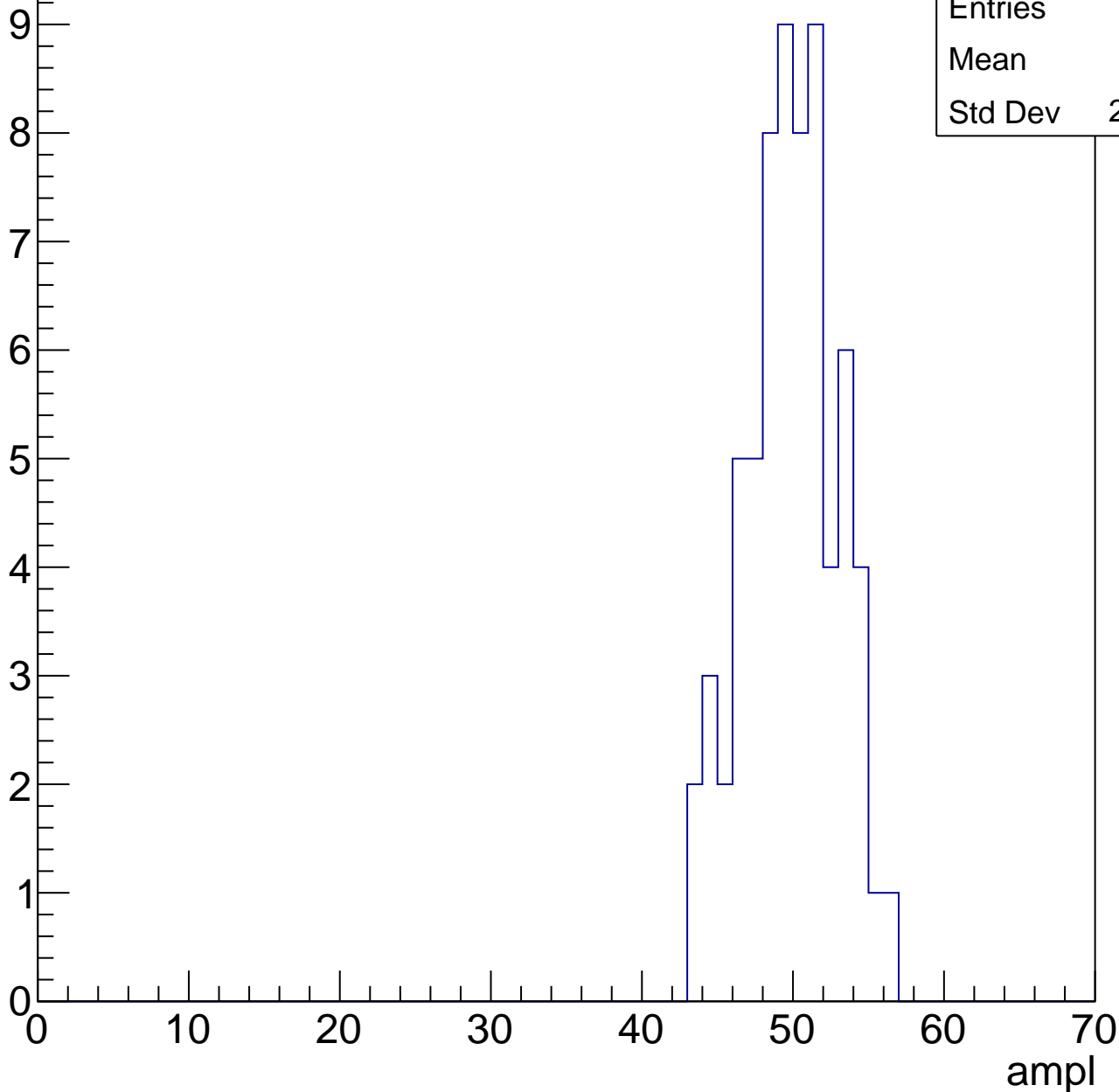


B1L103S, U17-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.4
Std Dev	2.998

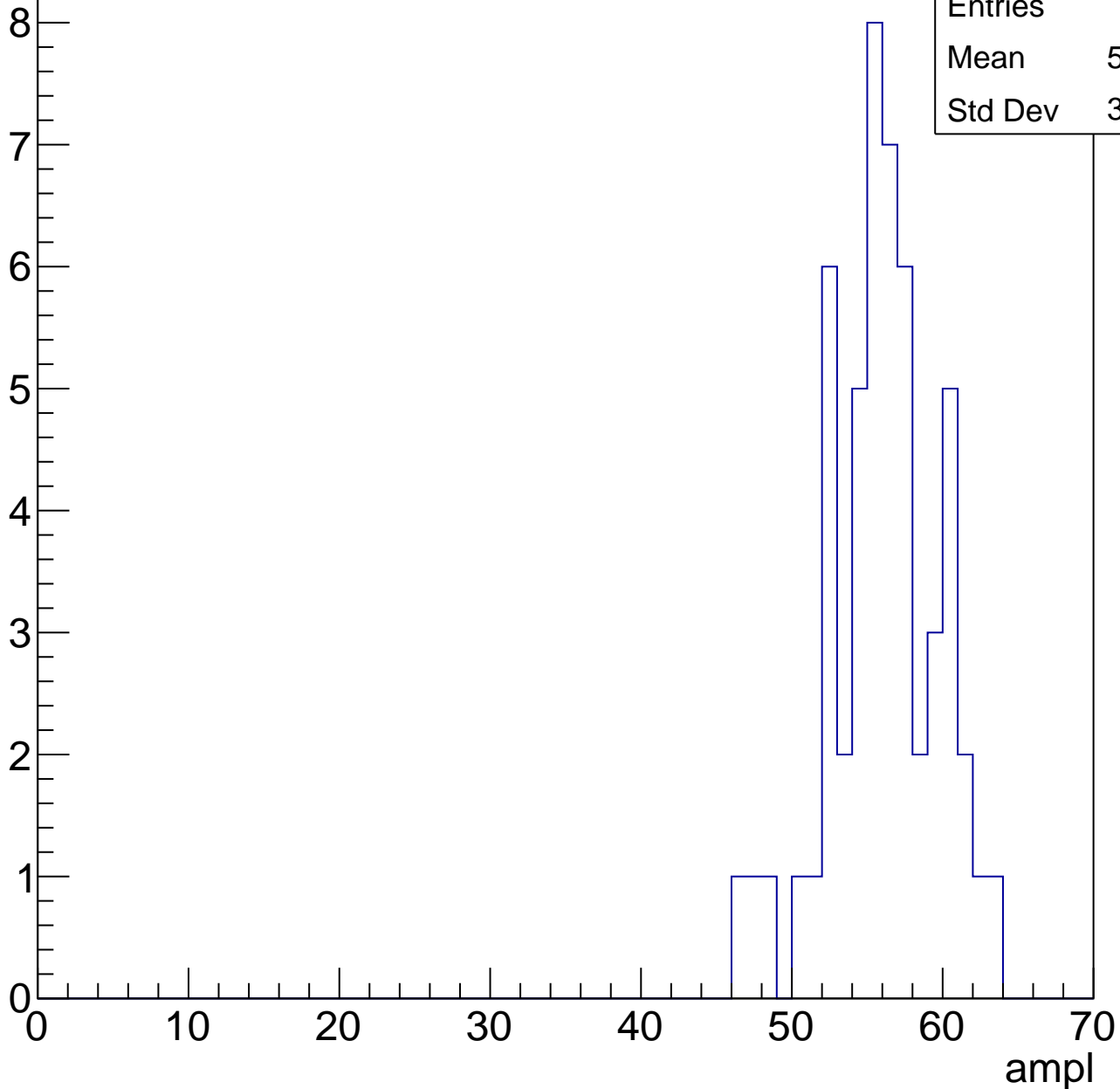


B1L103S, U17-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

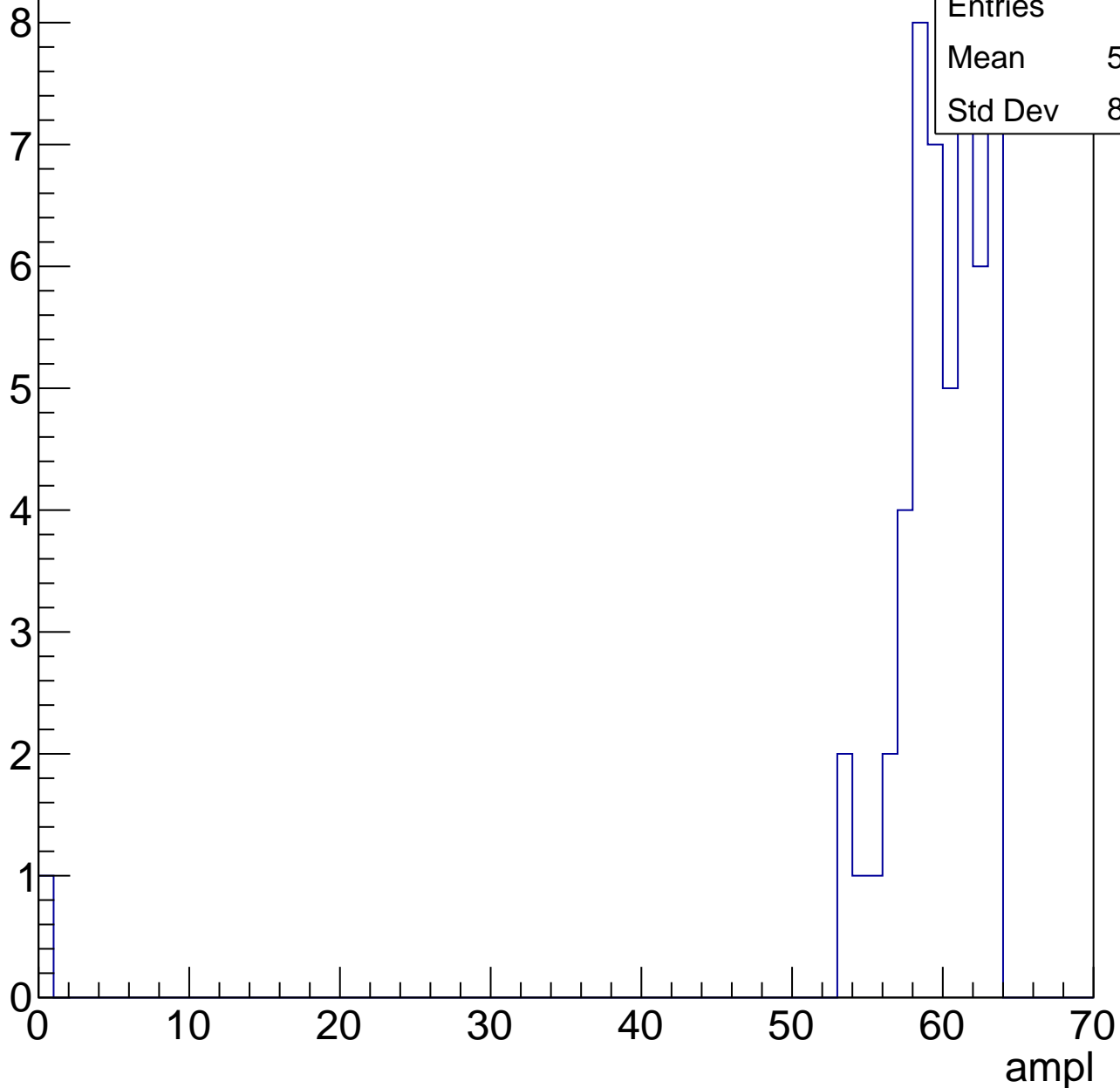
Entries	53
Mean	55.55
Std Dev	3.616



B1L103S, U17-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch34, adc0

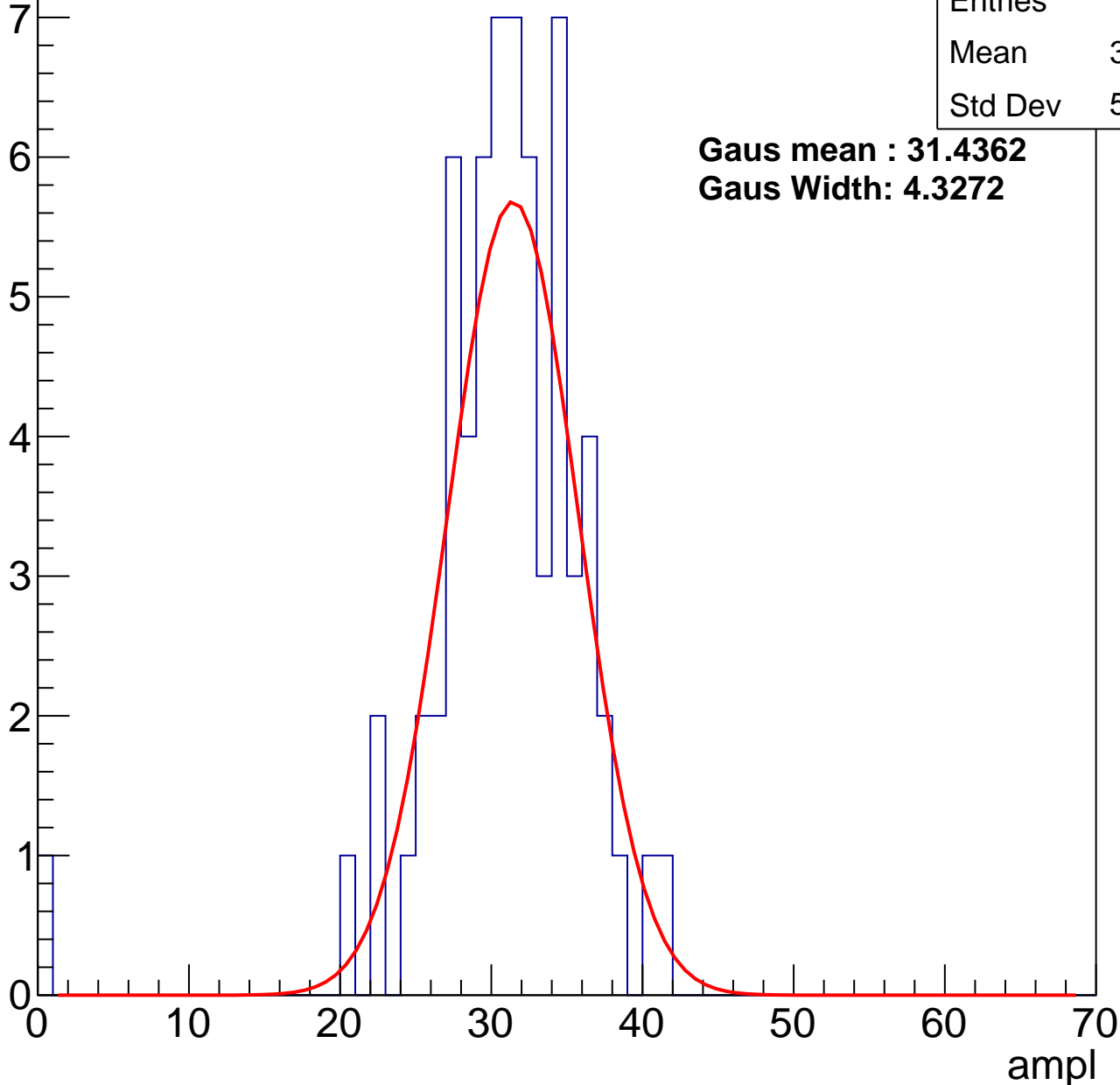
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.39
Std Dev	5.577

Gaus mean : 31.4362

Gaus Width: 4.3272



B1L103S, U17-ch34, adc1

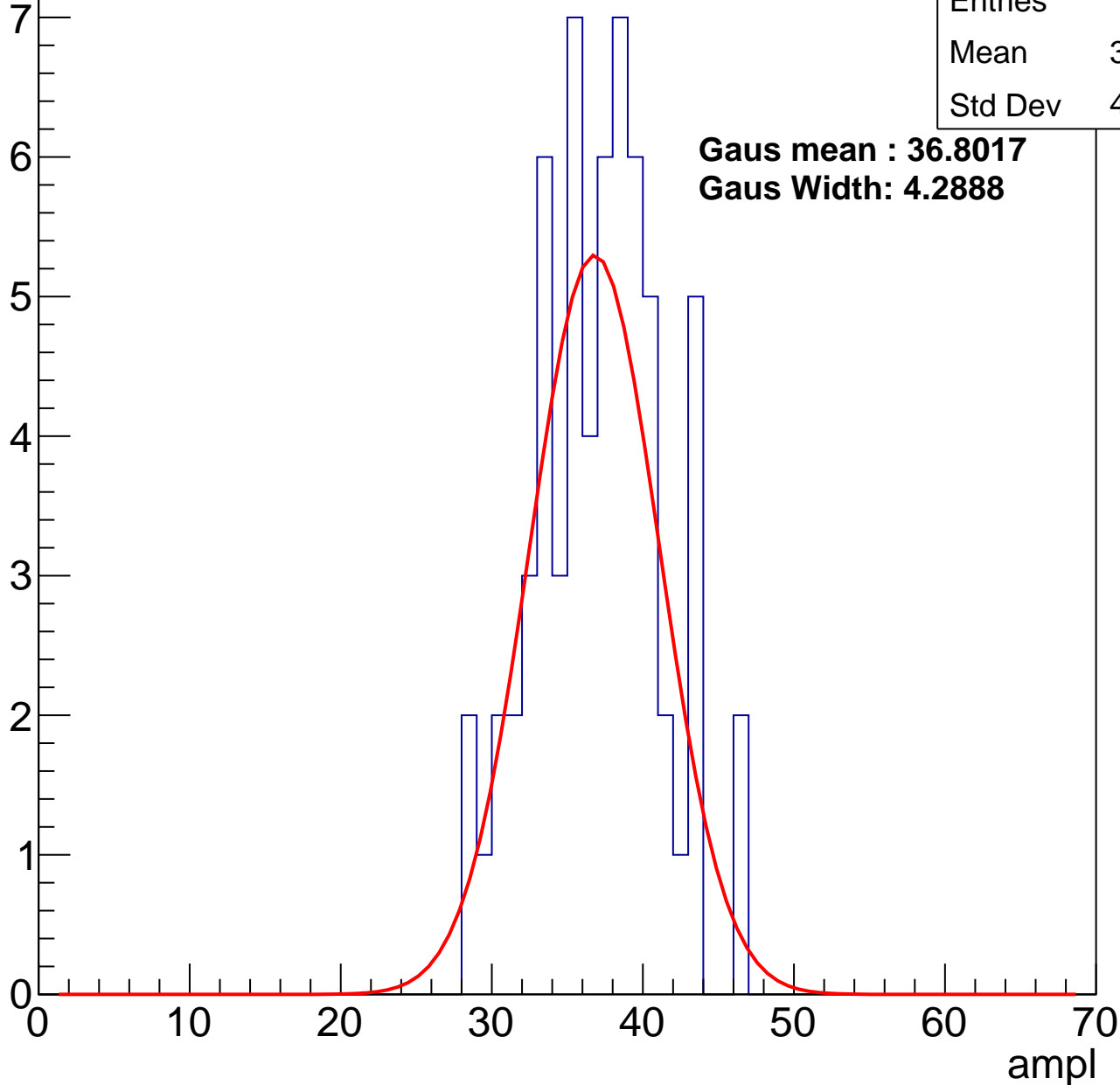
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.64
Std Dev	4.132

Gaus mean : 36.8017

Gaus Width: 4.2888



B1L103S, U17-ch34, adc2

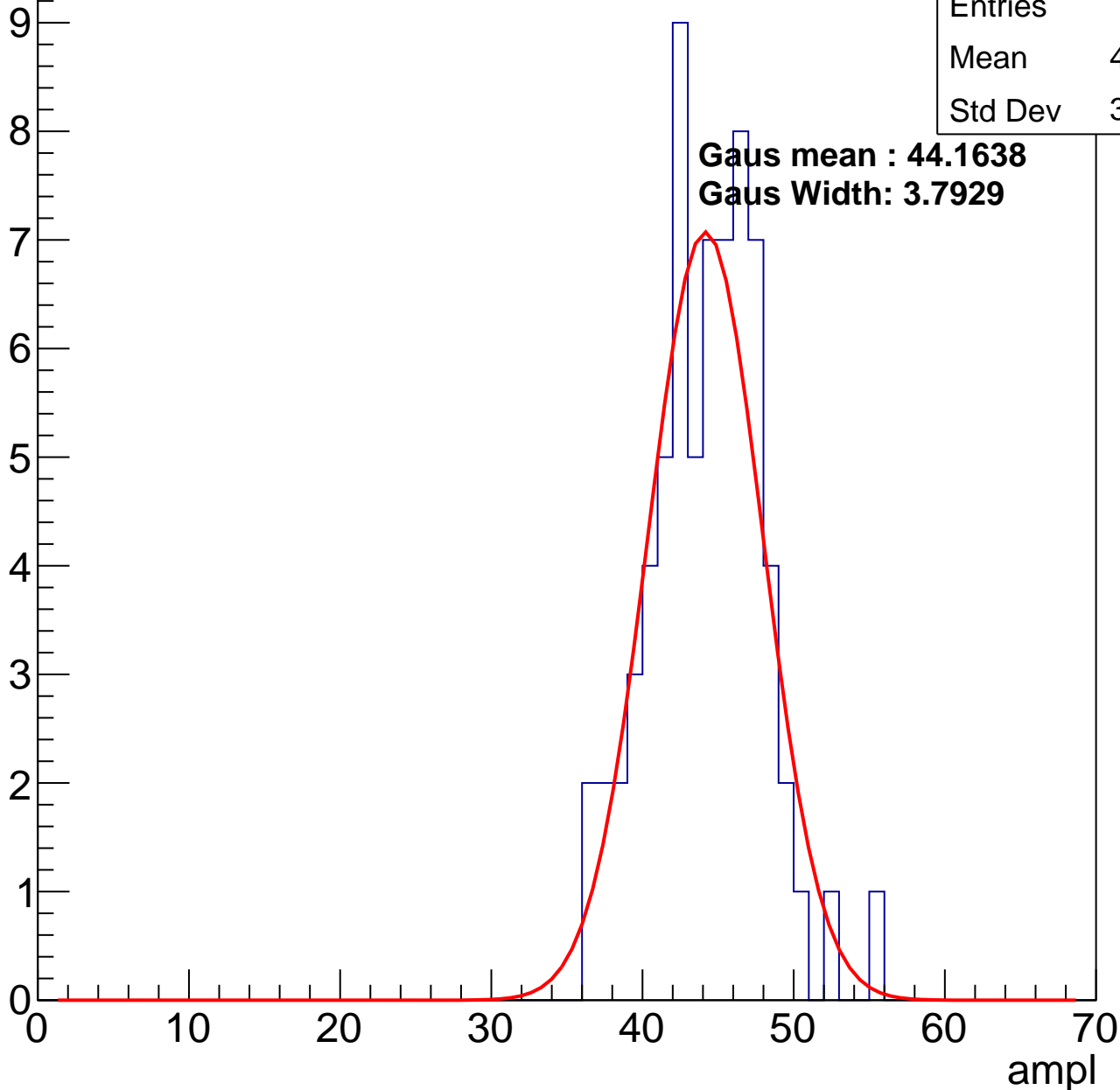
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.77
Std Dev	3.696

Gaus mean : 44.1638

Gaus Width: 3.7929

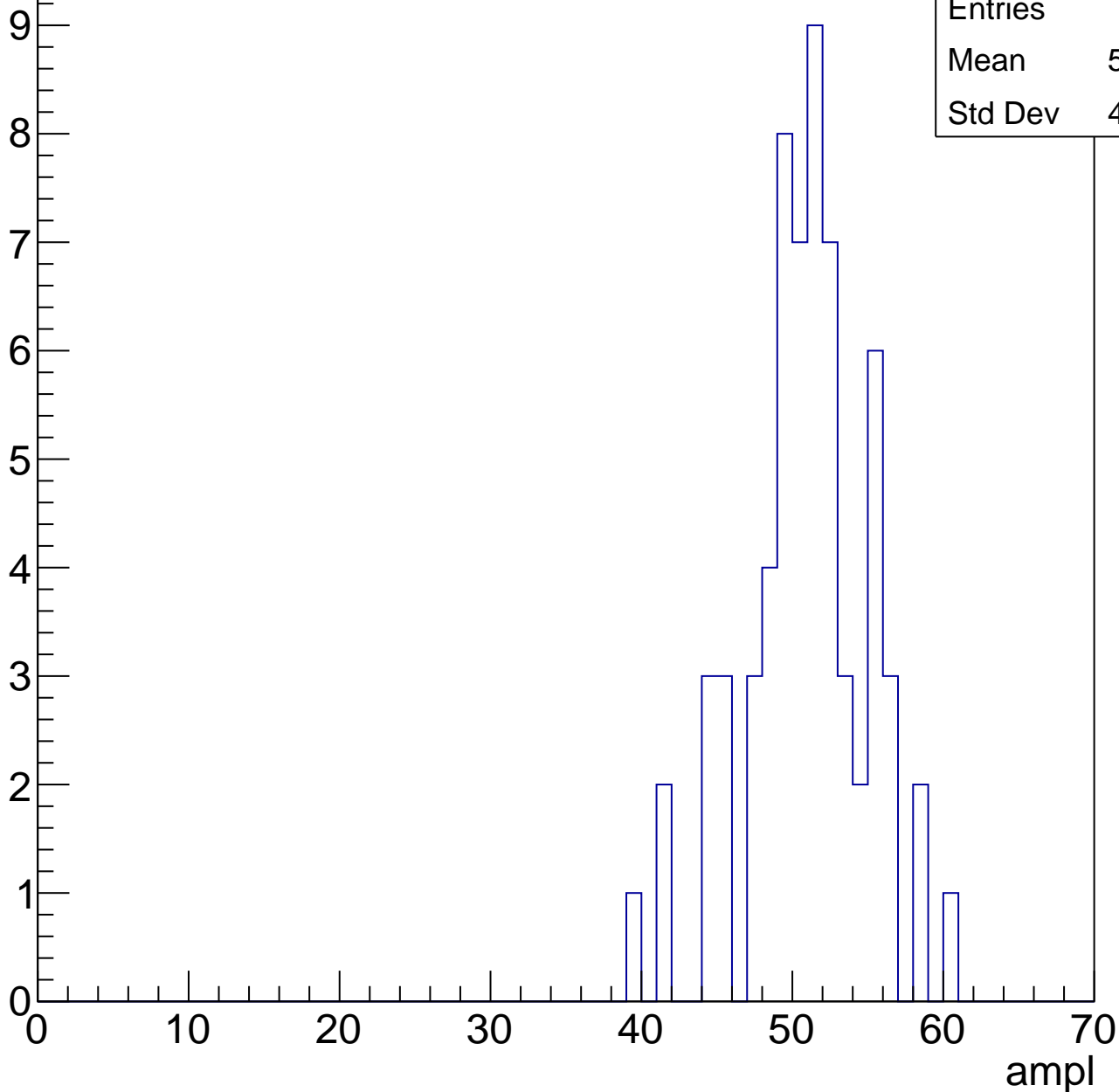


B1L103S, U17-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.42
Std Dev	4.122

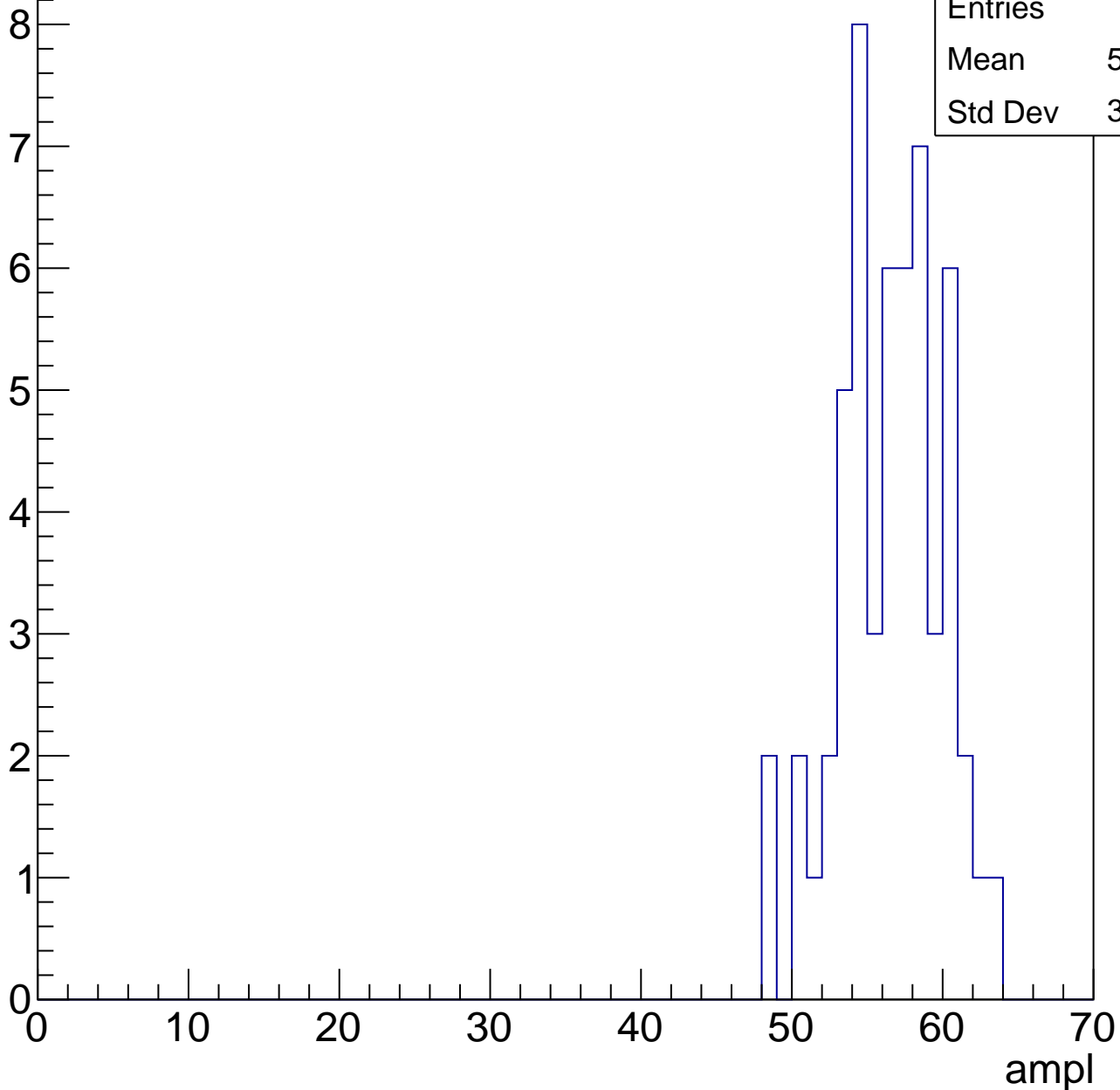


B1L103S, U17-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.02
Std Dev	3.382

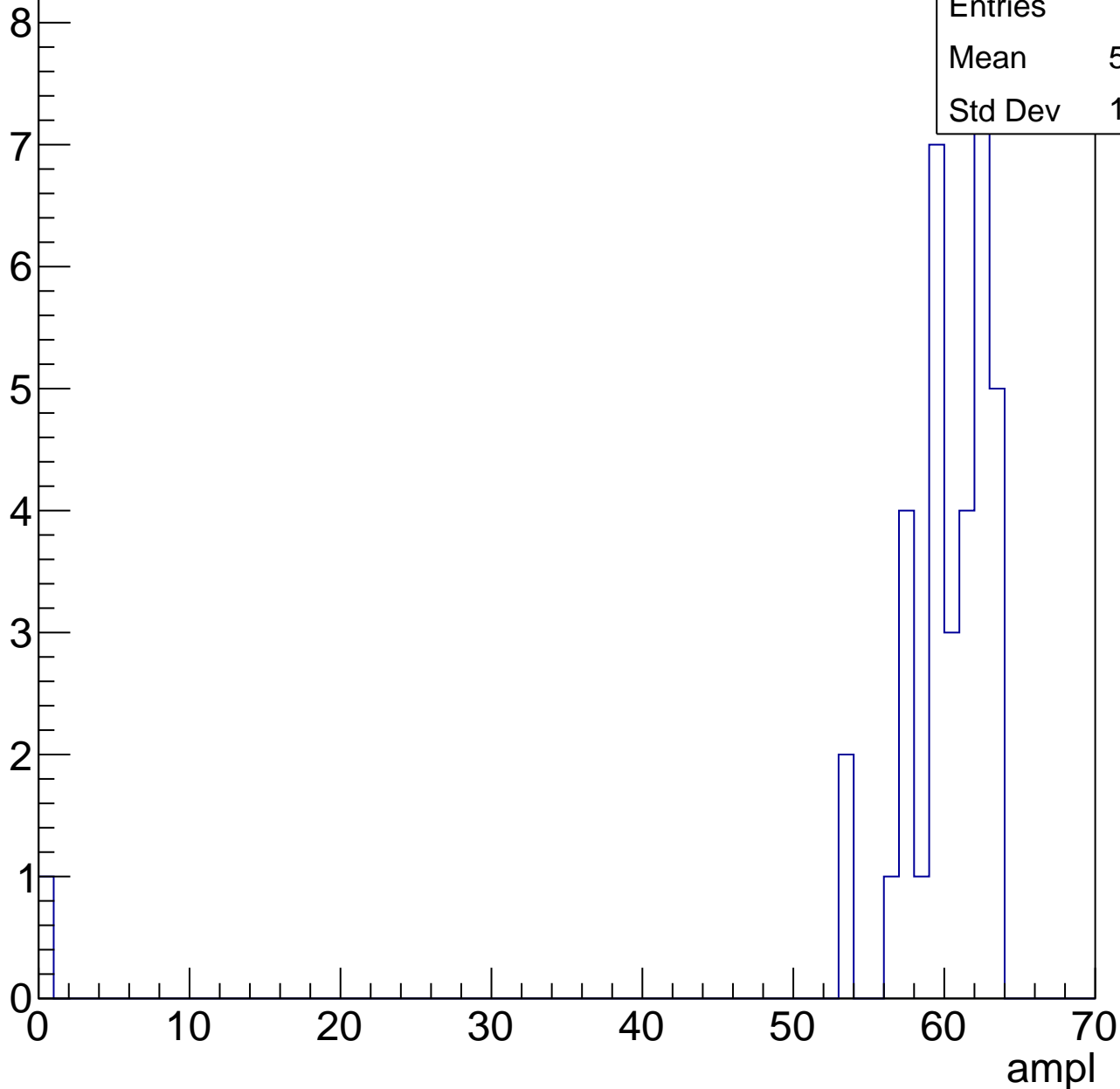


B1L103S, U17-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

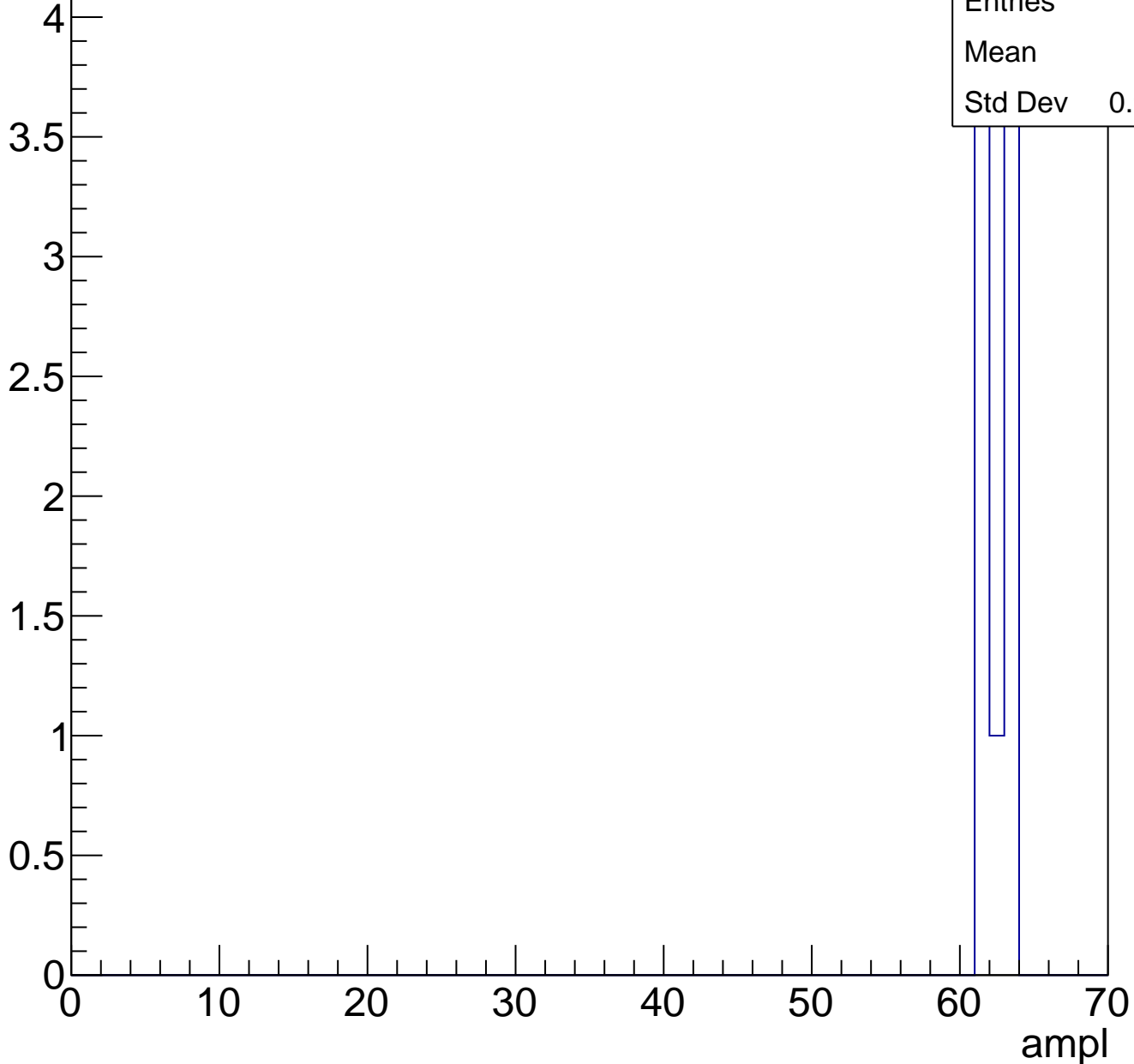
Entries	36
Mean	58.22
Std Dev	10.18



B1L103S, U17-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch35, adc0

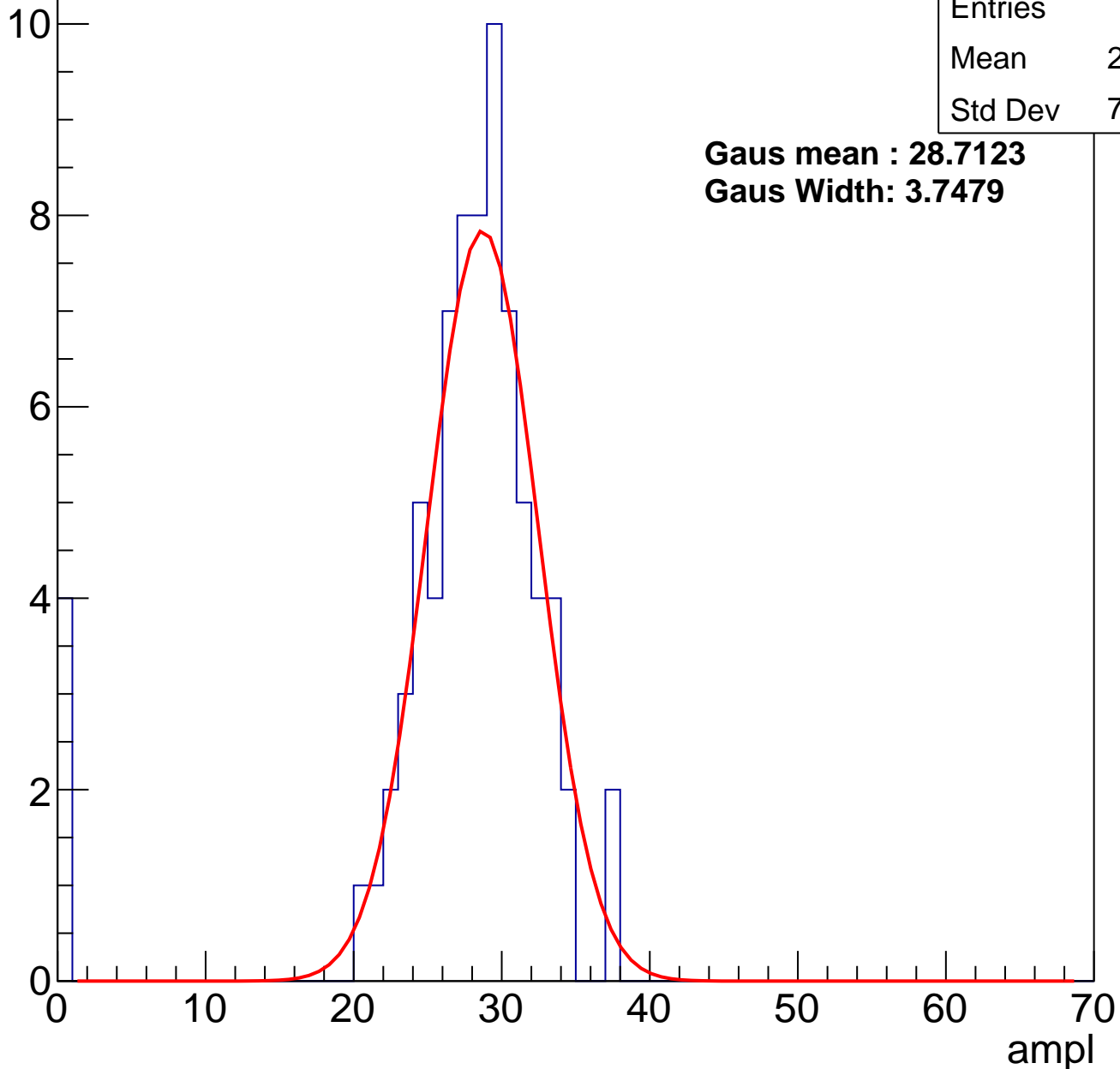
calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	26.66
Std Dev	7.109

Gaus mean : 28.7123

Gaus Width: 3.7479

Entry

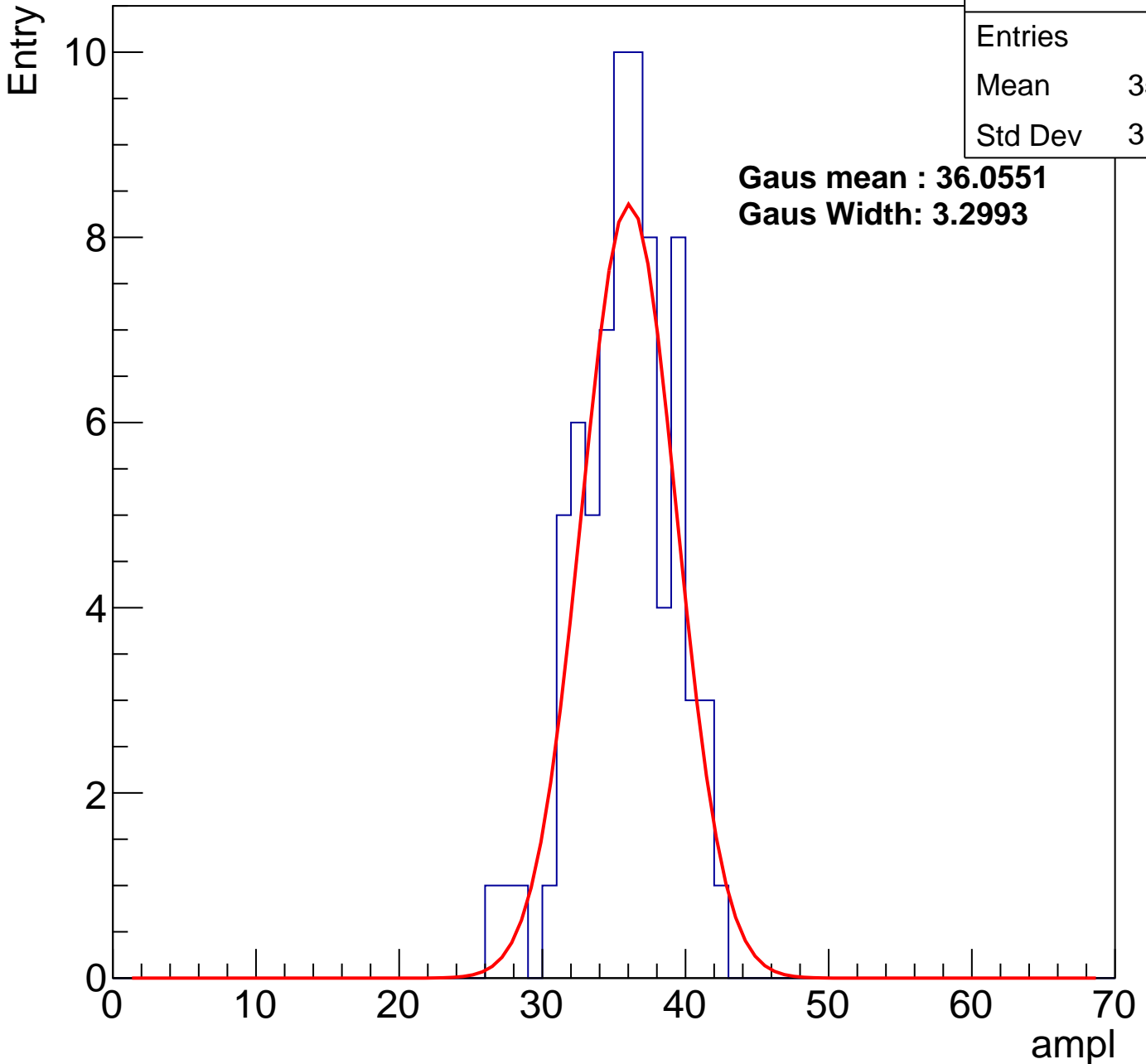


B1L103S, U17-ch35, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	35.35
Std Dev	3.306

Gaus mean : 36.0551
Gaus Width: 3.2993



B1L103S, U17-ch35, adc2

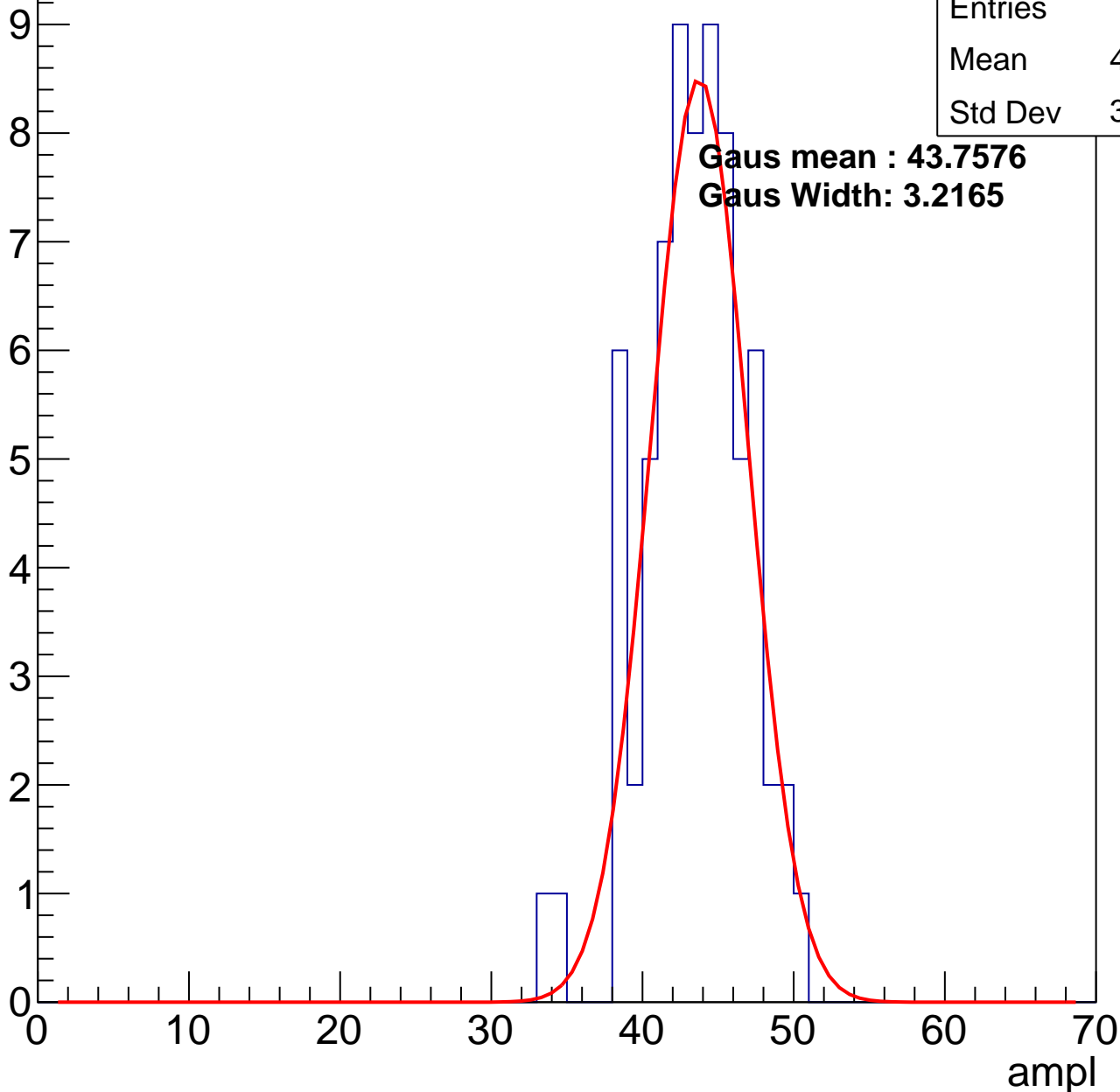
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.97
Std Dev	3.329

Gaus mean : 43.7576

Gaus Width: 3.2165

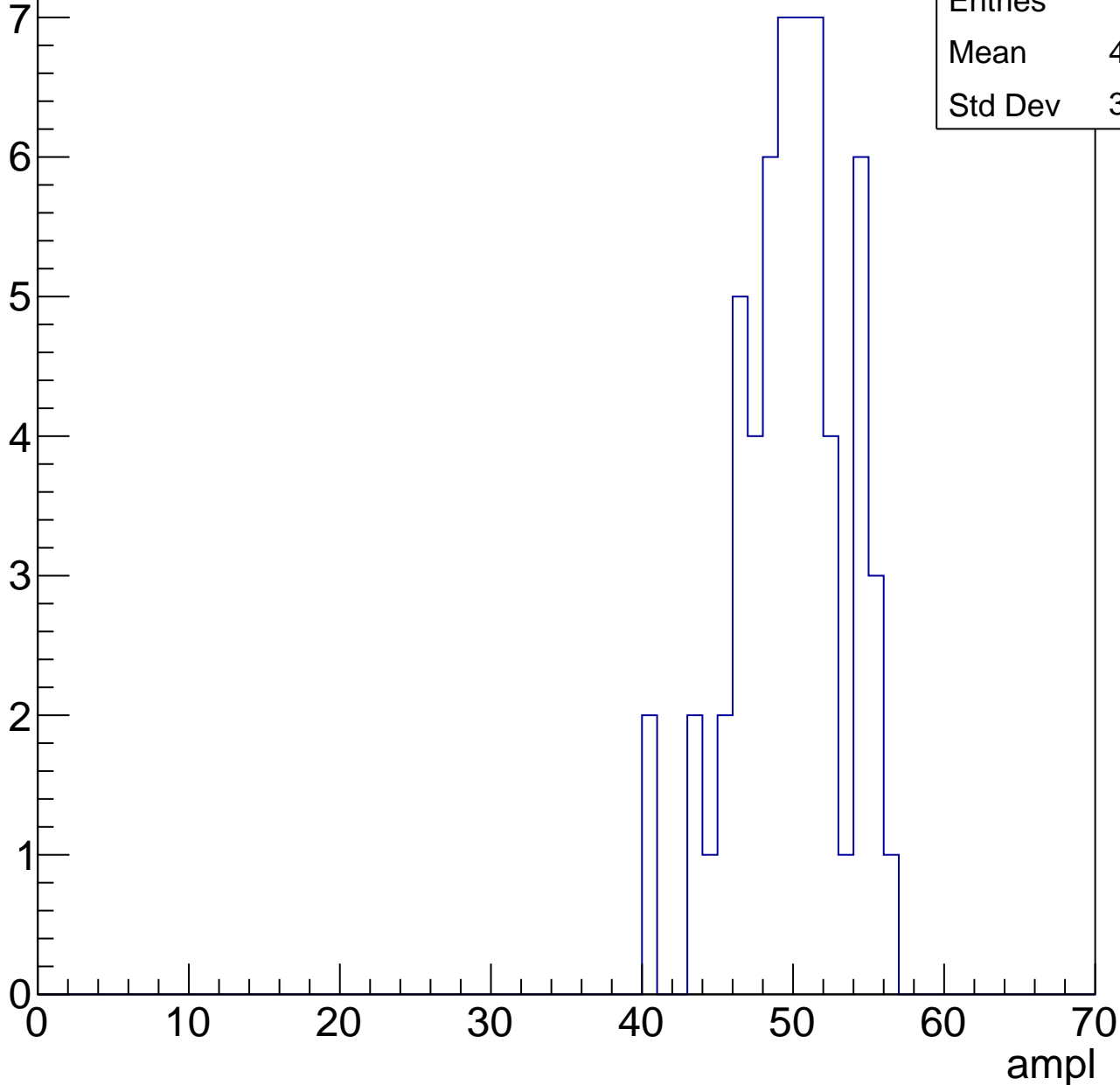


B1L103S, U17-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.34
Std Dev	3.584

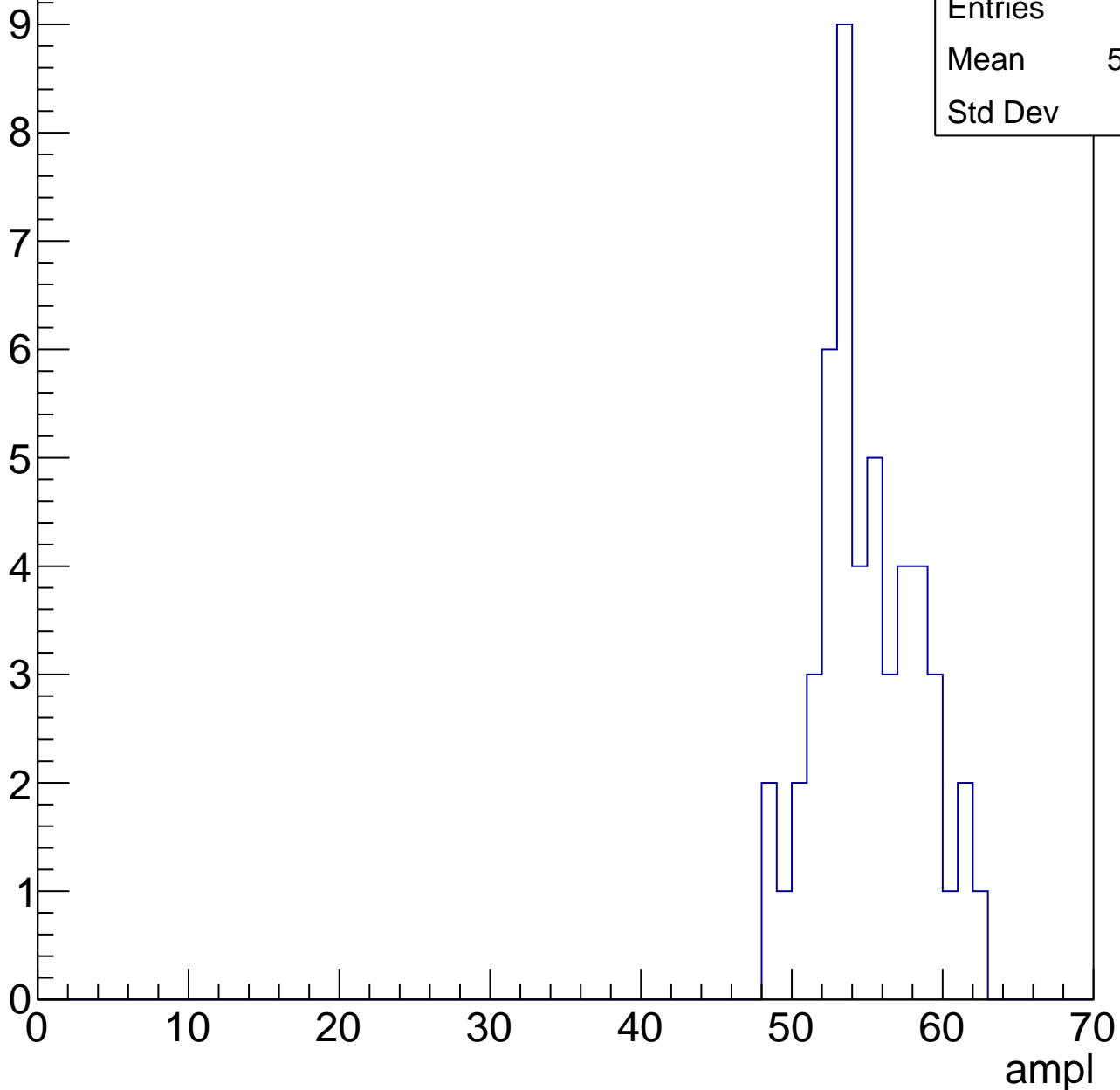


B1L103S, U17-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.54
Std Dev	3.36

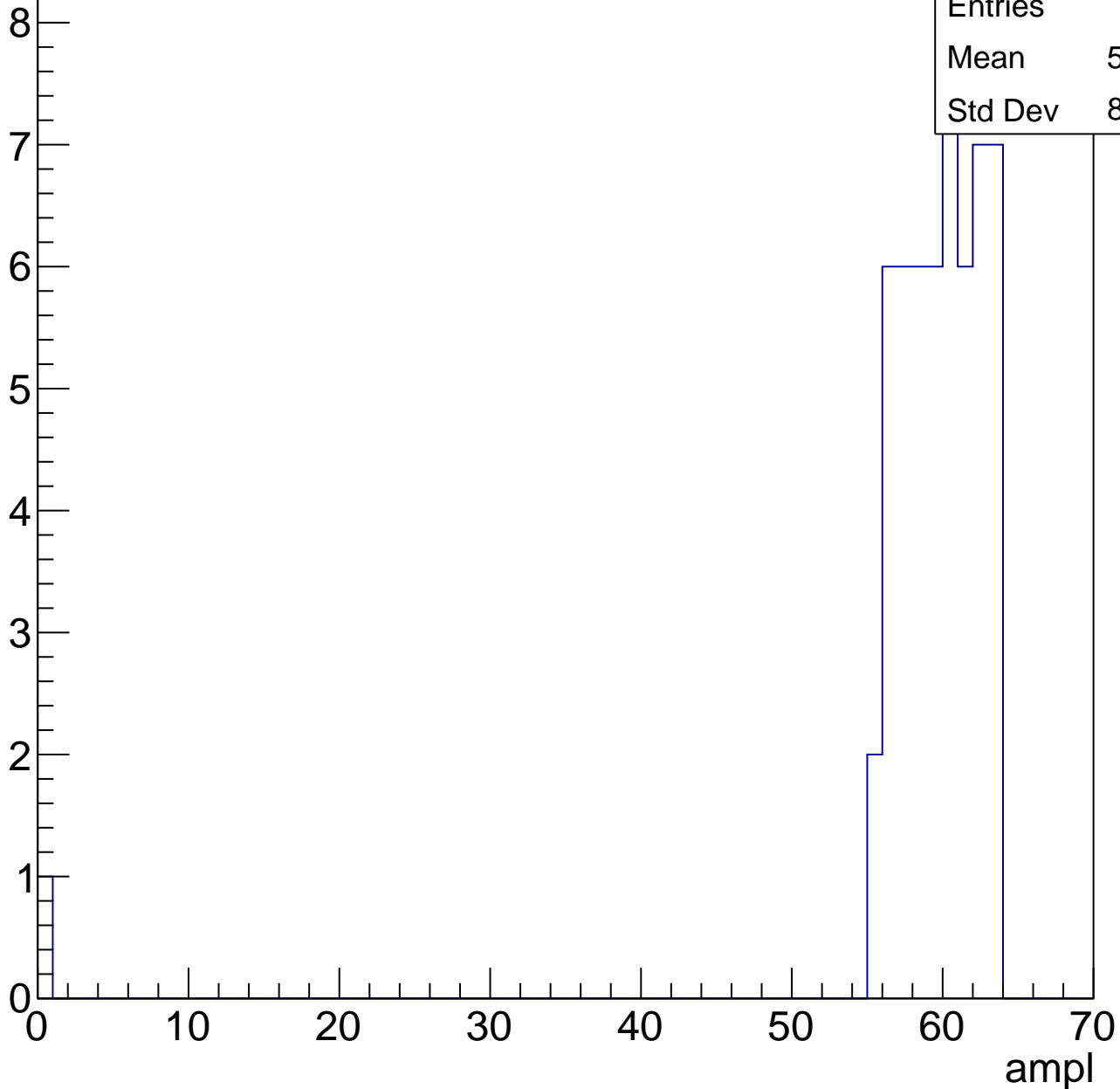


B1L103S, U17-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

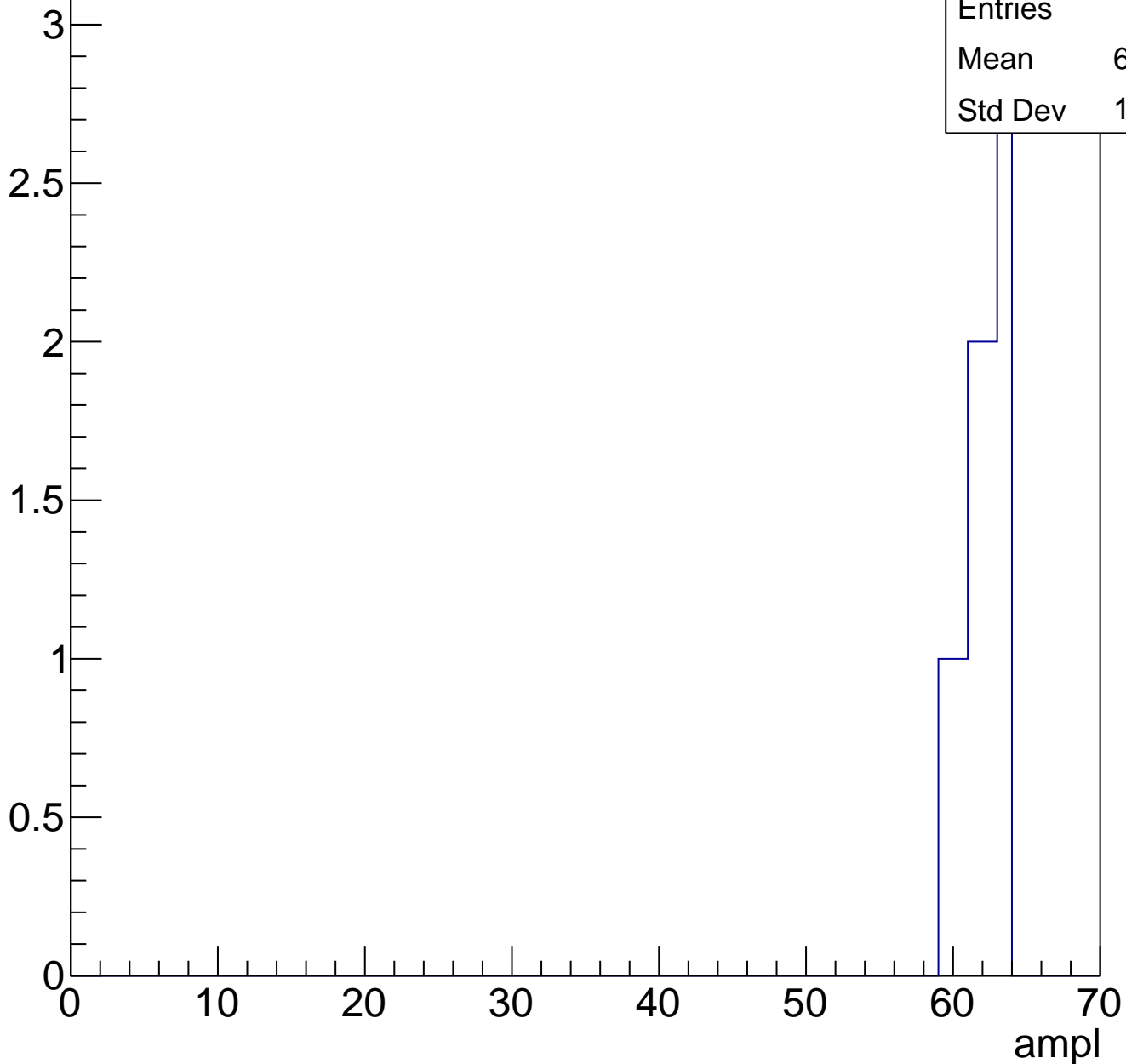
Entries	55
Mean	58.38
Std Dev	8.293



B1L103S, U17-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch36, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	30.57
Std Dev	3.454

Gaus mean : 31.5347

Gaus Width: 3.7655

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U17-ch36, adc1

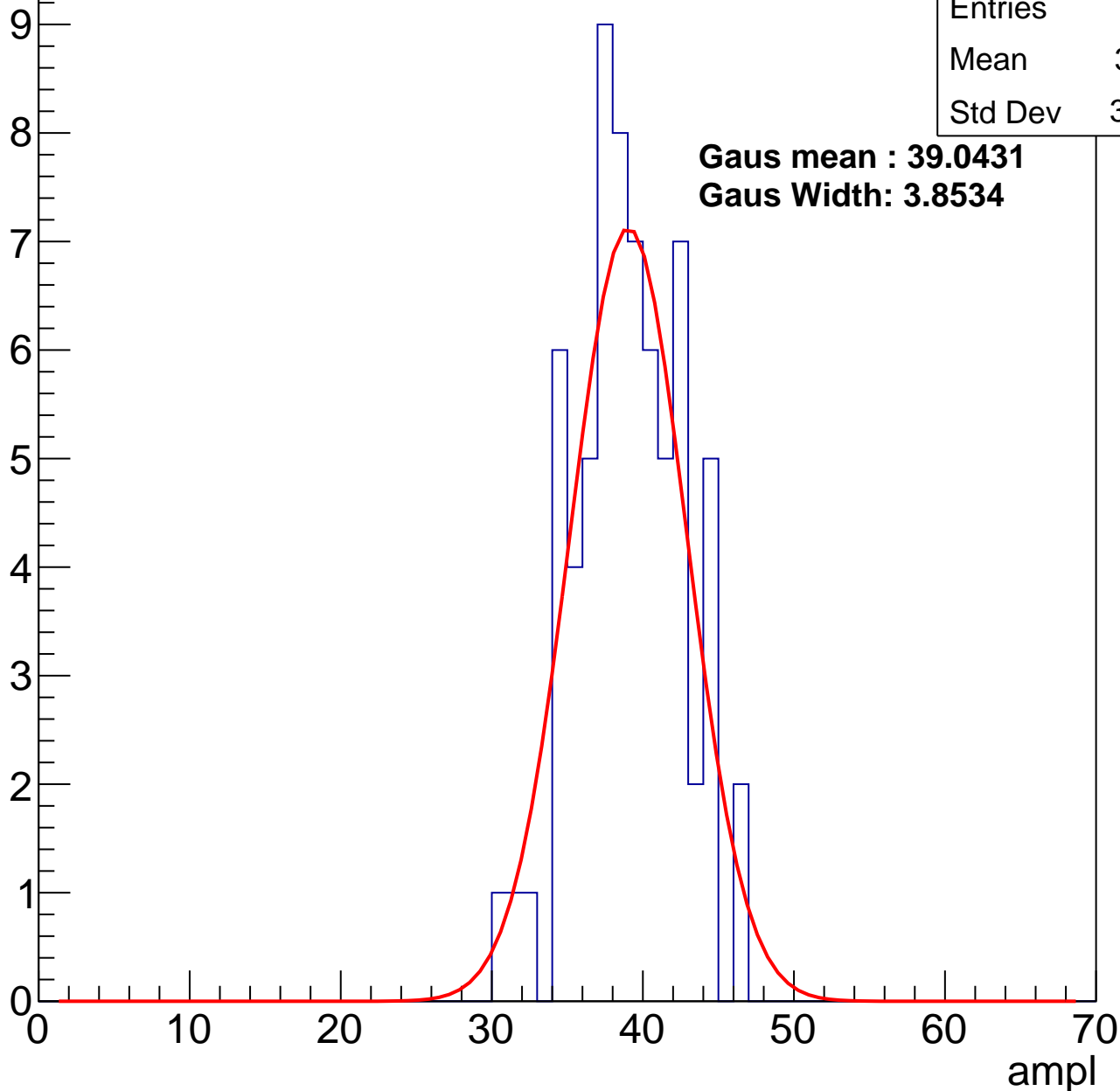
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	38.61
Std Dev	3.469

Gaus mean : 39.0431

Gaus Width: 3.8534



B1L103S, U17-ch36, adc2

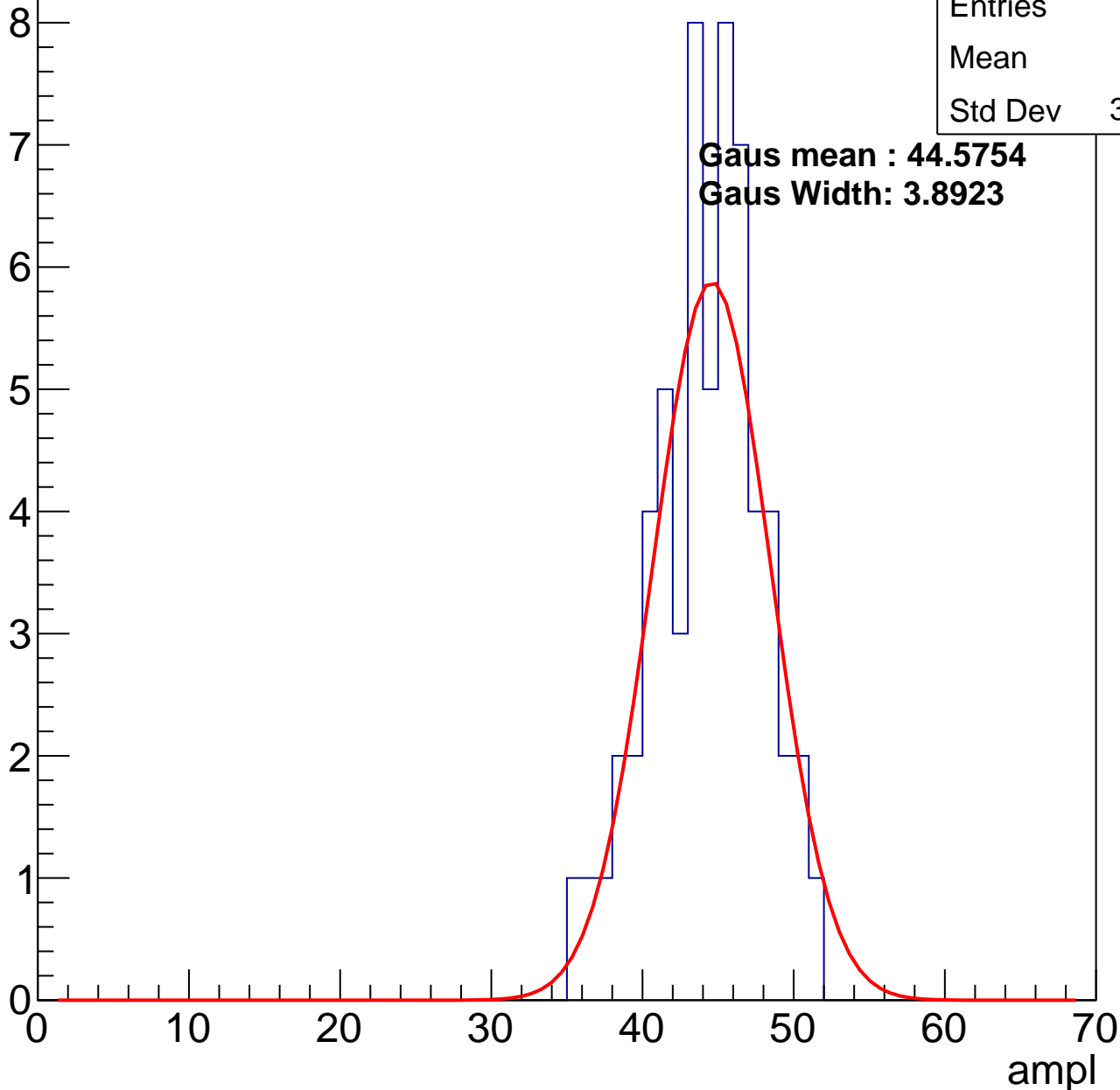
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.8
Std Dev	3.535

Gaus mean : 44.5754

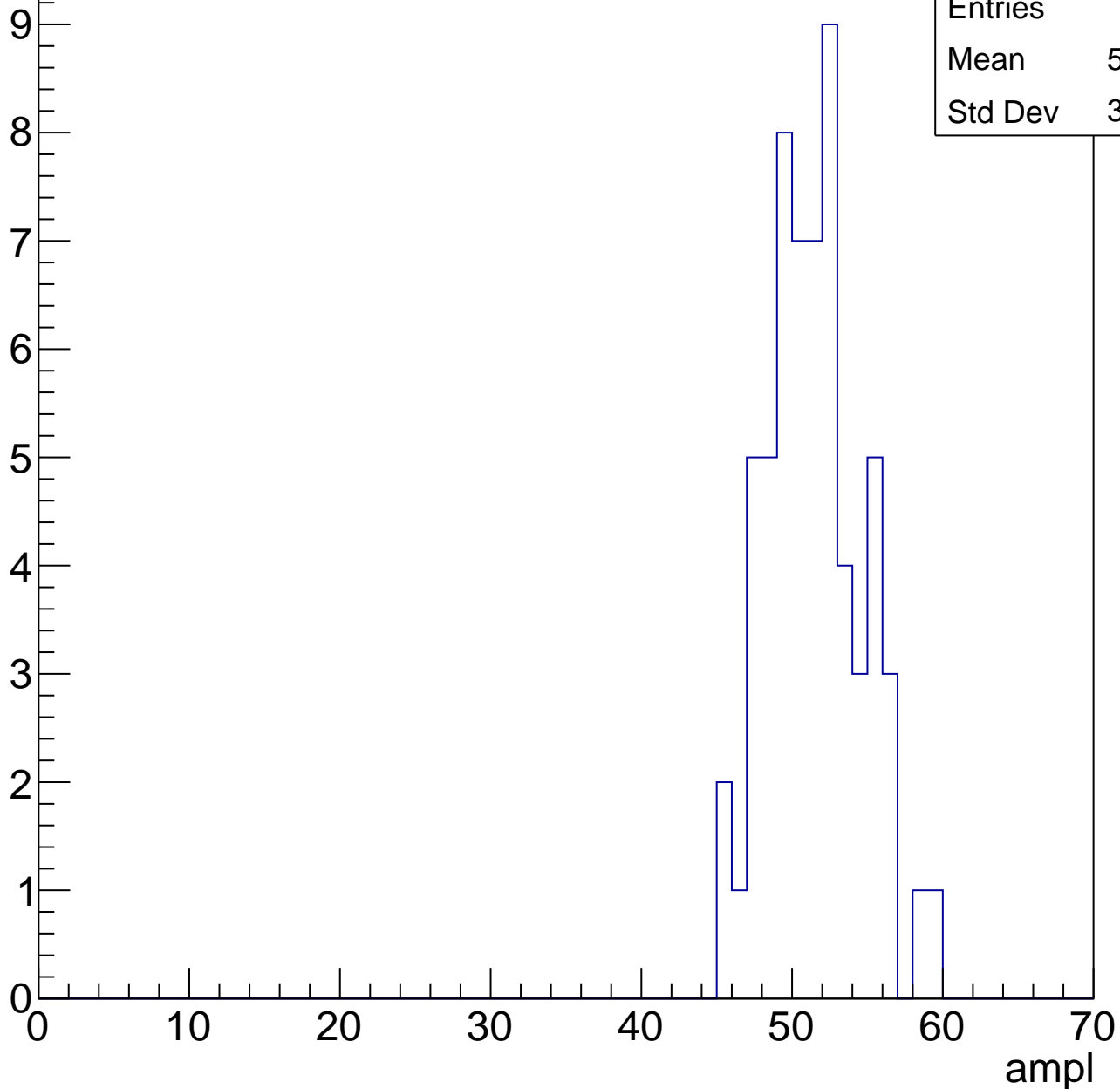
Gaus Width: 3.8923



B1L103S, U17-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

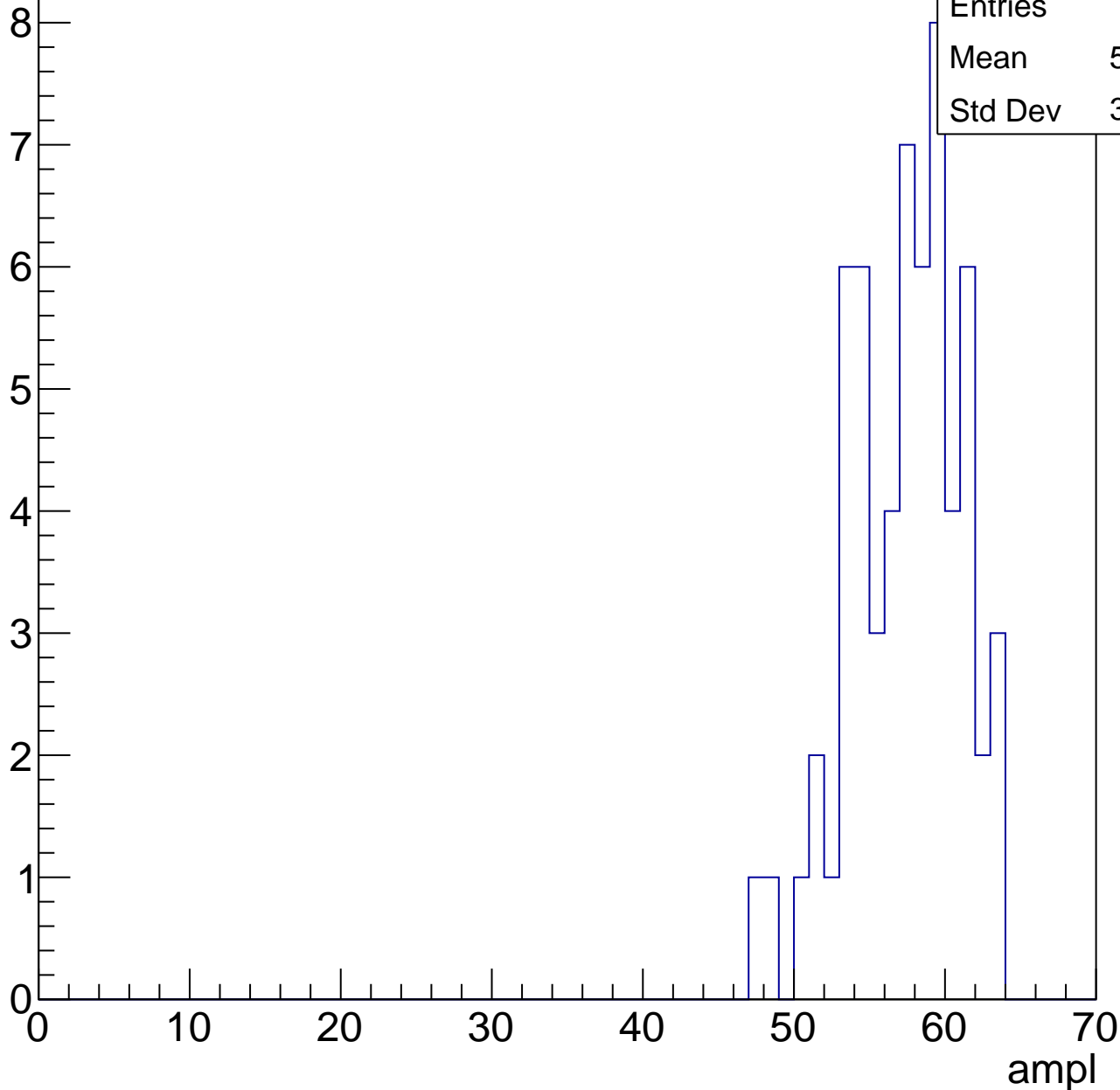


B1L103S, U17-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	56.85
Std Dev	3.666

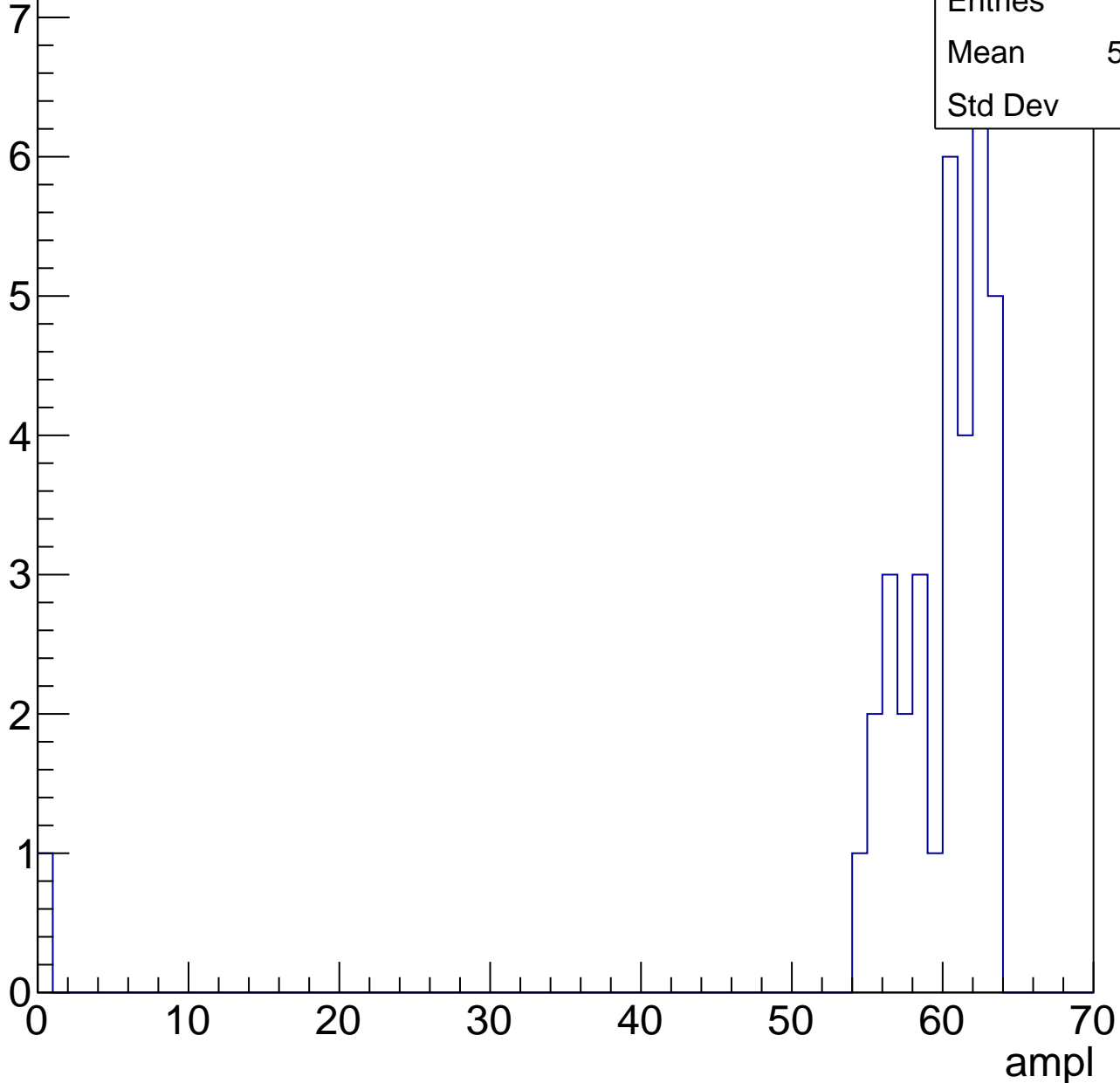


B1L103S, U17-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

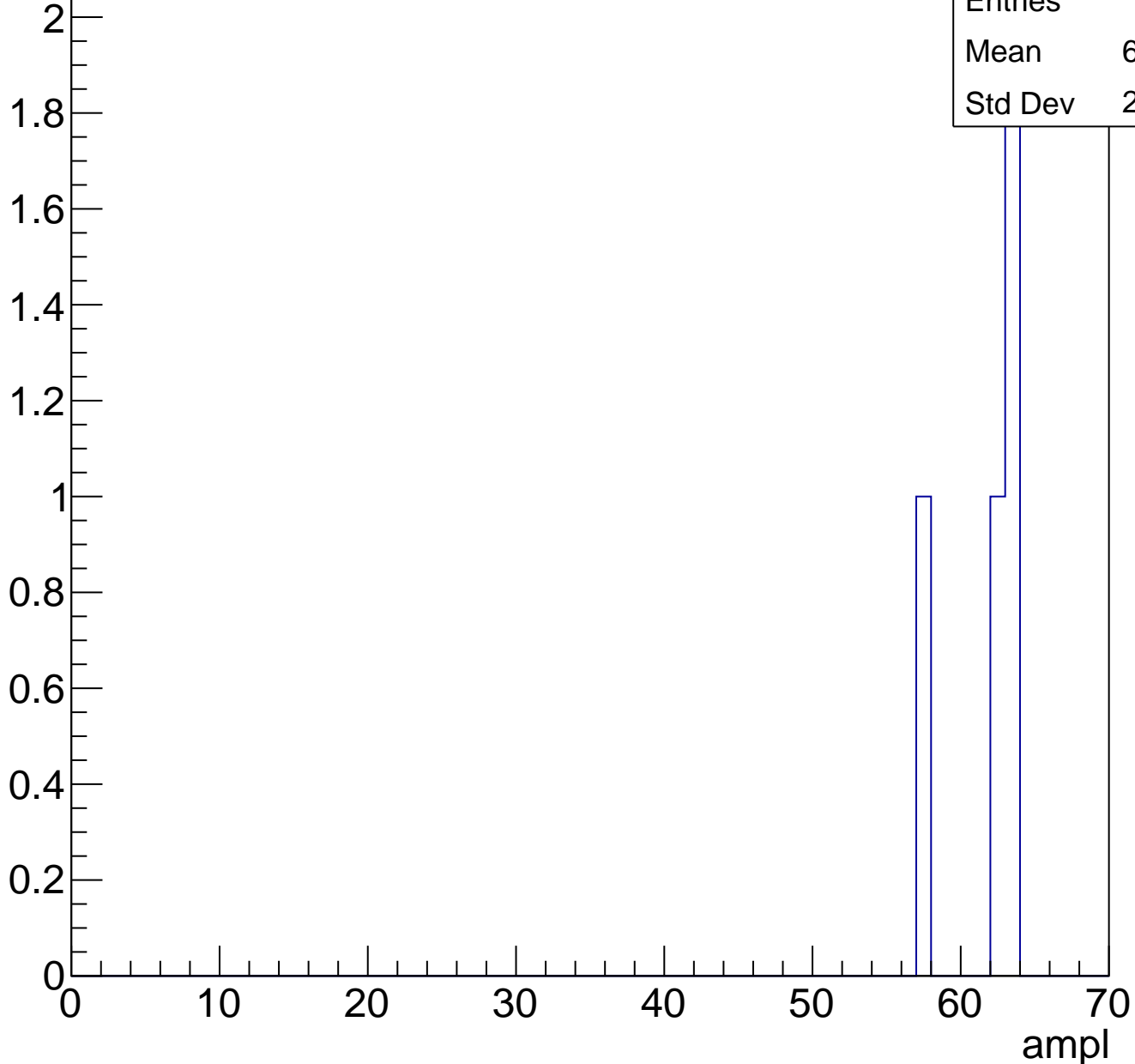
Entries	35
Mean	58.06
Std Dev	10.3



B1L103S, U17-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	61.25
Std Dev	2.487

B1L103S, U17-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch37, adc0

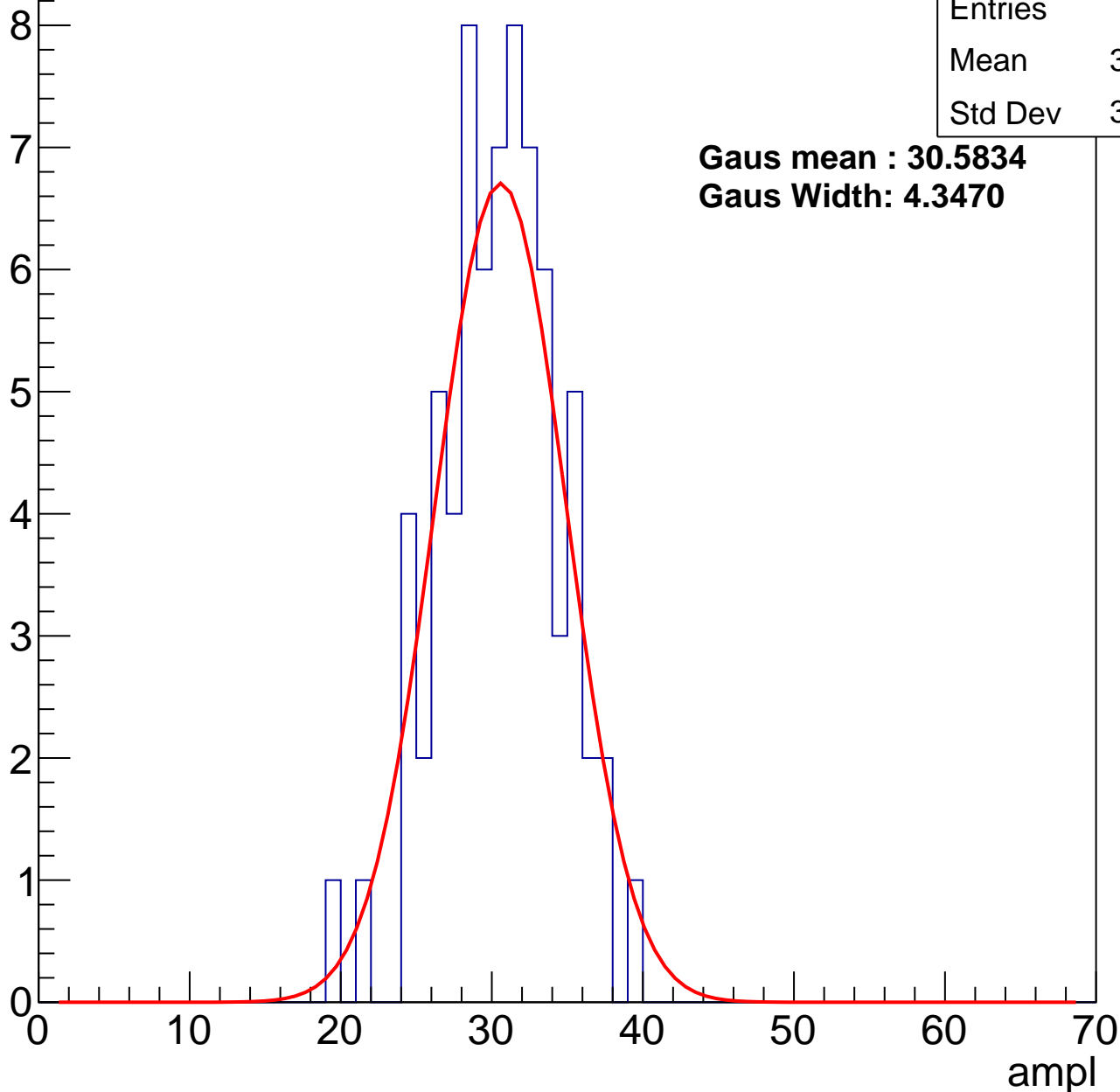
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.06
Std Dev	3.844

Gaus mean : 30.5834

Gaus Width: 4.3470



B1L103S, U17-ch37, adc1

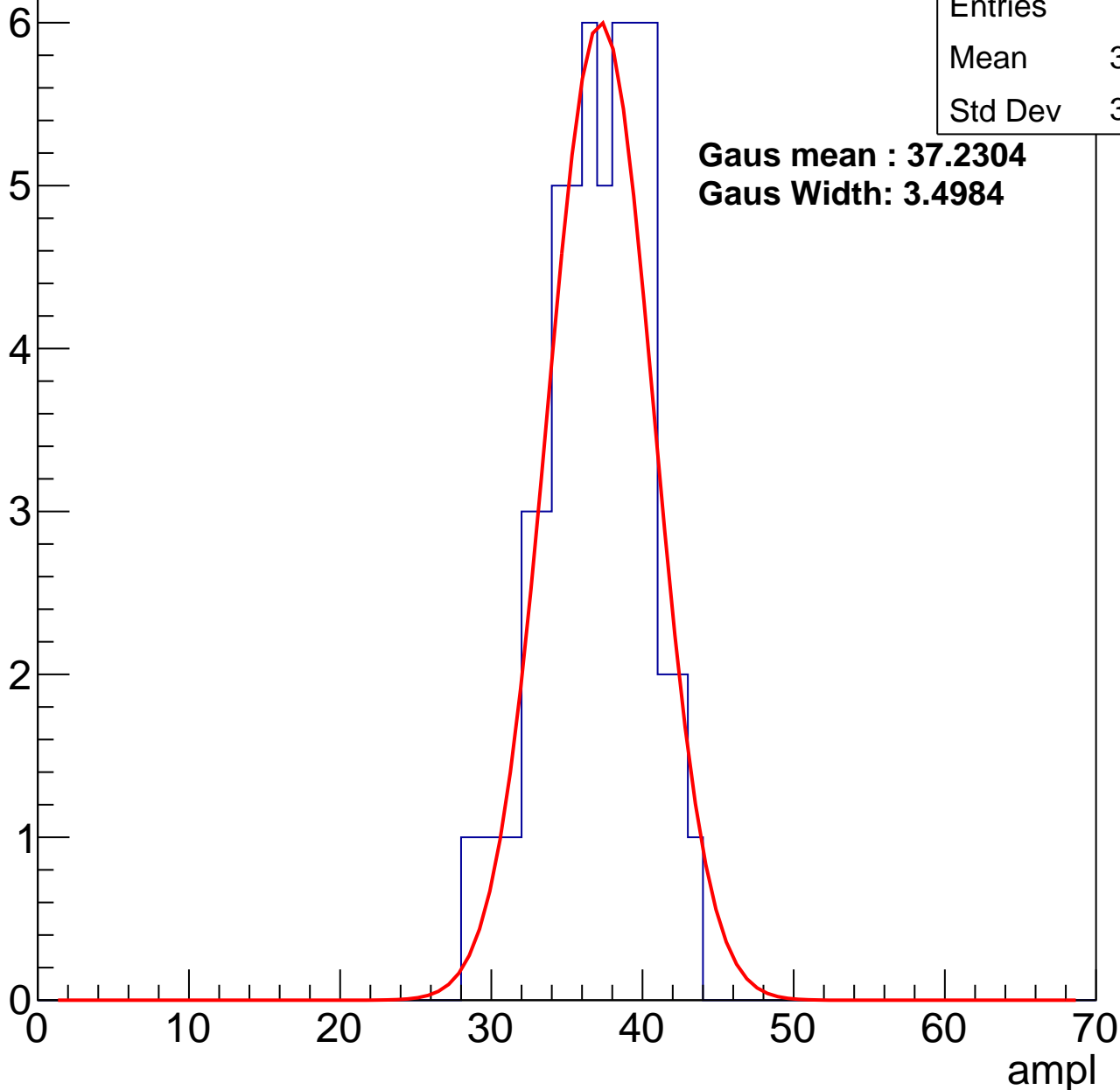
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.48
Std Dev	3.365

Gaus mean : 37.2304

Gaus Width: 3.4984



B1L103S, U17-ch37, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	42.78
Std Dev	3.87

Gaus mean : 43.0188

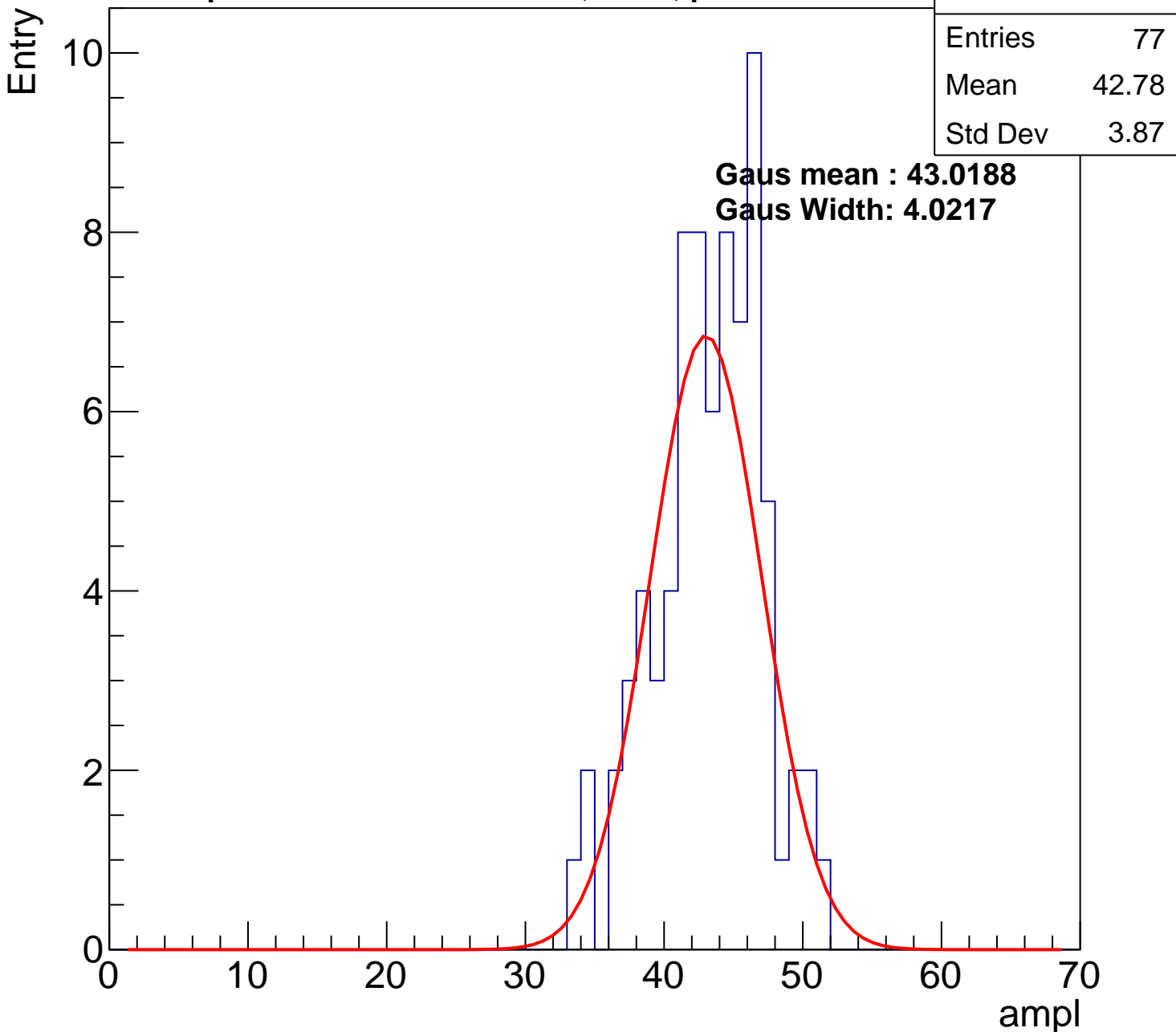
Gaus Width: 4.0217

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

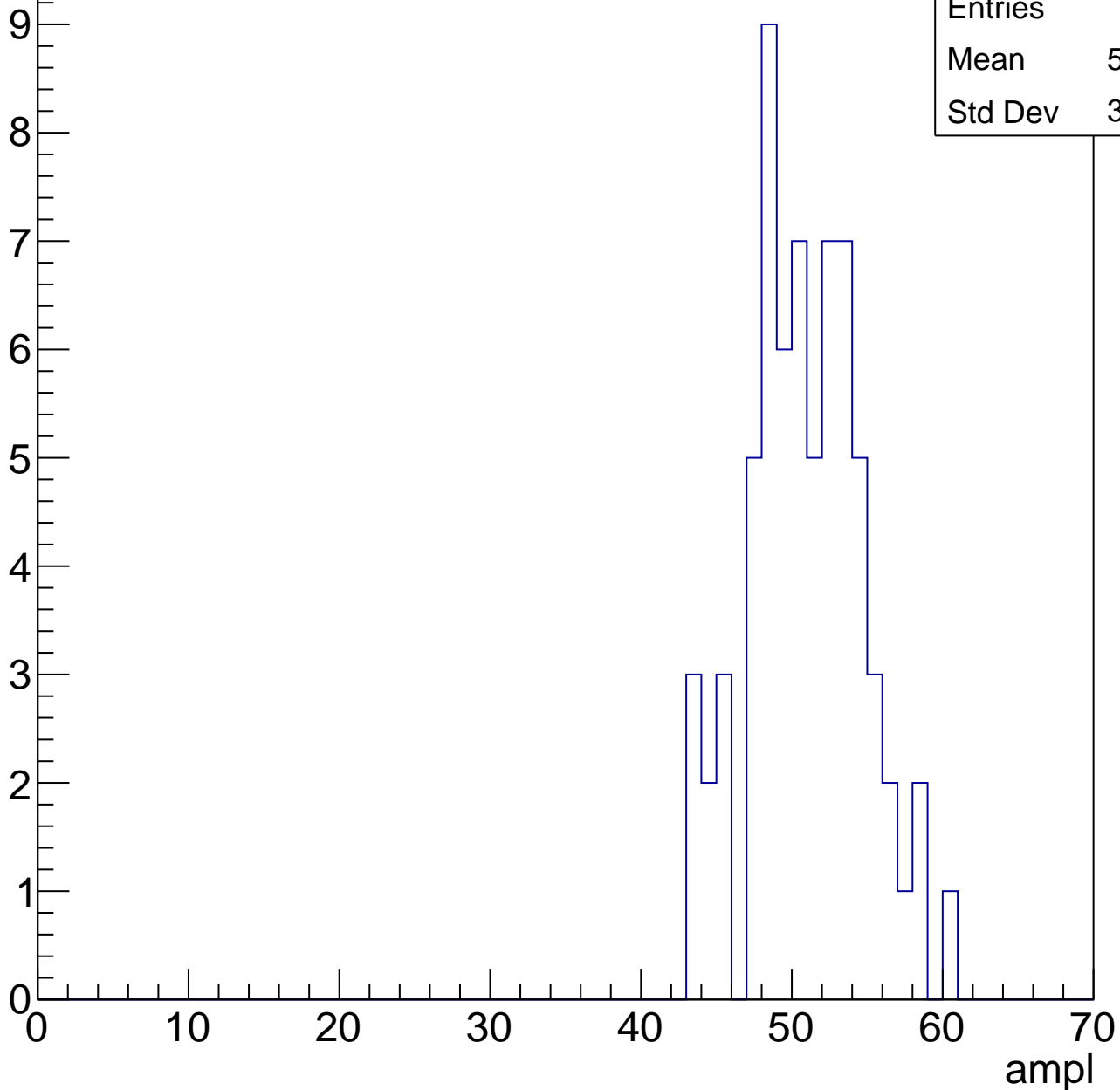


B1L103S, U17-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	50.49
Std Dev	3.767

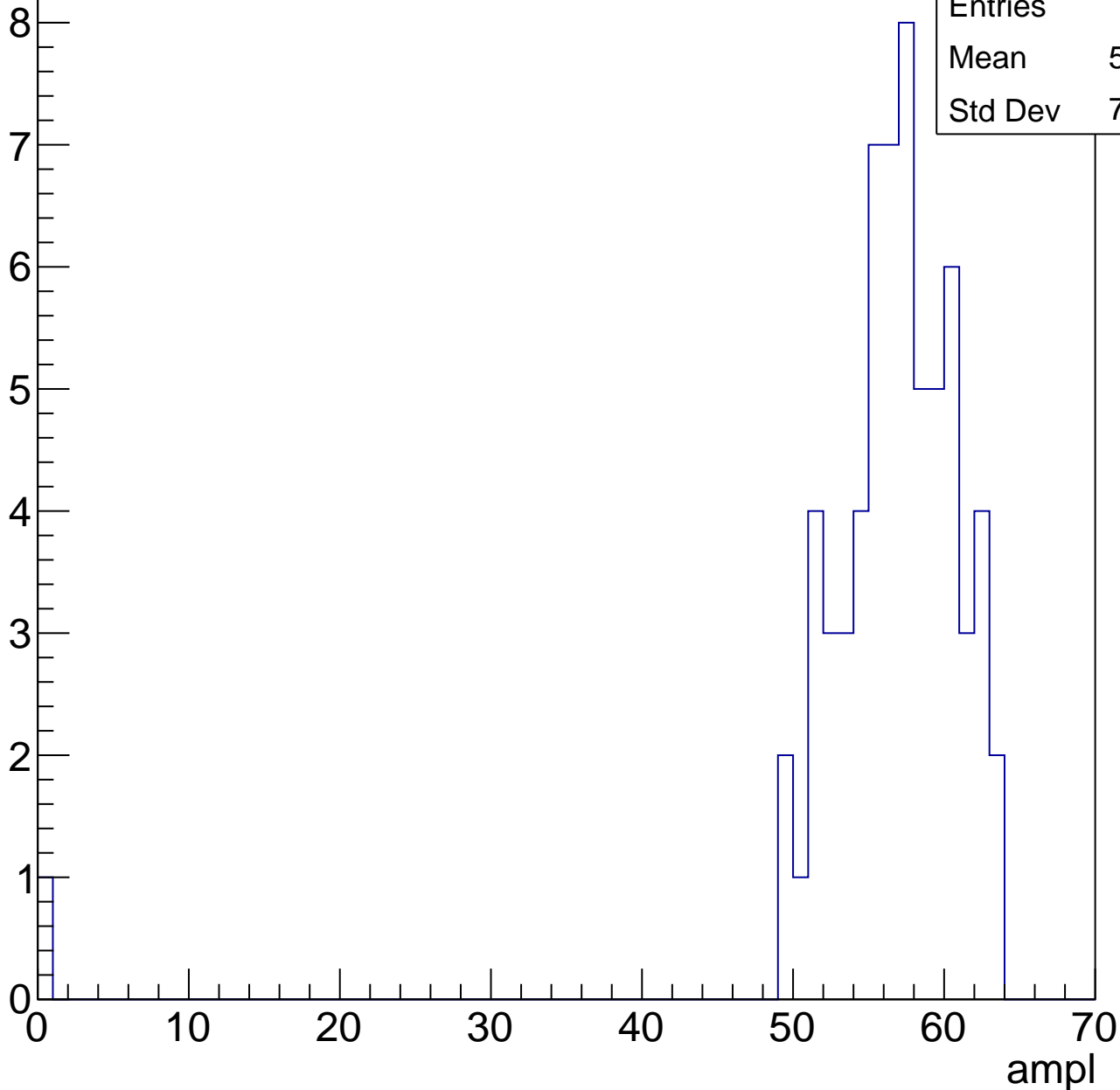


B1L103S, U17-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.66
Std Dev	7.792

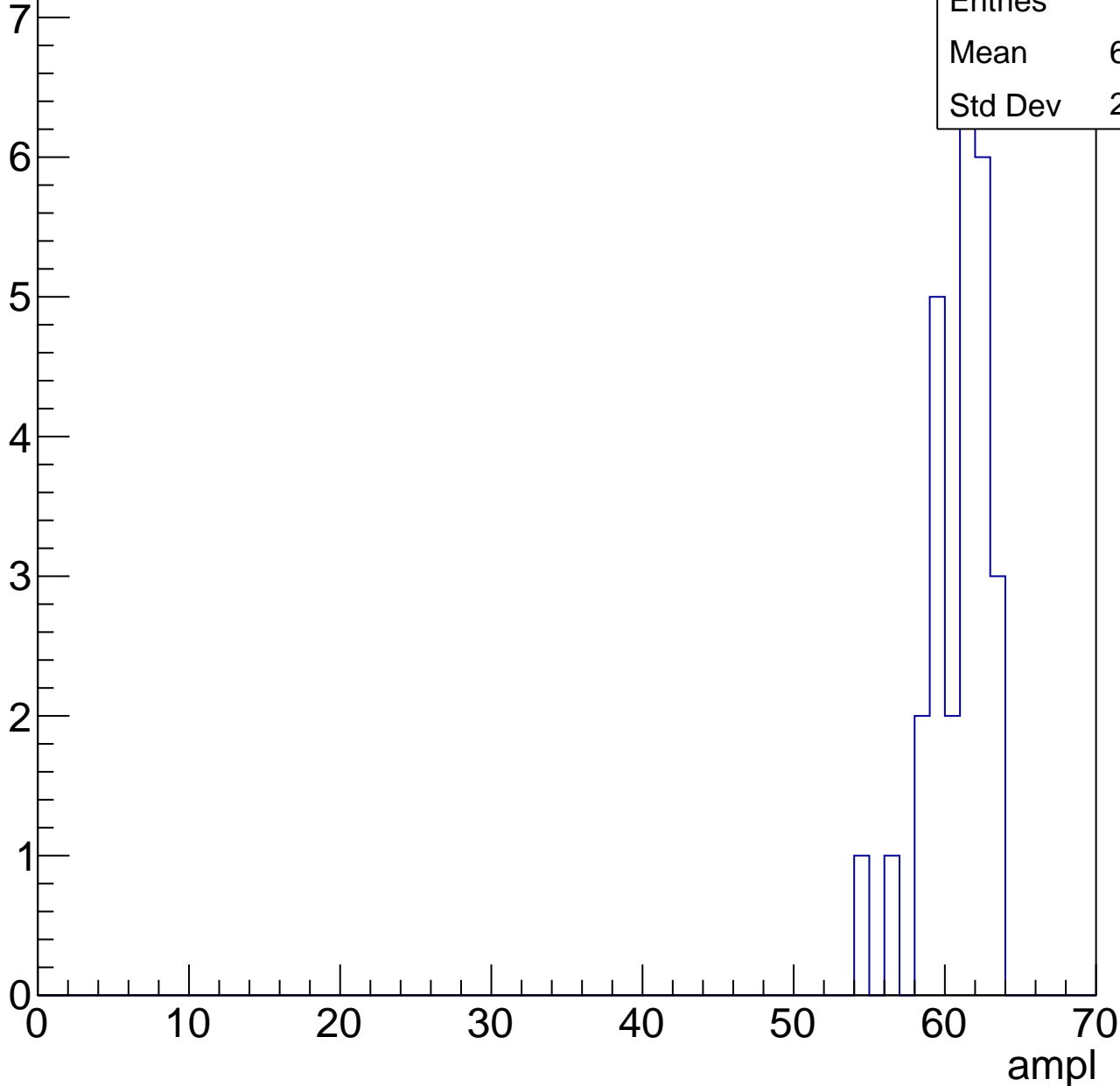


B1L103S, U17-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	60.33
Std Dev	2.108

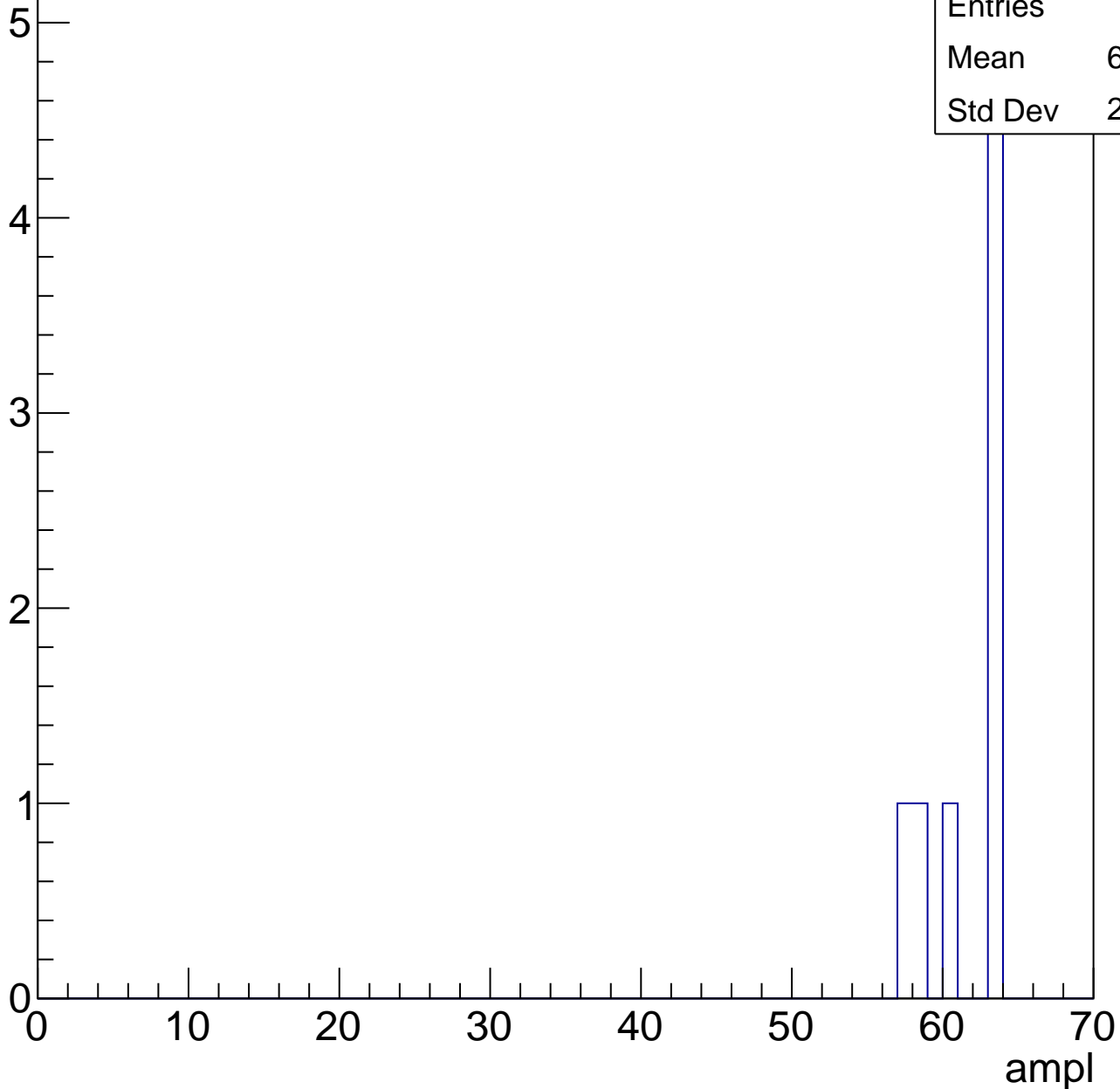


B1L103S, U17-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	61.25
Std Dev	2.385



B1L103S, U17-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch38, adc0

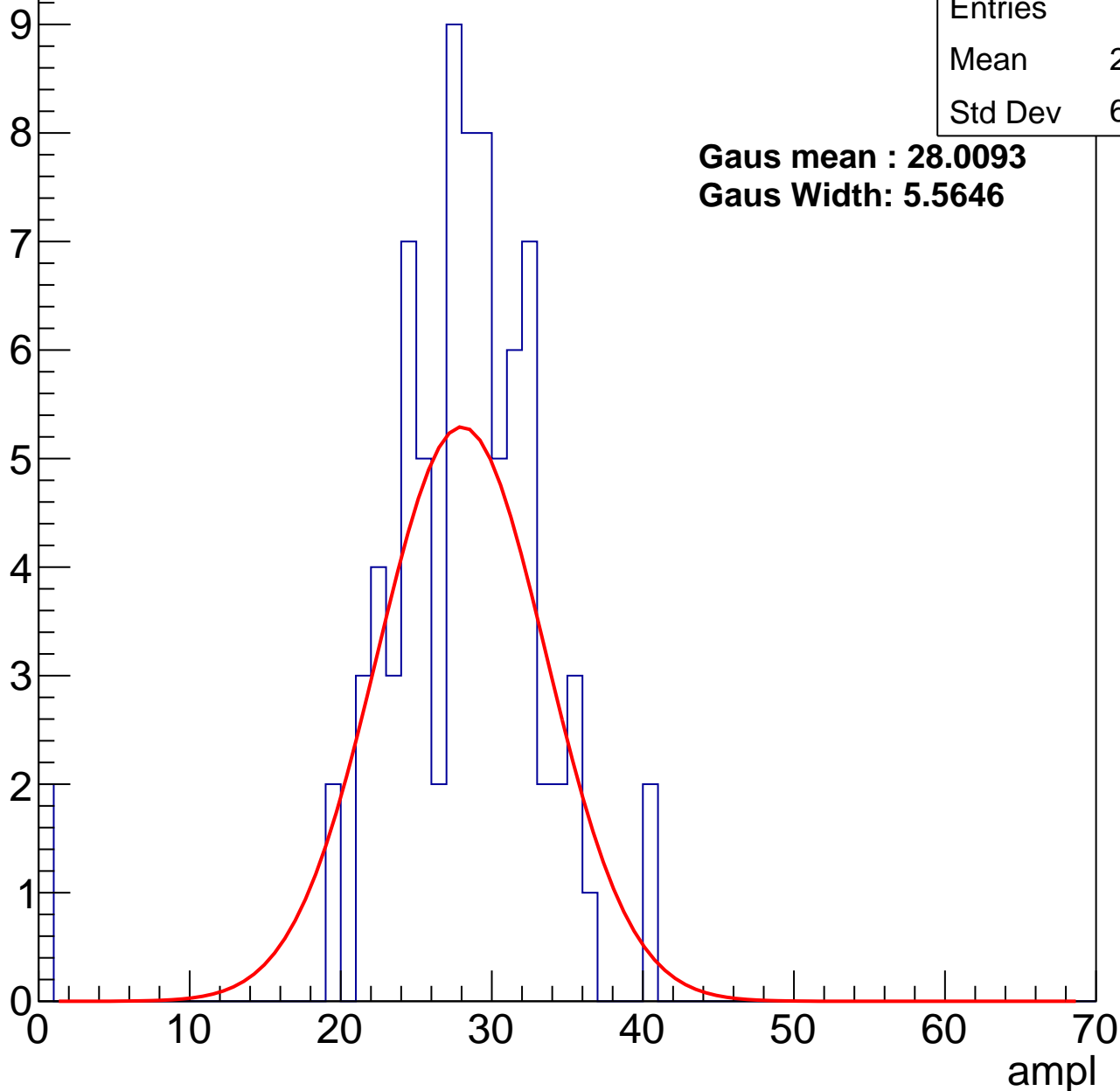
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	27.37
Std Dev	6.139

Gaus mean : 28.0093

Gaus Width: 5.5646



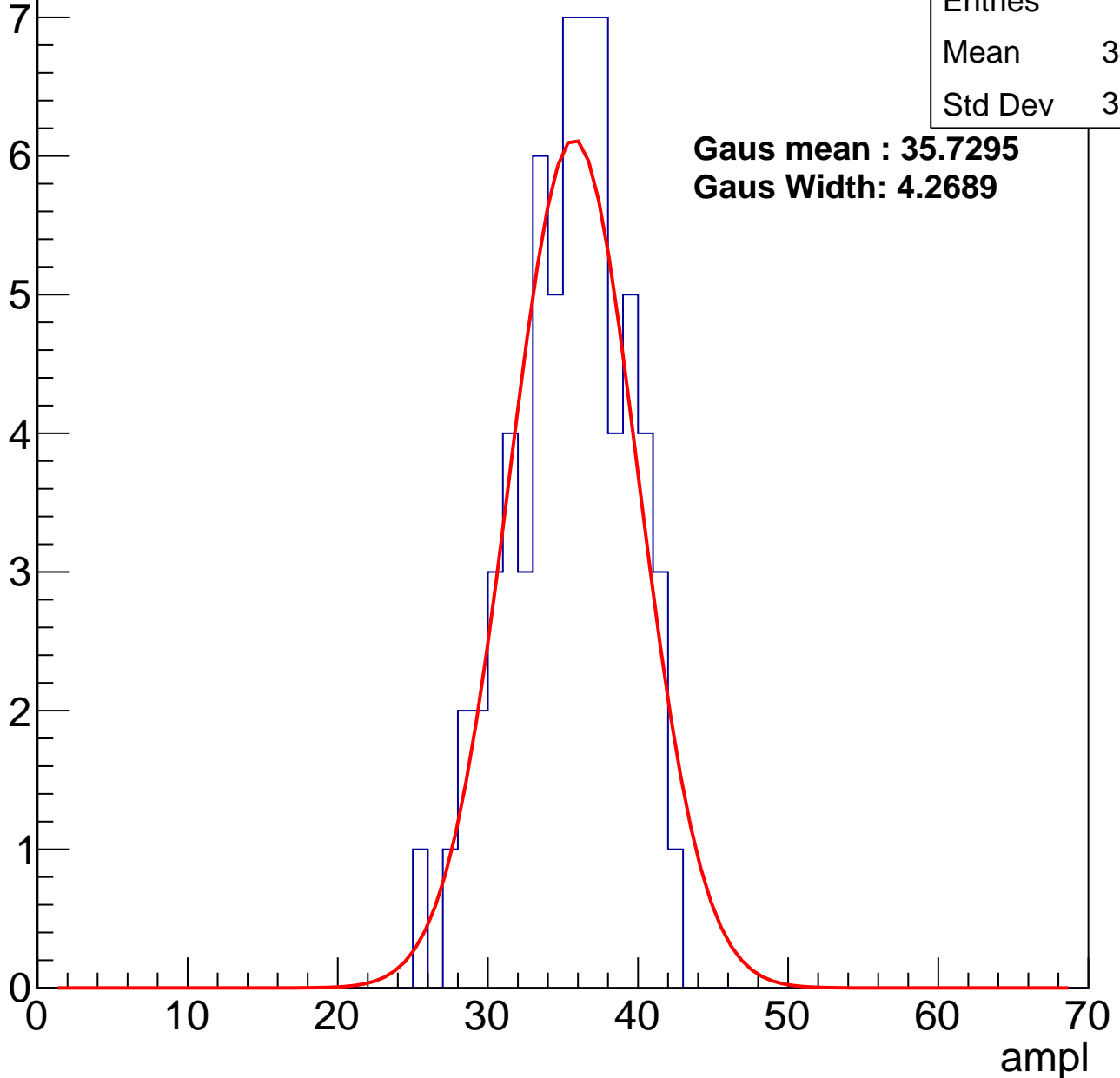
B1L103S, U17-ch38, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.95
Std Dev	3.796

Gaus mean : 35.7295
Gaus Width: 4.2689



B1L103S, U17-ch38, adc2

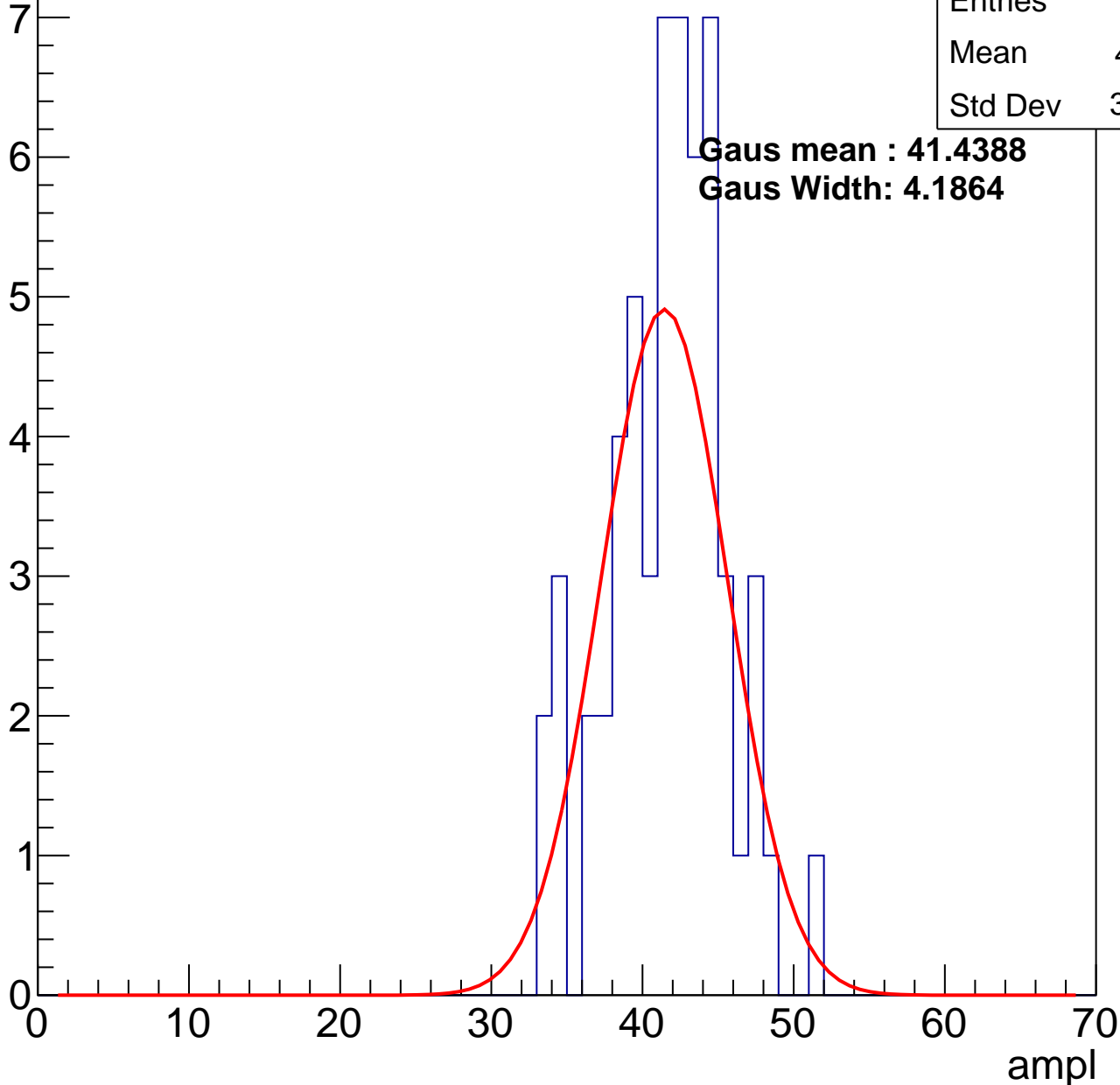
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.21
Std Dev	3.833

Gaus mean : 41.4388

Gaus Width: 4.1864

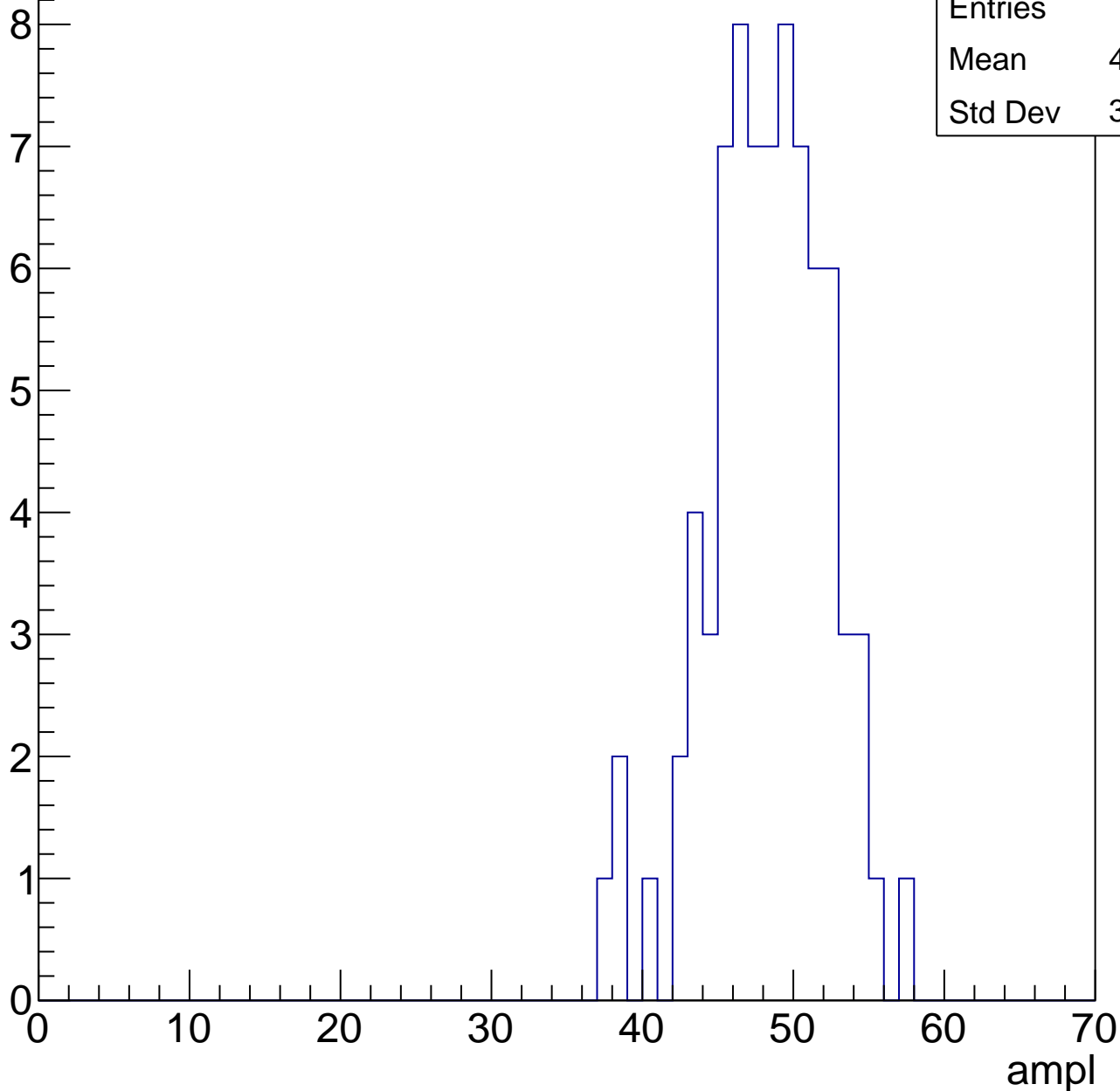


B1L103S, U17-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	47.82
Std Dev	3.953

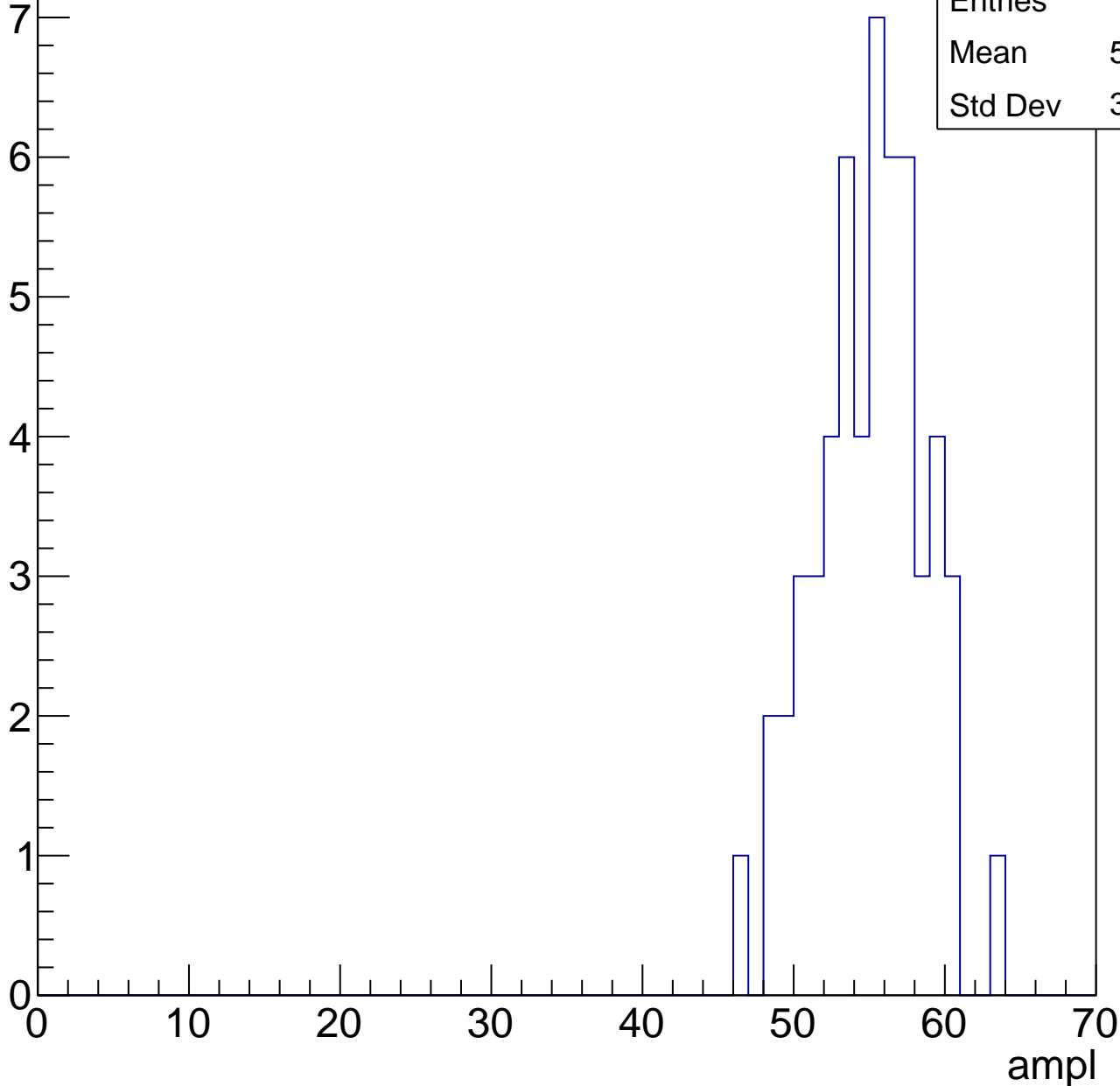


B1L103S, U17-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.56
Std Dev	3.536

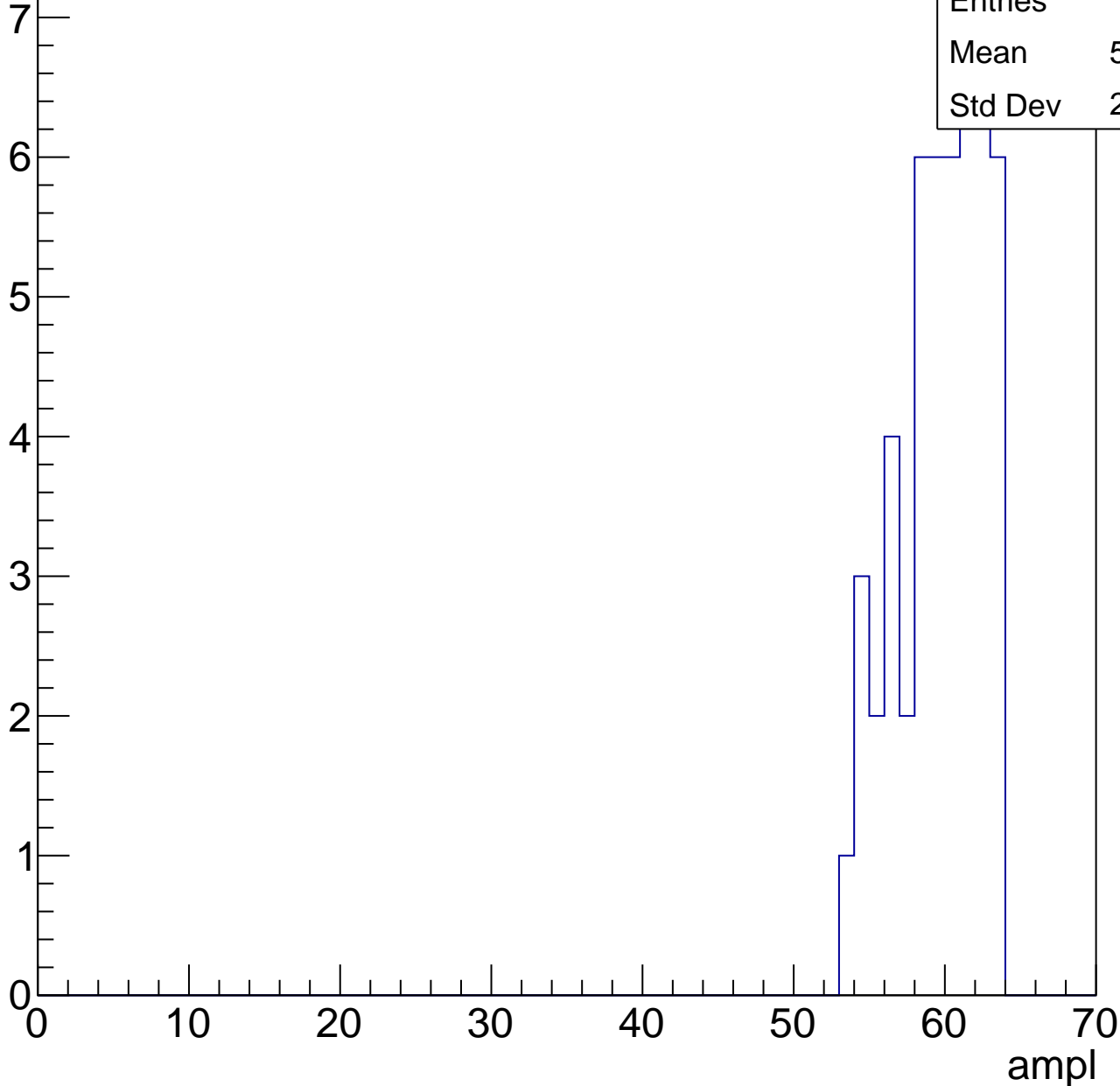


B1L103S, U17-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

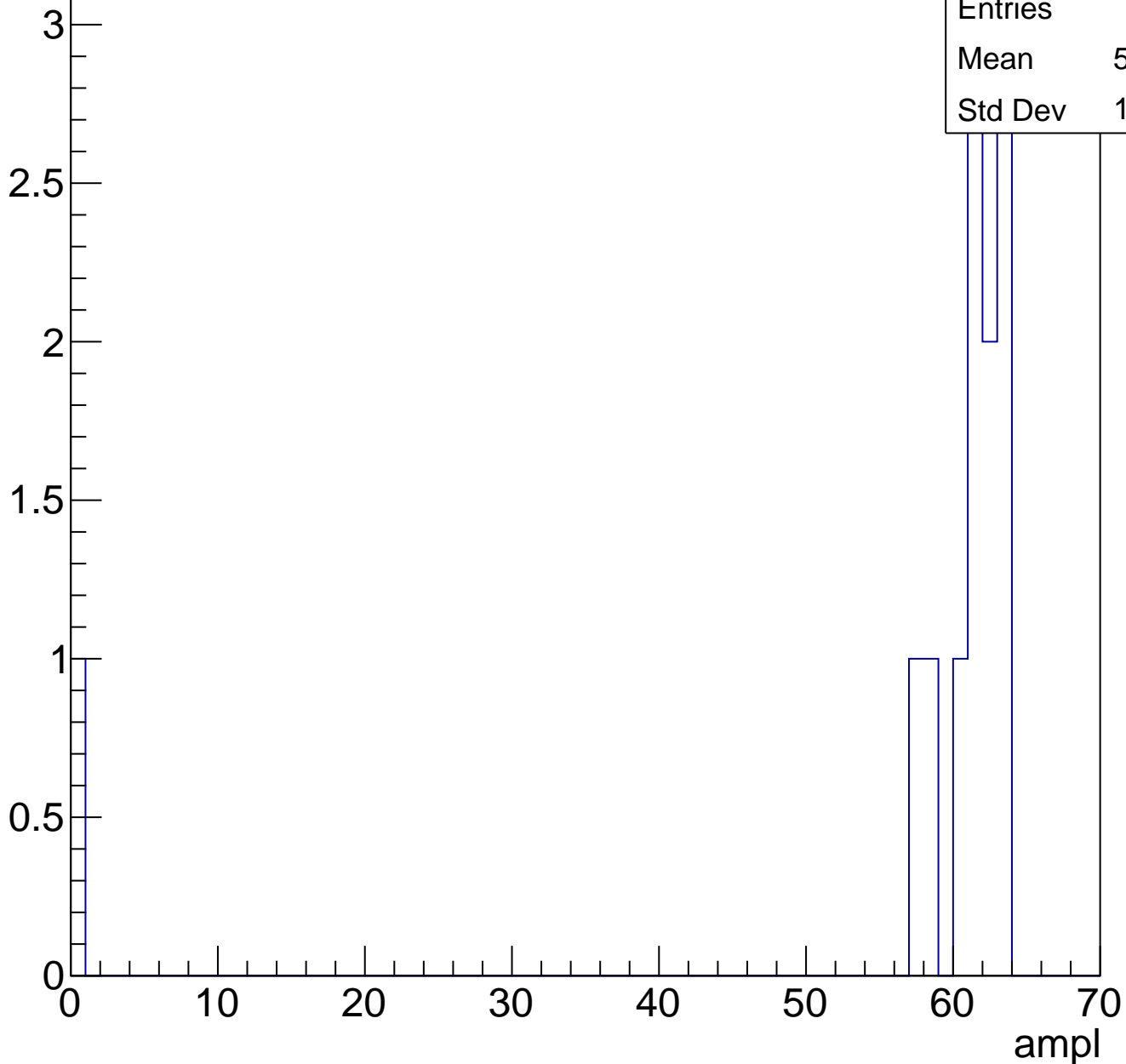
Entries	50
Mean	59.28
Std Dev	2.764



B1L103S, U17-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch39, adc0

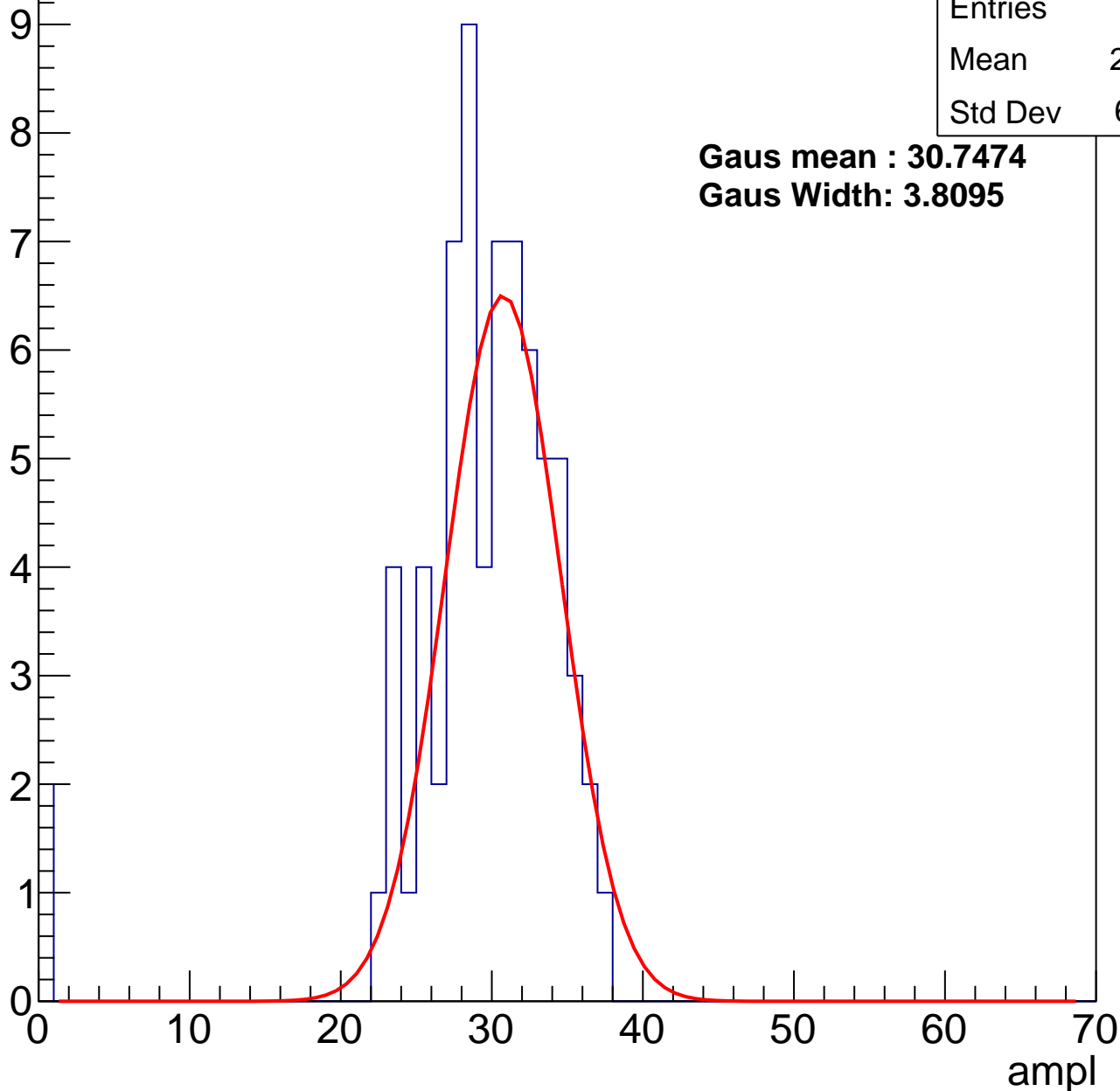
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.79
Std Dev	6.071

Gaus mean : 30.7474

Gaus Width: 3.8095



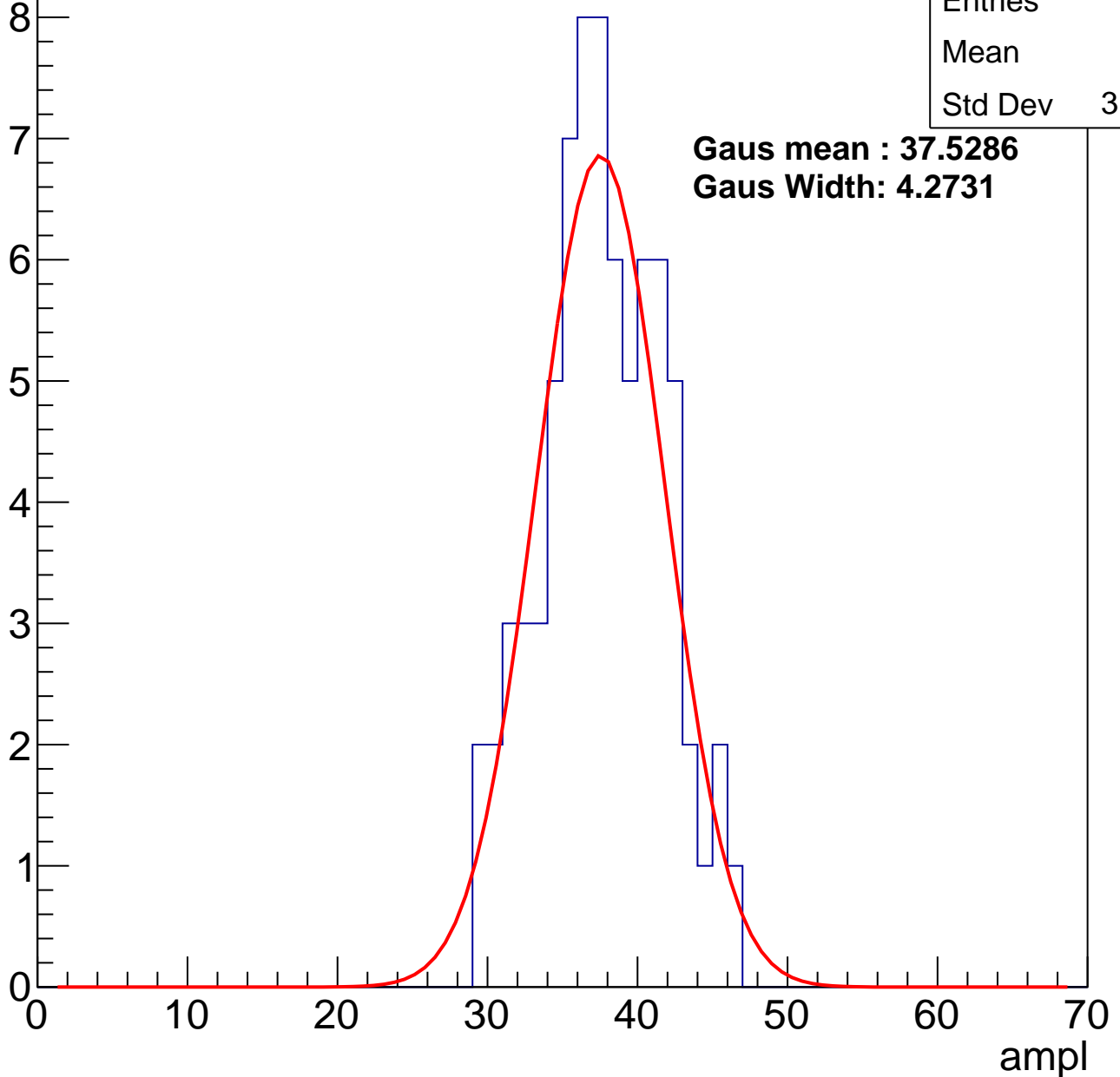
B1L103S, U17-ch39, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.2
Std Dev	3.943

Gaus mean : 37.5286
Gaus Width: 4.2731



B1L103S, U17-ch39, adc2

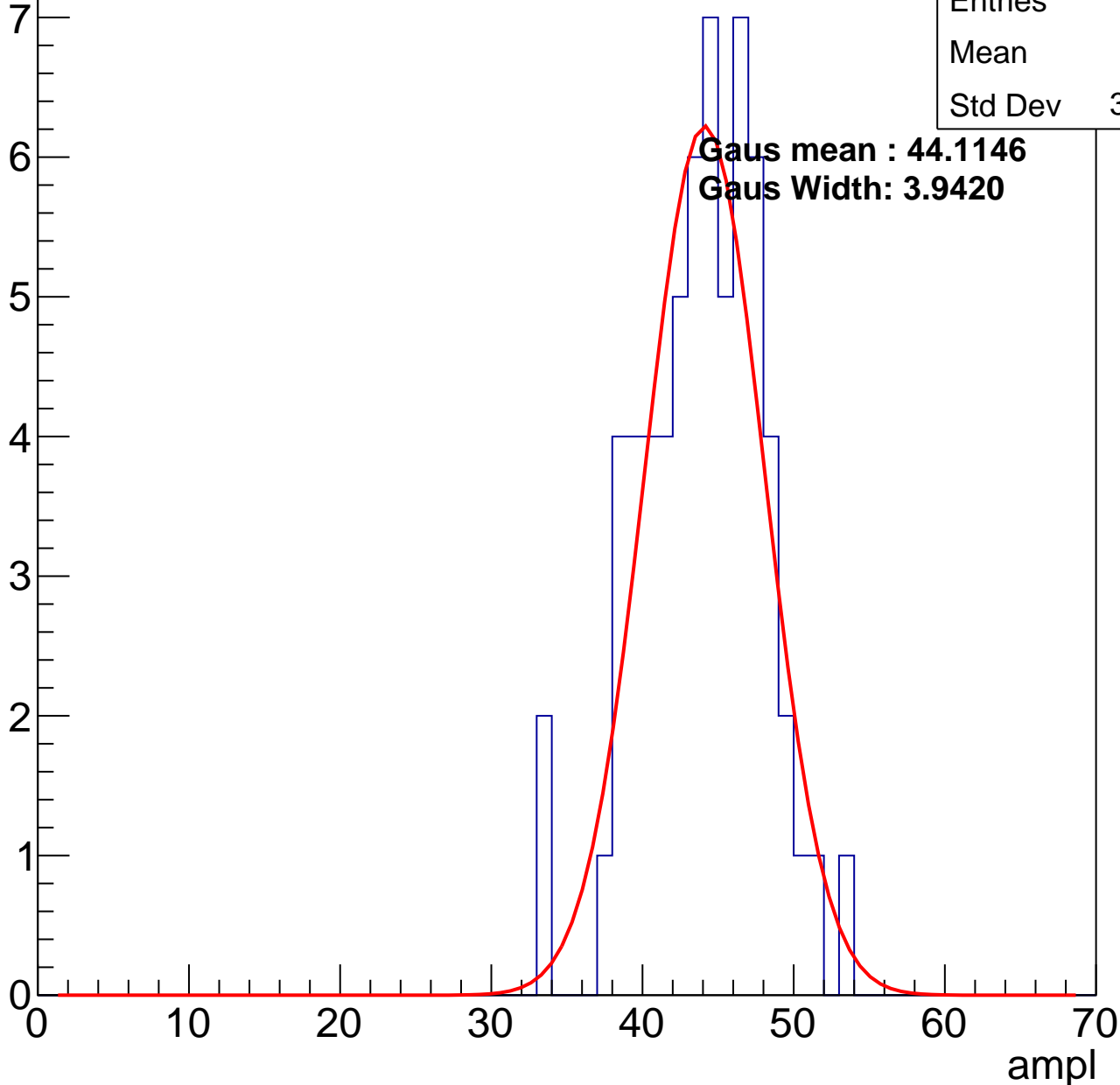
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.5
Std Dev	3.976

Gaus mean : 44.1146

Gaus Width: 3.9420

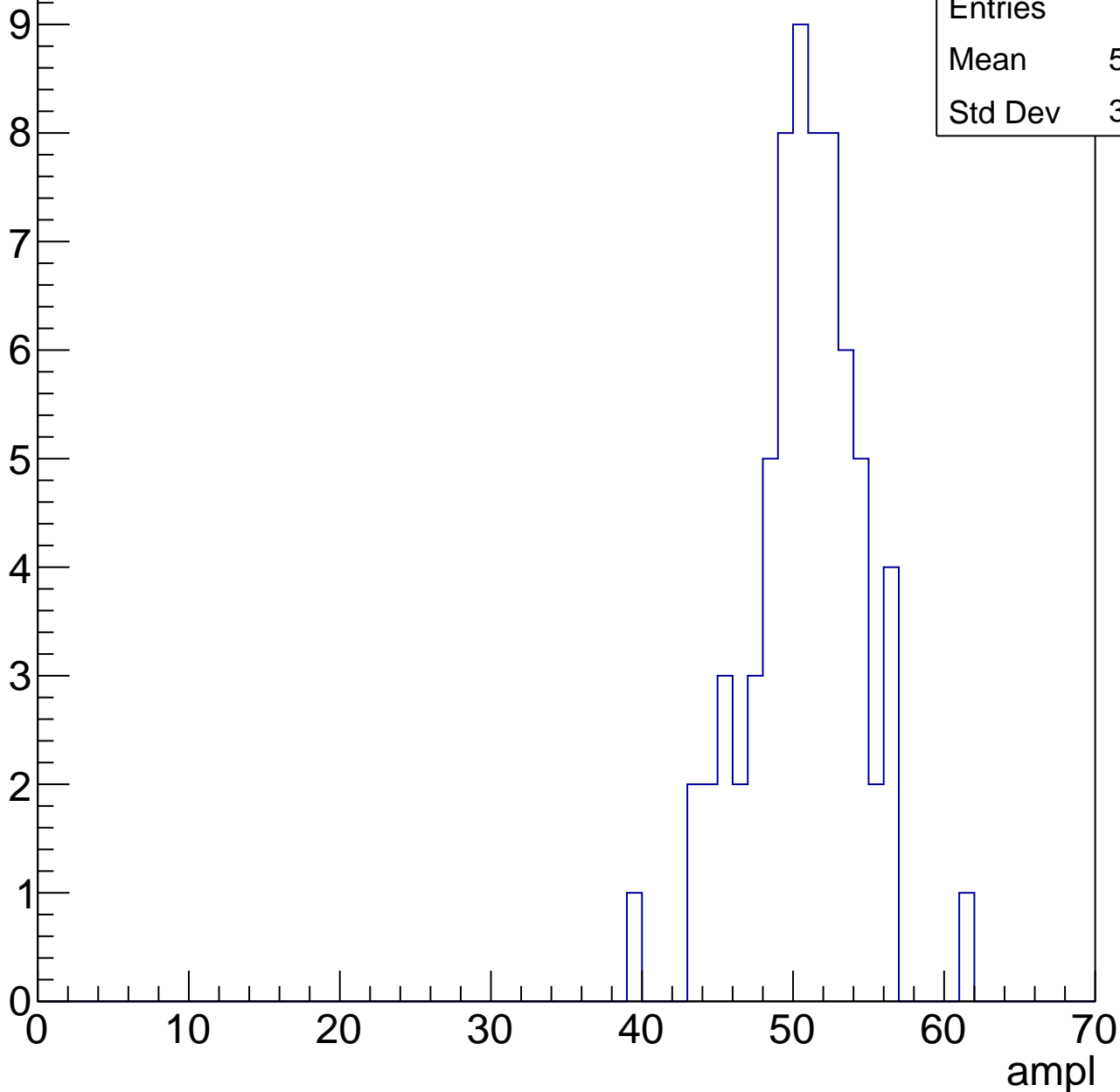


B1L103S, U17-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	50.29
Std Dev	3.683



B1L103S, U17-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

6

5

4

3

2

1

0

Entries 45

Mean 56.16

Std Dev 3.272

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

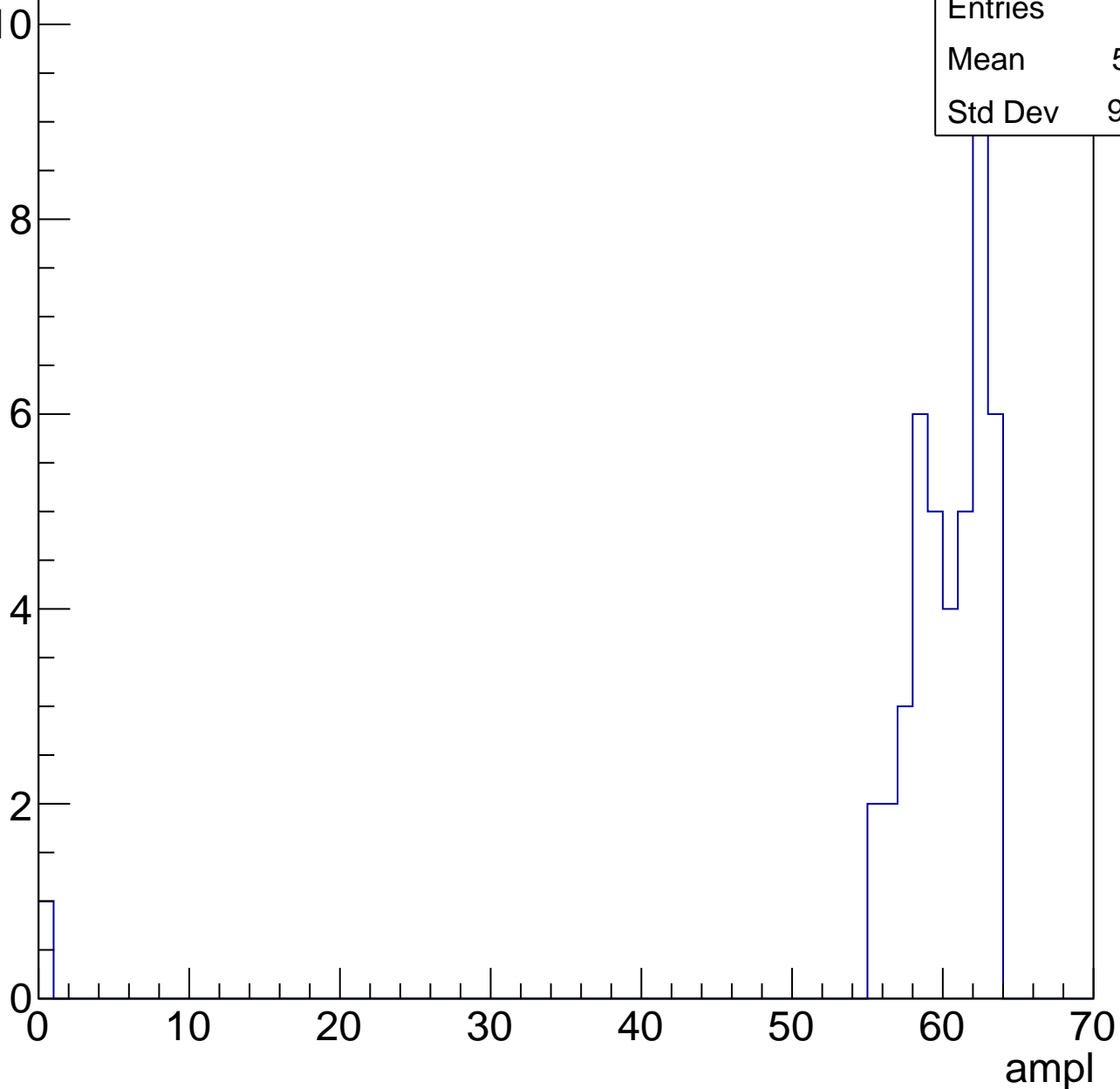
ampl

B1L103S, U17-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.61
Std Dev	9.235



B1L103S, U17-ch39, adc6

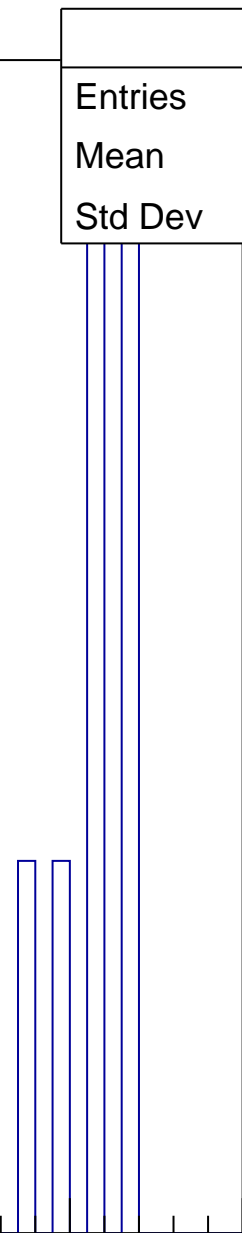
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61
Std Dev	2

ampl



B1L103S, U17-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch40, adc0

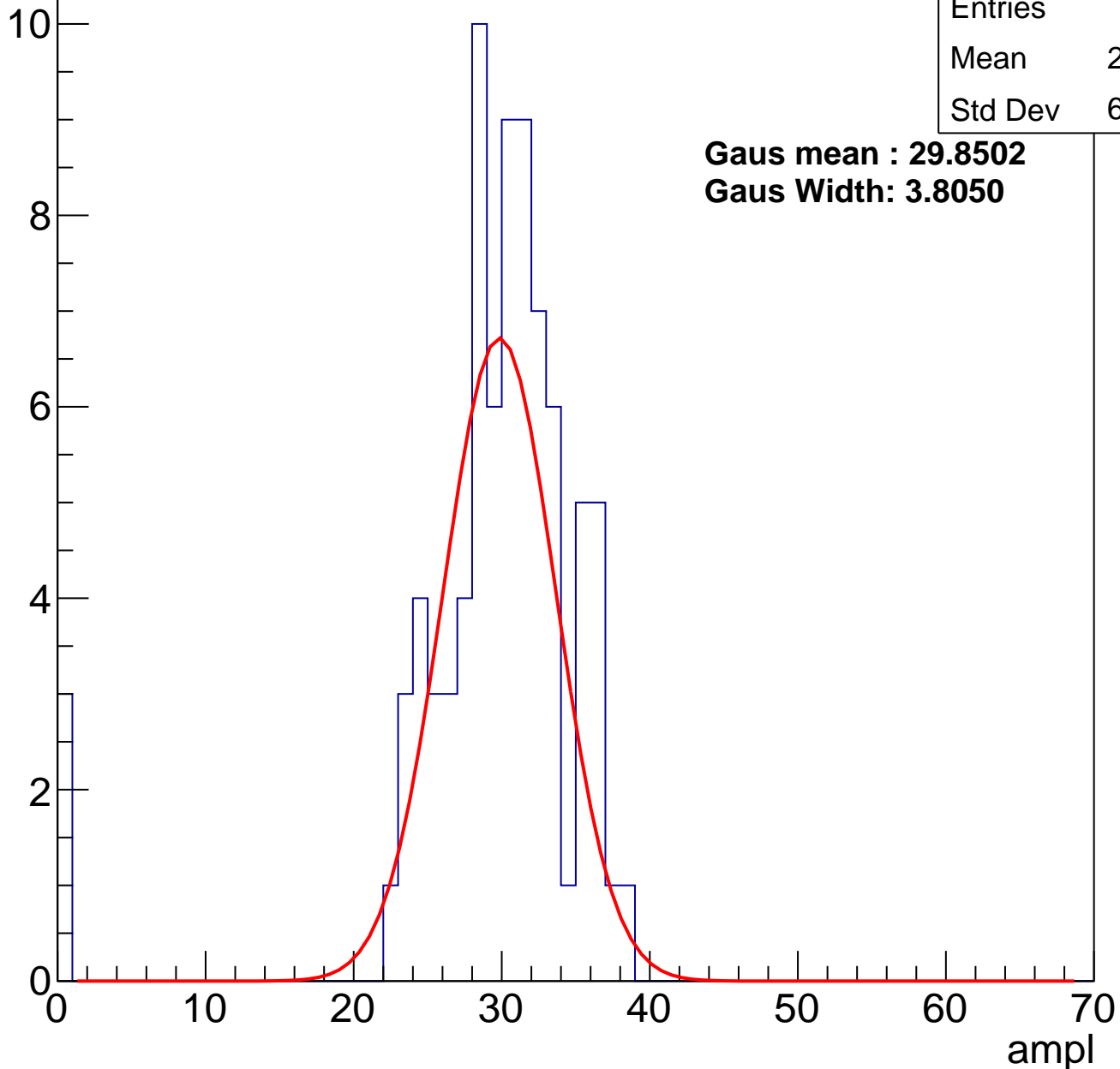
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	28.85
Std Dev	6.739

Gaus mean : 29.8502

Gaus Width: 3.8050

Entry



B1L103S, U17-ch40, adc1

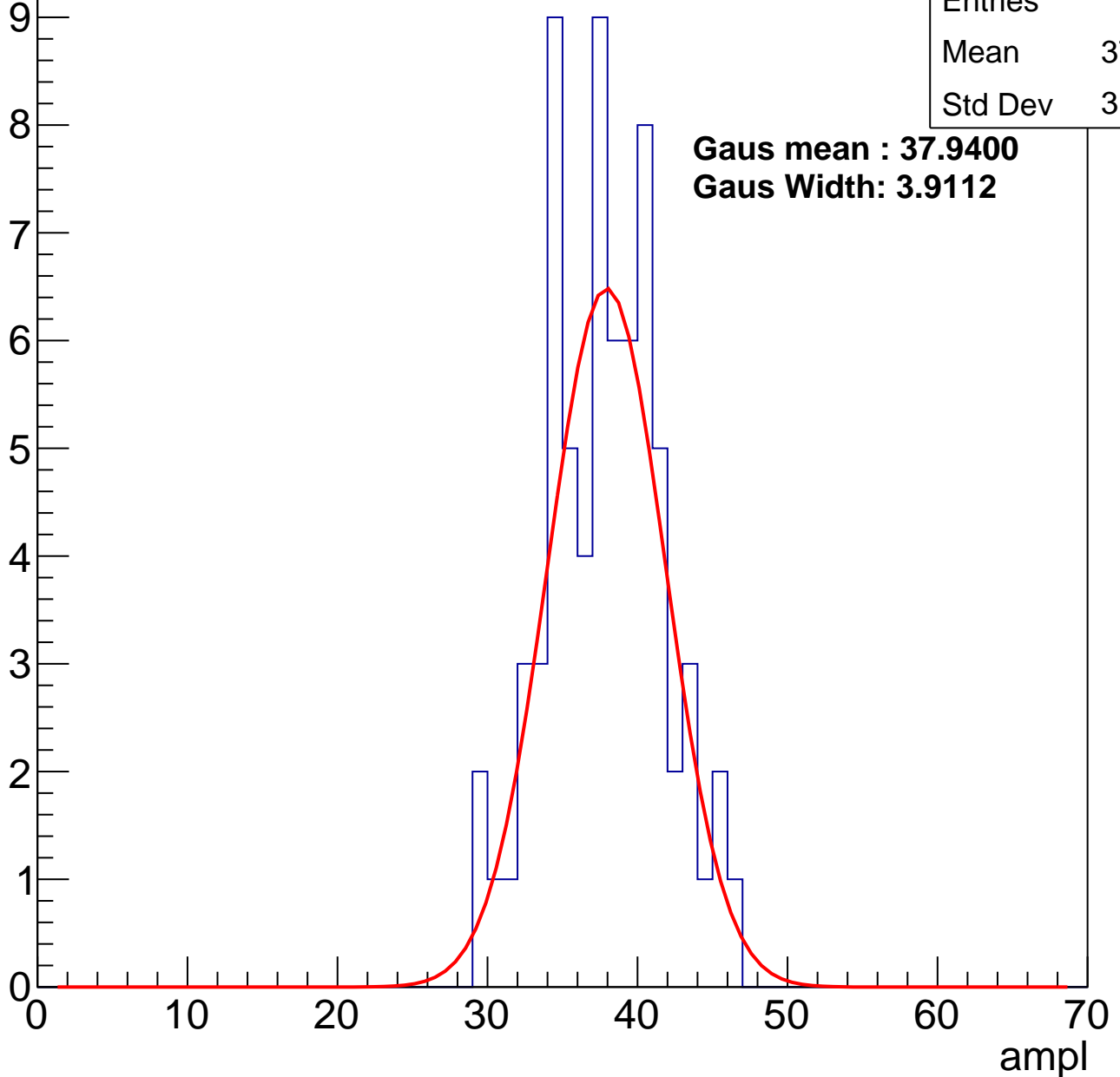
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	37.35
Std Dev	3.809

Gaus mean : 37.9400

Gaus Width: 3.9112



B1L103S, U17-ch40, adc2

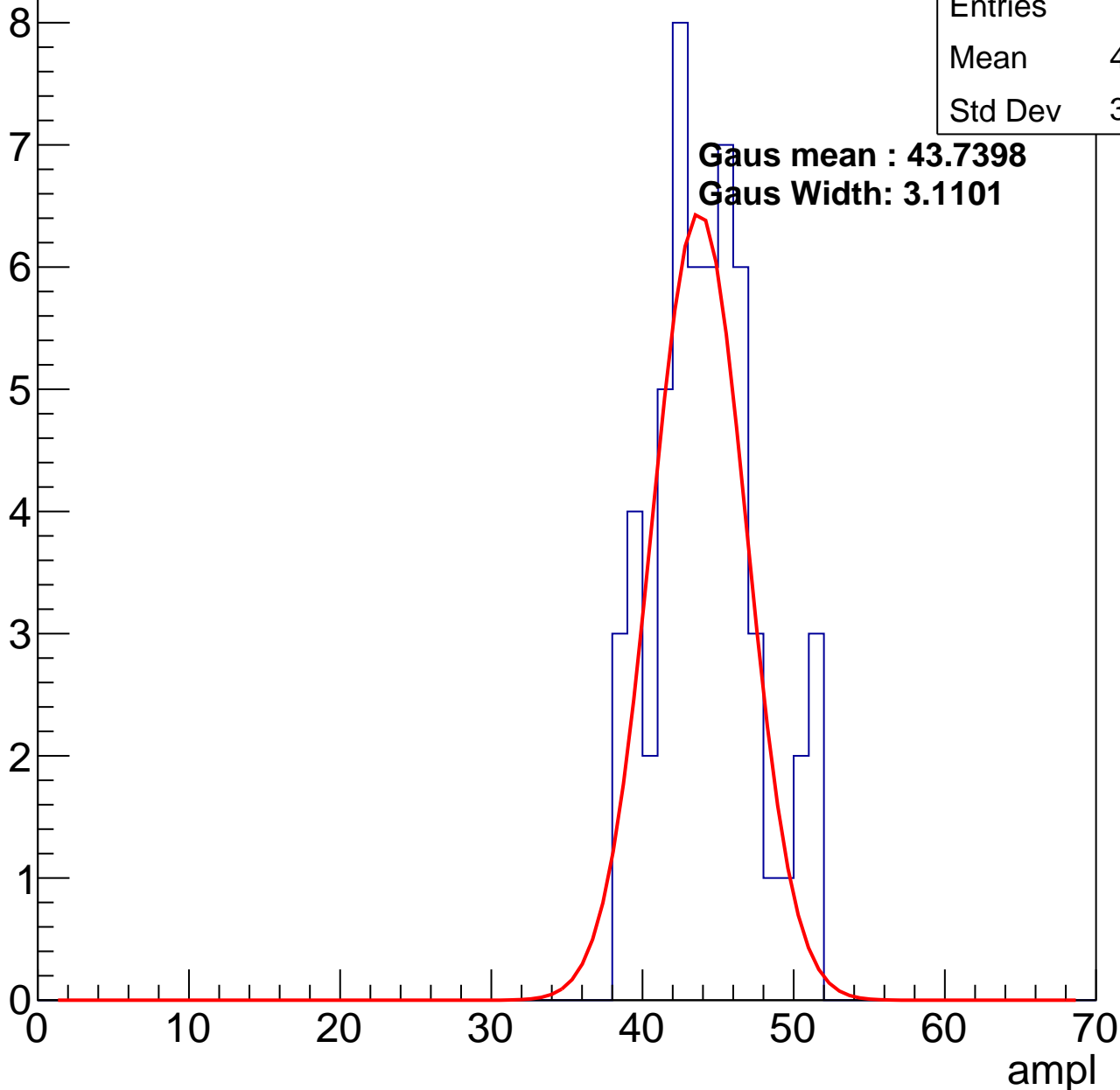
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.77
Std Dev	3.346

Gaus mean : 43.7398

Gaus Width: 3.1101

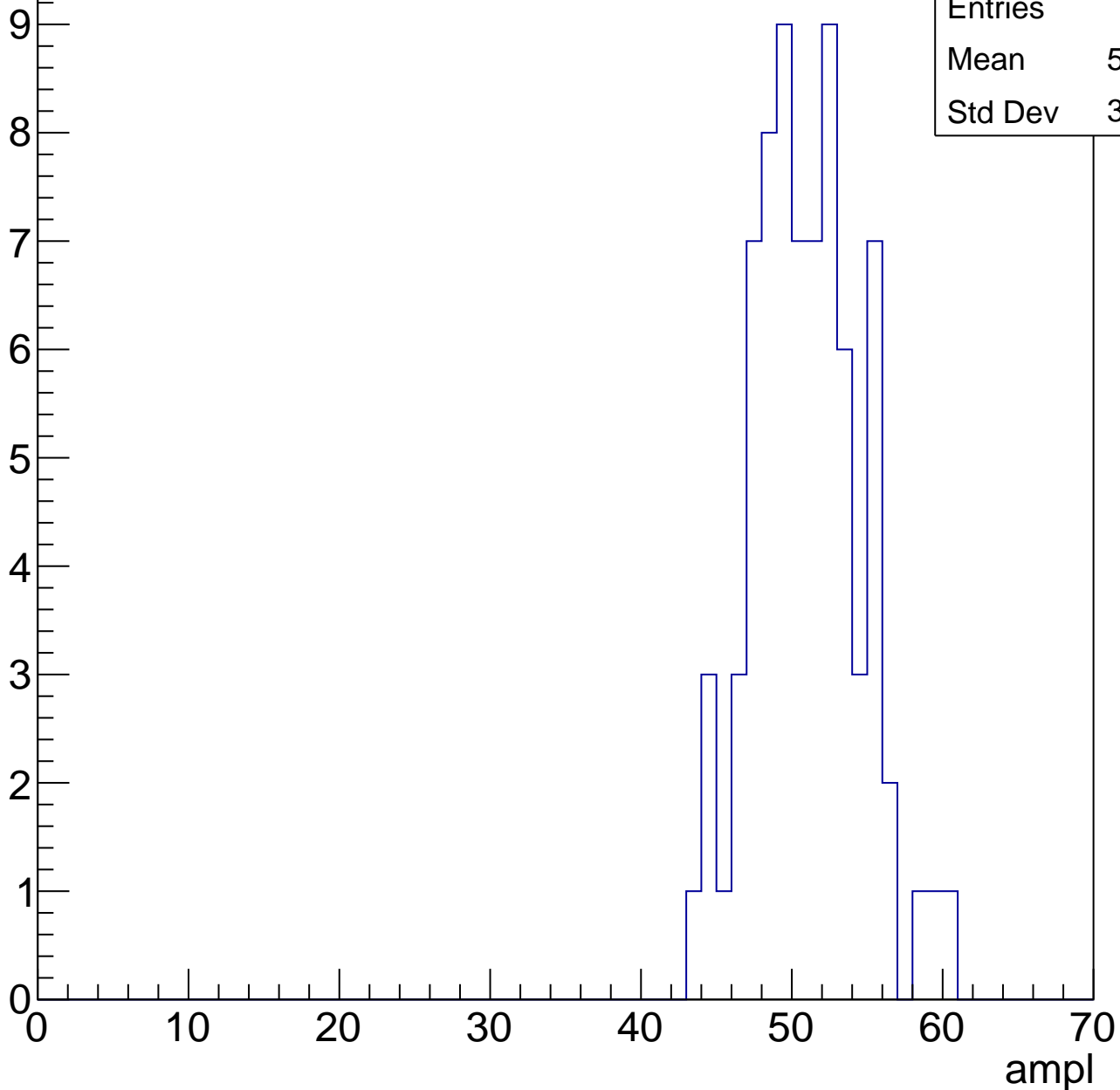


B1L103S, U17-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	50.54
Std Dev	3.533

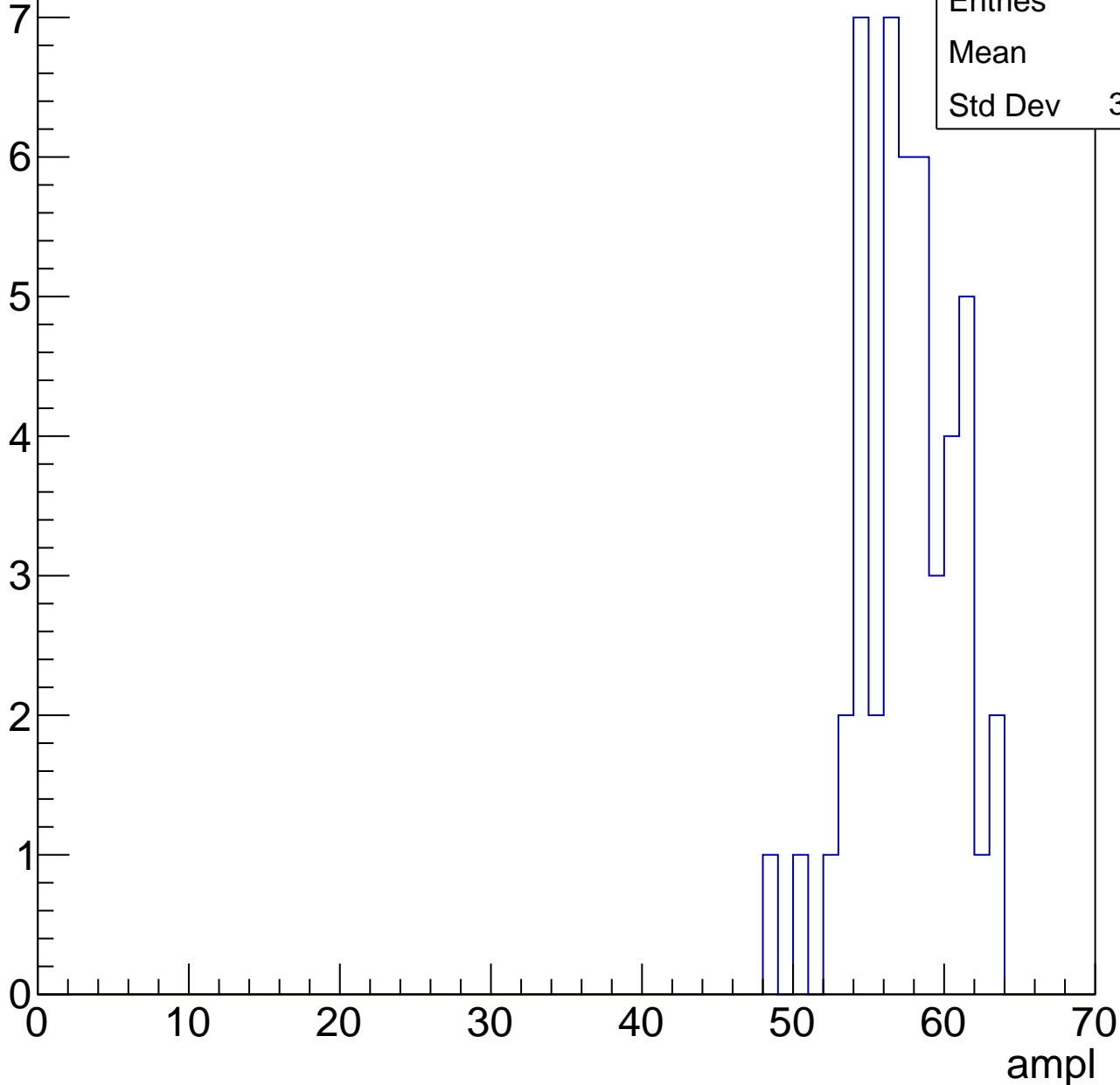


B1L103S, U17-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	57
Std Dev	3.215

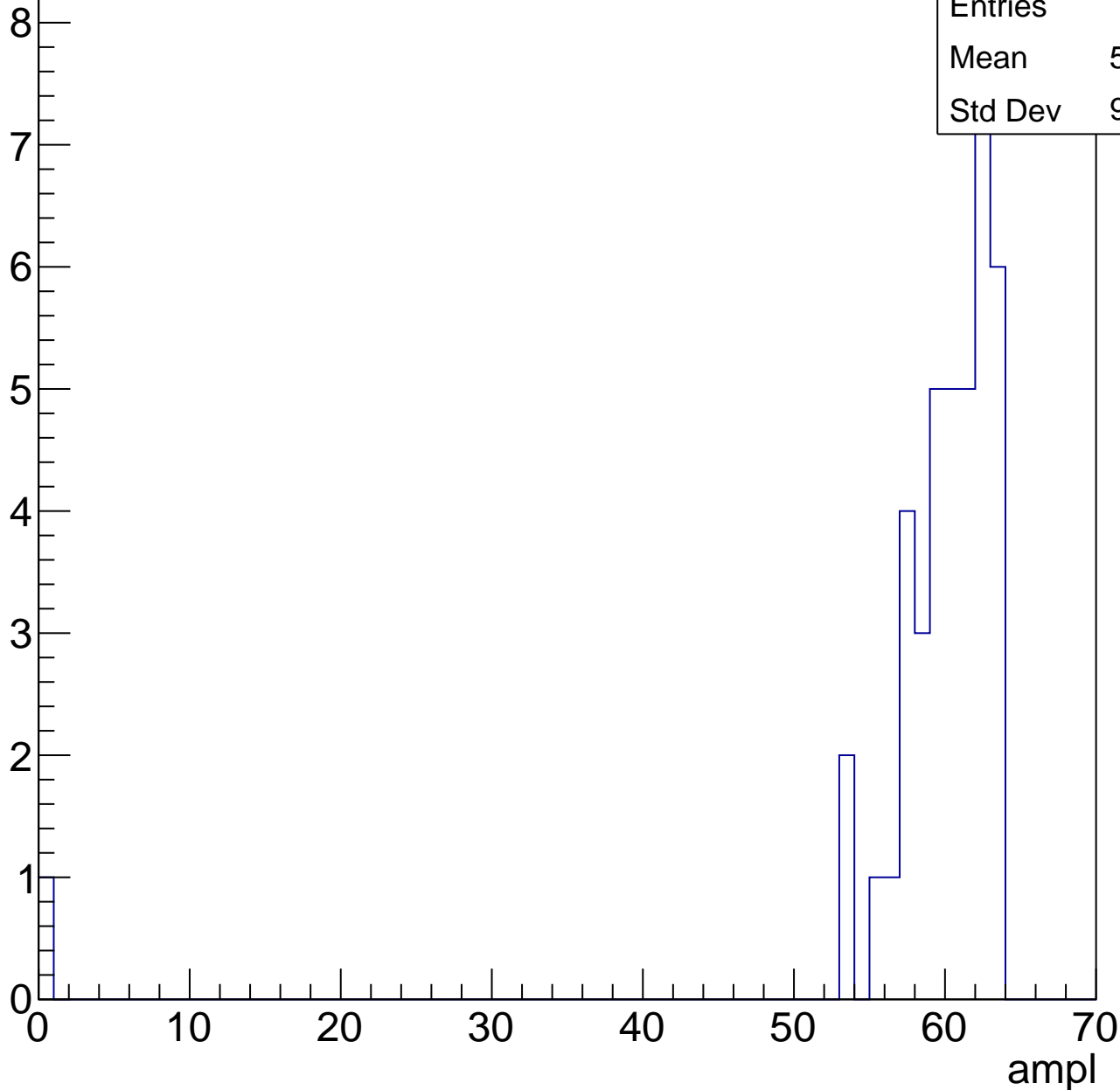


B1L103S, U17-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	58.37
Std Dev	9.594



B1L103S, U17-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

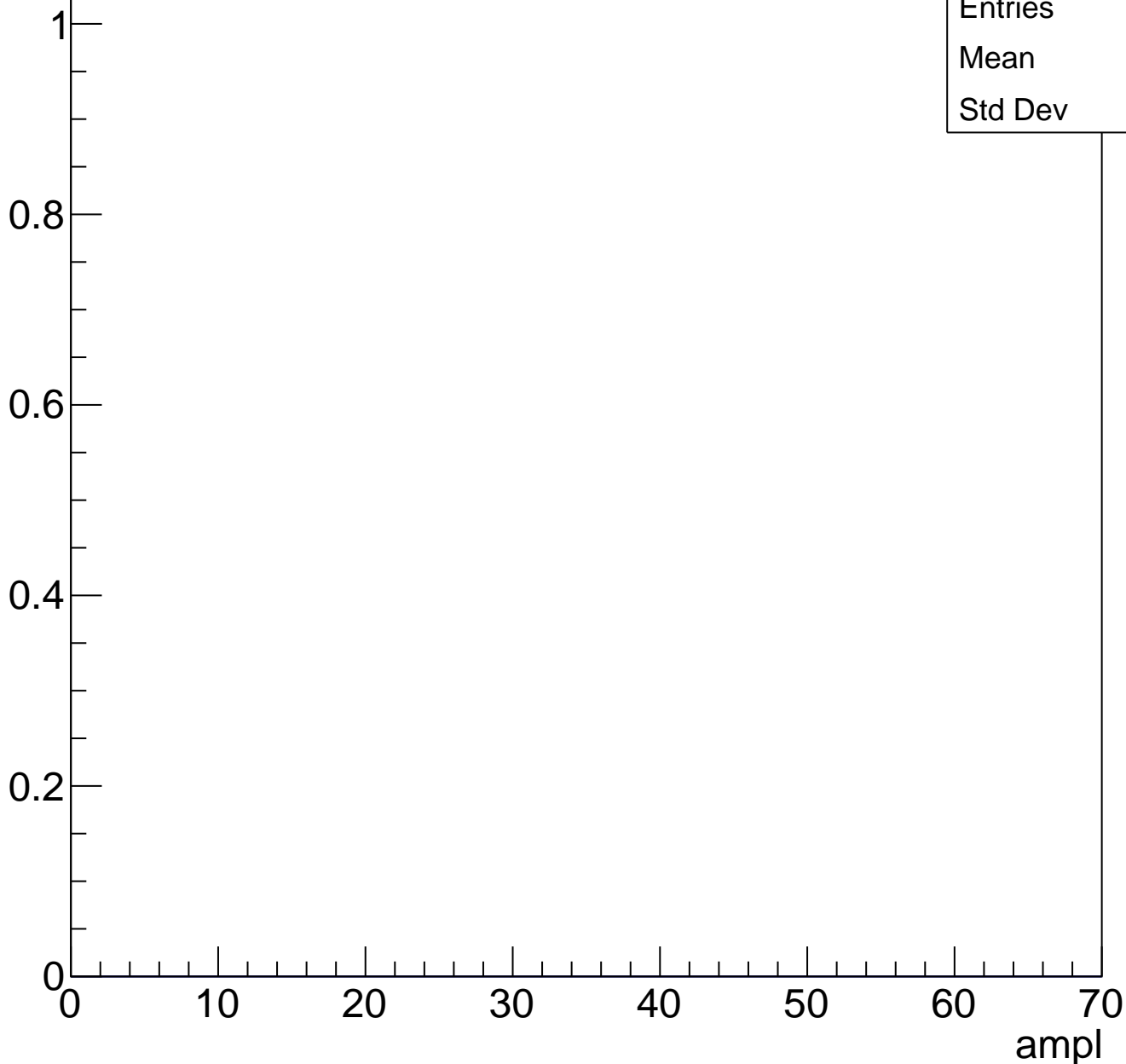
Entry



B1L103S, U17-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch41, adc0

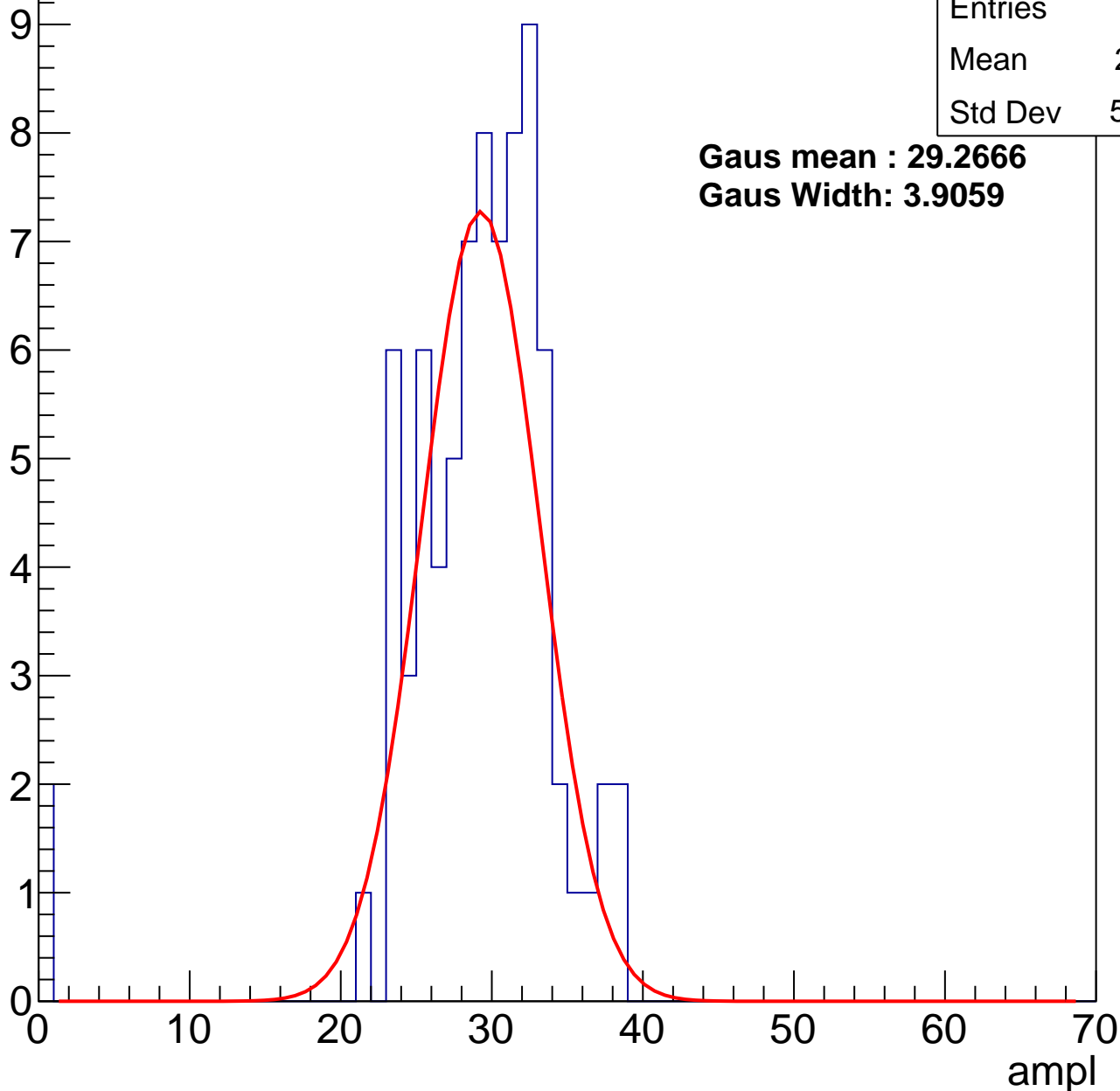
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	28.51
Std Dev	5.935

Gaus mean : 29.2666

Gaus Width: 3.9059



B1L103S, U17-ch41, adc1

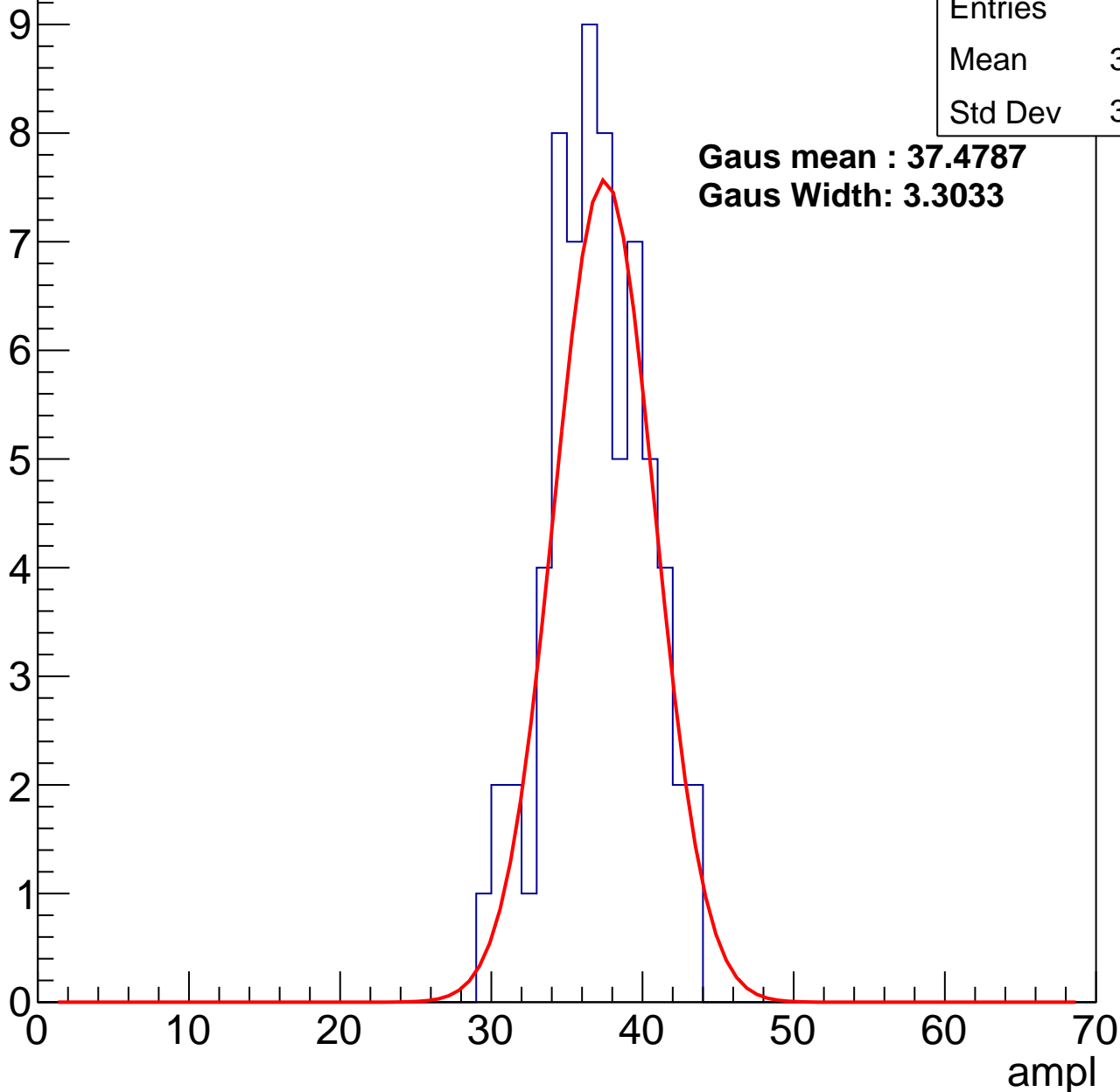
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.55
Std Dev	3.192

Gaus mean : 37.4787

Gaus Width: 3.3033



B1L103S, U17-ch41, adc2

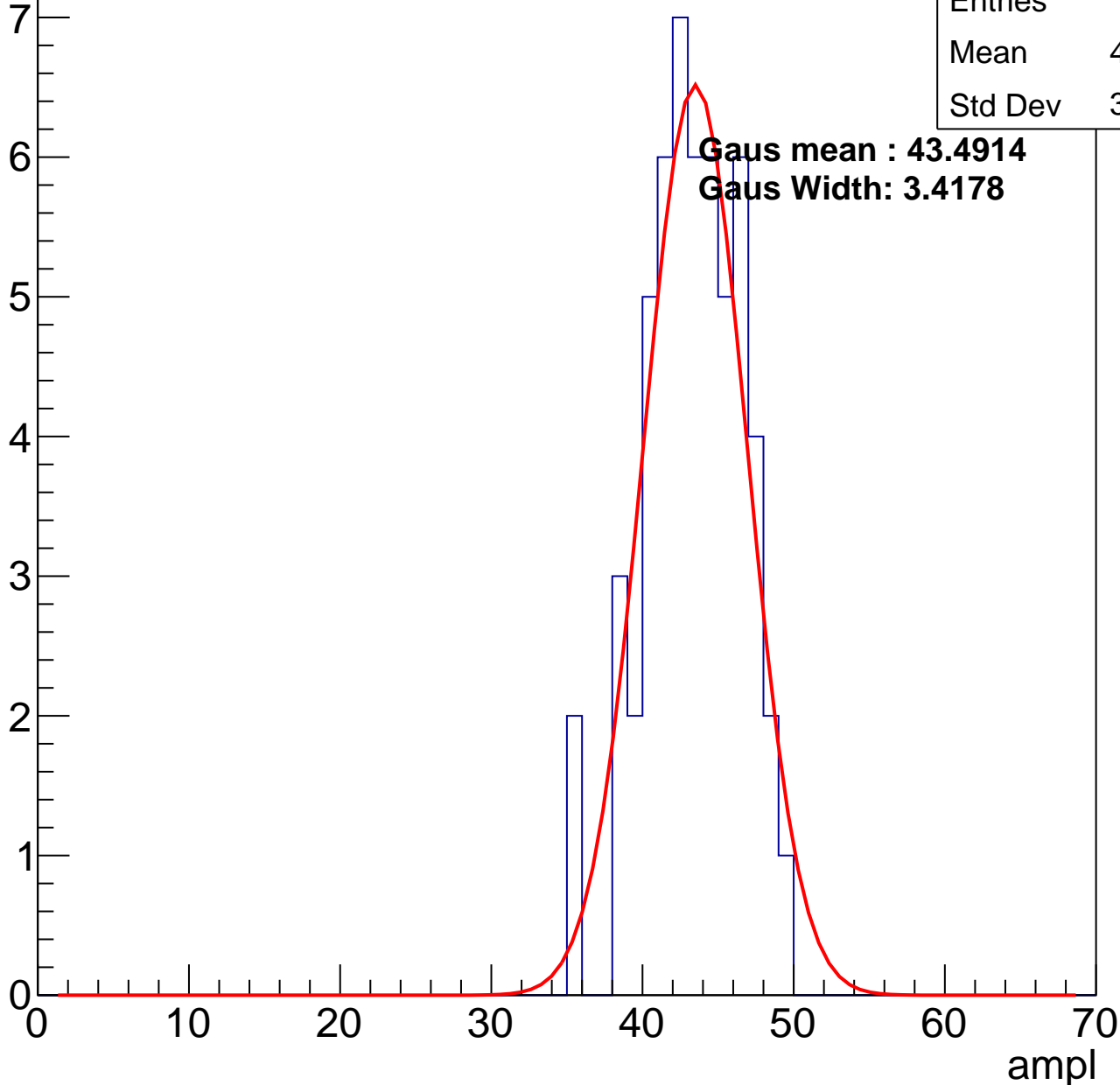
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.87
Std Dev	3.134

Gaus mean : 43.4914

Gaus Width: 3.4178

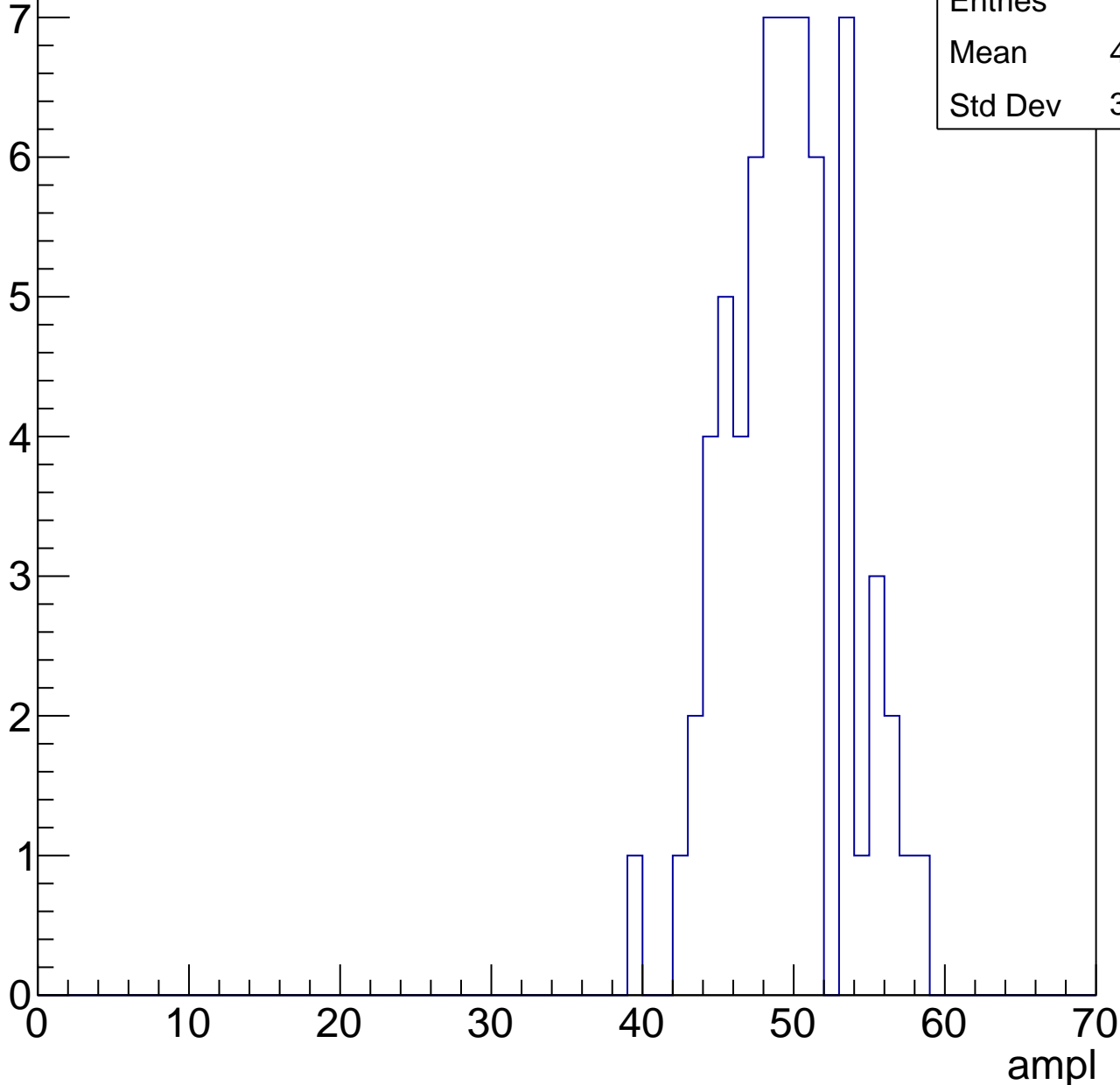


B1L103S, U17-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.02
Std Dev	3.897

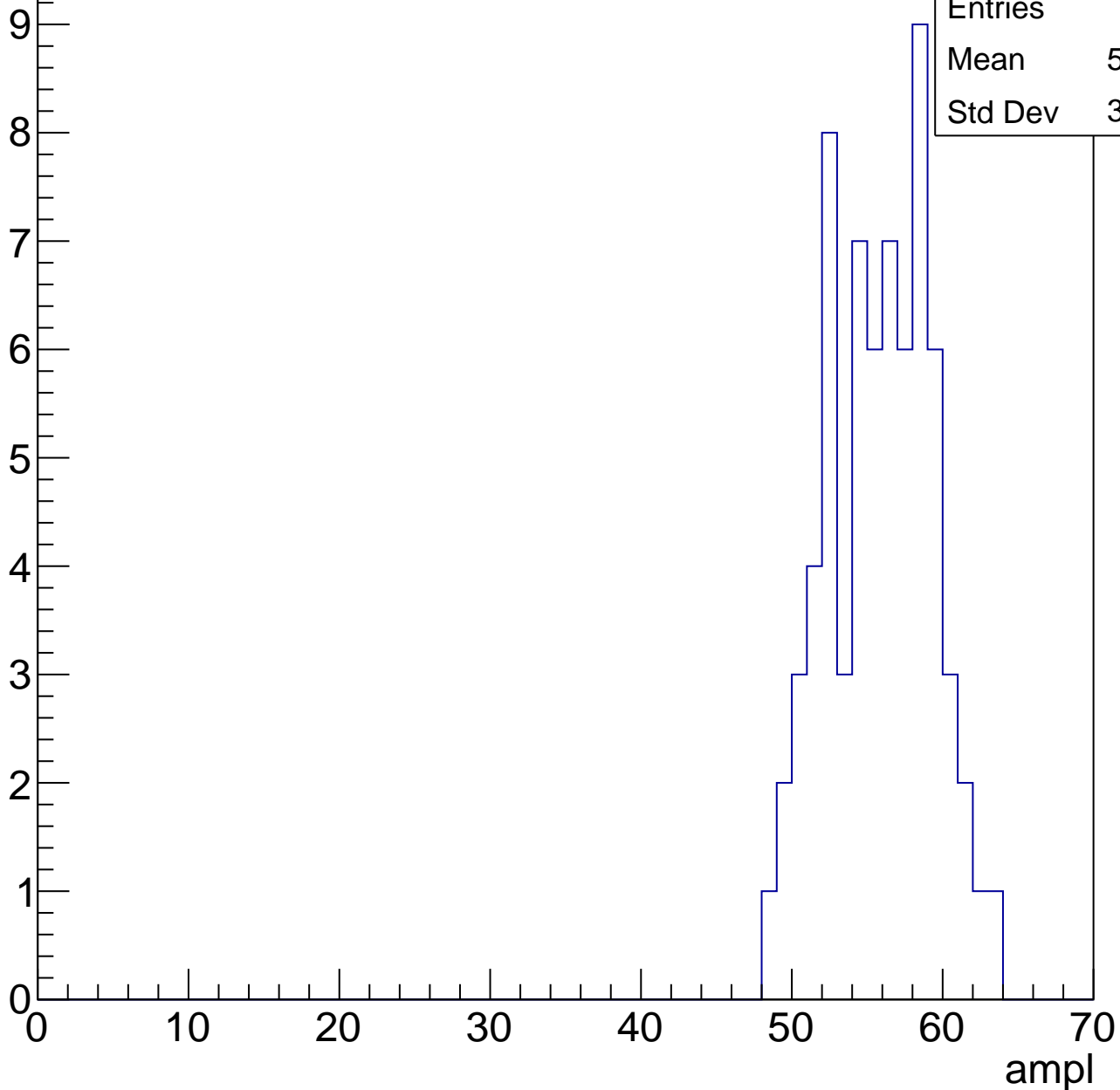


B1L103S, U17-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	55.36
Std Dev	3.426

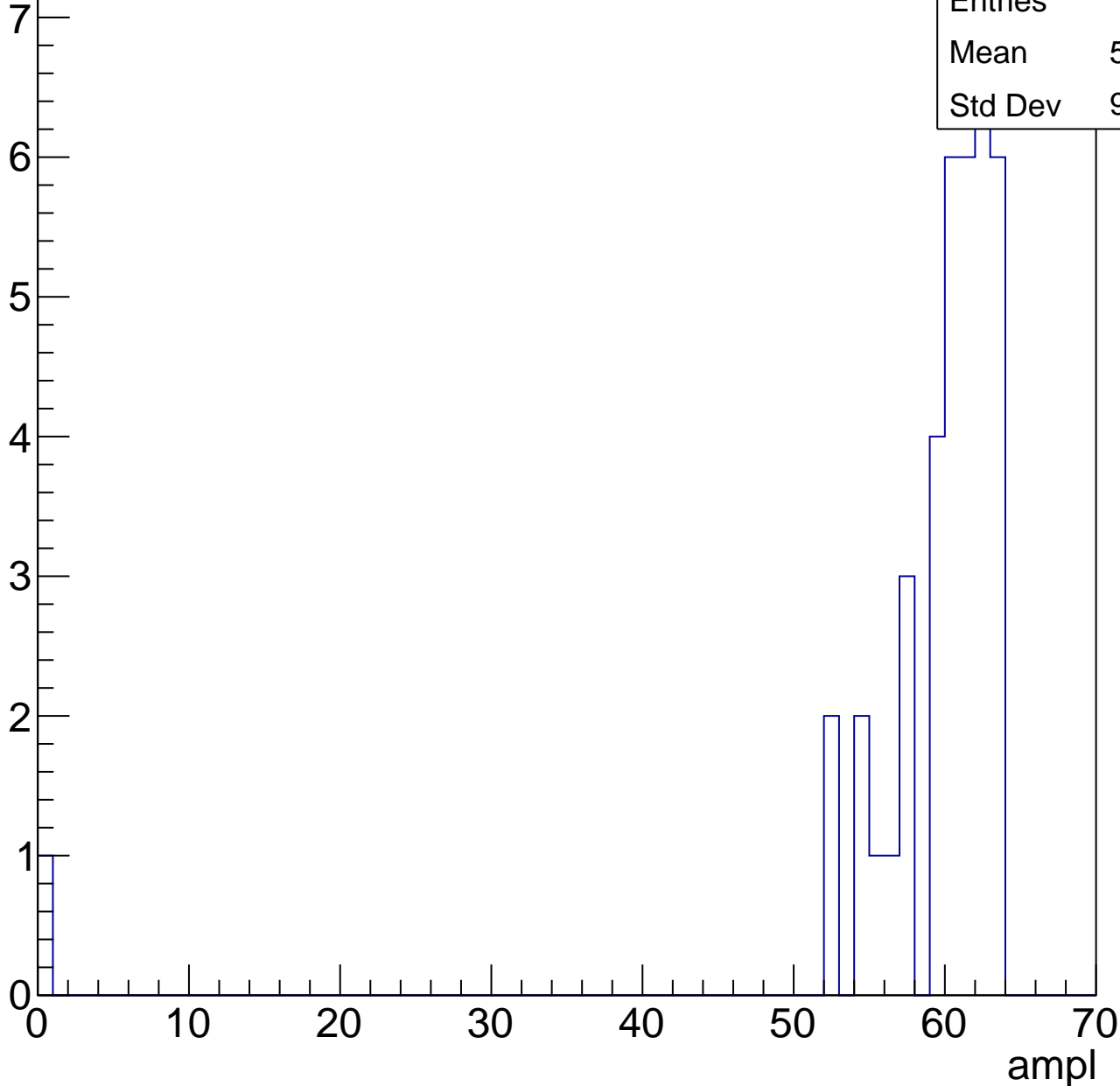


B1L103S, U17-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.15
Std Dev	9.906



B1L103S, U17-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch42, adc0

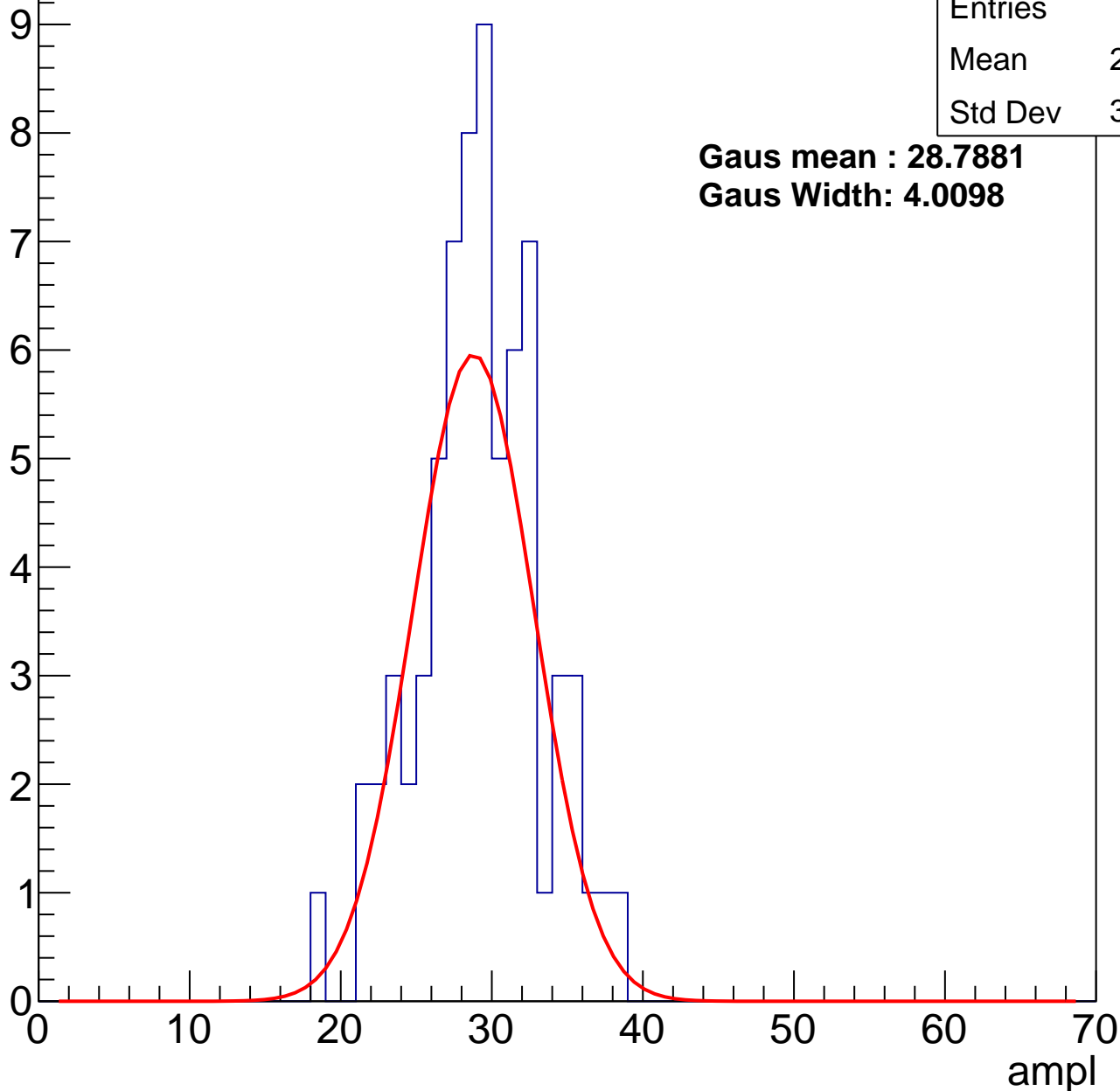
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.73
Std Dev	3.989

Gaus mean : 28.7881

Gaus Width: 4.0098



B1L103S, U17-ch42, adc1

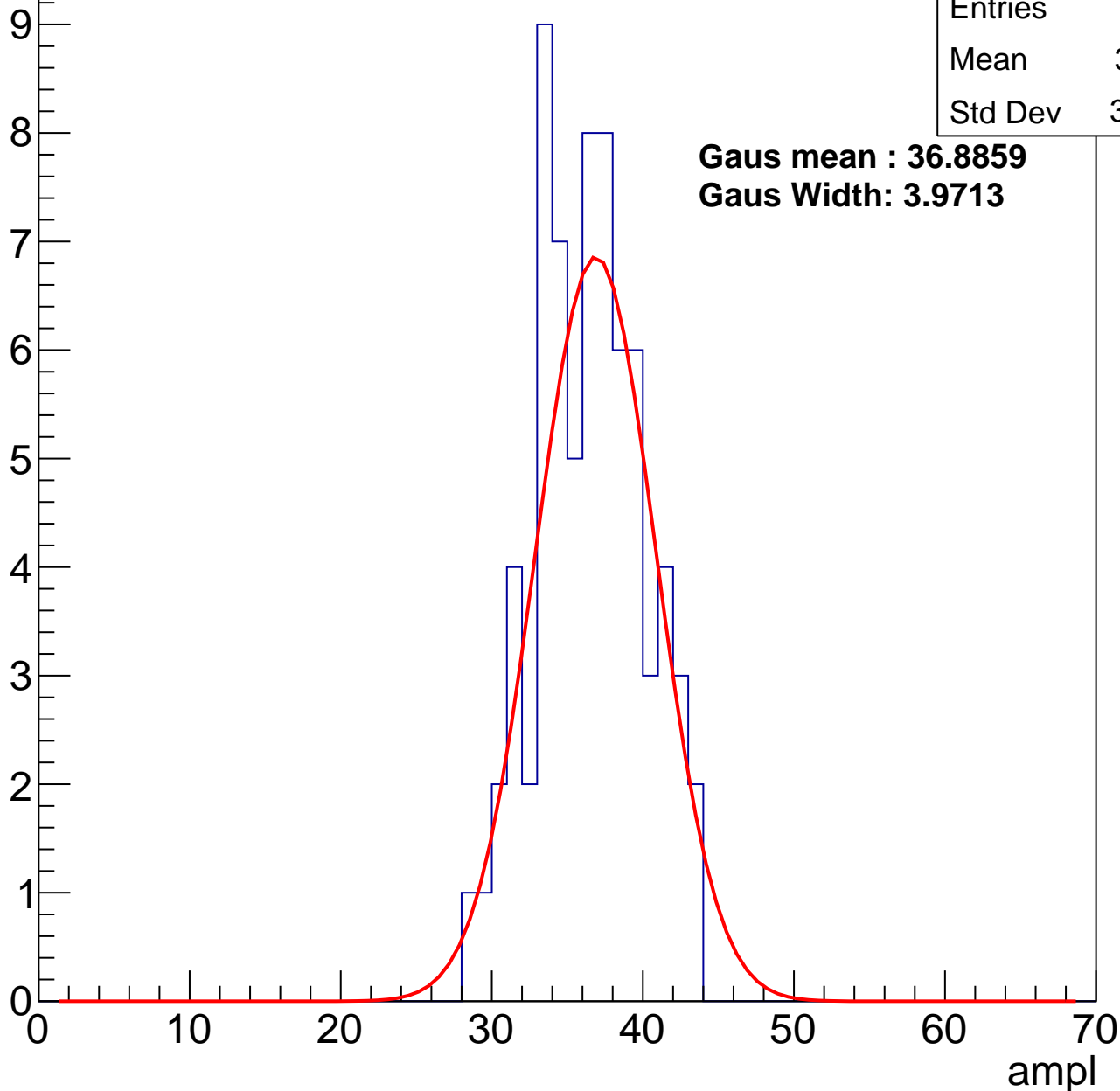
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.01
Std Dev	3.502

Gaus mean : 36.8859

Gaus Width: 3.9713



B1L103S, U17-ch42, adc2

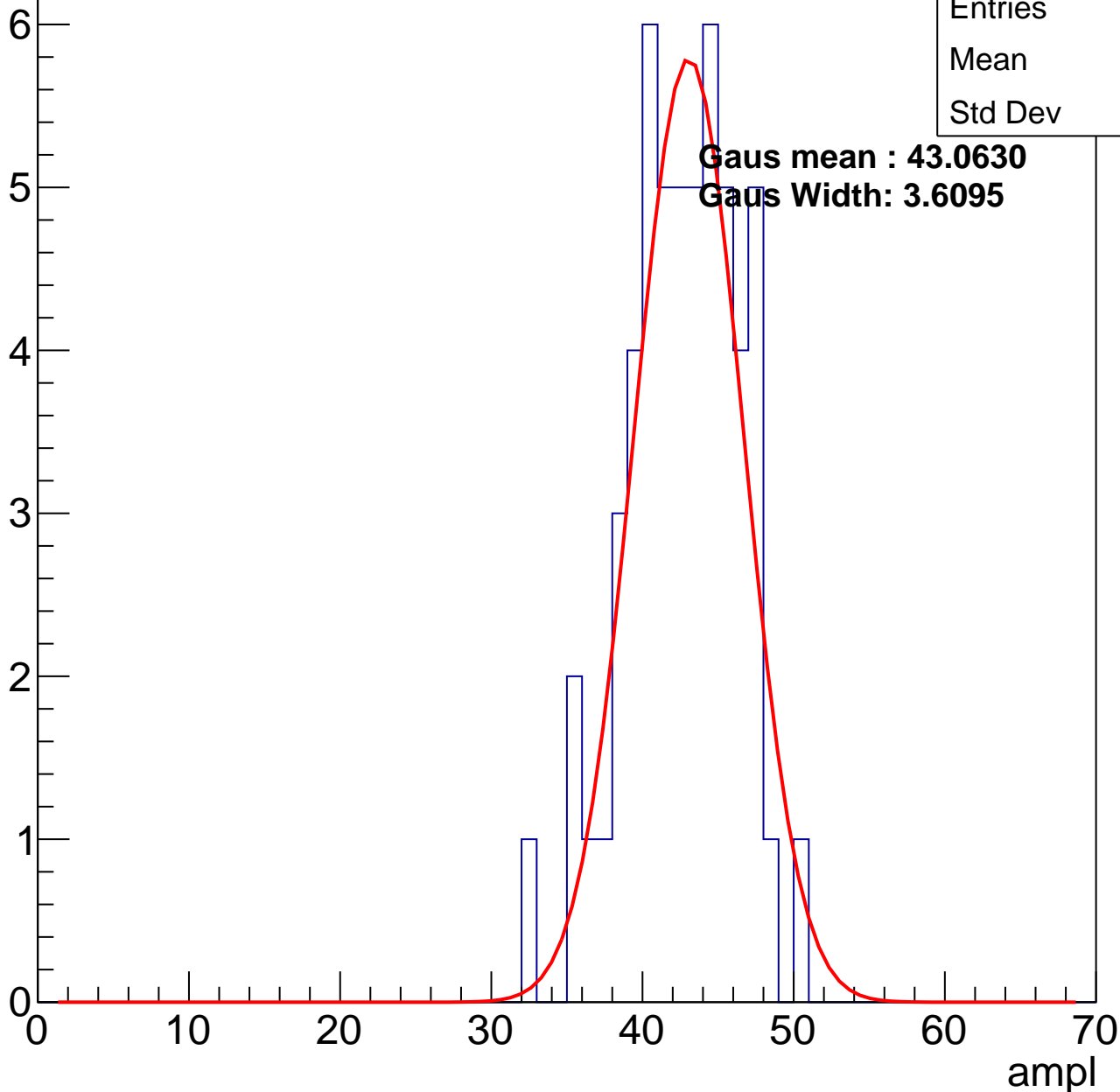
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.2
Std Dev	3.65

Gaus mean : 43.0630

Gaus Width: 3.6095

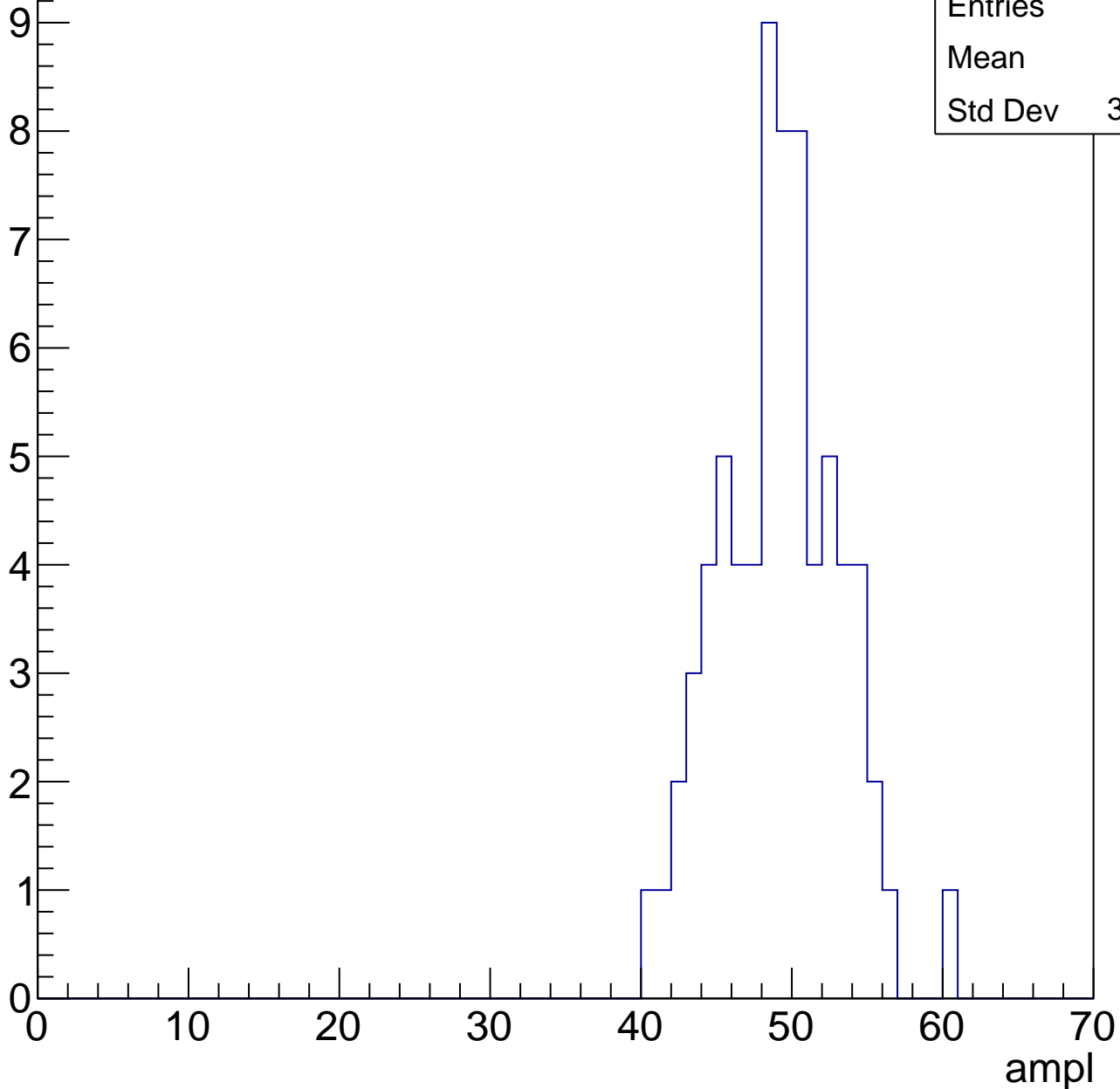


B1L103S, U17-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	48.7
Std Dev	3.893

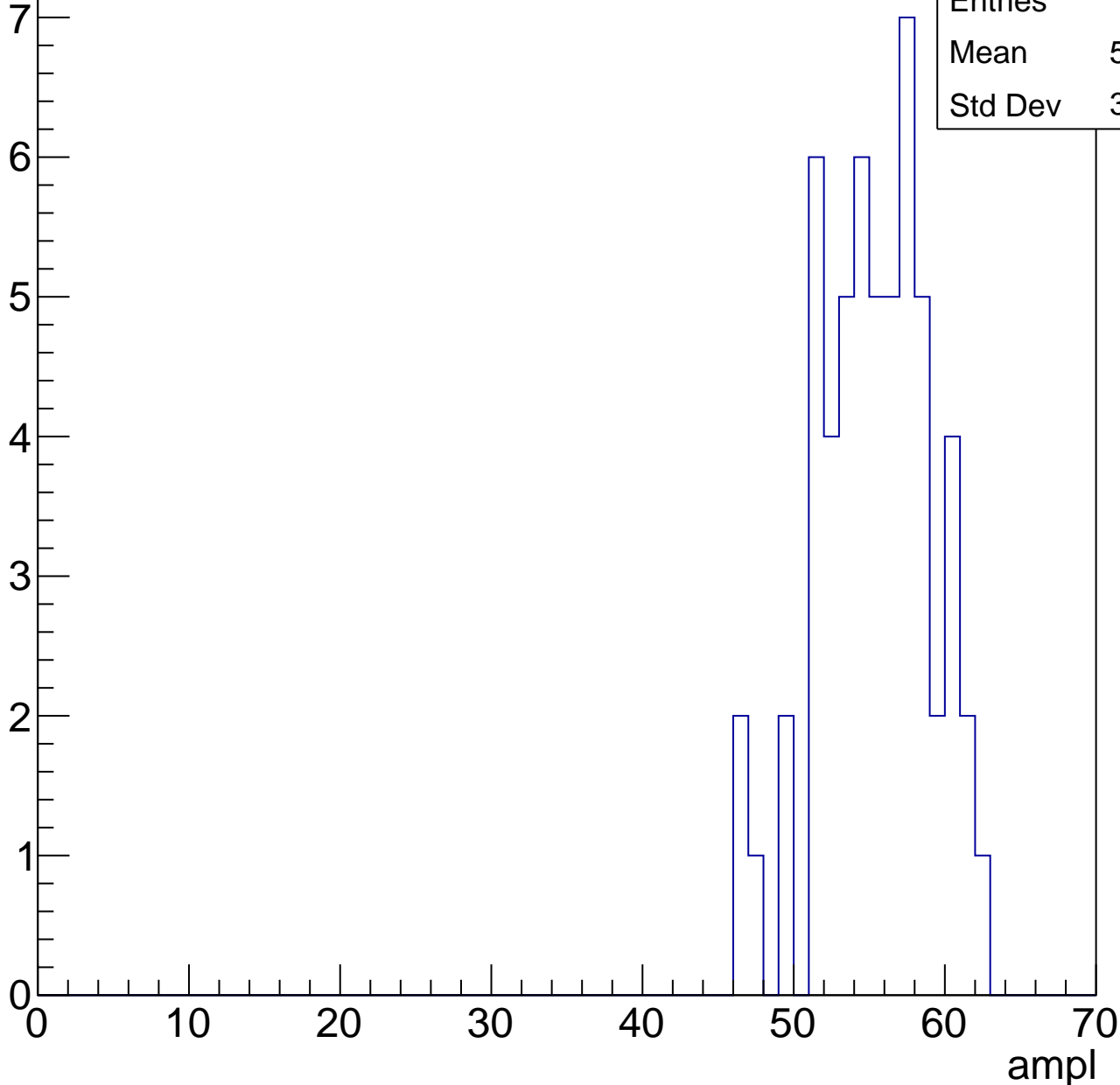


B1L103S, U17-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.84
Std Dev	3.712



B1L103S, U17-ch42, adc5

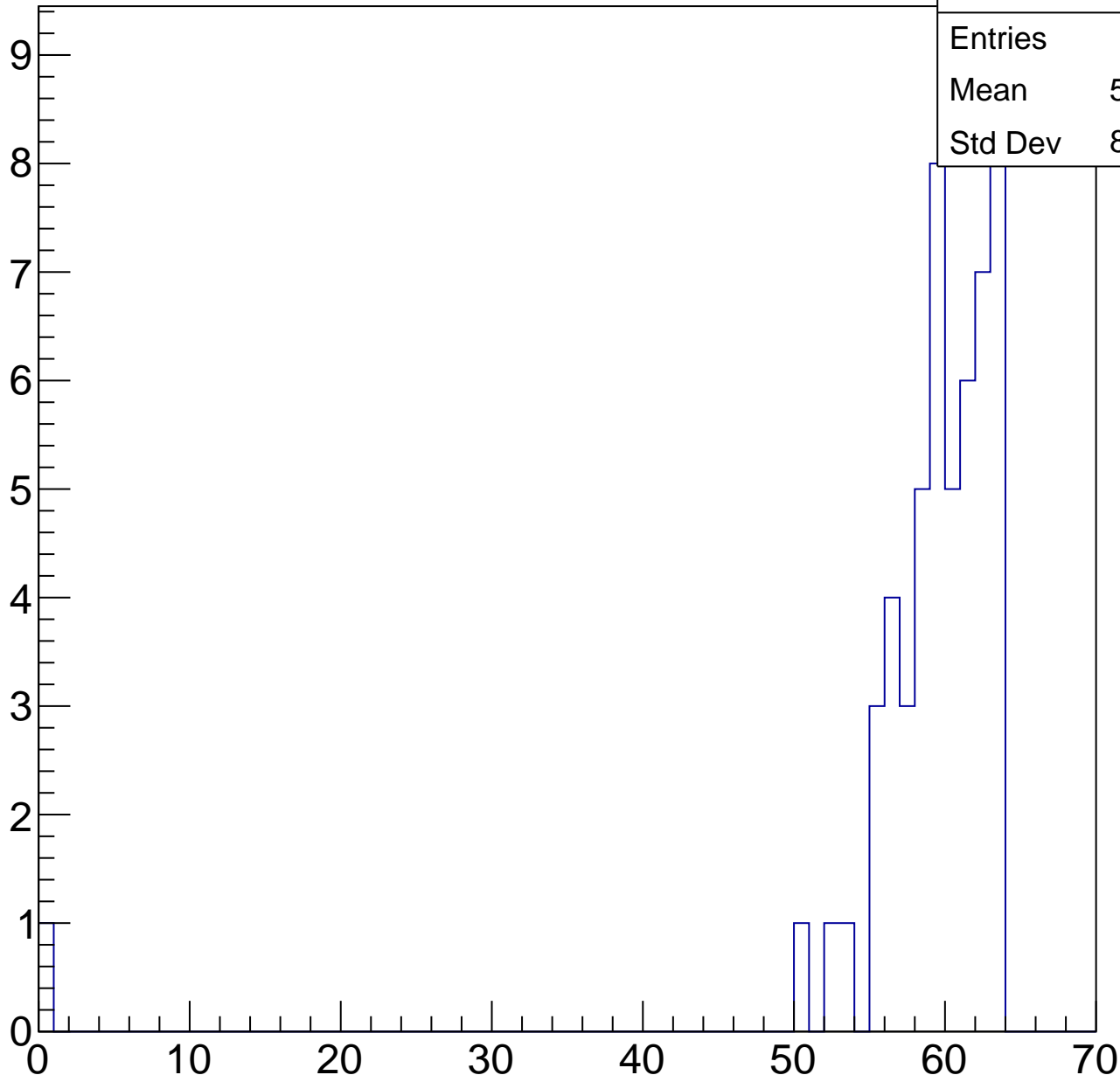
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.22
Std Dev	8.554

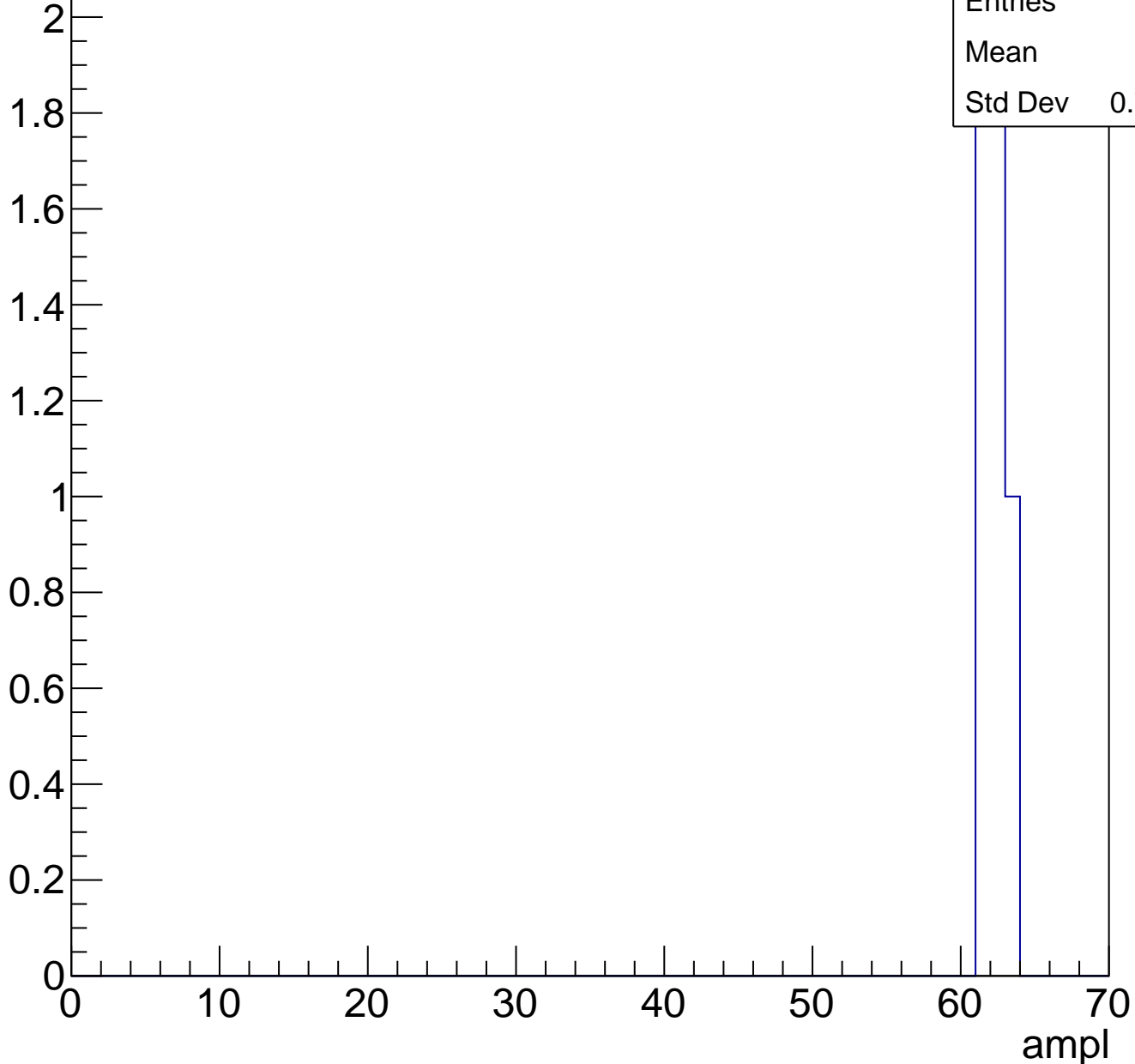
ampl



B1L103S, U17-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	61.8
Std Dev	0.7483

B1L103S, U17-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch43, adc0

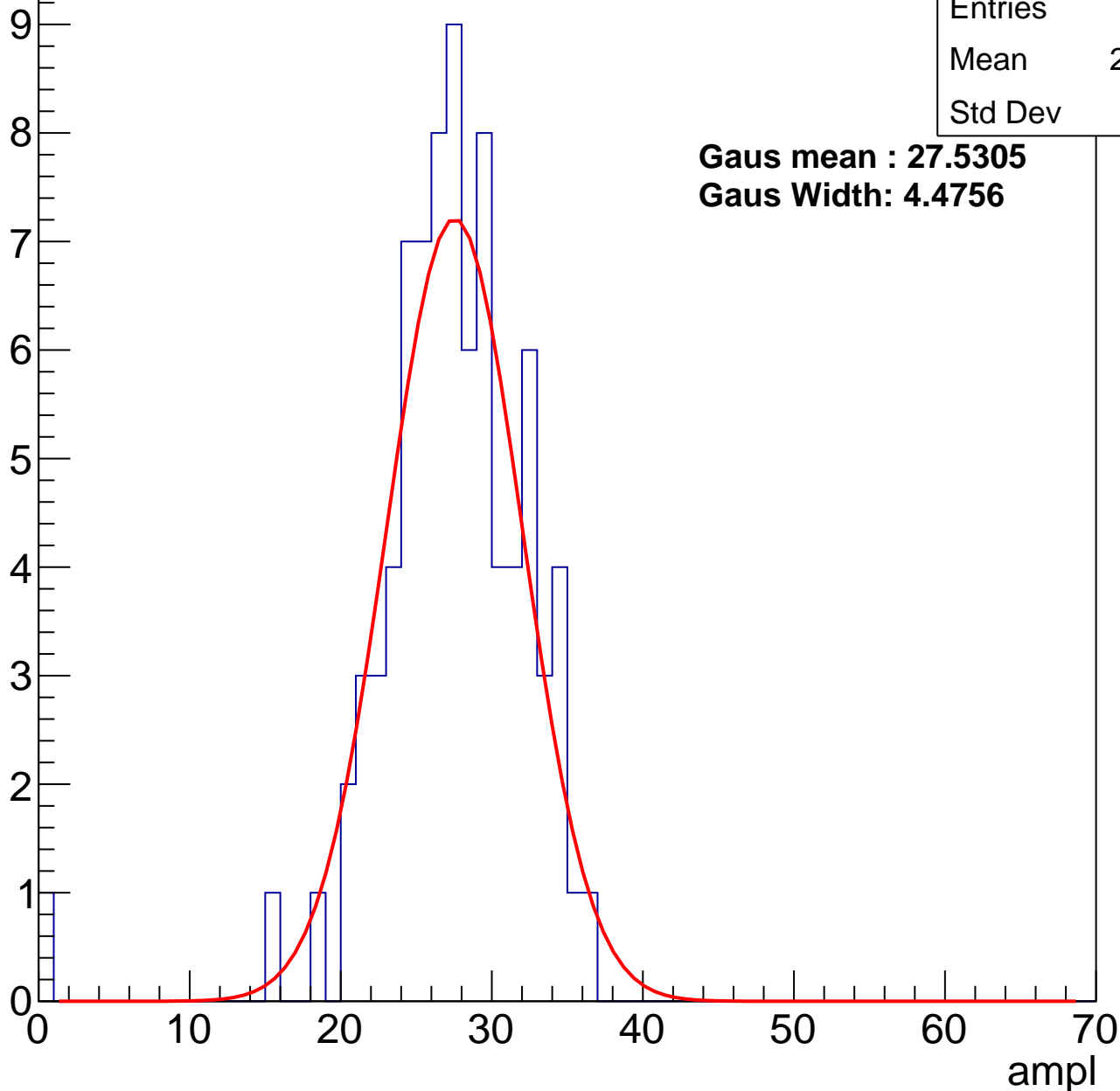
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	26.87
Std Dev	5.07

Gaus mean : 27.5305

Gaus Width: 4.4756



B1L103S, U17-ch43, adc1

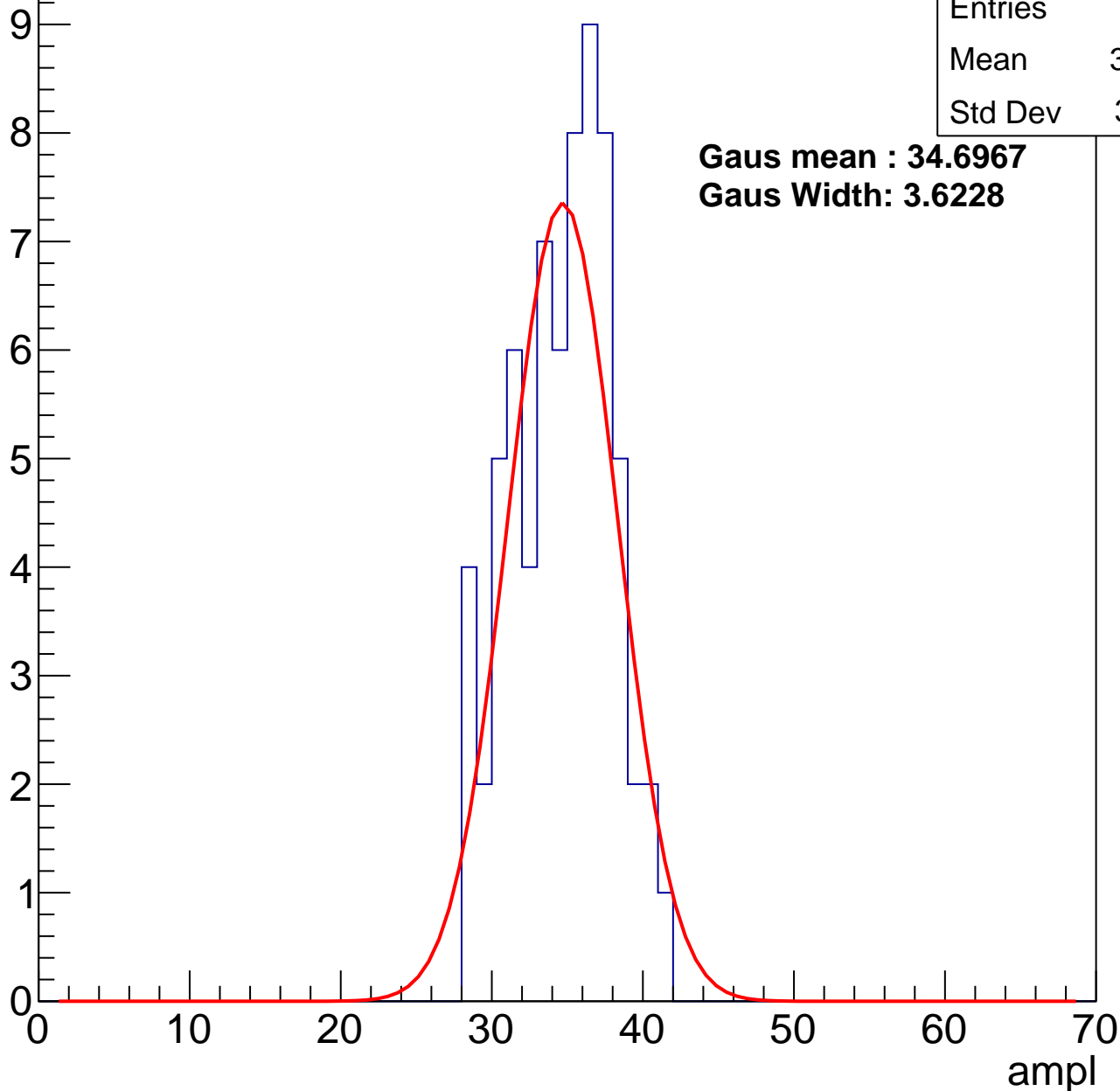
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.17
Std Dev	3.221

Gaus mean : 34.6967

Gaus Width: 3.6228



B1L103S, U17-ch43, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.56
Std Dev	2.733

Gaus mean : 41.8200

Gaus Width: 2.9745

10

8

6

4

2

0

0

10

20

30

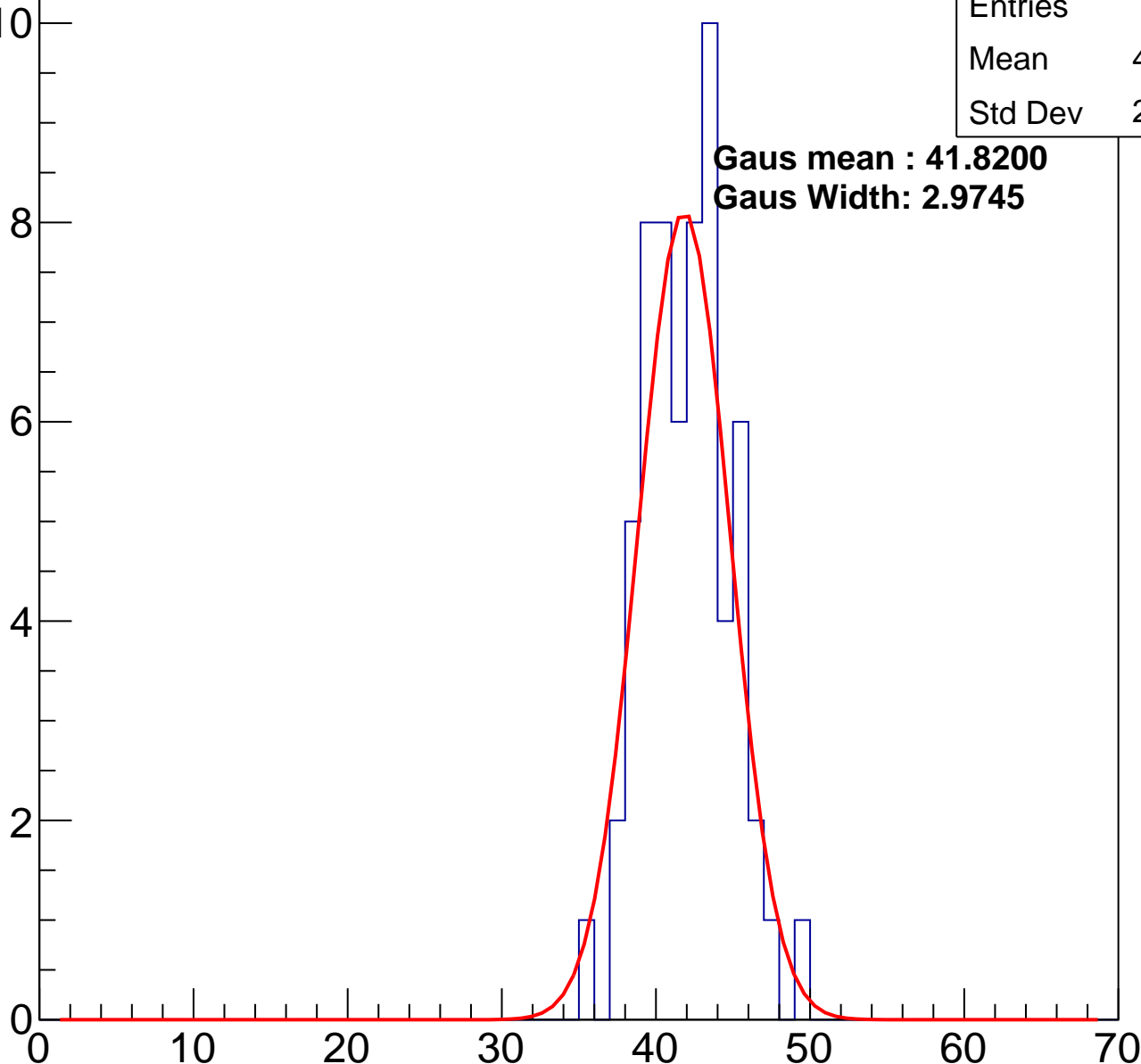
40

50

60

70

ampl

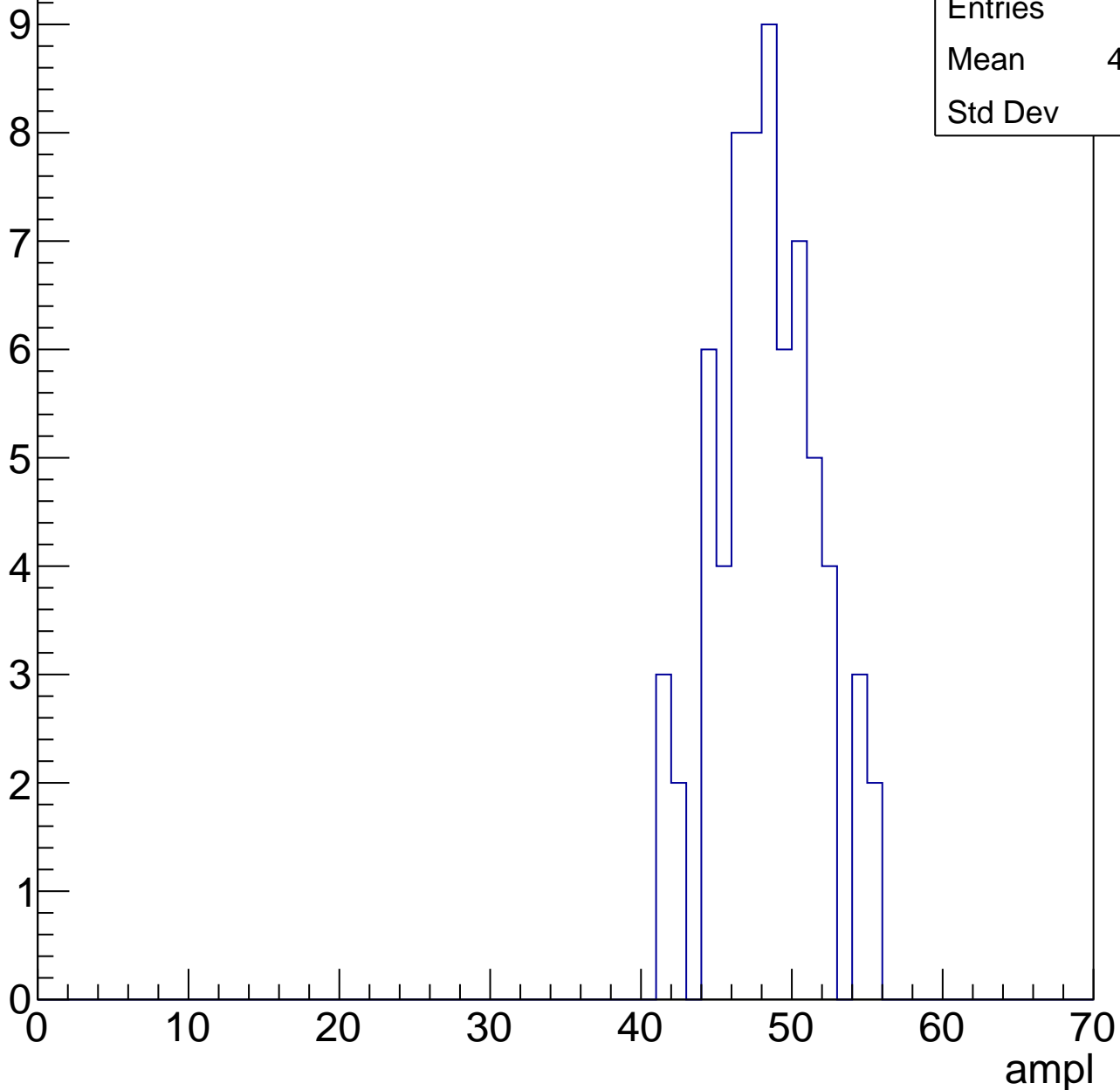


B1L103S, U17-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	47.85
Std Dev	3.32

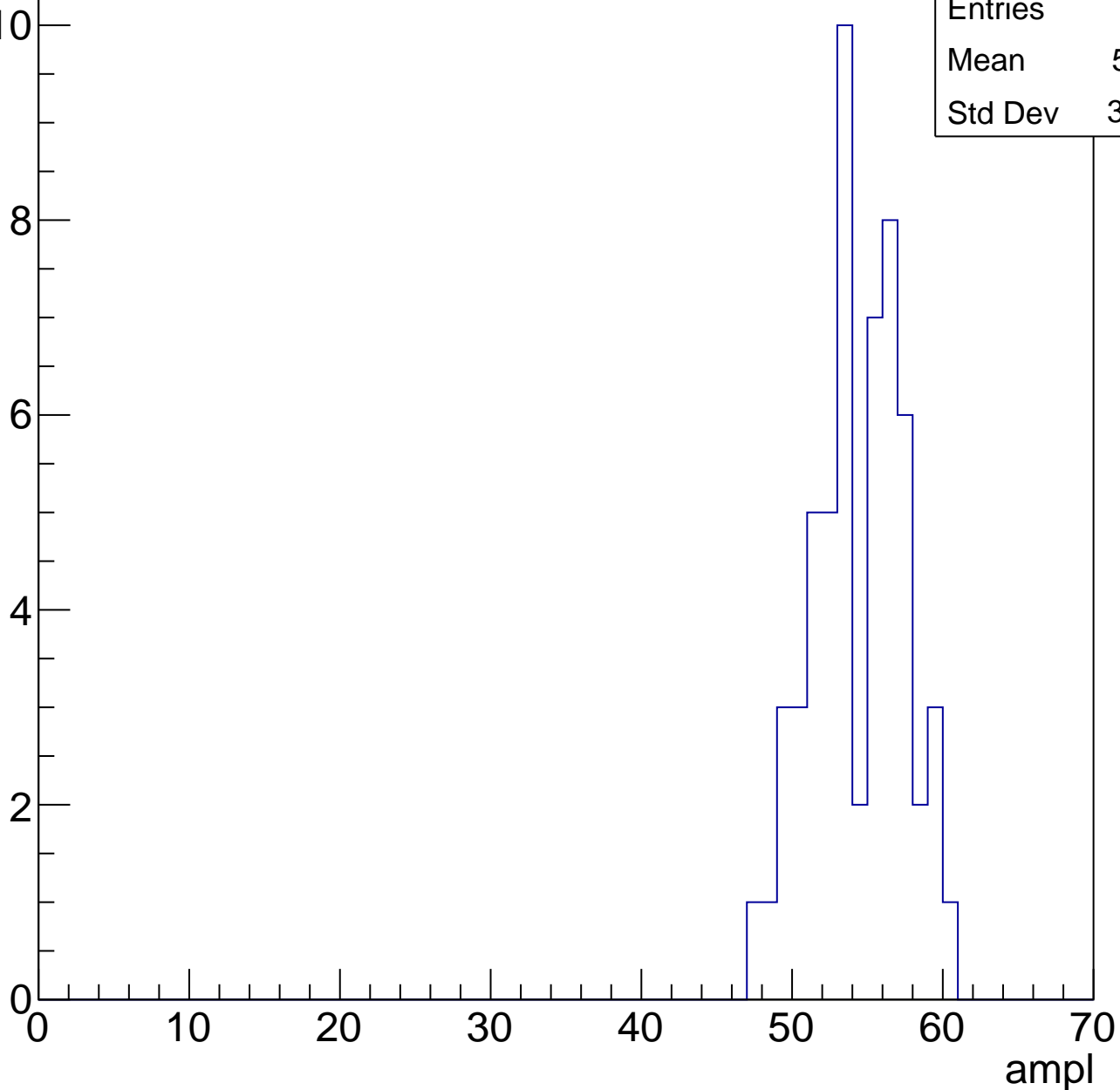


B1L103S, U17-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	53.91
Std Dev	3.016

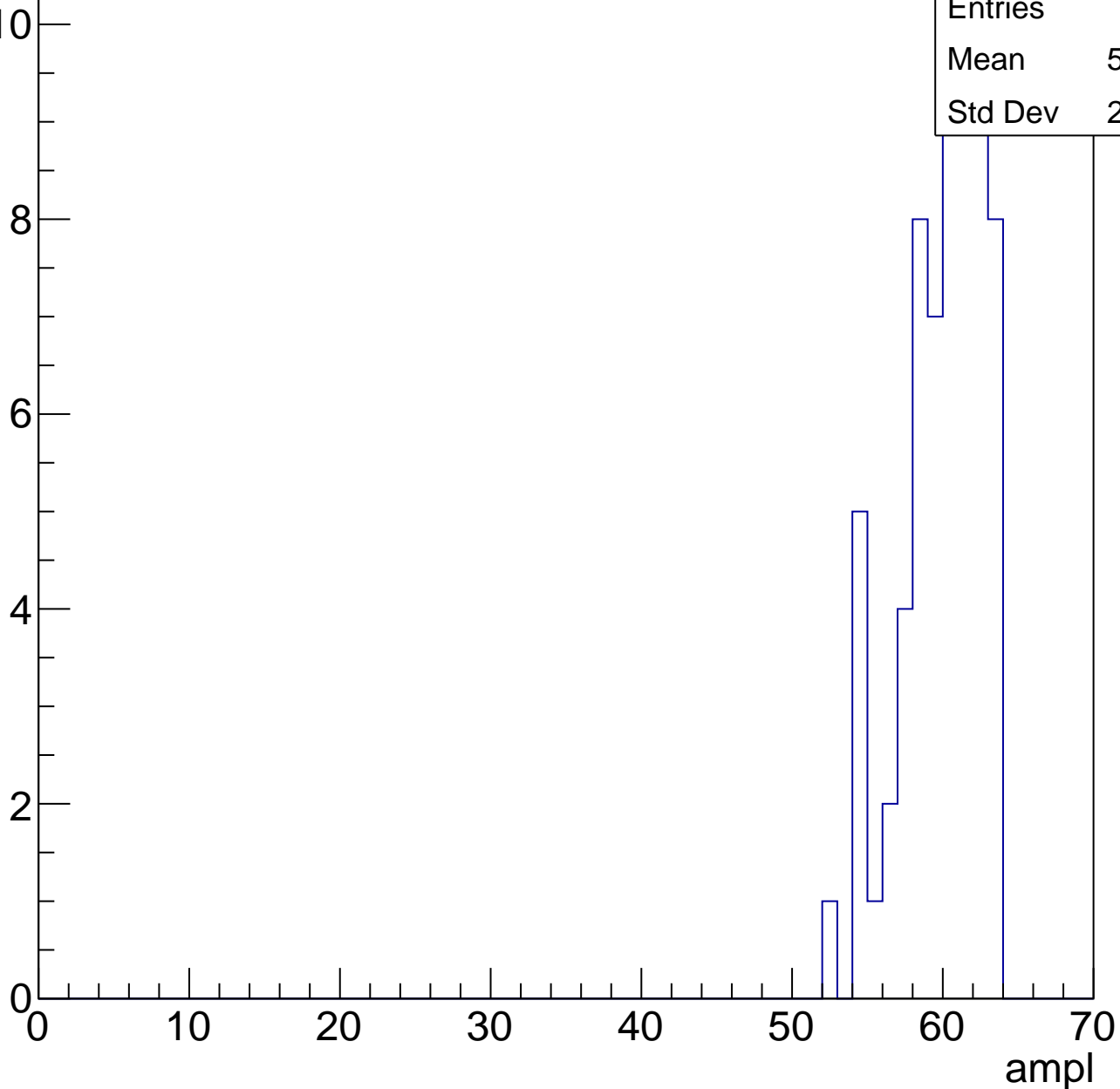


B1L103S, U17-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	59.47
Std Dev	2.727



B1L103S, U17-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch44, adc0

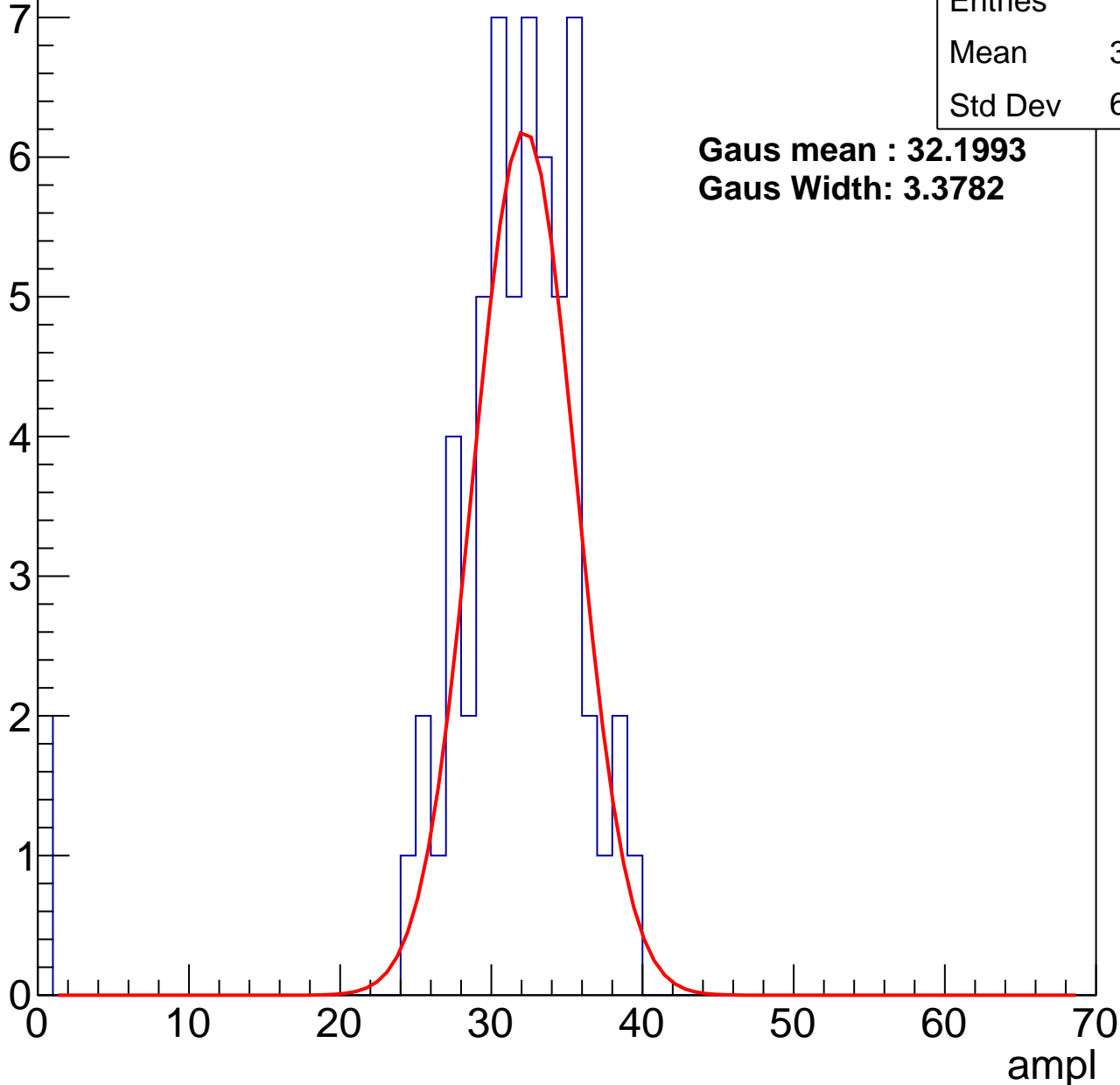
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	30.58
Std Dev	6.586

Gaus mean : 32.1993

Gaus Width: 3.3782



B1L103S, U17-ch44, adc1

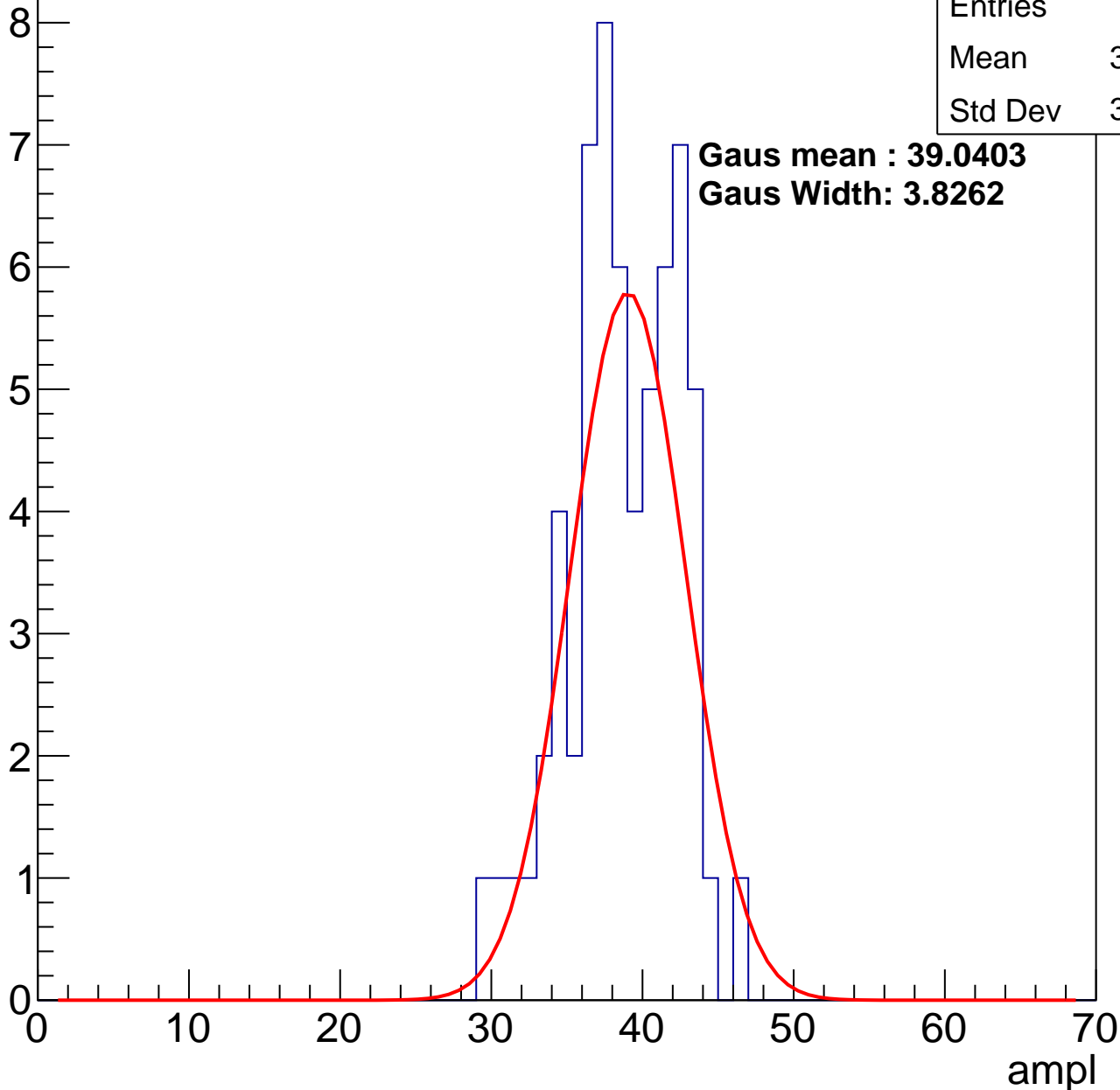
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	38.24
Std Dev	3.613

Gaus mean : 39.0403

Gaus Width: 3.8262



B1L103S, U17-ch44, adc2

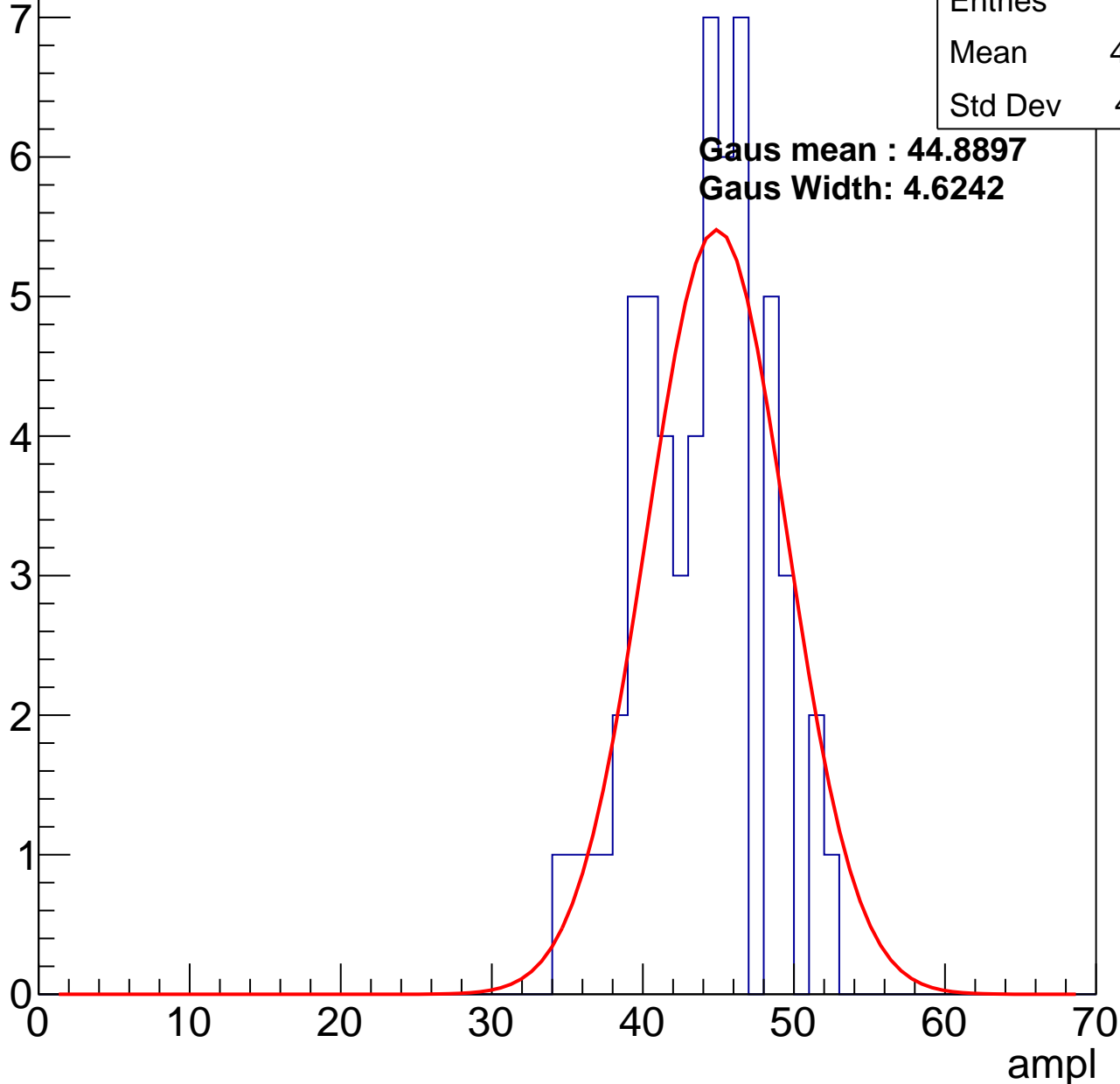
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.38
Std Dev	4.051

Gaus mean : 44.8897

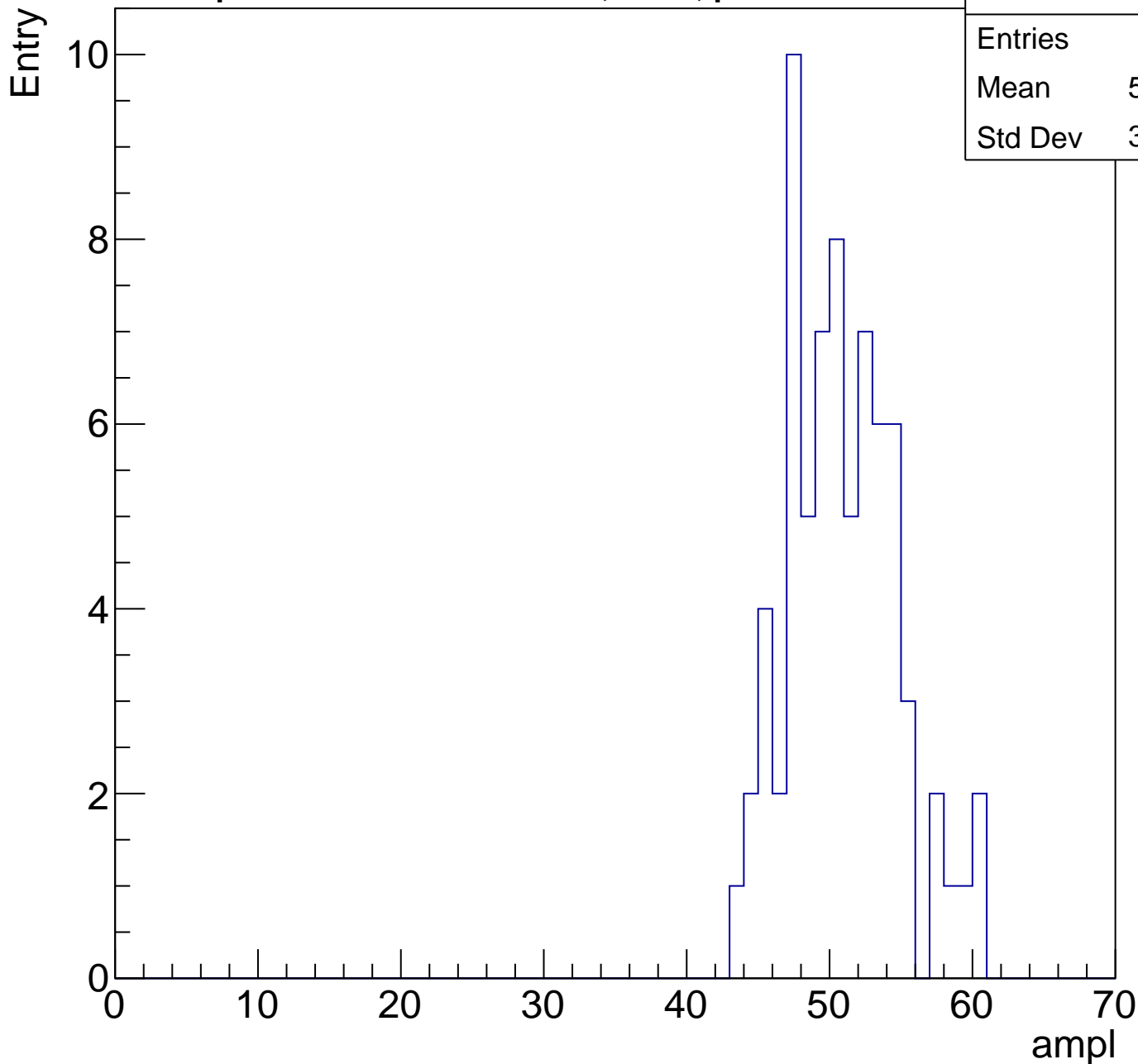
Gaus Width: 4.6242



B1L103S, U17-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	50.46
Std Dev	3.829

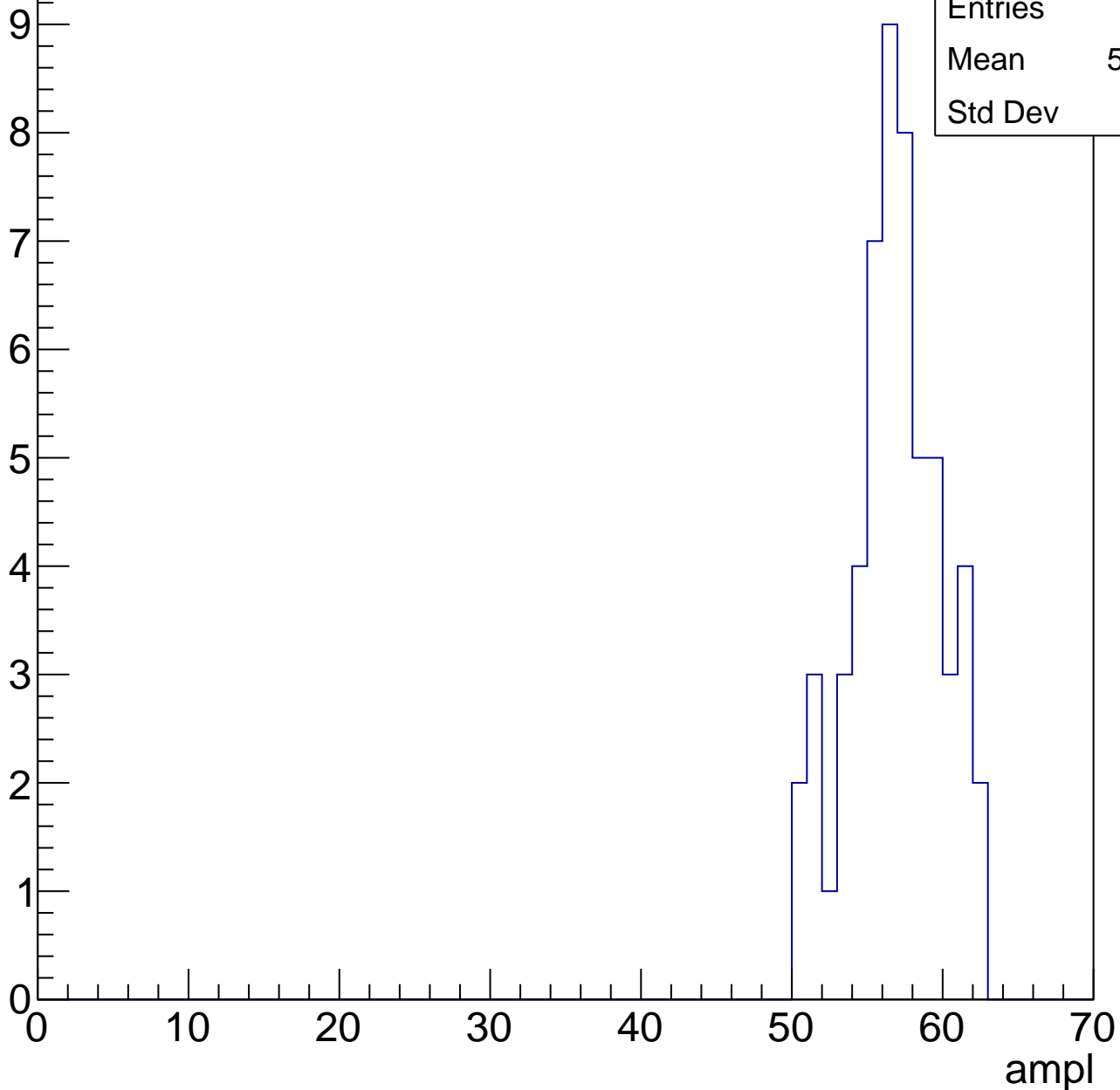


B1L103S, U17-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	56.39
Std Dev	2.98

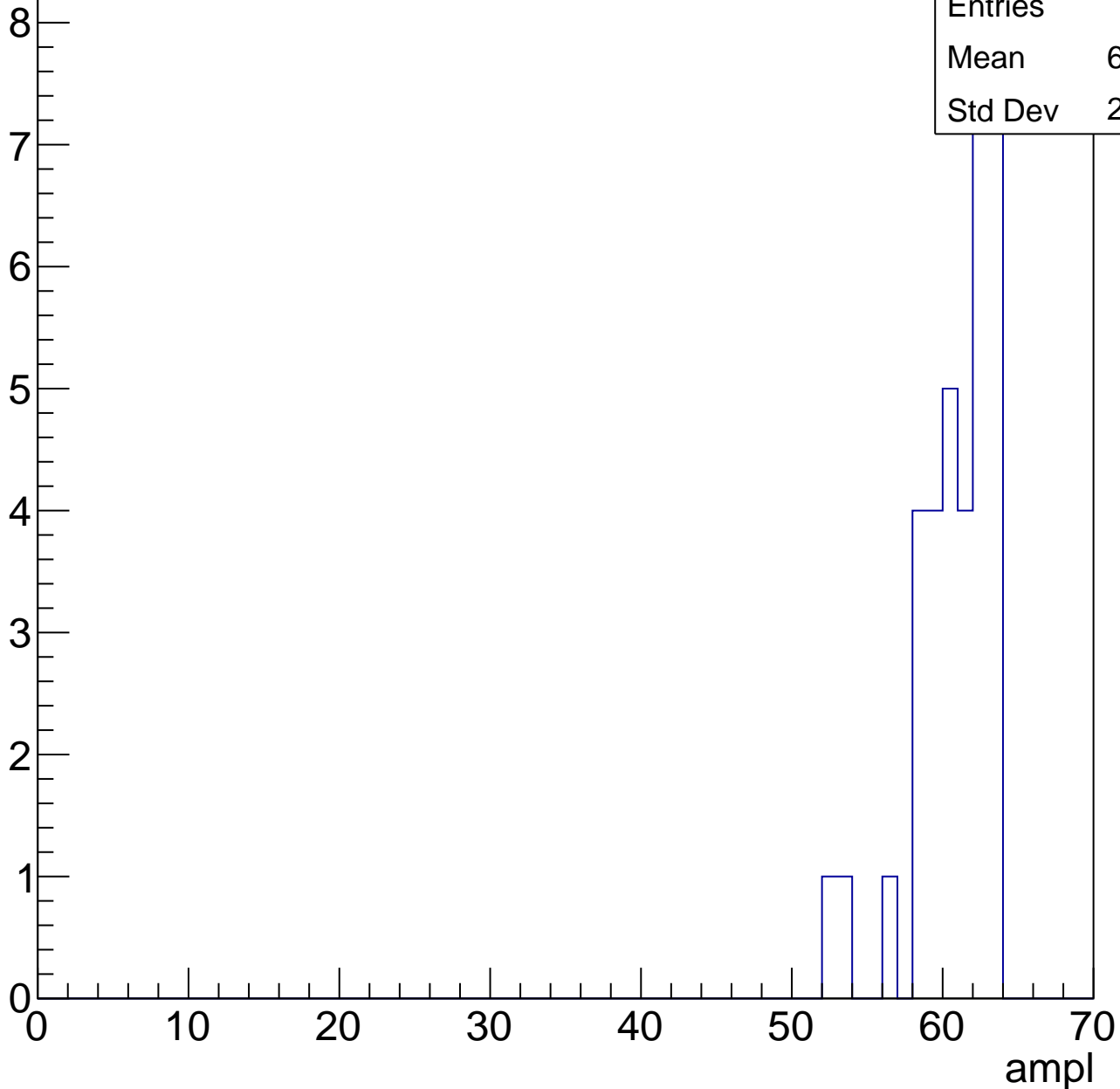


B1L103S, U17-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

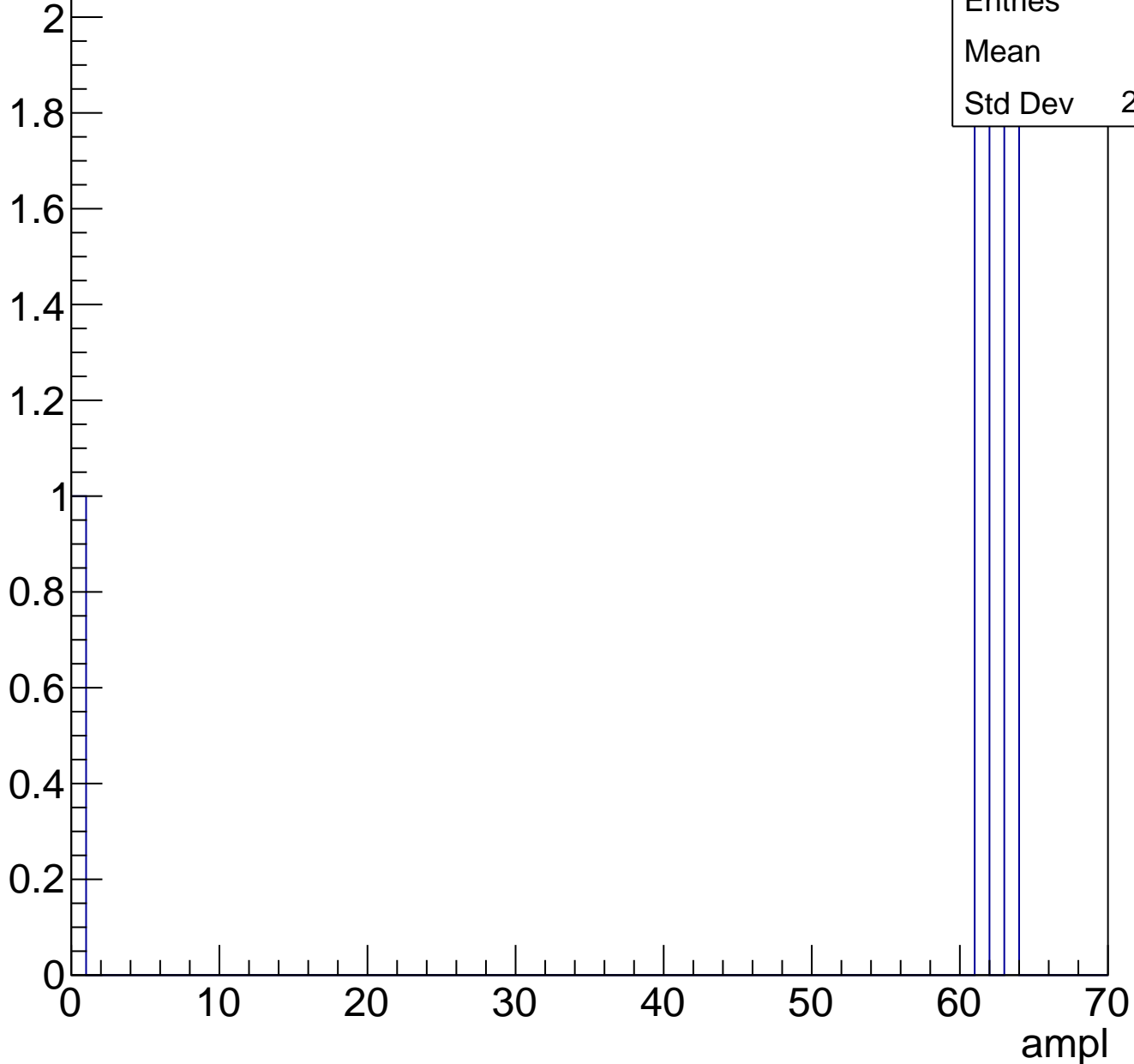
Entries	36
Mean	60.36
Std Dev	2.647



B1L103S, U17-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch45, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	90
Mean	29.27
Std Dev	3.929

Gaus mean : 30.6486

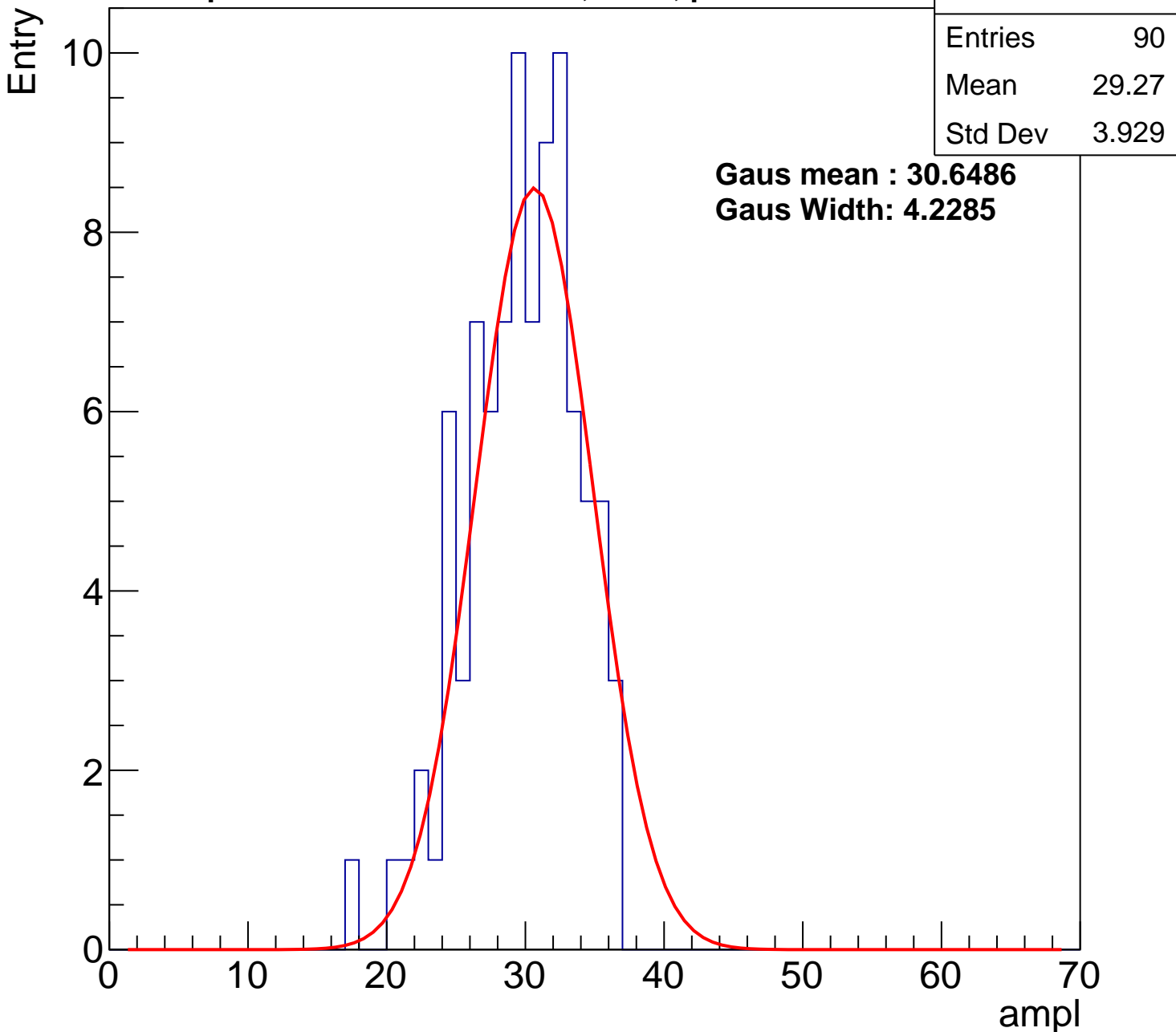
Gaus Width: 4.2285

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch45, adc1

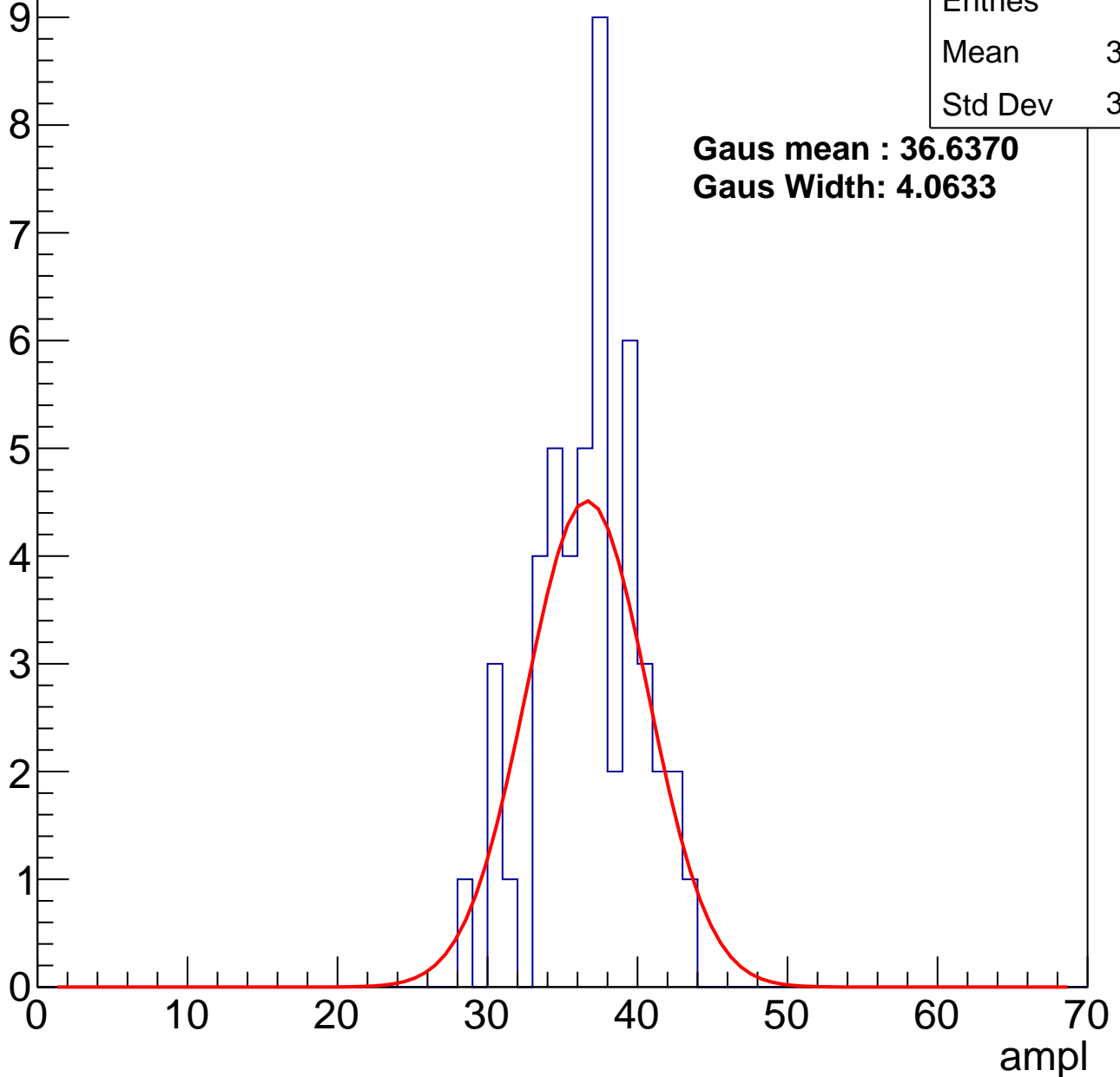
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	36.31
Std Dev	3.361

Gaus mean : 36.6370

Gaus Width: 4.0633



B1L103S, U17-ch45, adc2

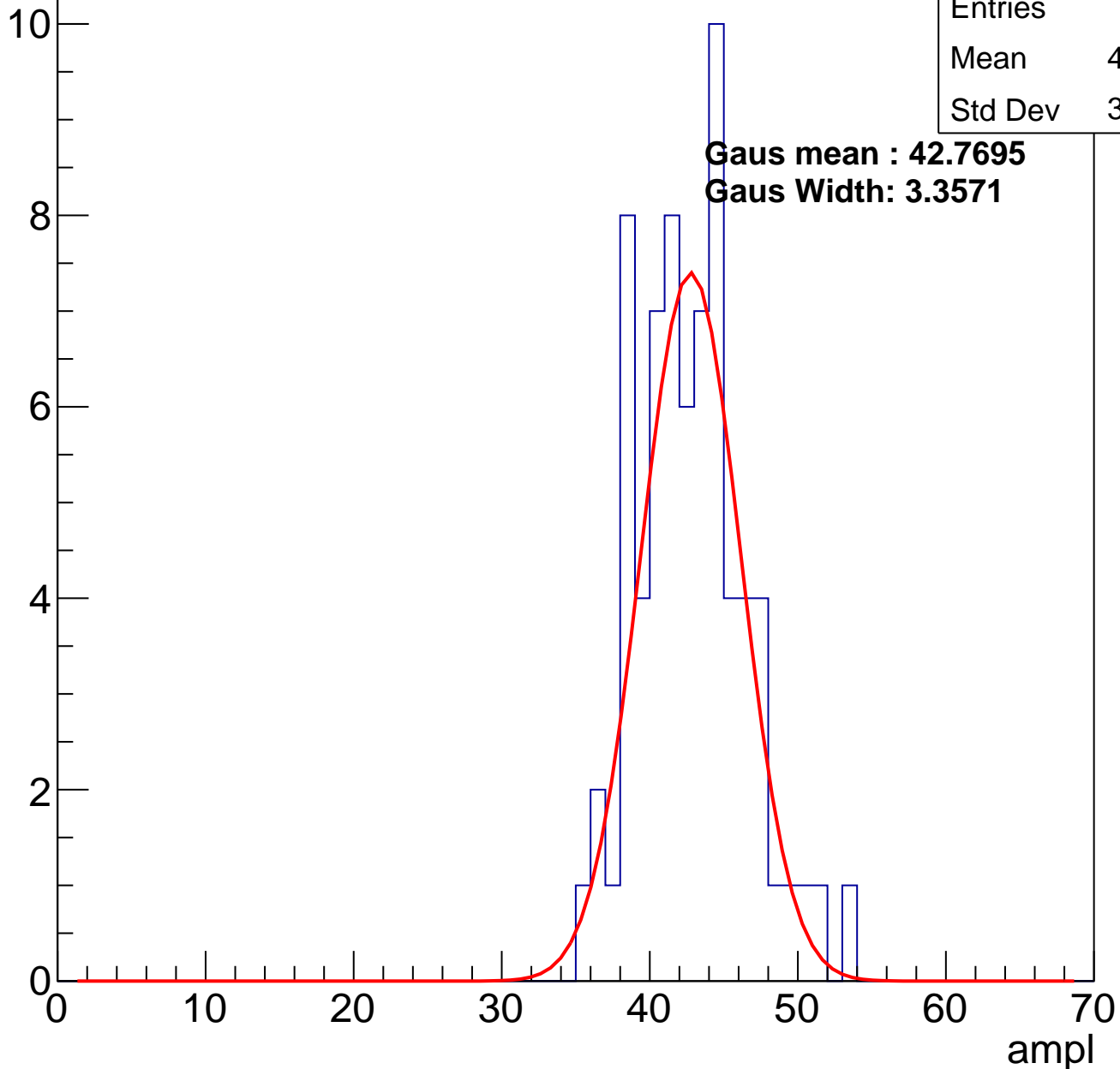
calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	42.37
Std Dev	3.628

Gaus mean : 42.7695

Gaus Width: 3.3571

Entry

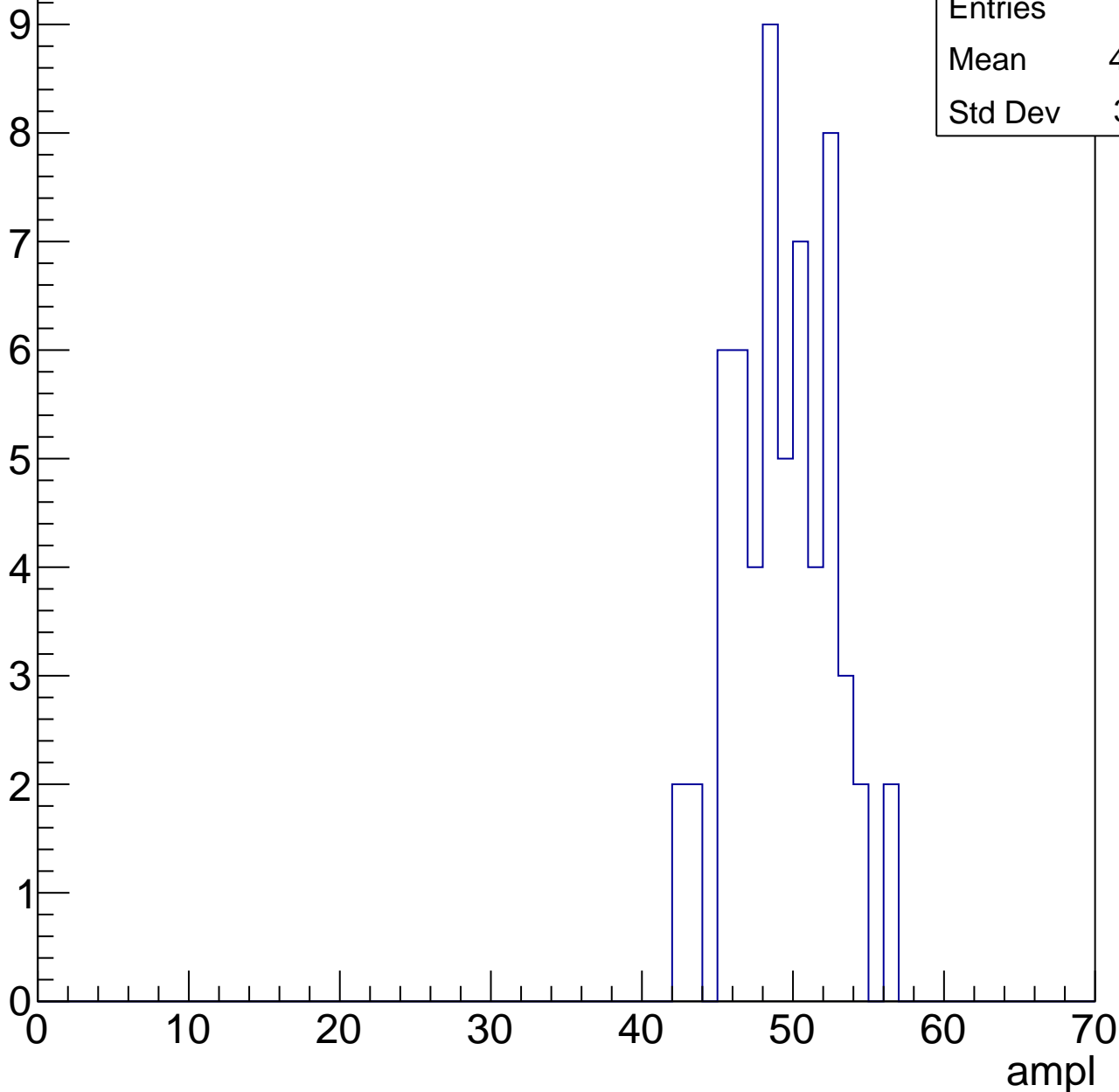


B1L103S, U17-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

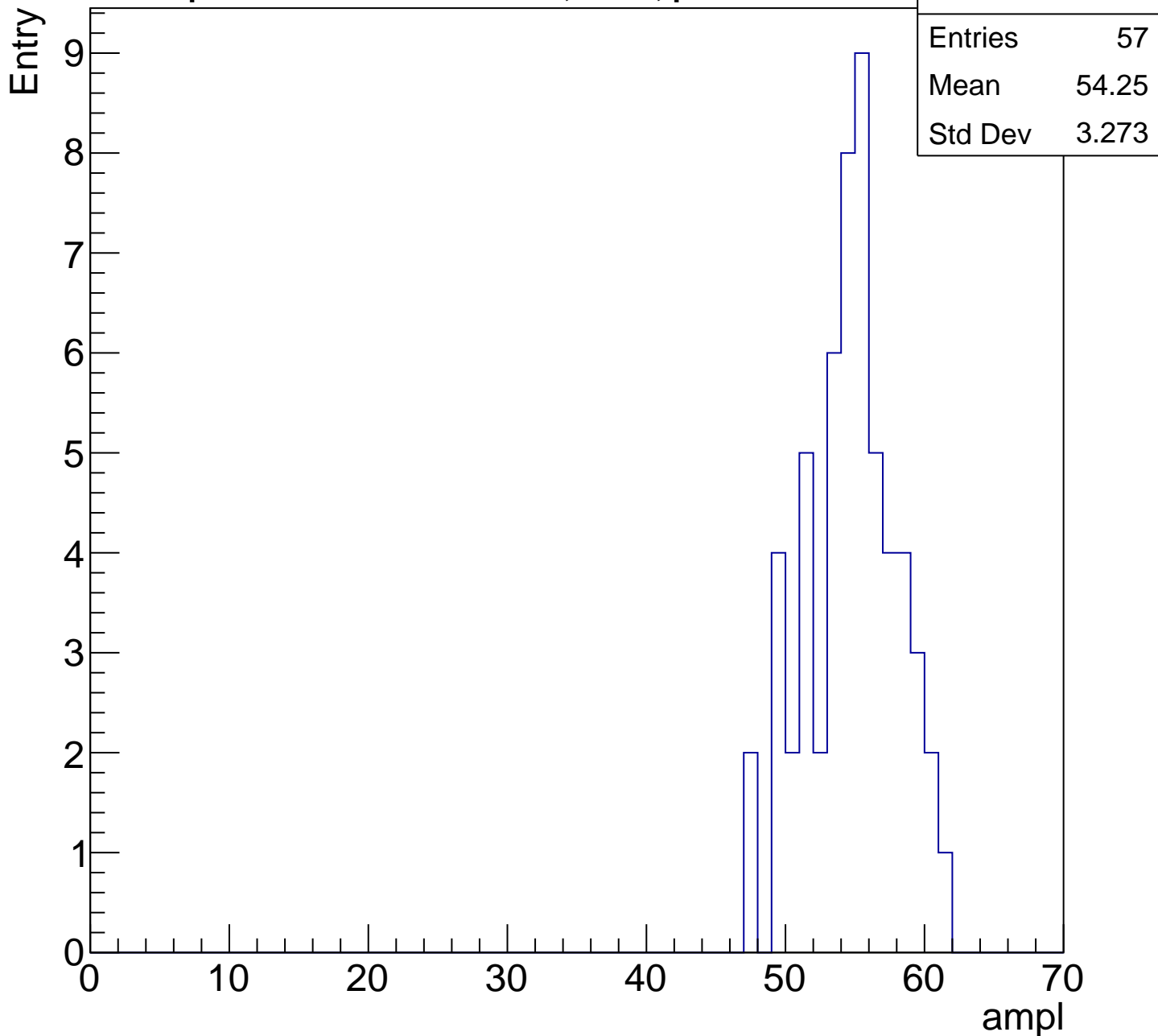
Entry

Entries	60
Mean	48.83
Std Dev	3.251



B1L103S, U17-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

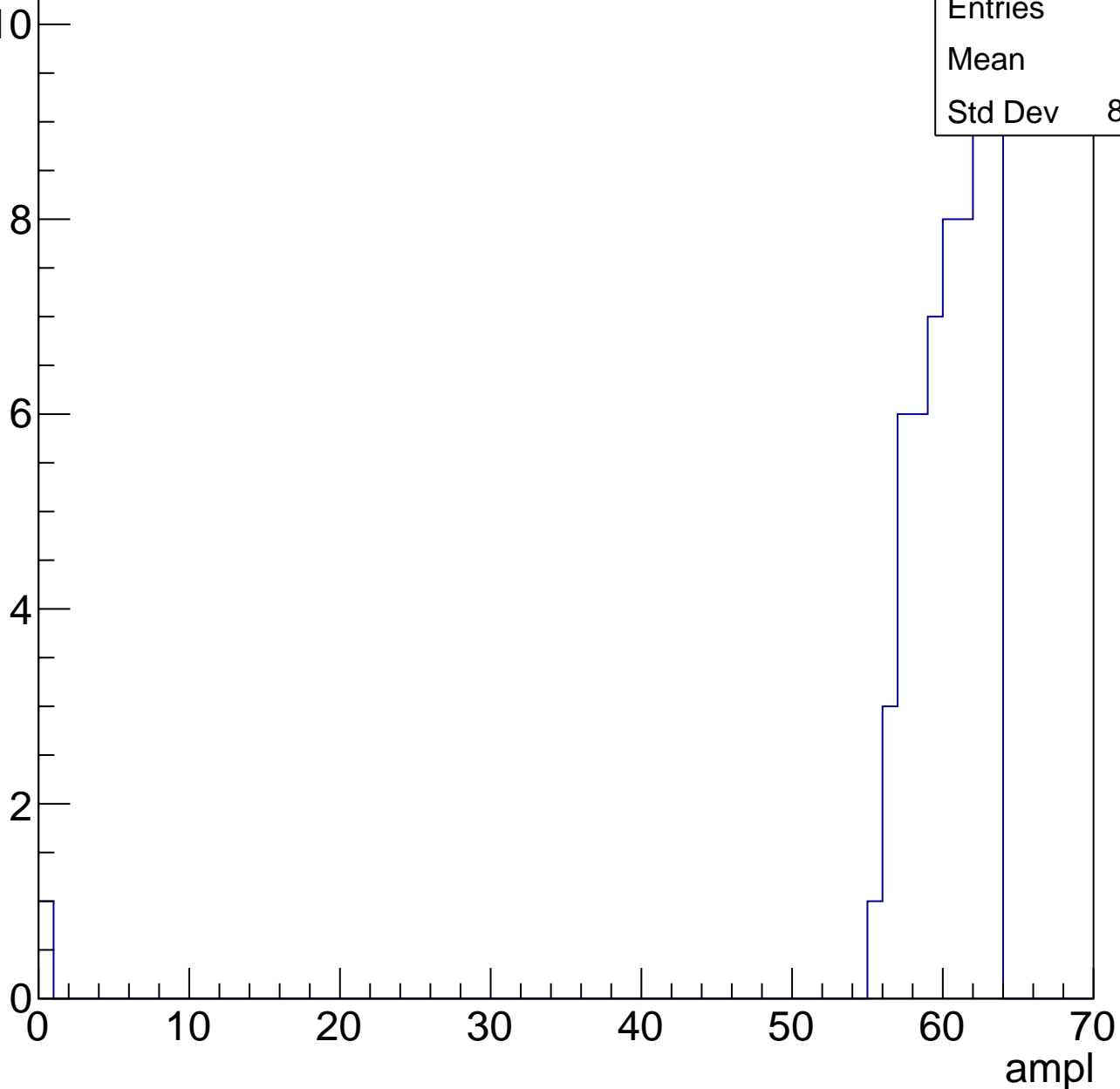


B1L103S, U17-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	59
Std Dev	8.055



B1L103S, U17-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch46, adc0

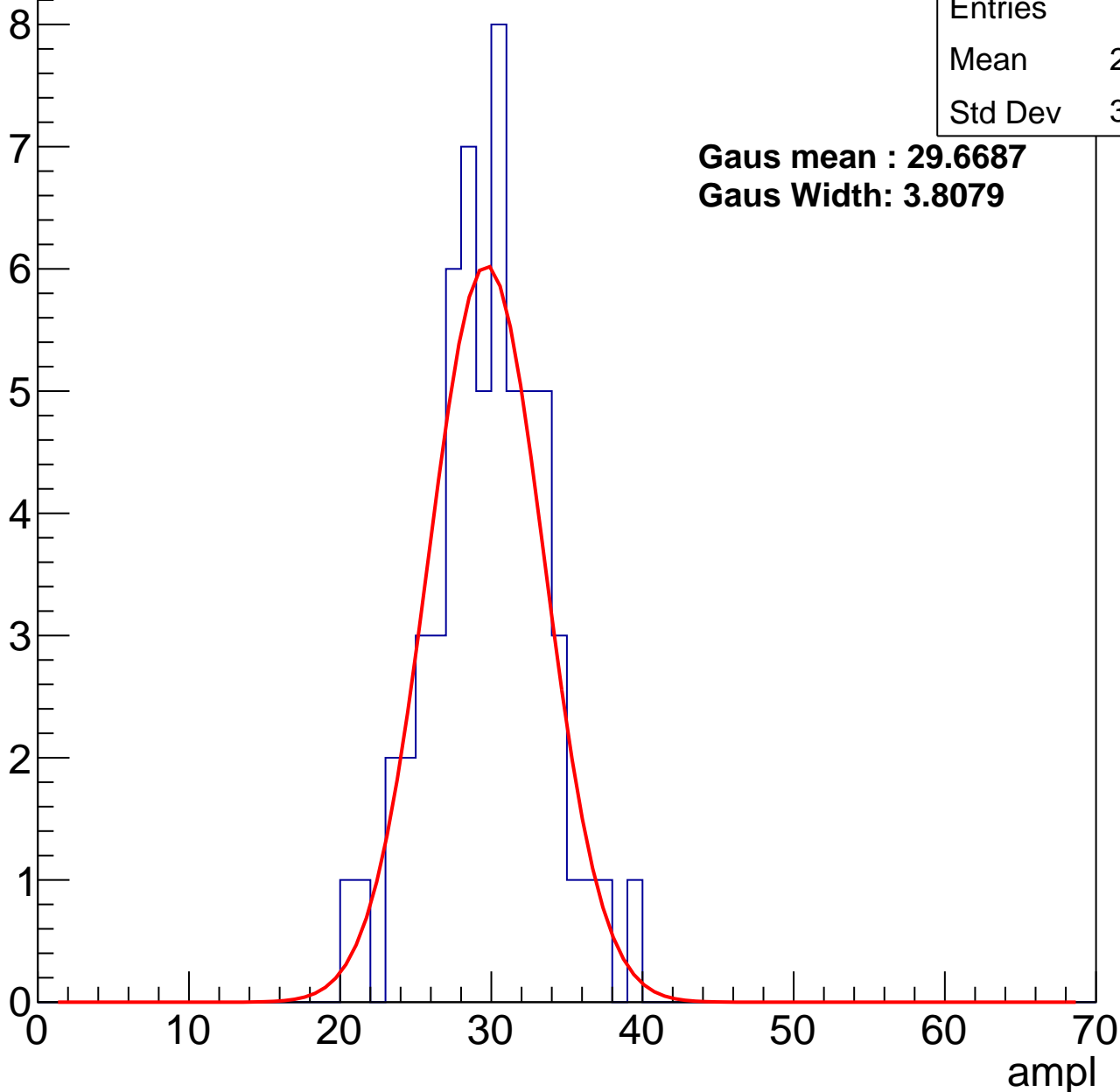
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	29.33
Std Dev	3.749

Gaus mean : 29.6687

Gaus Width: 3.8079



B1L103S, U17-ch46, adc1

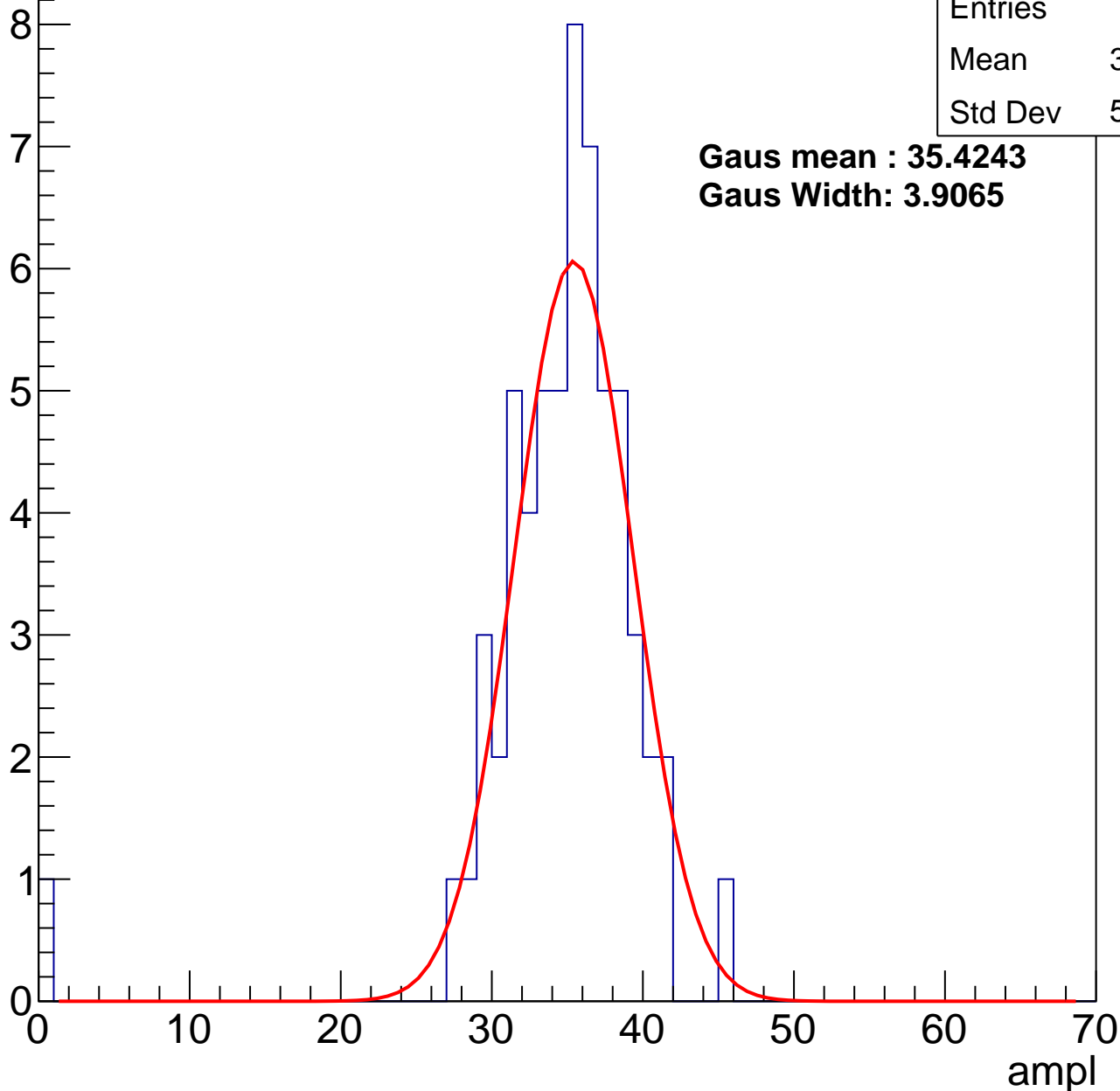
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	34.18
Std Dev	5.685

Gaus mean : 35.4243

Gaus Width: 3.9065



B1L103S, U17-ch46, adc2

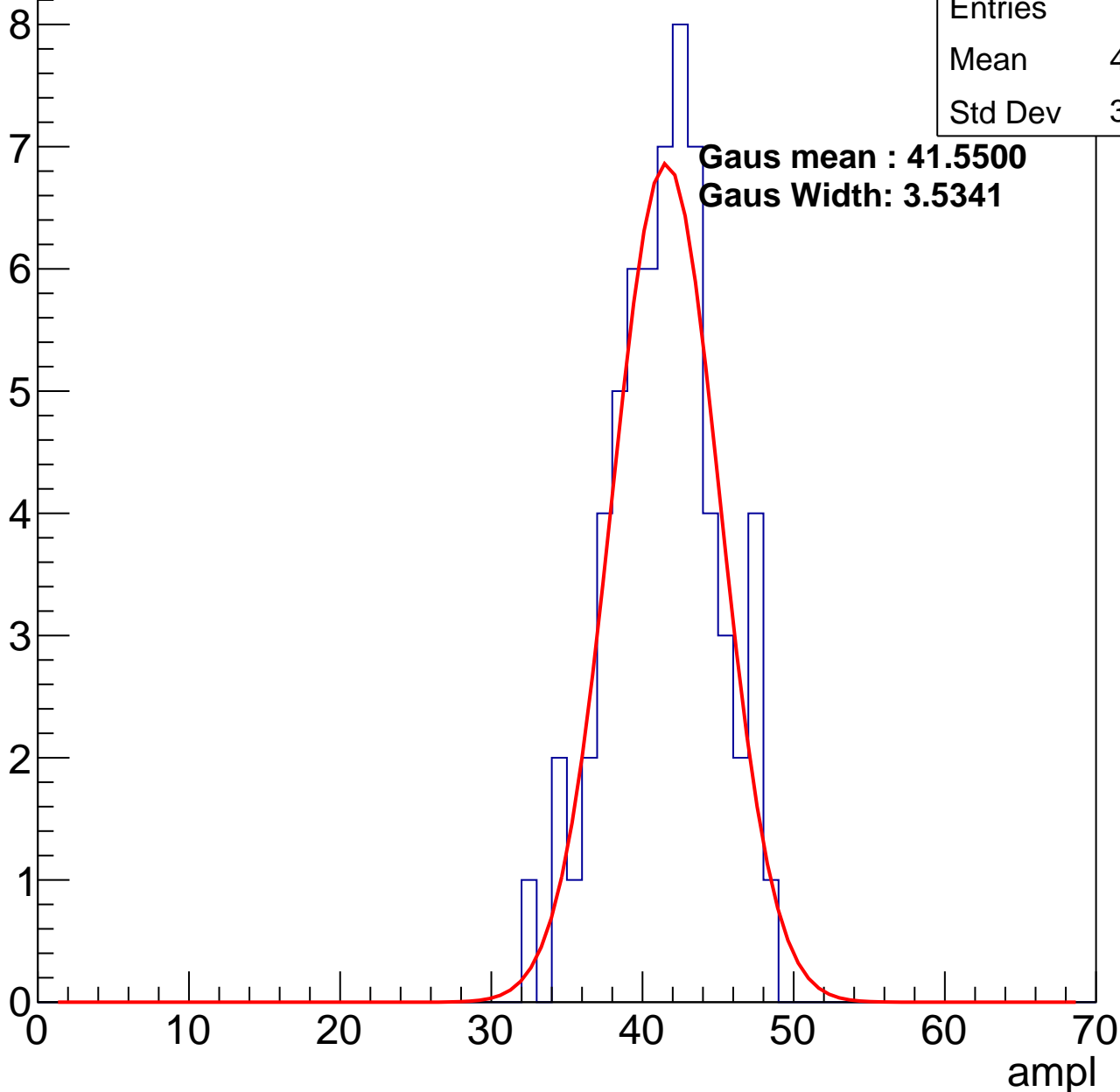
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	40.98
Std Dev	3.489

Gaus mean : 41.5500

Gaus Width: 3.5341

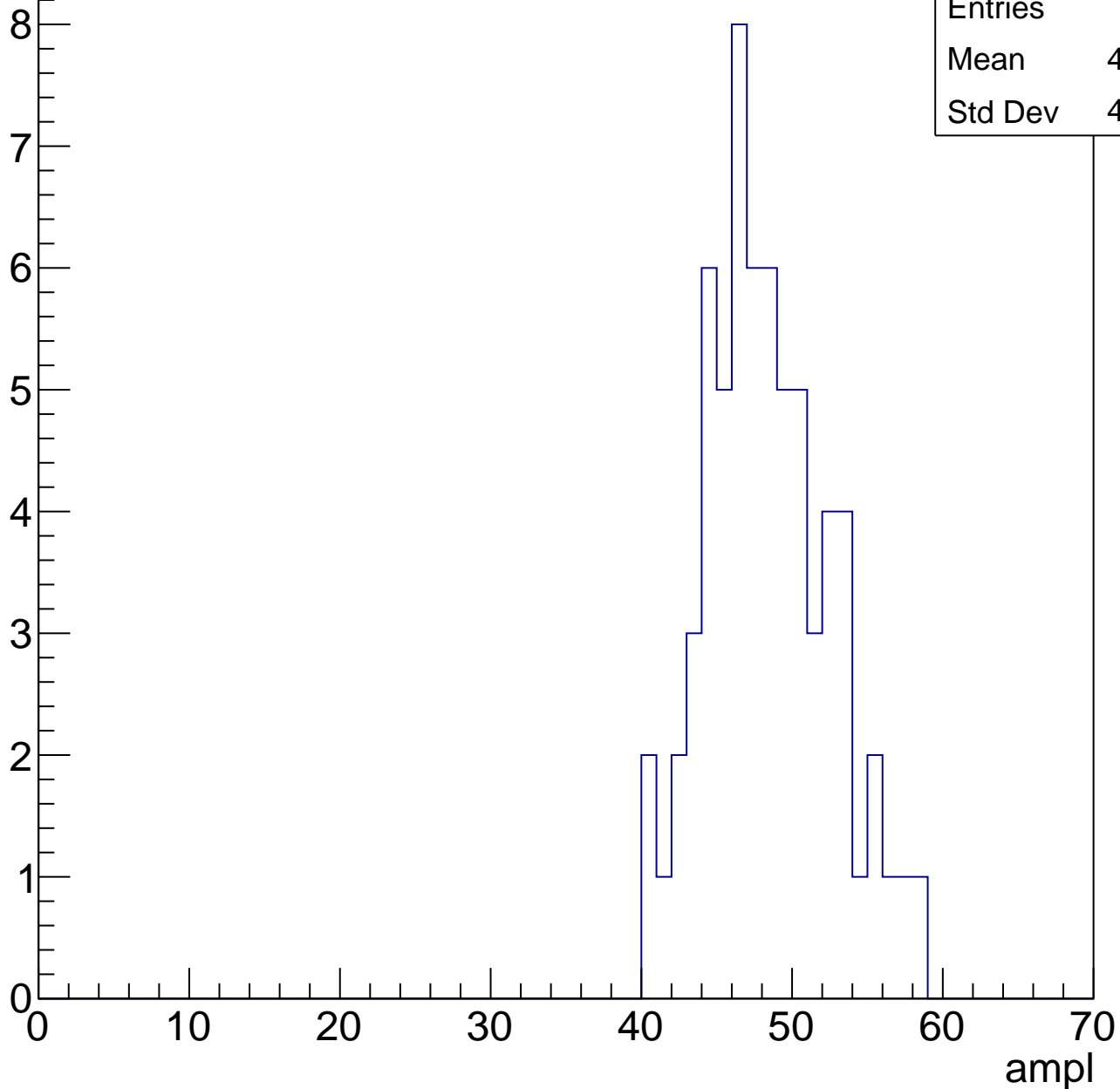


B1L103S, U17-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	47.94
Std Dev	4.078

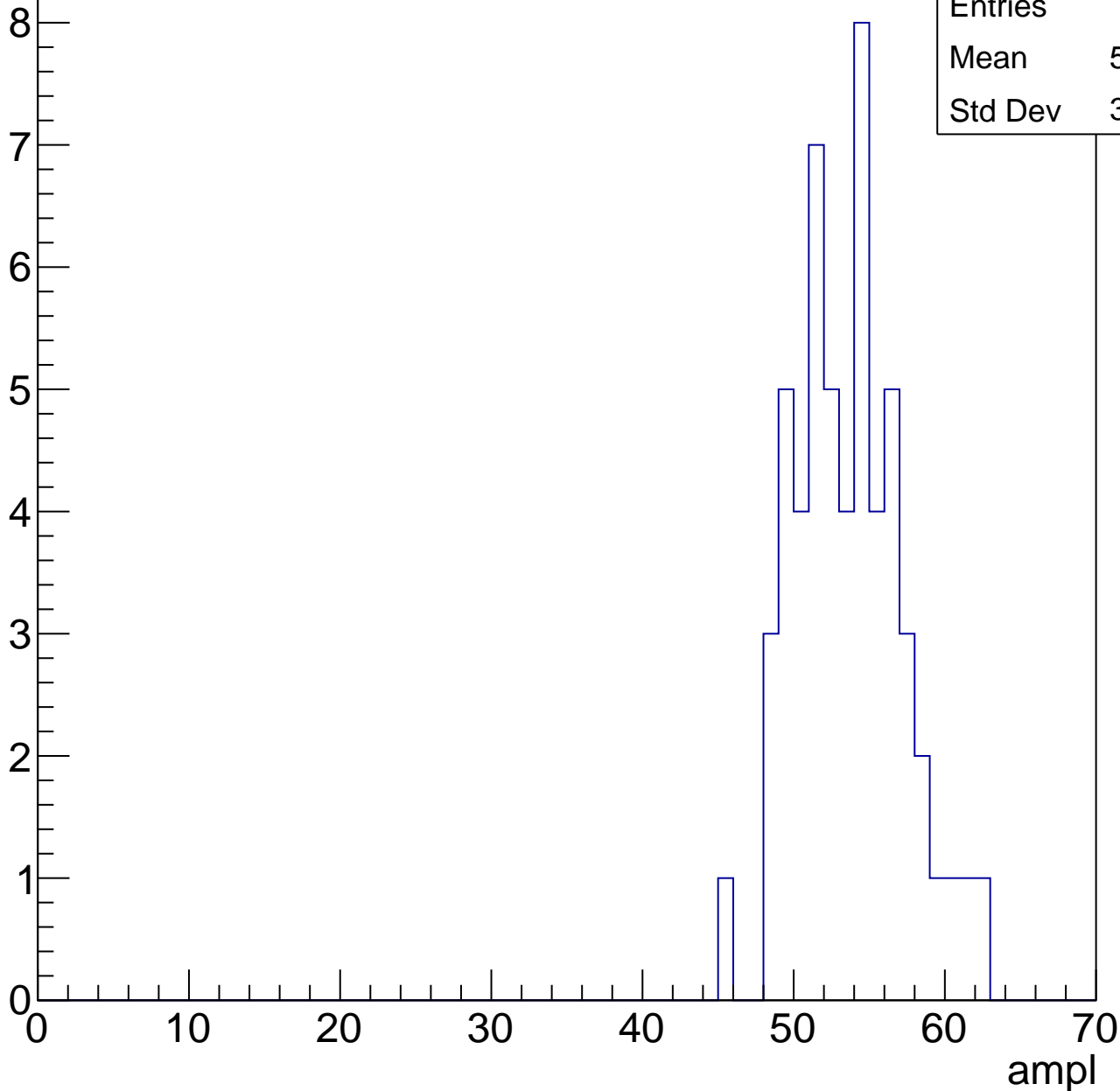


B1L103S, U17-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

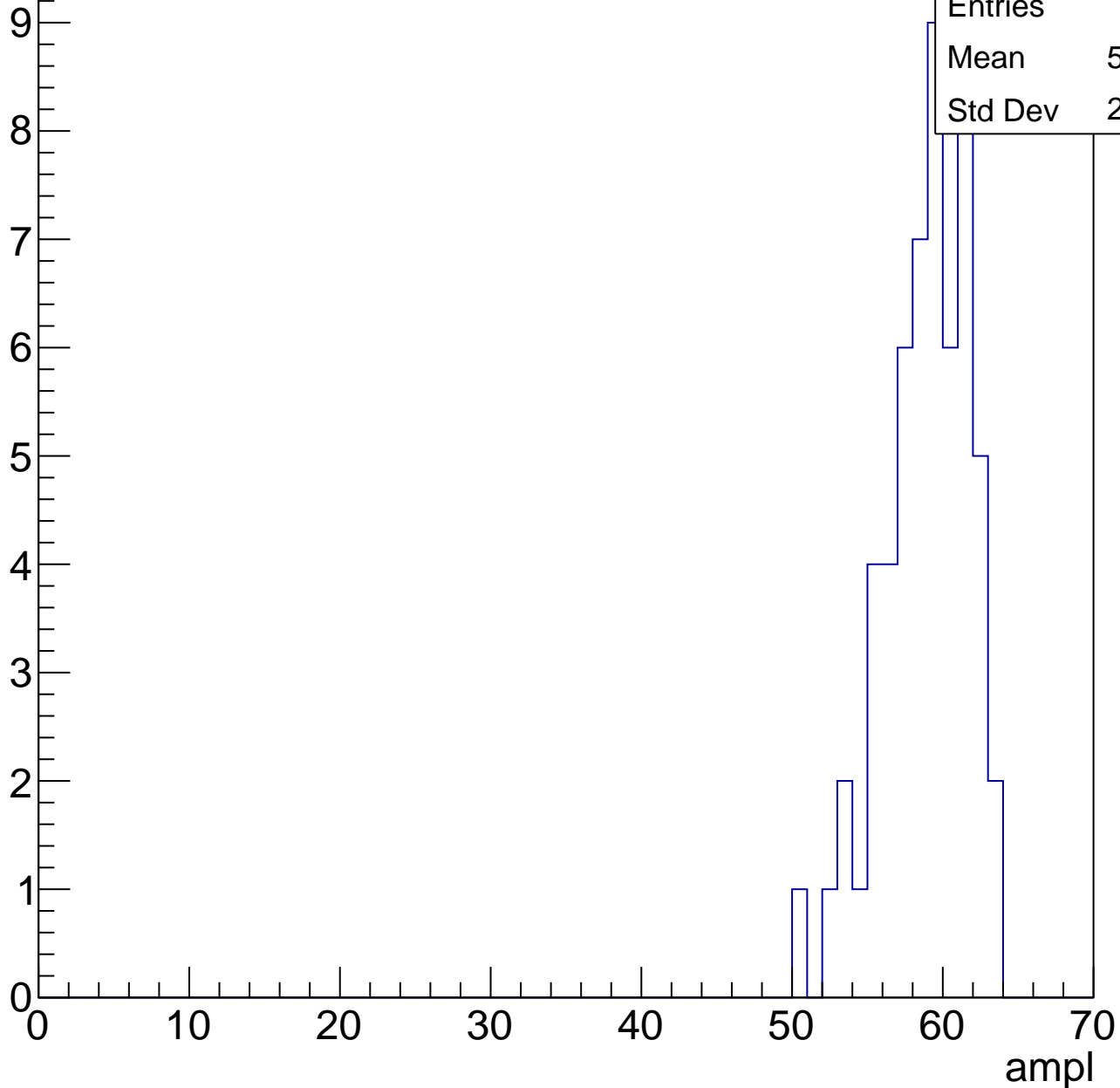
Entries	55
Mean	53.16
Std Dev	3.525



B1L103S, U17-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

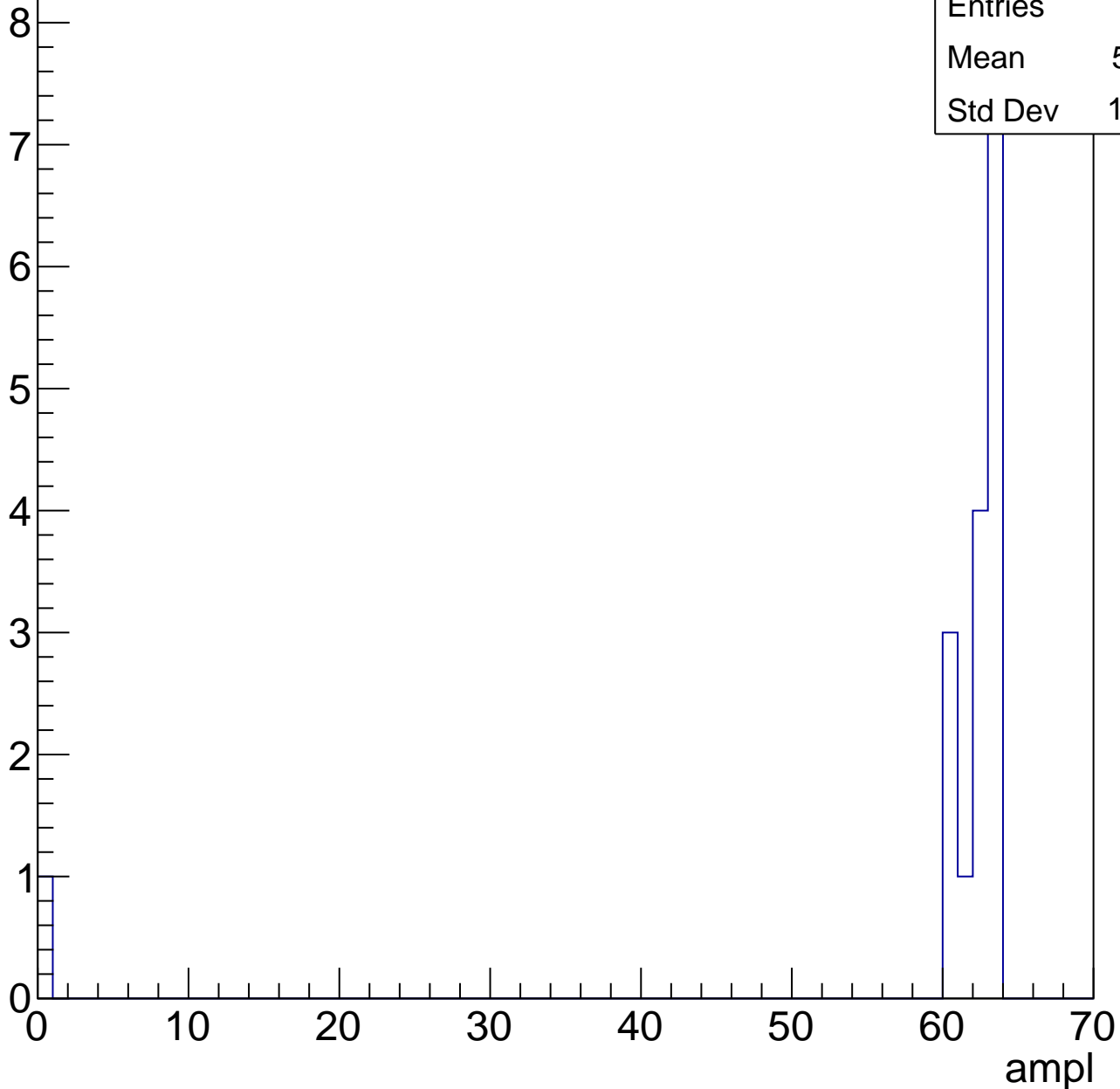


B1L103S, U17-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	58.41
Std Dev	14.65



B1L103S, U17-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch47, adc0

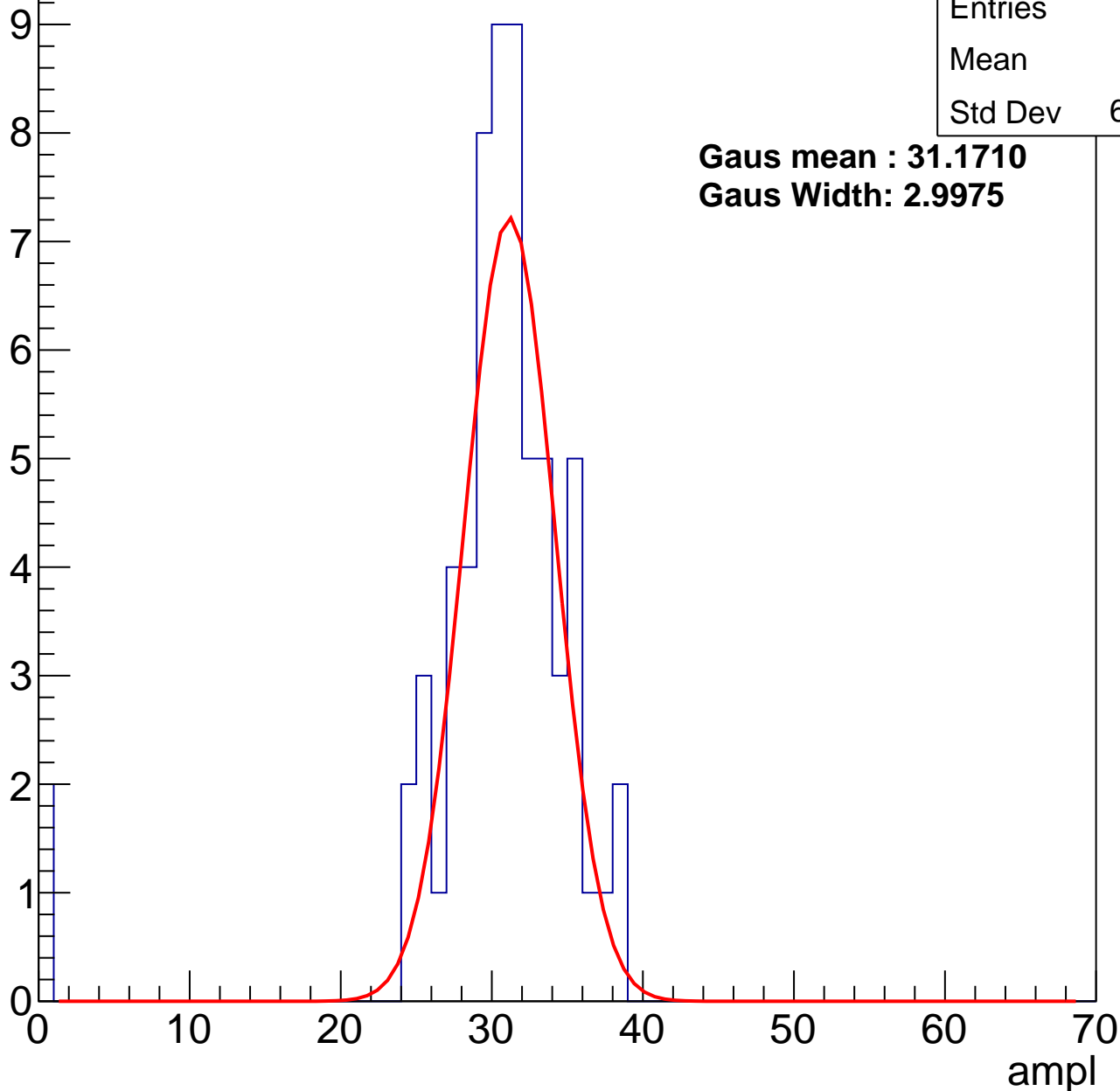
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.7
Std Dev	6.222

Gaus mean : 31.1710

Gaus Width: 2.9975



B1L103S, U17-ch47, adc1

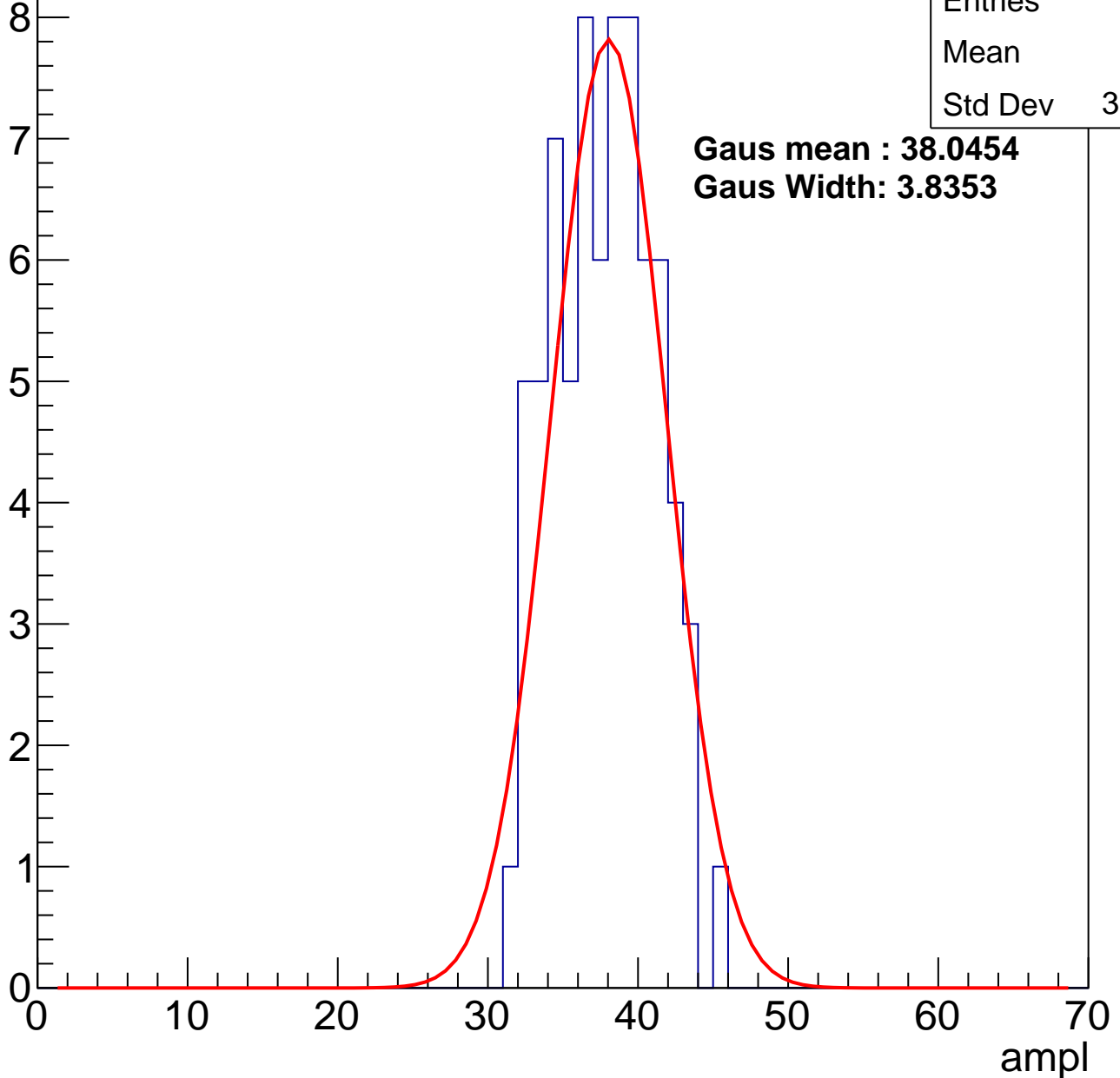
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	37.3
Std Dev	3.284

Gaus mean : 38.0454

Gaus Width: 3.8353



B1L103S, U17-ch47, adc2

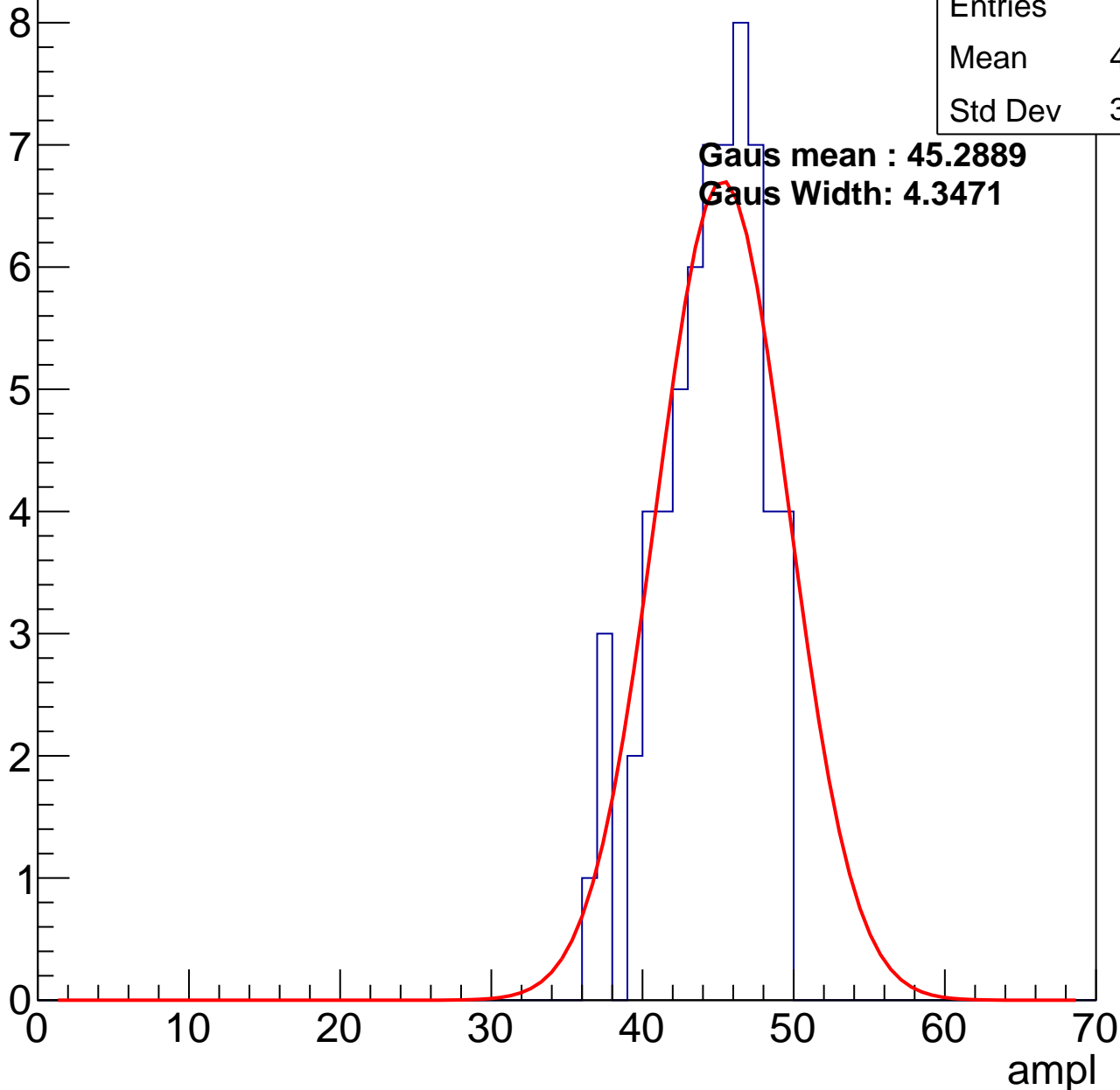
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.95
Std Dev	3.245

Gaus mean : 45.2889

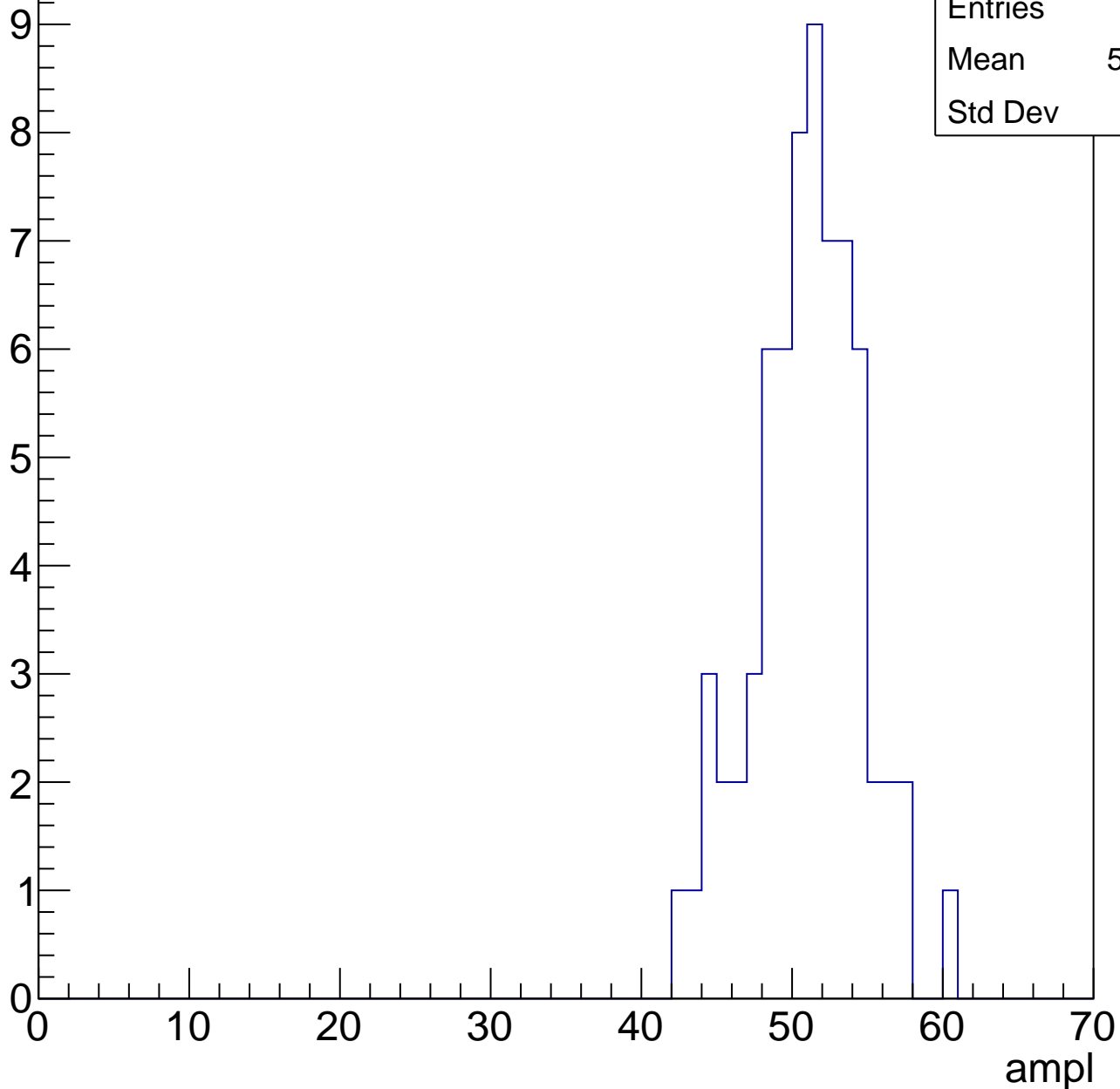
Gaus Width: 4.3471



B1L103S, U17-ch47, adc3

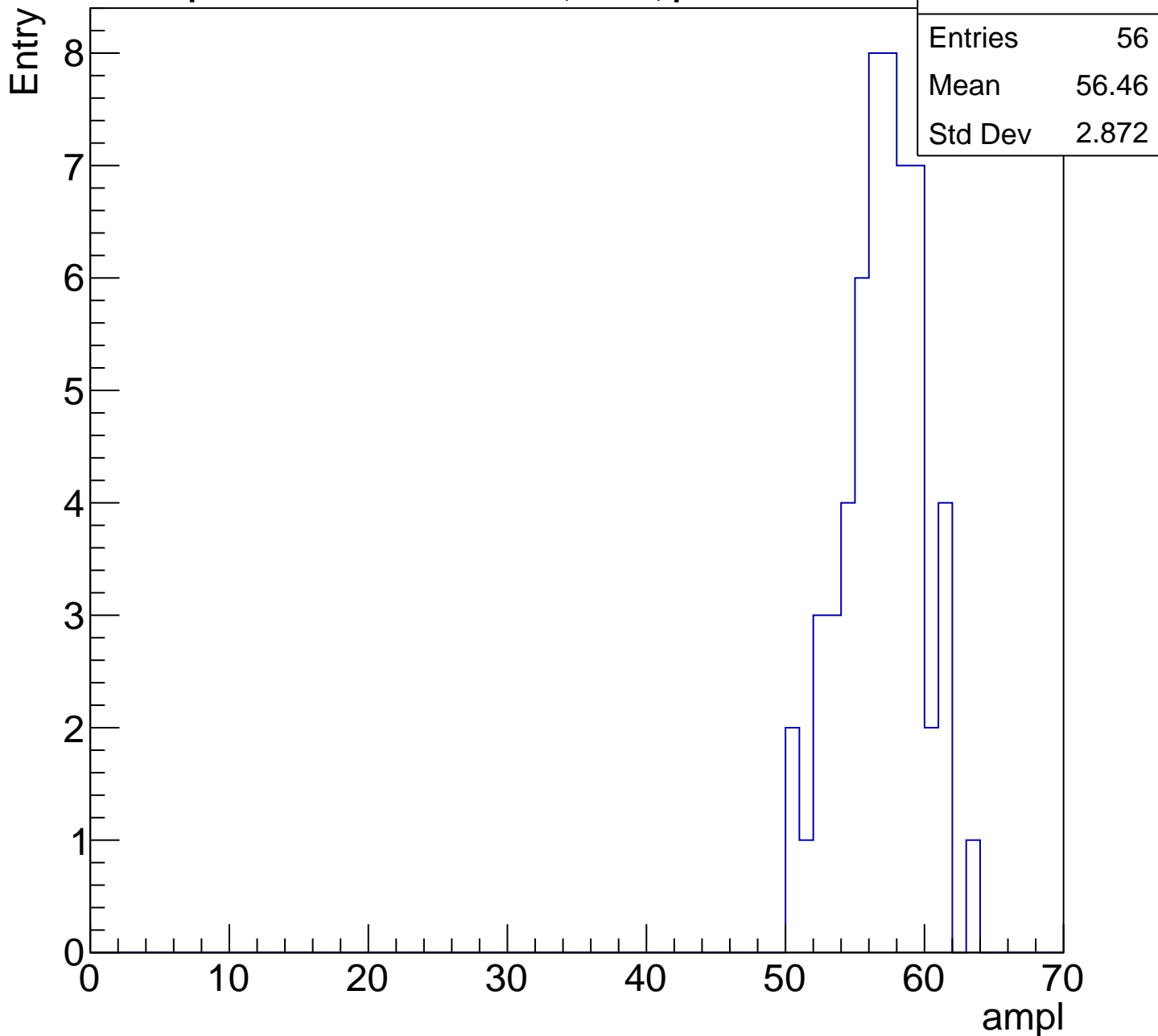
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

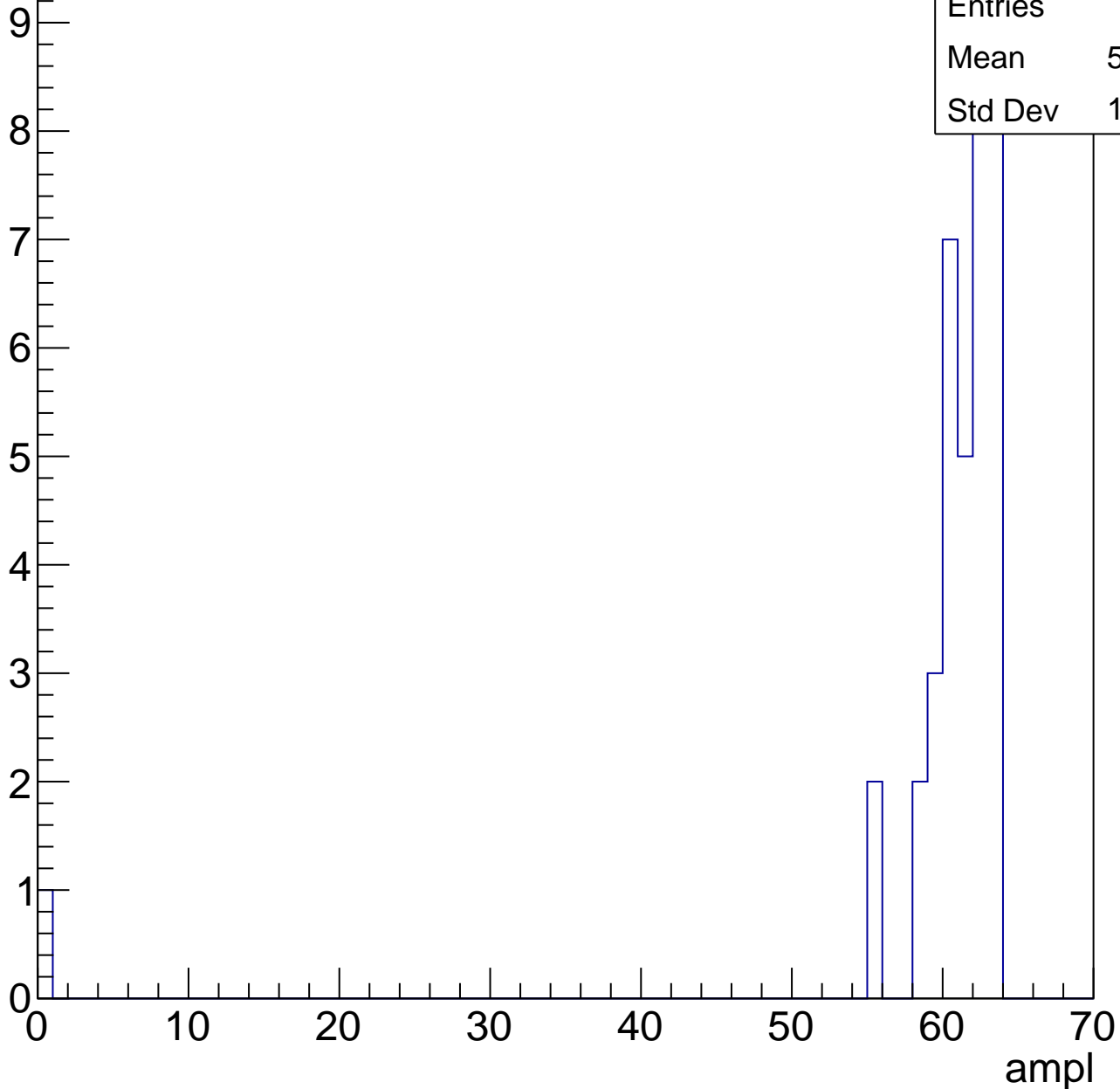


B1L103S, U17-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

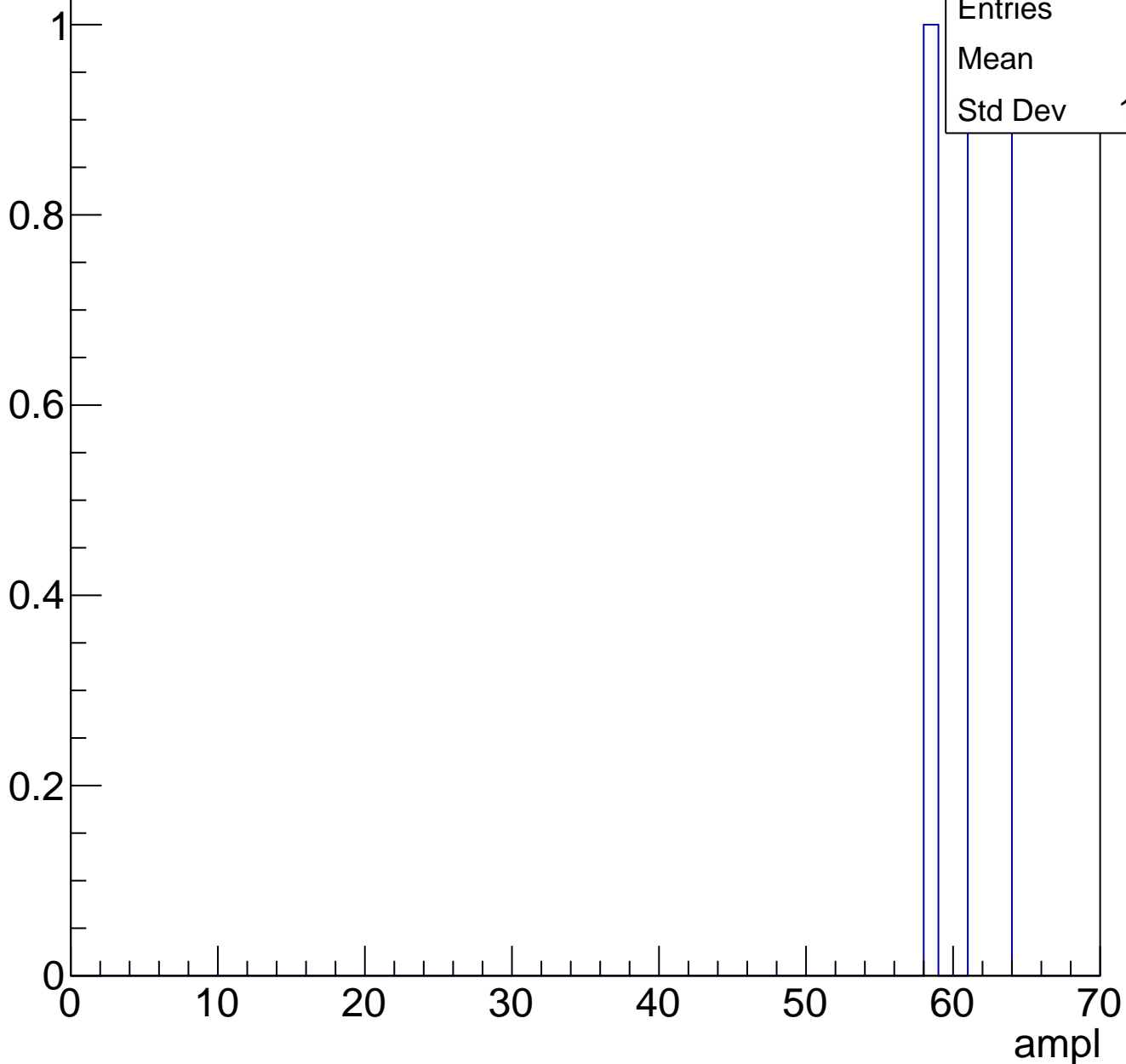
Entries	37
Mean	59.19
Std Dev	10.07



B1L103S, U17-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch48, adc0

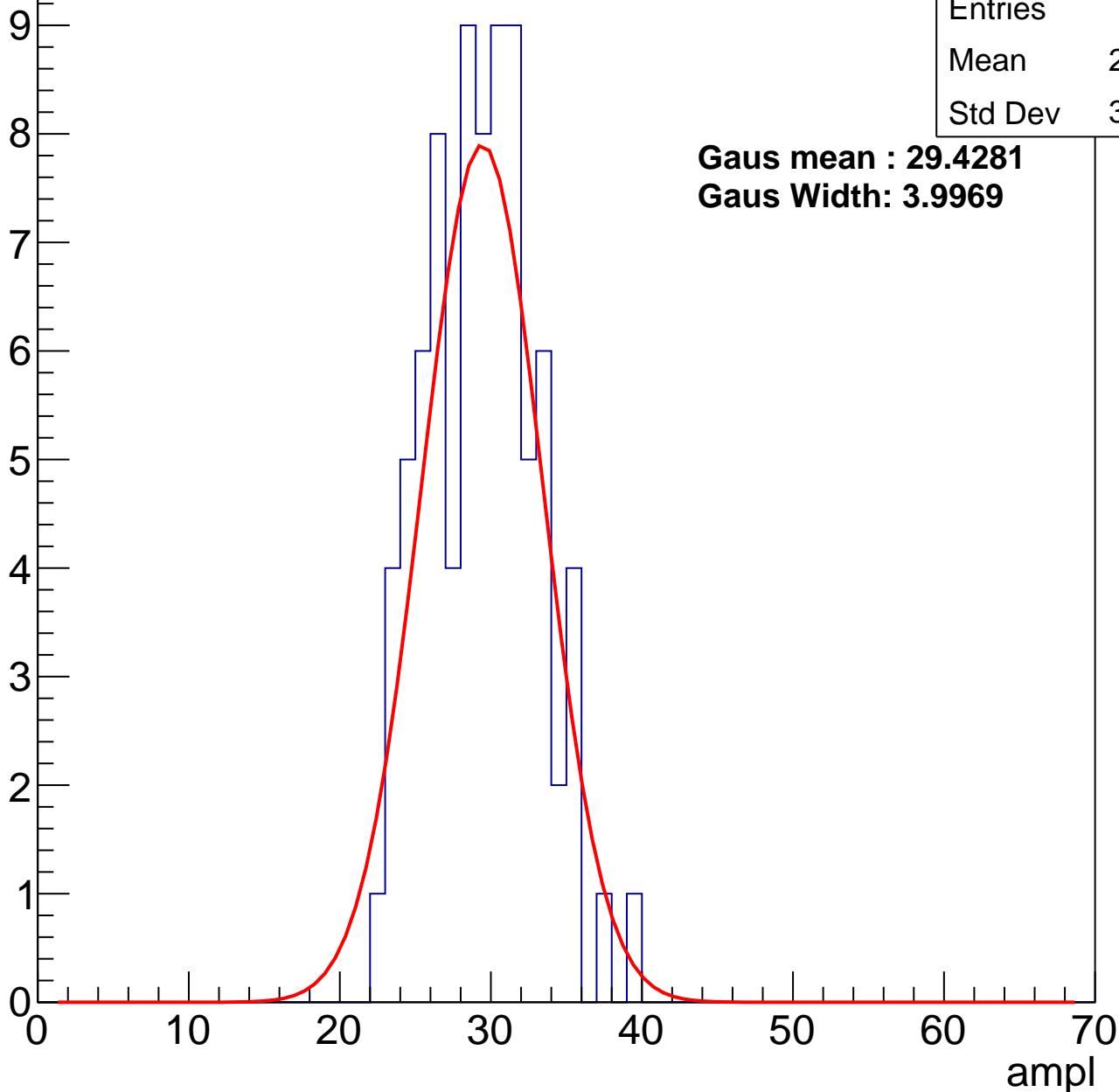
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.96
Std Dev	3.587

Gaus mean : 29.4281

Gaus Width: 3.9969



B1L103S, U17-ch48, adc1

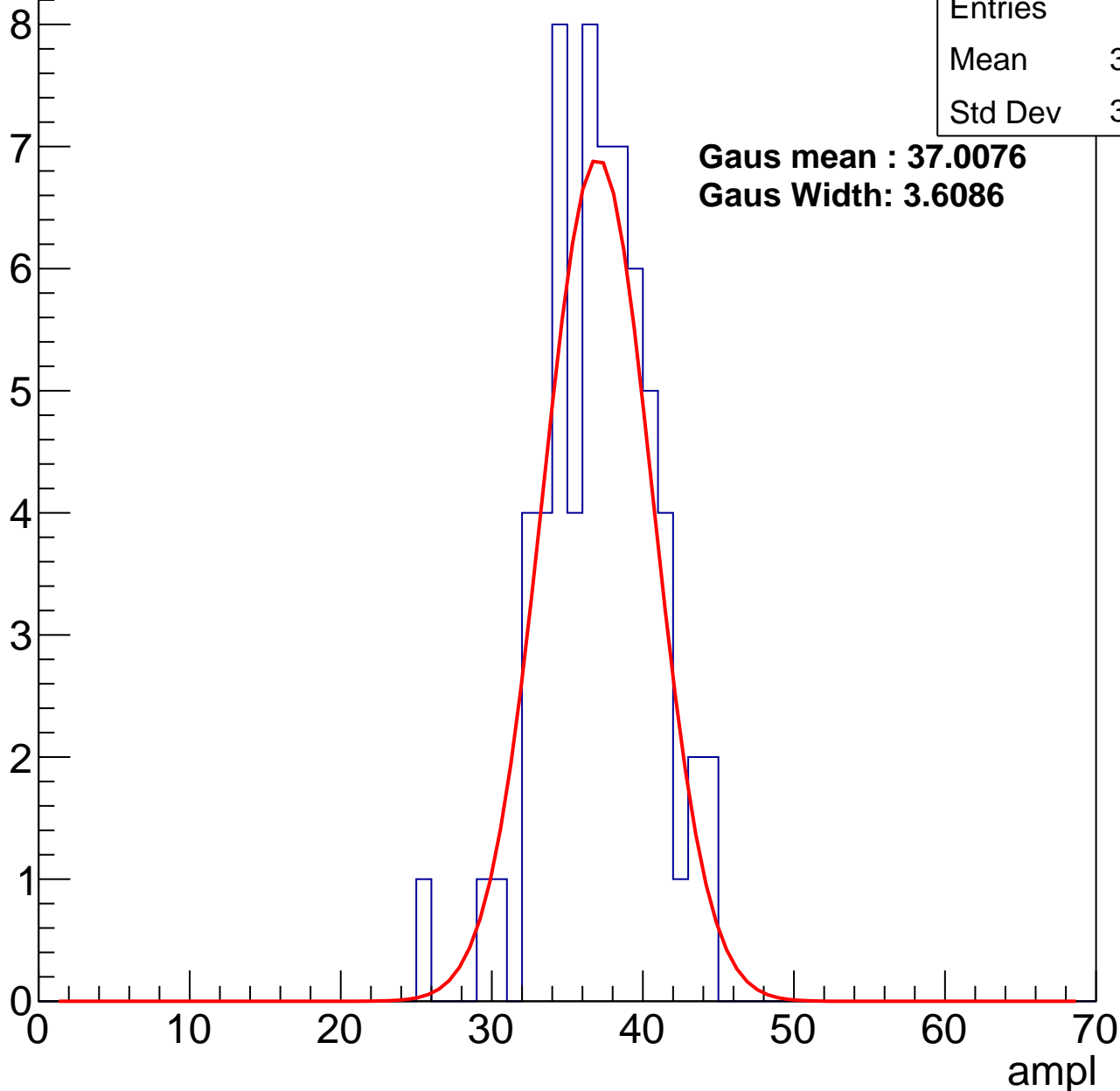
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.66
Std Dev	3.605

Gaus mean : 37.0076

Gaus Width: 3.6086



B1L103S, U17-ch48, adc2

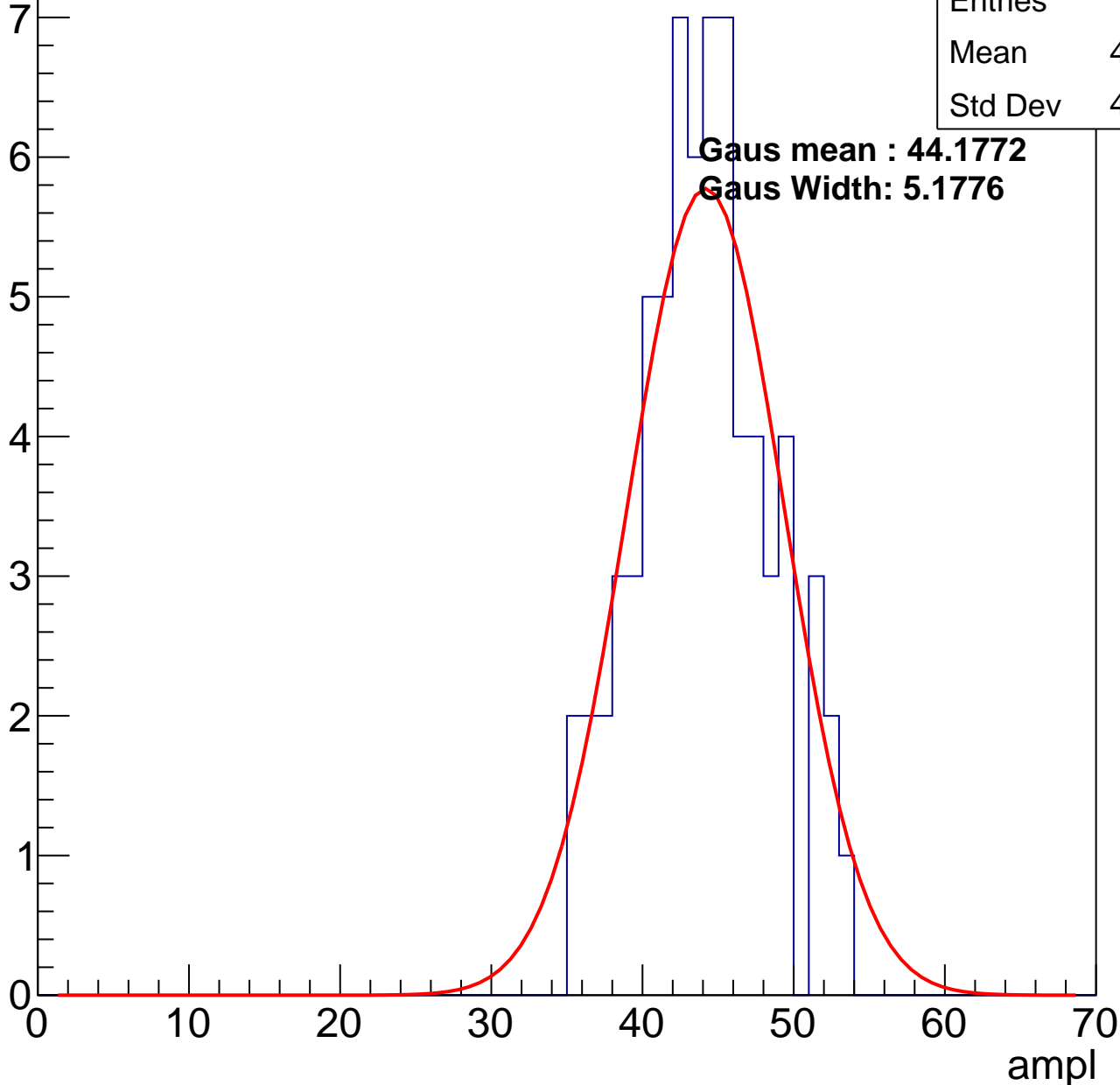
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.56
Std Dev	4.275

Gaus mean : 44.1772

Gaus Width: 5.1776

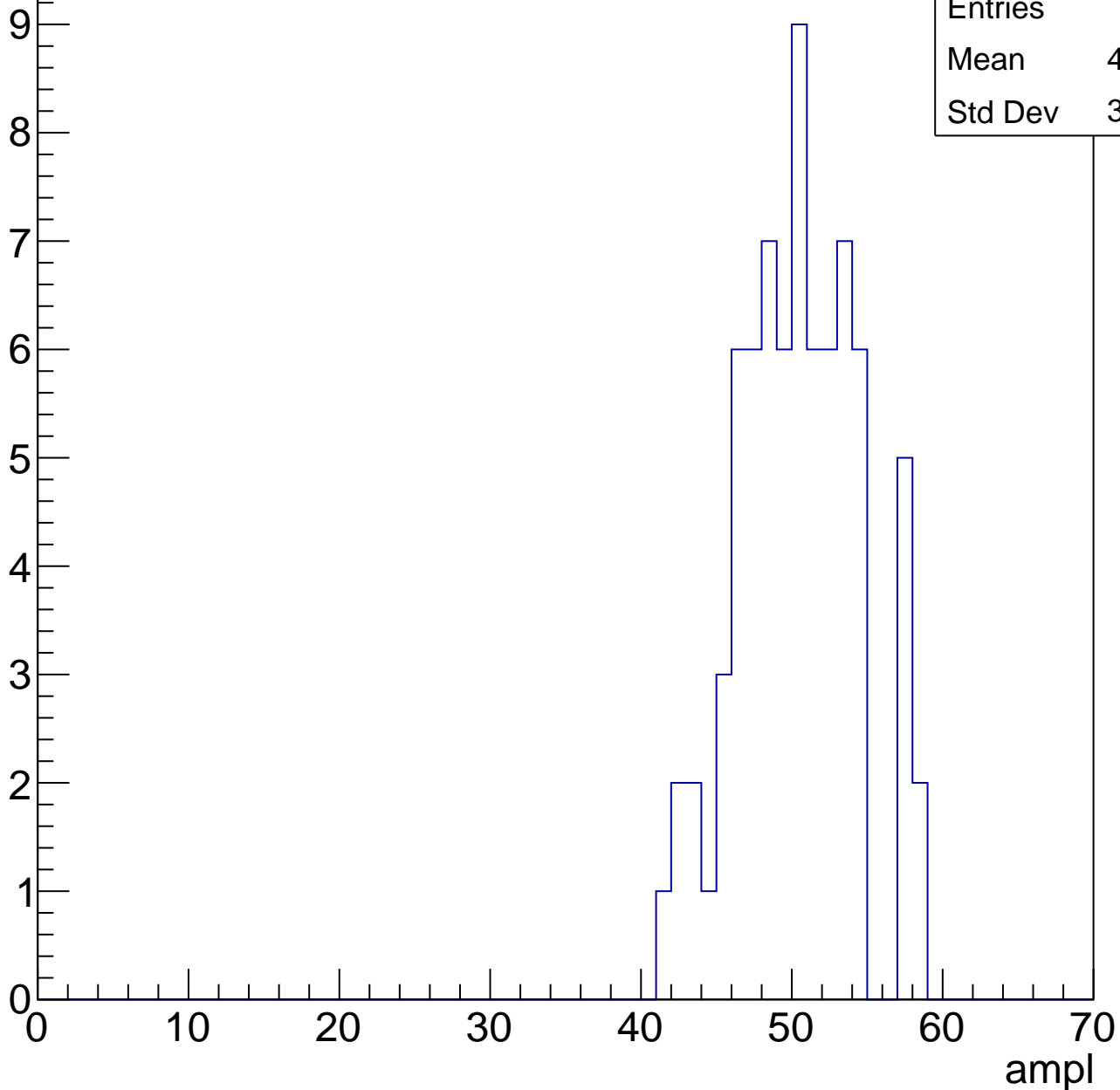


B1L103S, U17-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	49.89
Std Dev	3.938

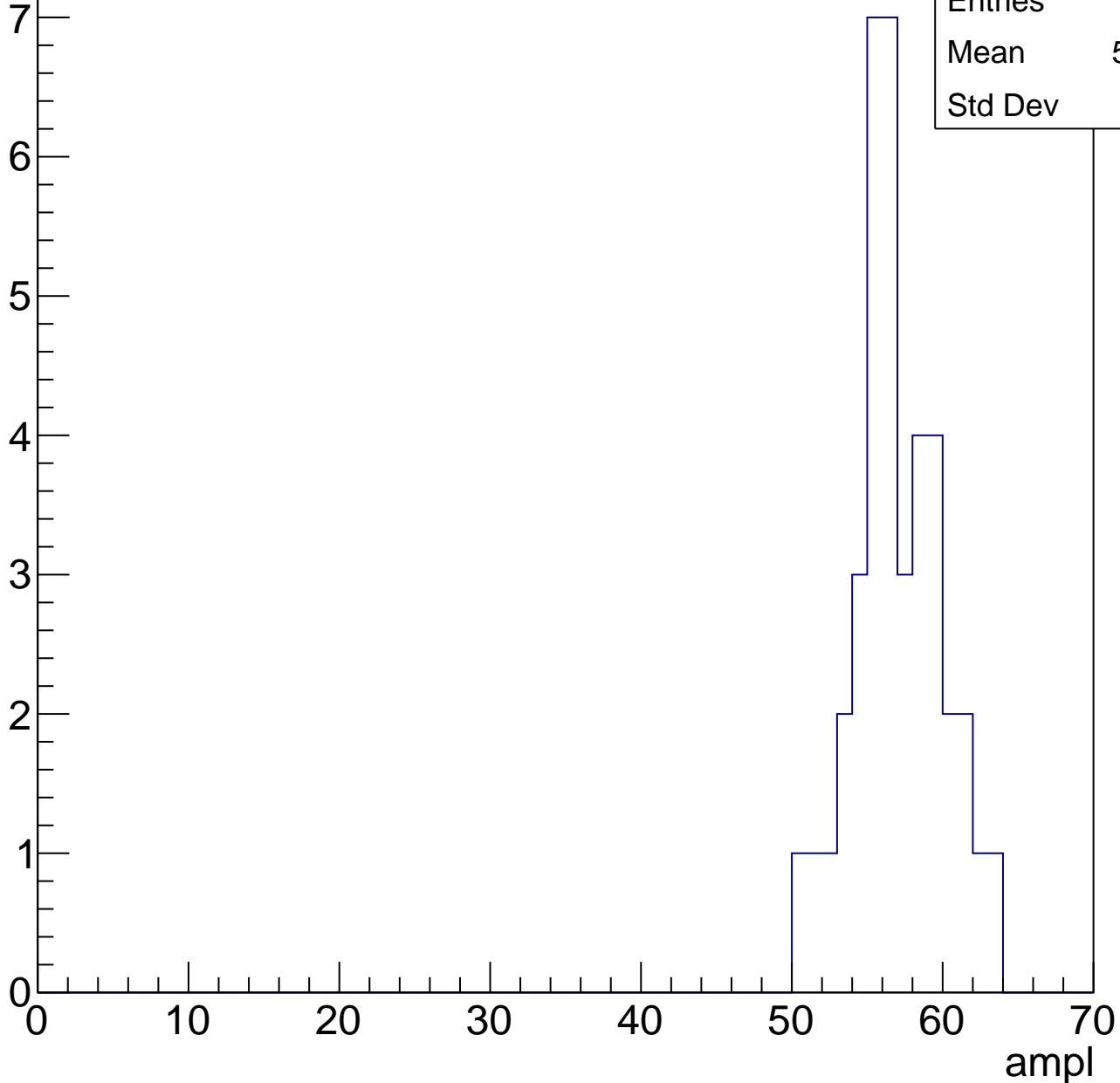


B1L103S, U17-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	56.51
Std Dev	2.89

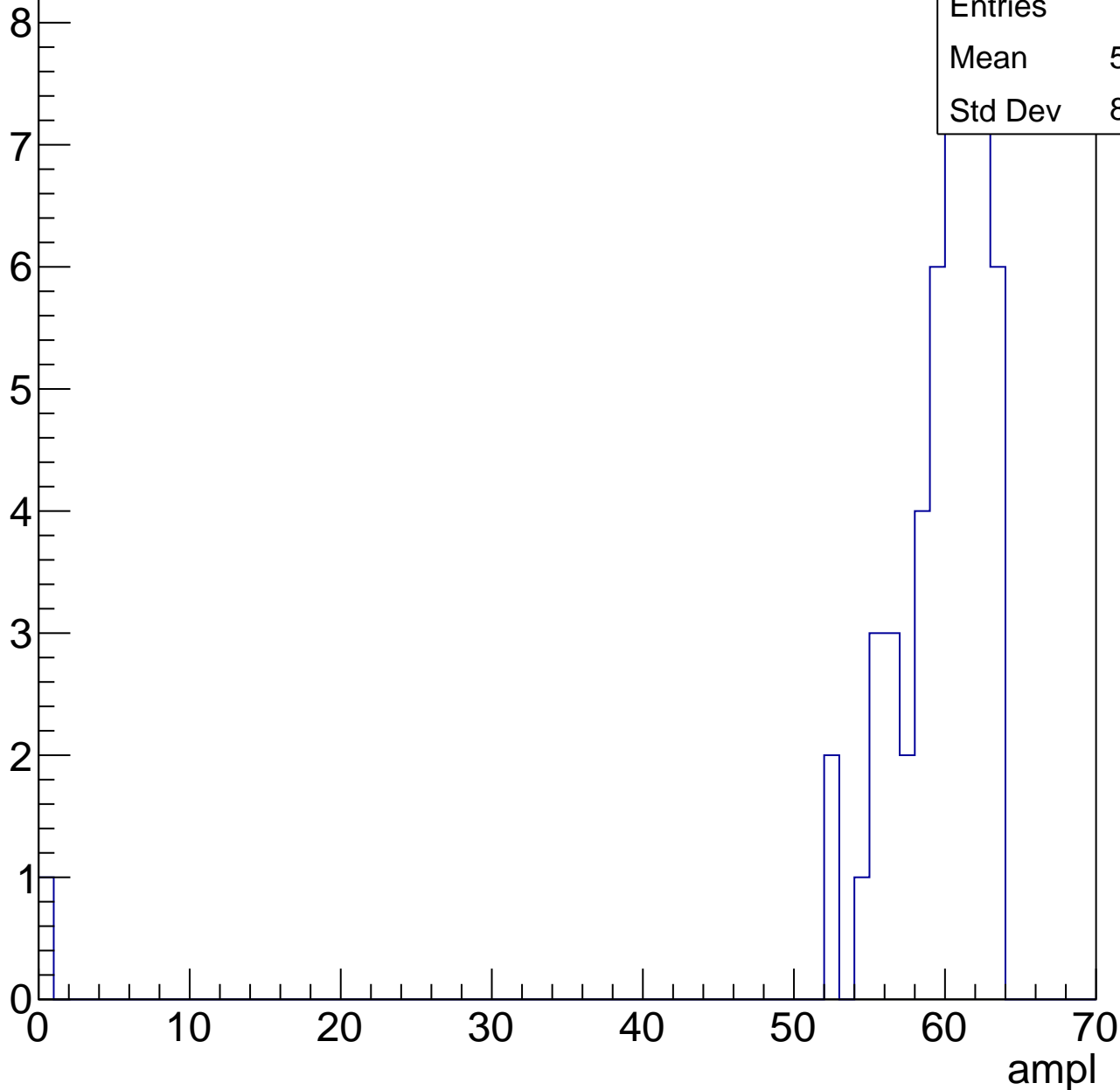


B1L103S, U17-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

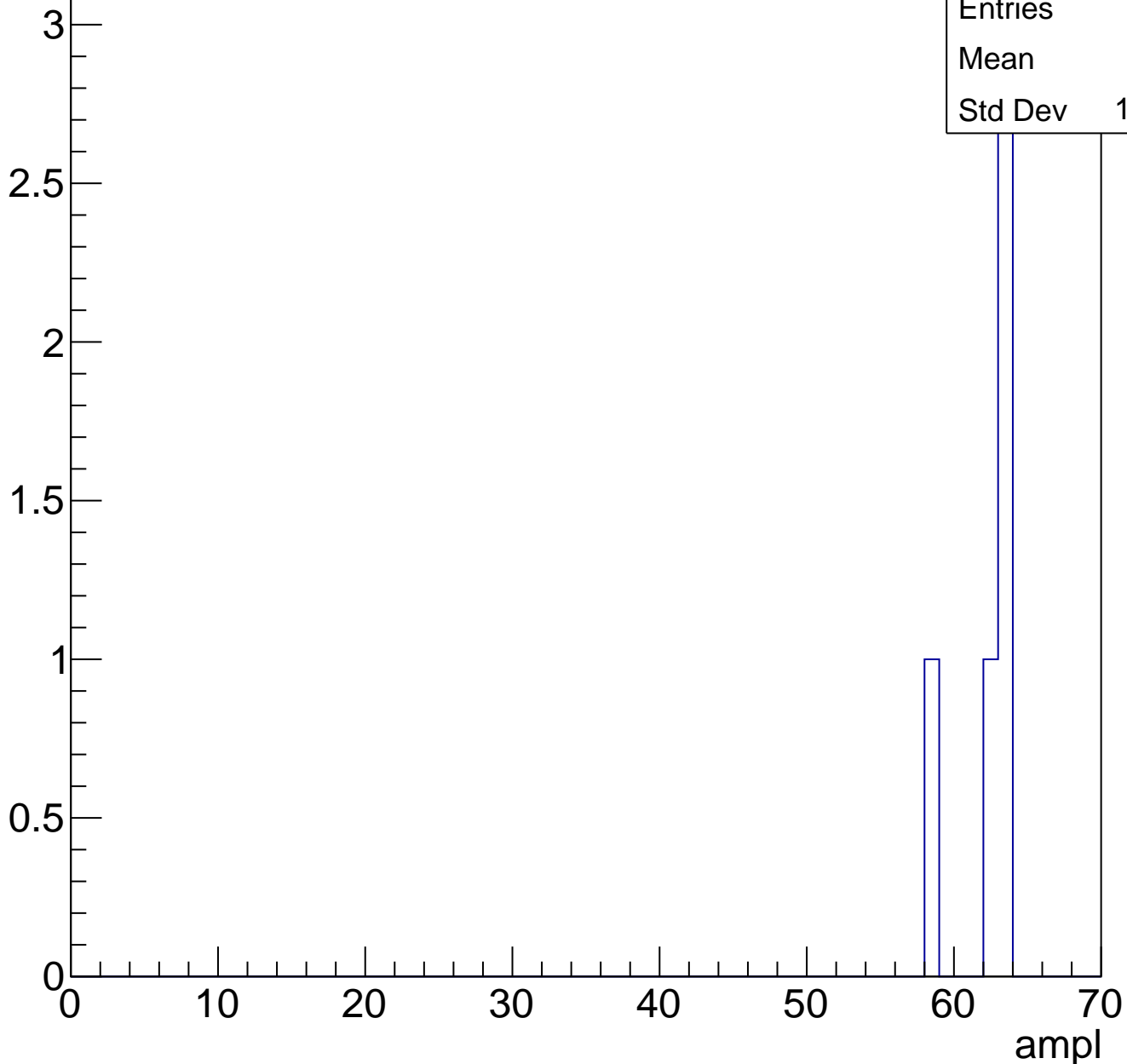
Entries	52
Mean	58.33
Std Dev	8.633



B1L103S, U17-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch49, adc0

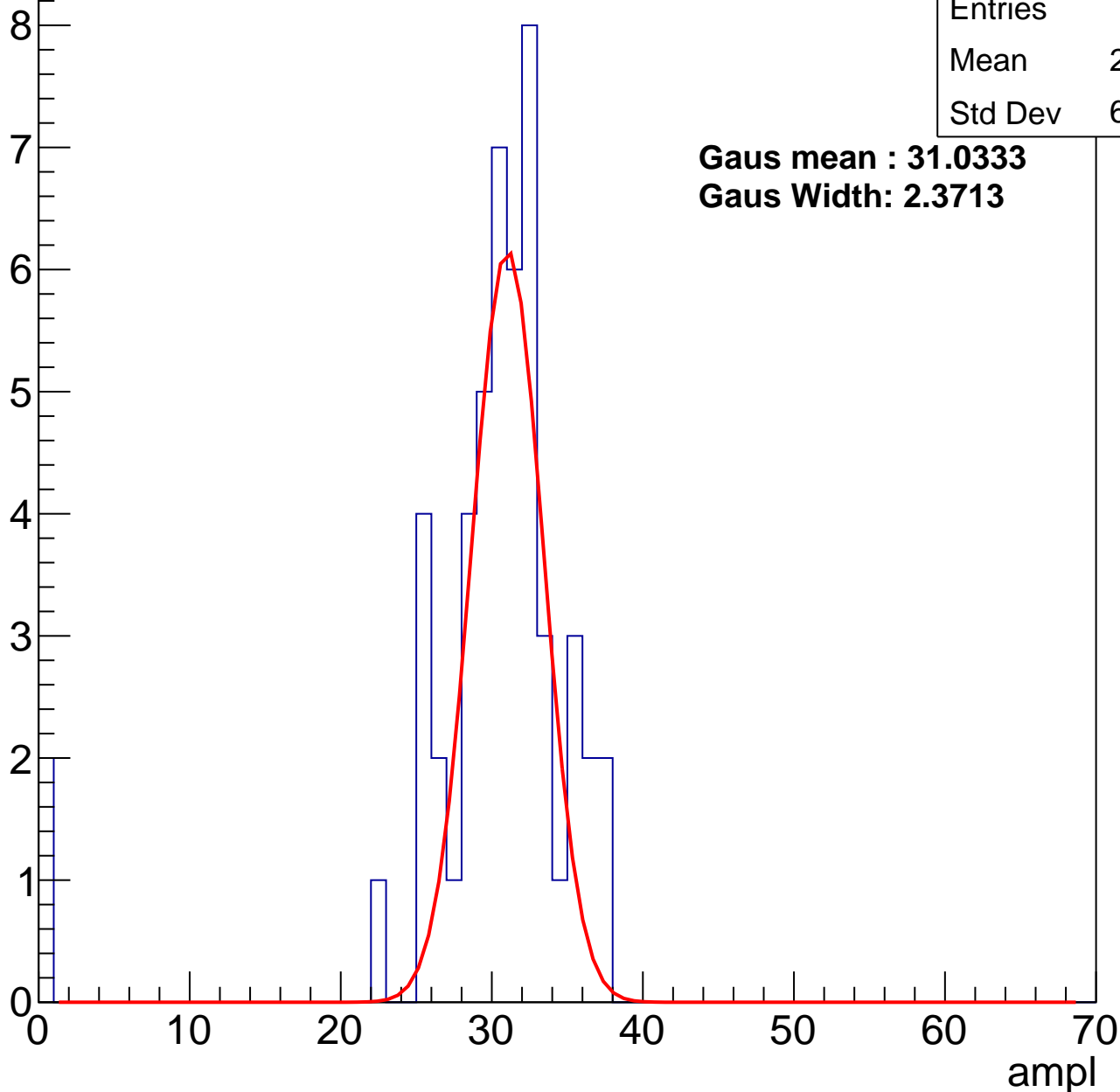
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	29.29
Std Dev	6.754

Gaus mean : 31.0333

Gaus Width: 2.3713



B1L103S, U17-ch49, adc1

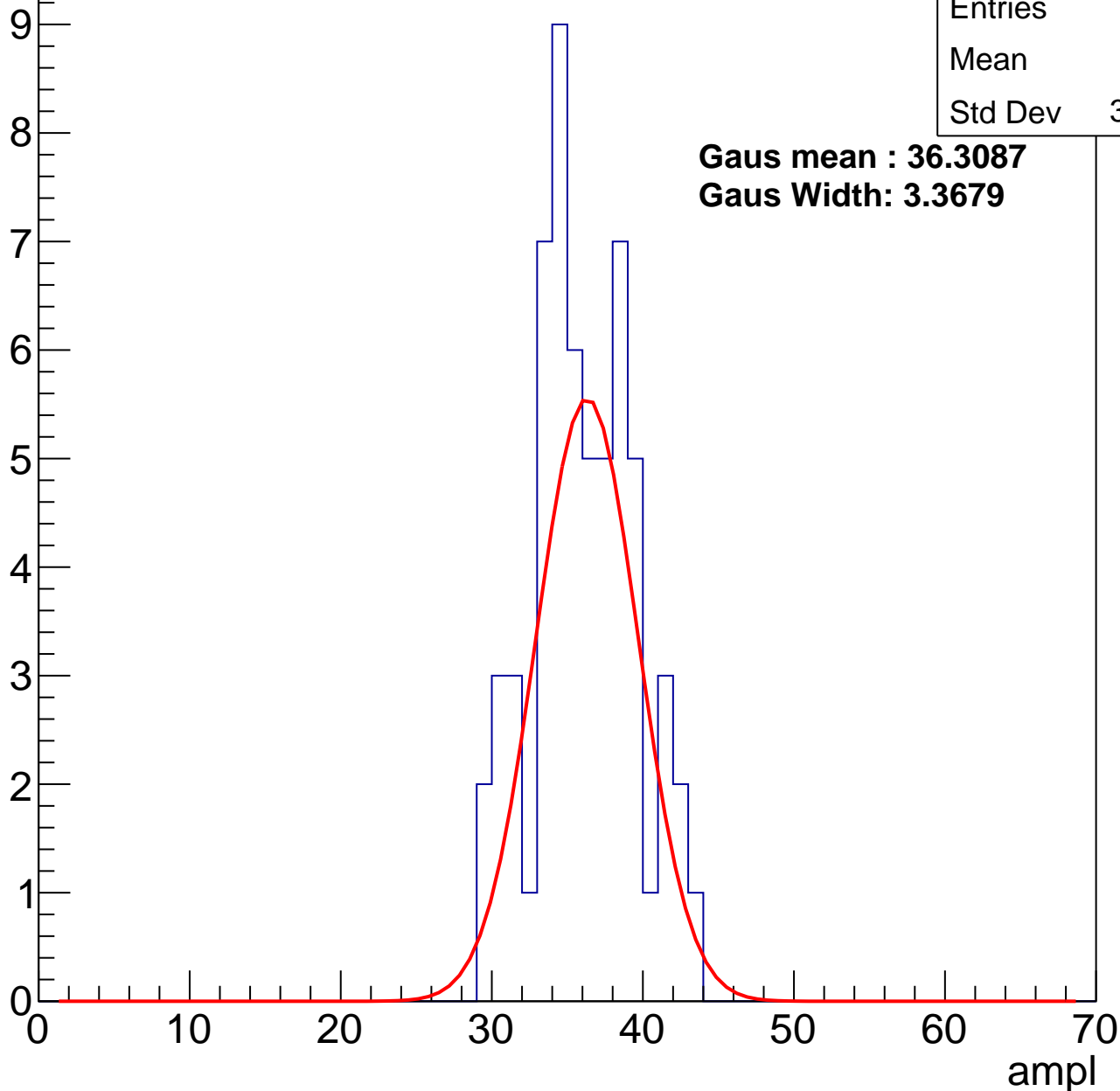
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.6
Std Dev	3.372

Gaus mean : 36.3087

Gaus Width: 3.3679



B1L103S, U17-ch49, adc2

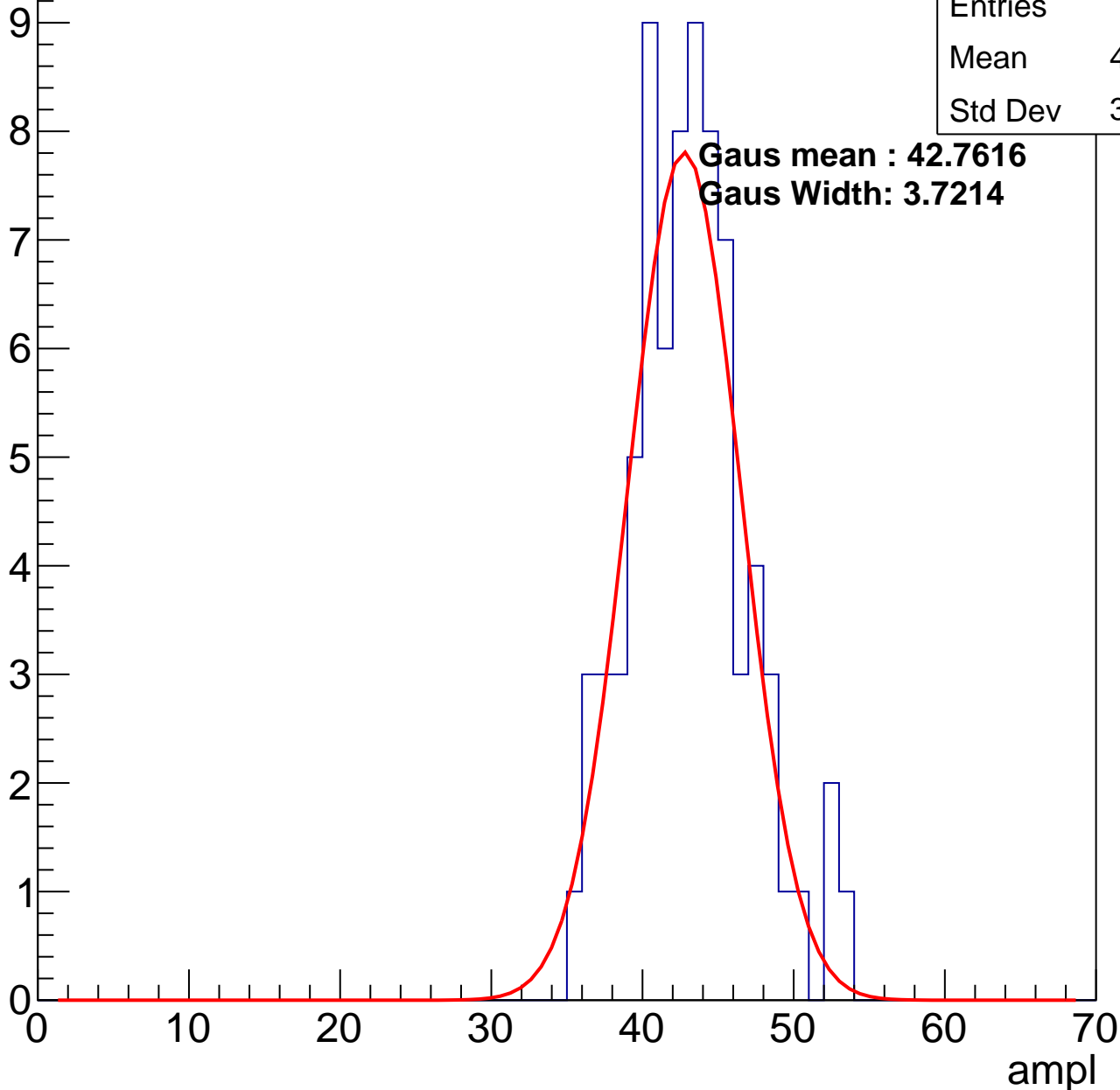
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	42.66
Std Dev	3.826

Gaus mean : 42.7616

Gaus Width: 3.7214

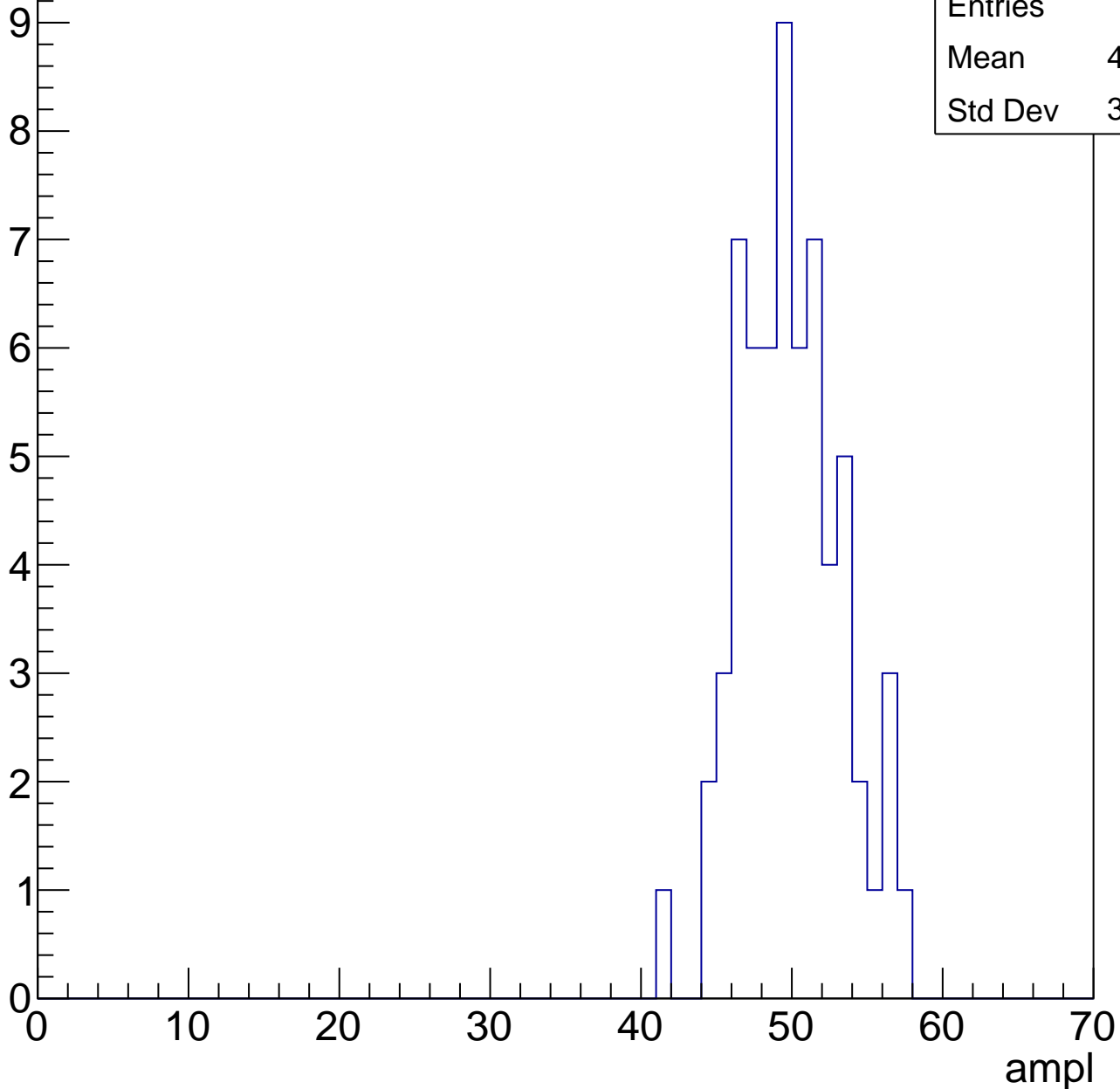


B1L103S, U17-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.44
Std Dev	3.308

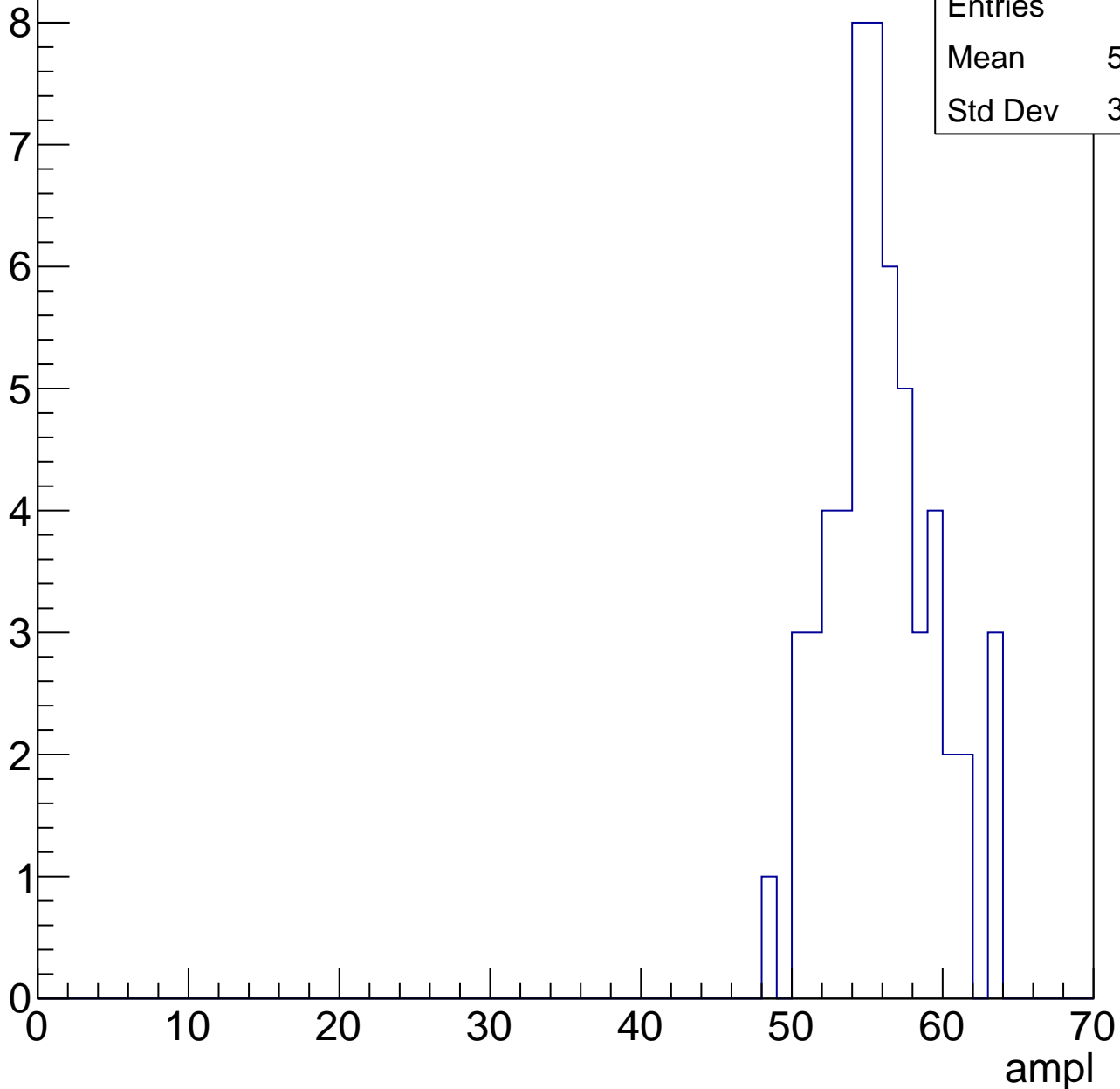


B1L103S, U17-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.45
Std Dev	3.412

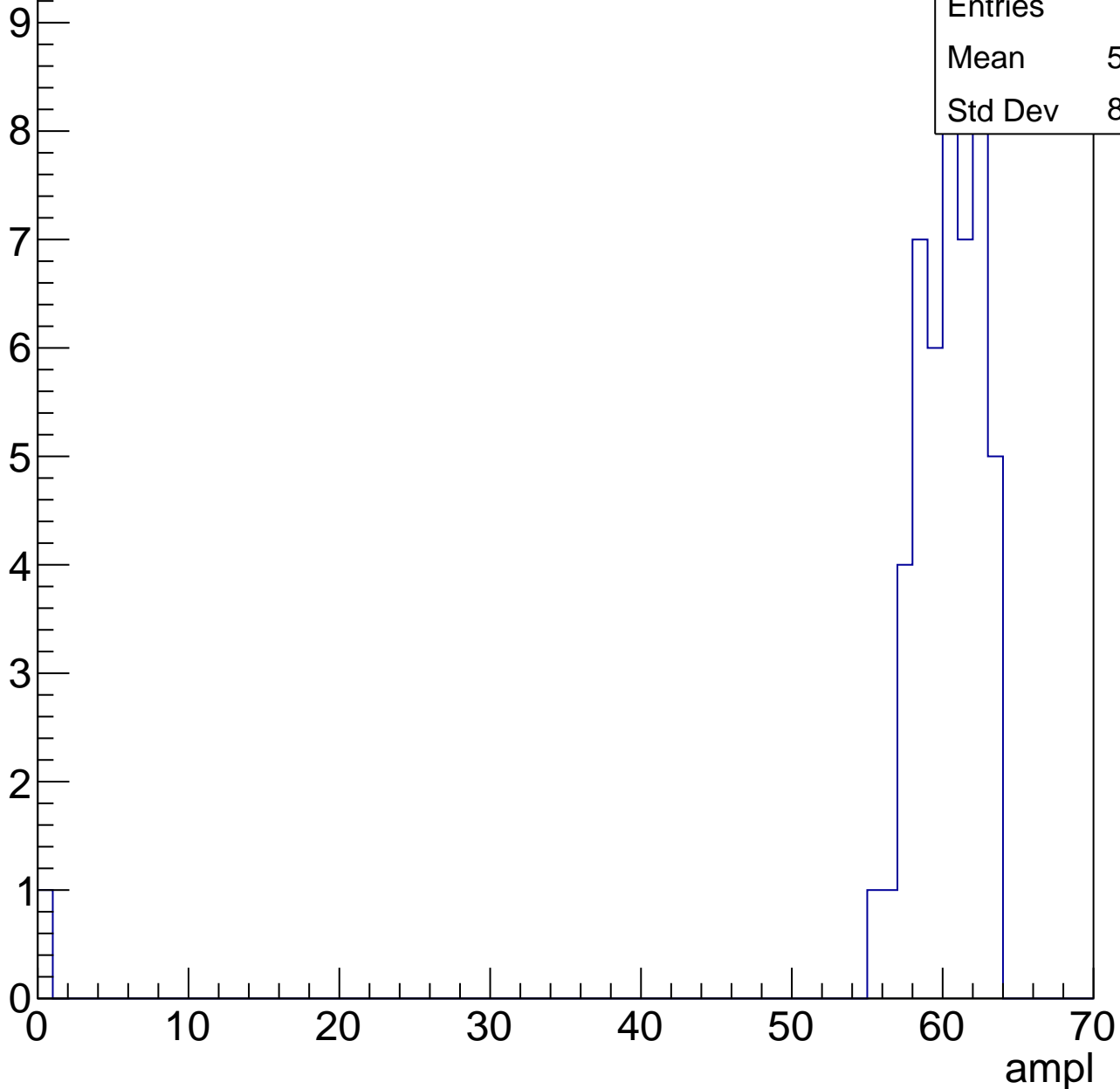


B1L103S, U17-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.76
Std Dev	8.717



B1L103S, U17-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch50, adc0

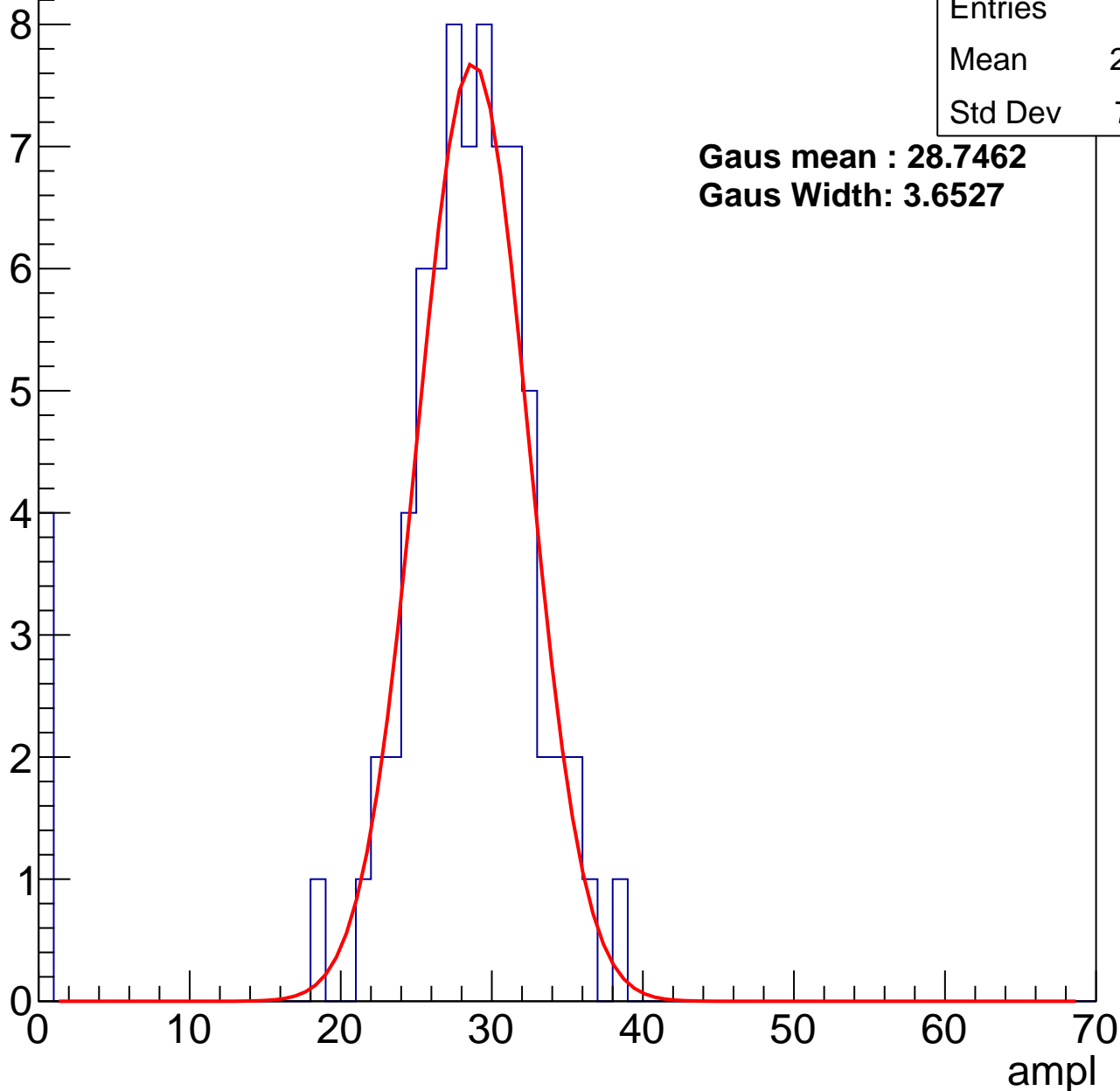
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	26.84
Std Dev	7.271

Gaus mean : 28.7462

Gaus Width: 3.6527



B1L103S, U17-ch50, adc1

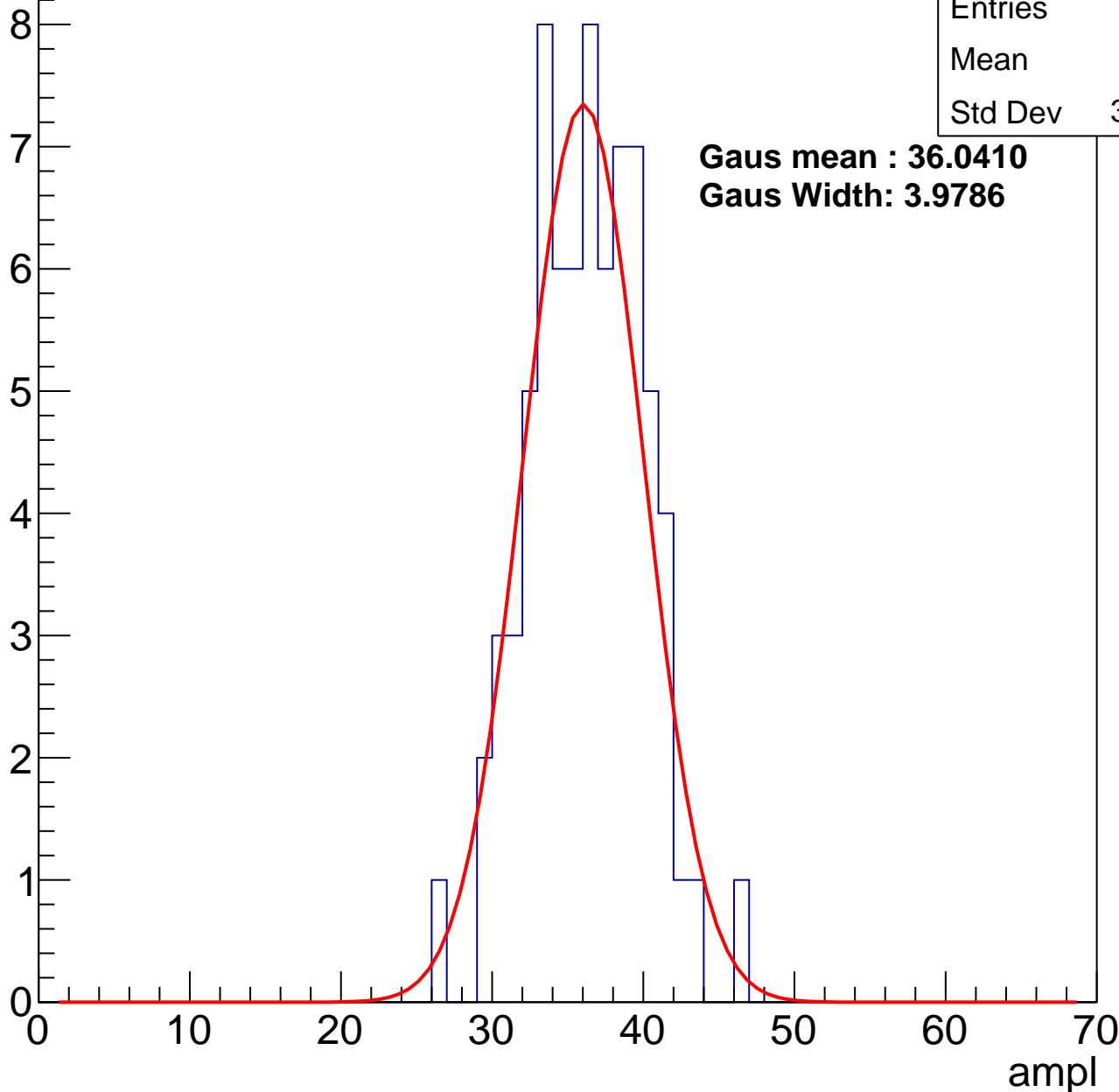
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.8
Std Dev	3.716

Gaus mean : 36.0410

Gaus Width: 3.9786



B1L103S, U17-ch50, adc2

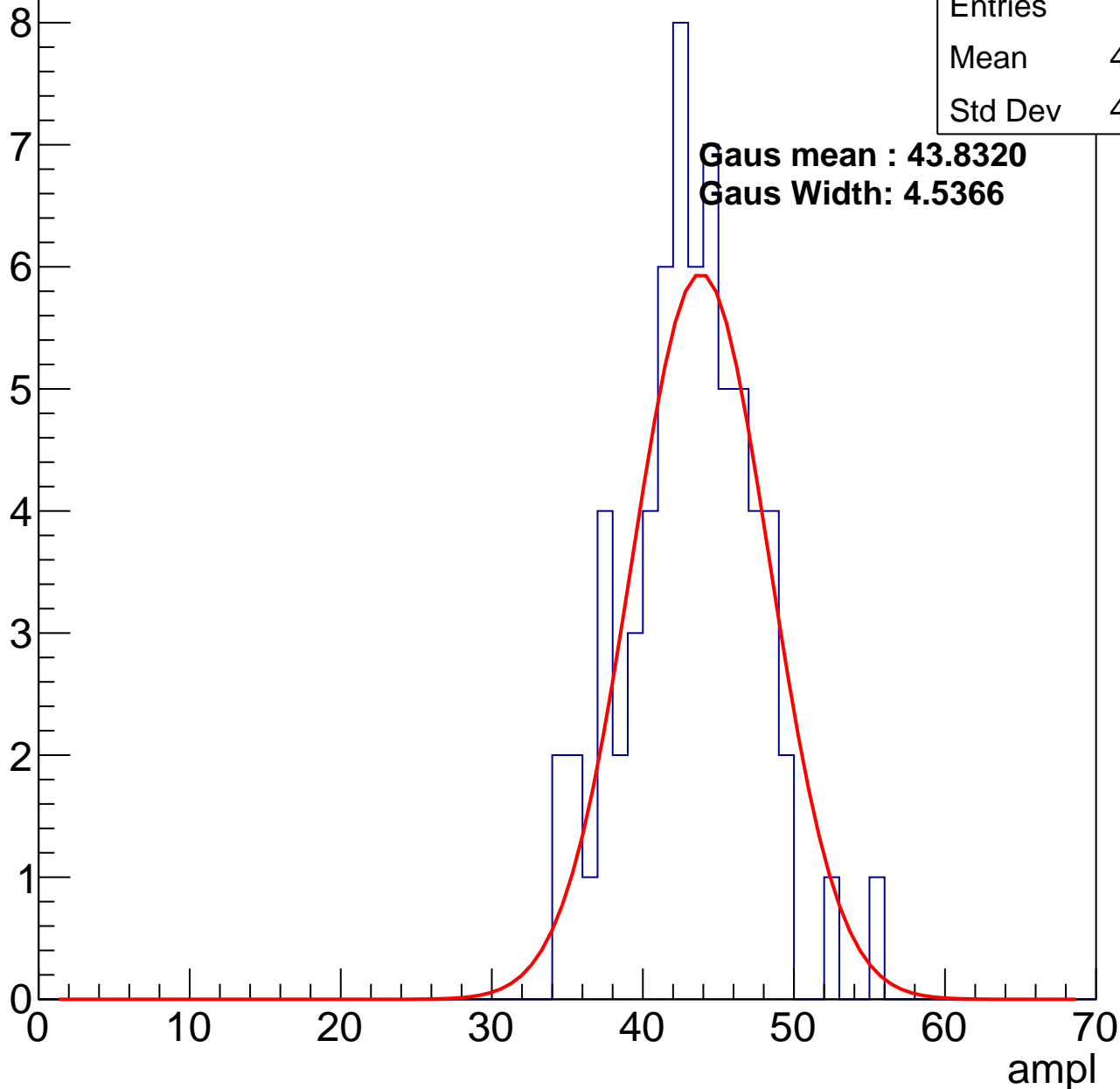
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.73
Std Dev	4.192

Gaus mean : 43.8320

Gaus Width: 4.5366

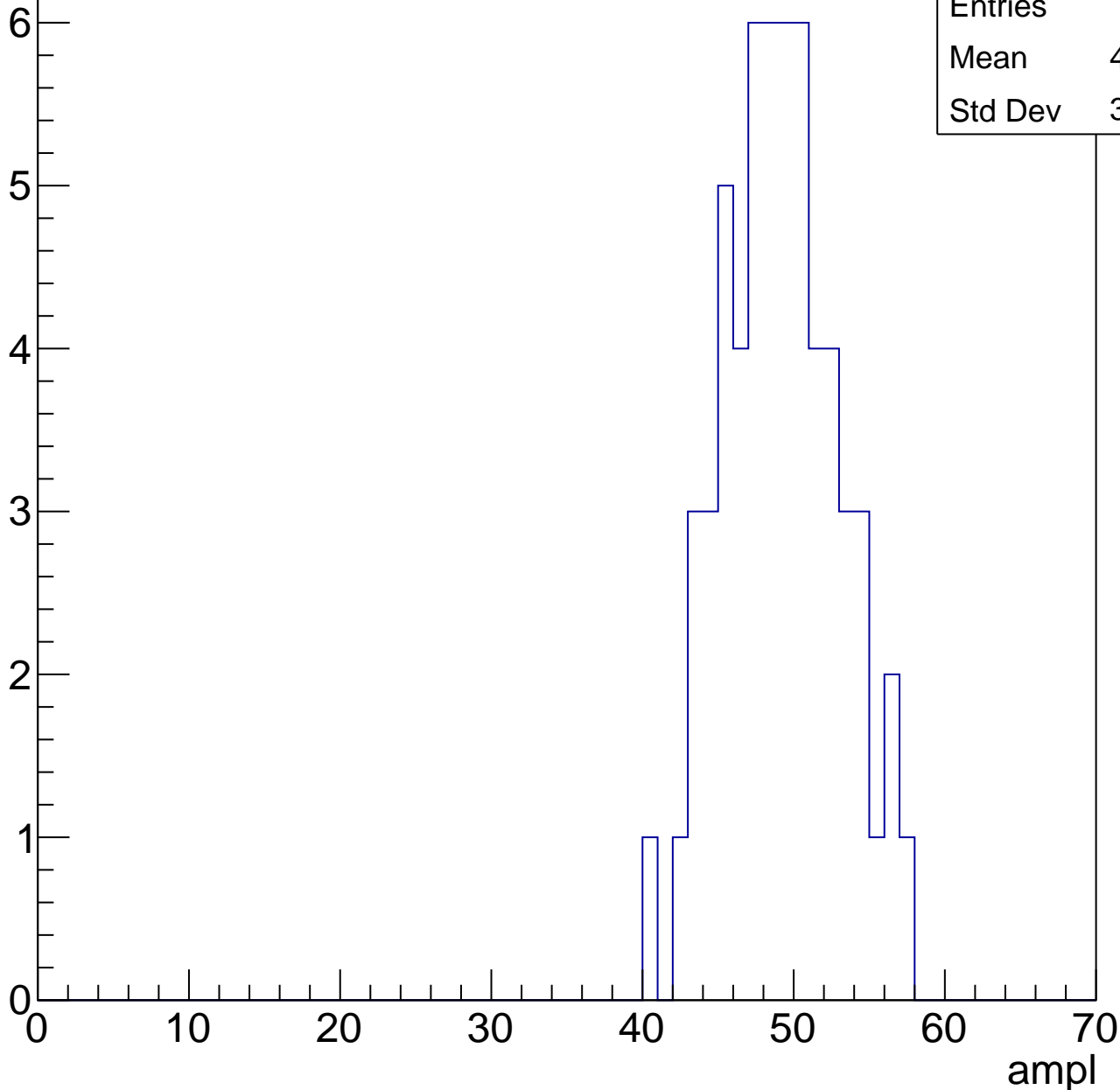


B1L103S, U17-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.69
Std Dev	3.756

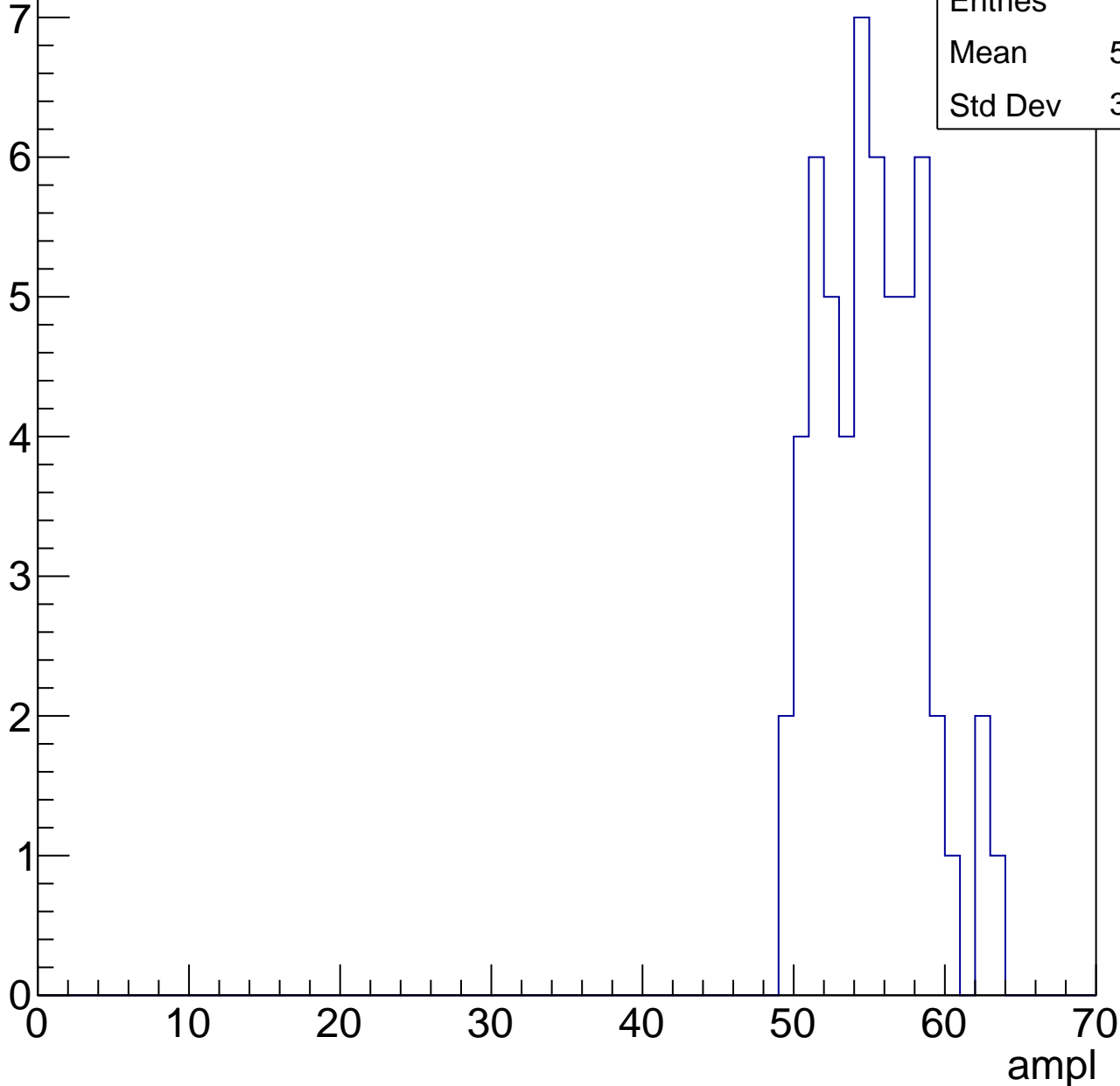


B1L103S, U17-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.68
Std Dev	3.349

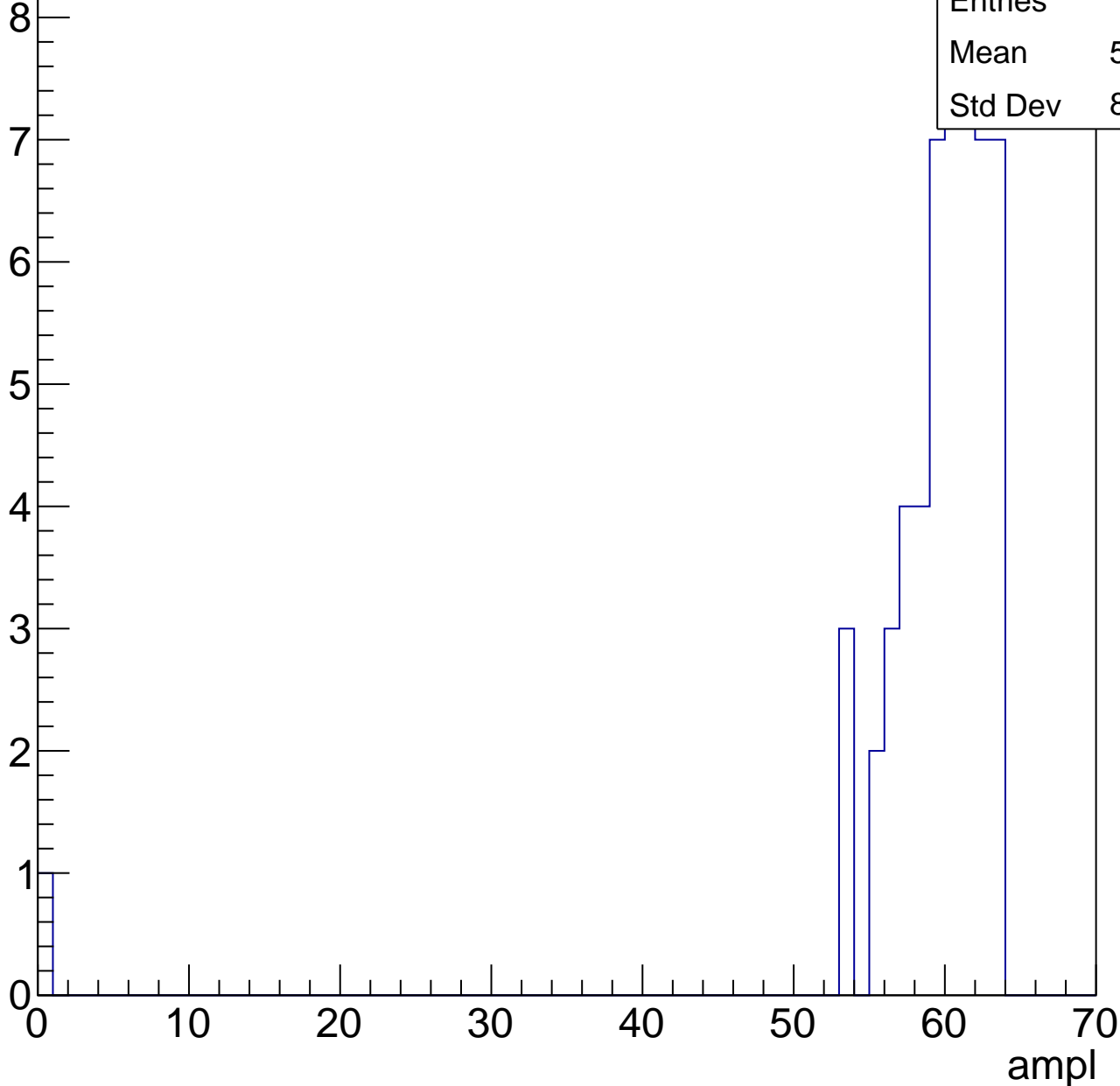


B1L103S, U17-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58.39
Std Dev	8.458



B1L103S, U17-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.385

ampl

0 10 20 30 40 50 60 70

B1L103S, U17-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch51, adc0

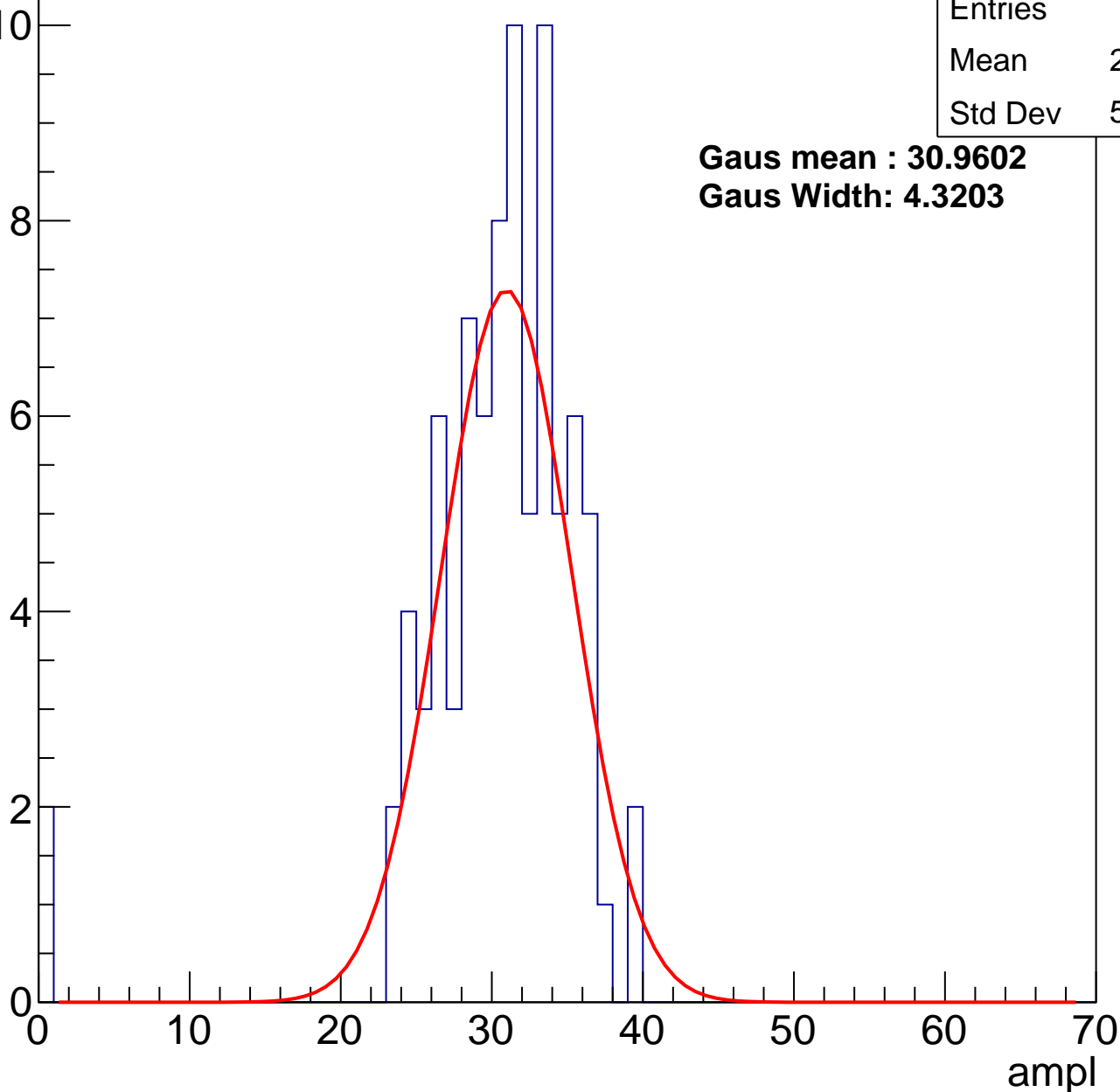
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	29.87
Std Dev	5.954

Gaus mean : 30.9602

Gaus Width: 4.3203



B1L103S, U17-ch51, adc1

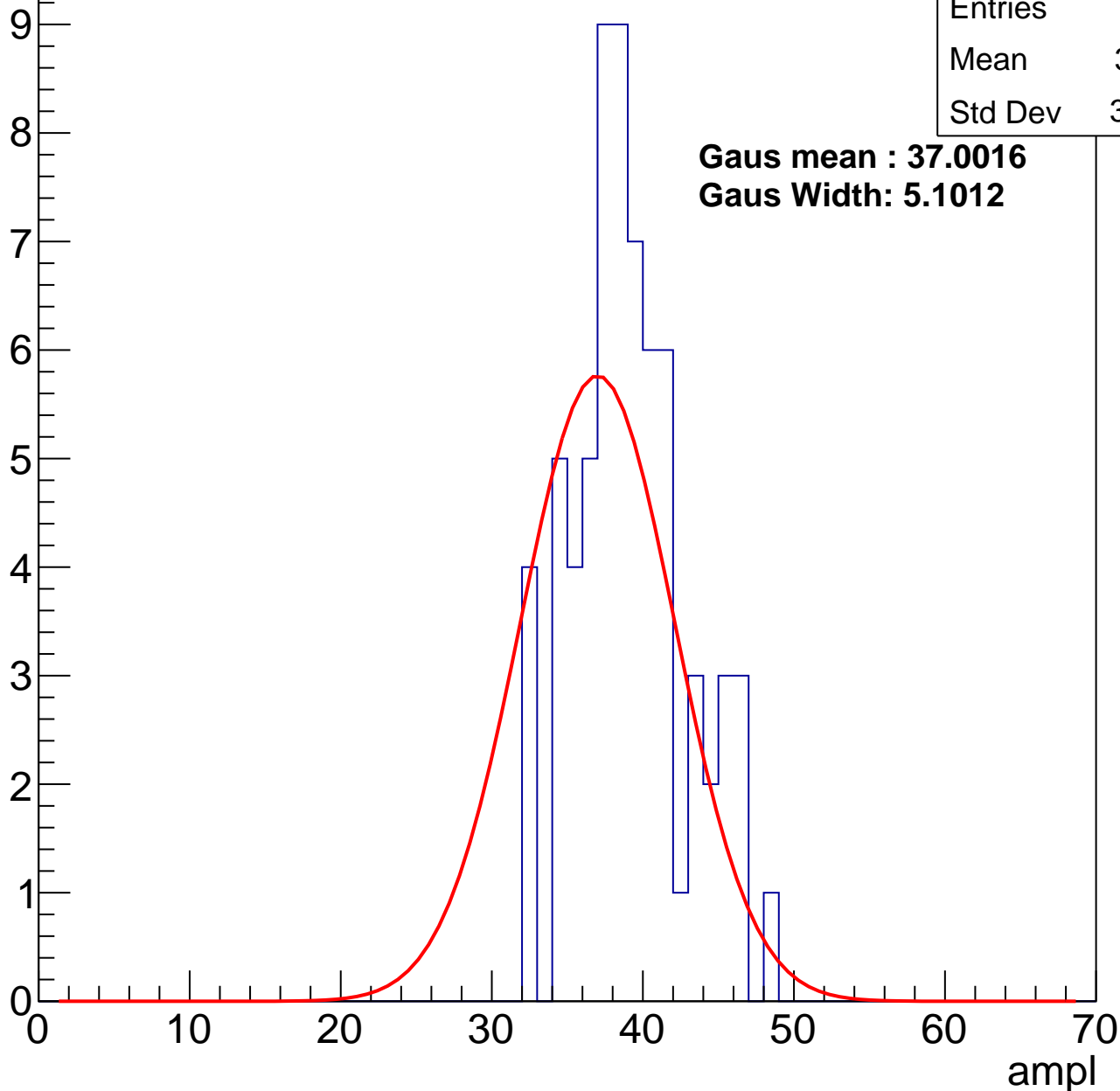
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	38.71
Std Dev	3.722

Gaus mean : 37.0016

Gaus Width: 5.1012



B1L103S, U17-ch51, adc2

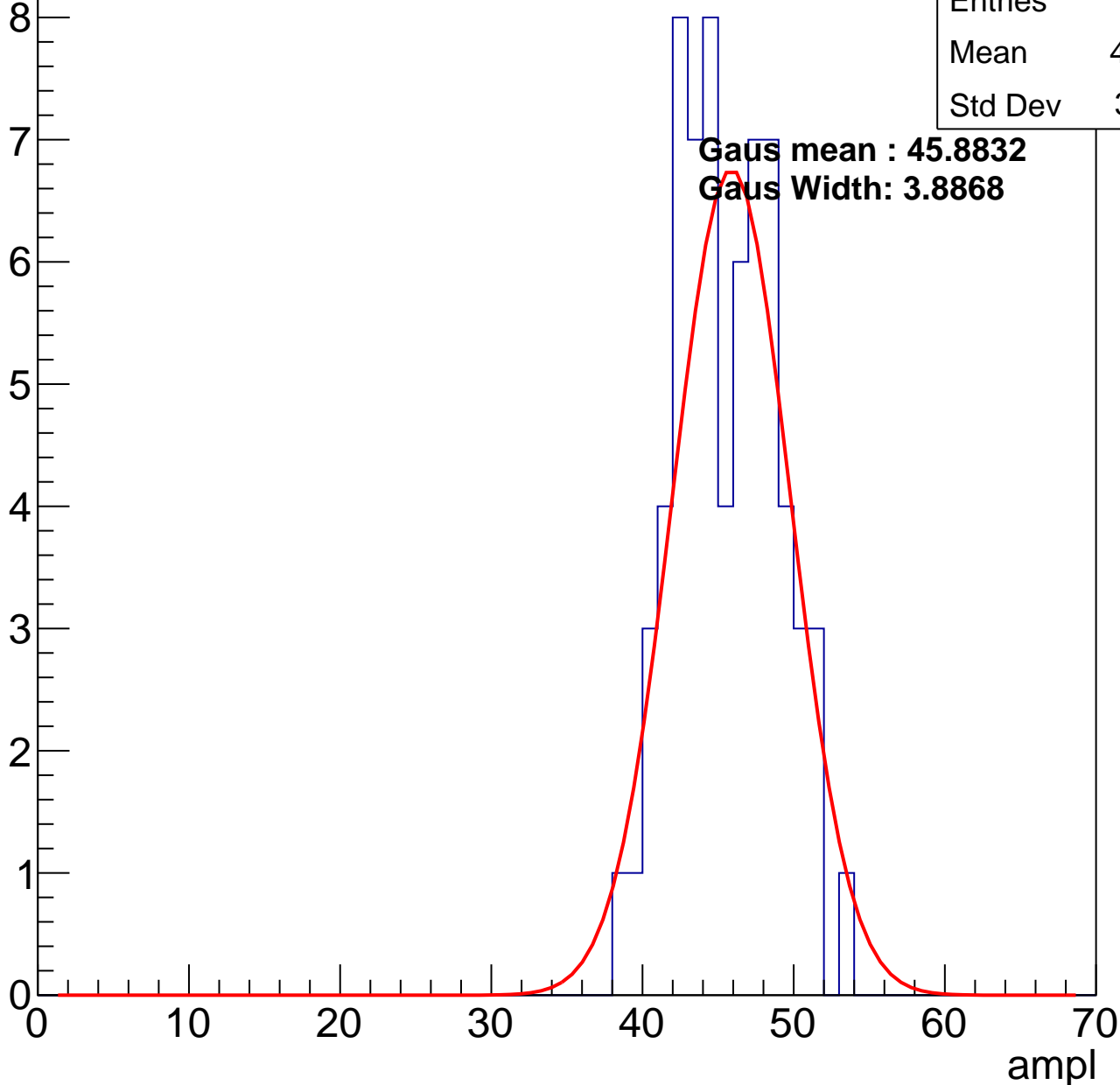
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	45.12
Std Dev	3.321

Gaus mean : 45.8832

Gaus Width: 3.8868

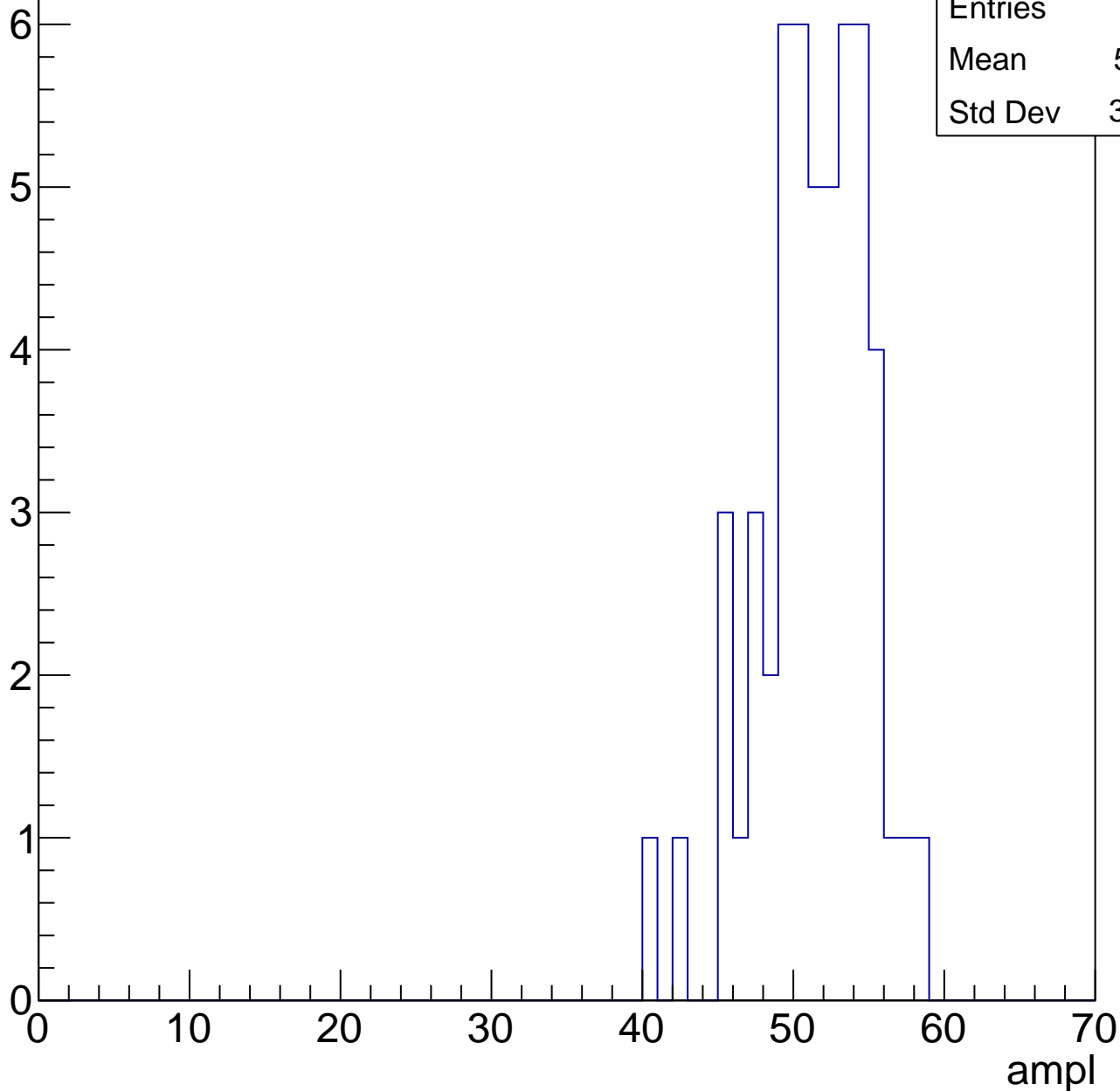


B1L103S, U17-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

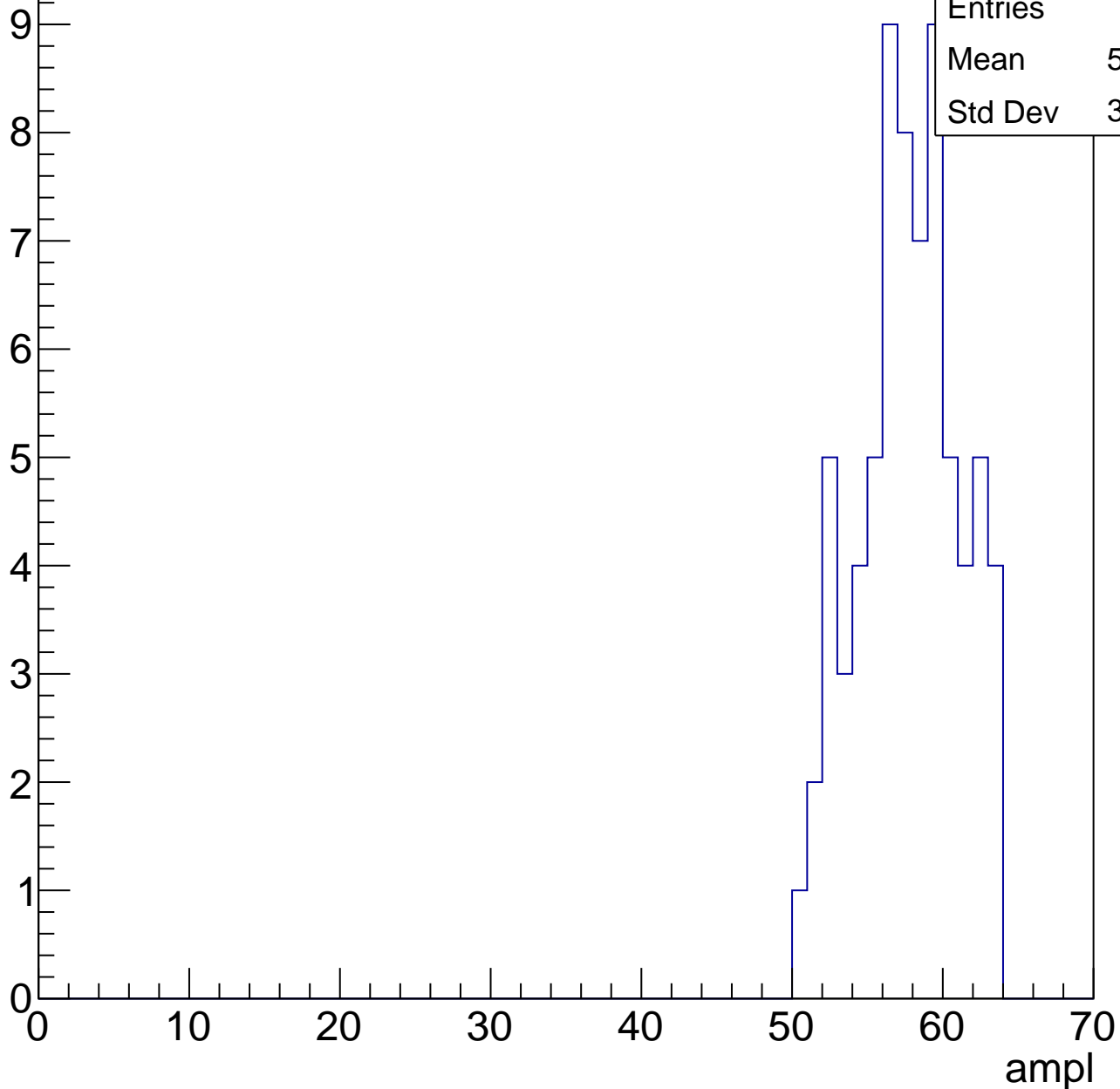
Entries	52
Mean	50.81
Std Dev	3.632



B1L103S, U17-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

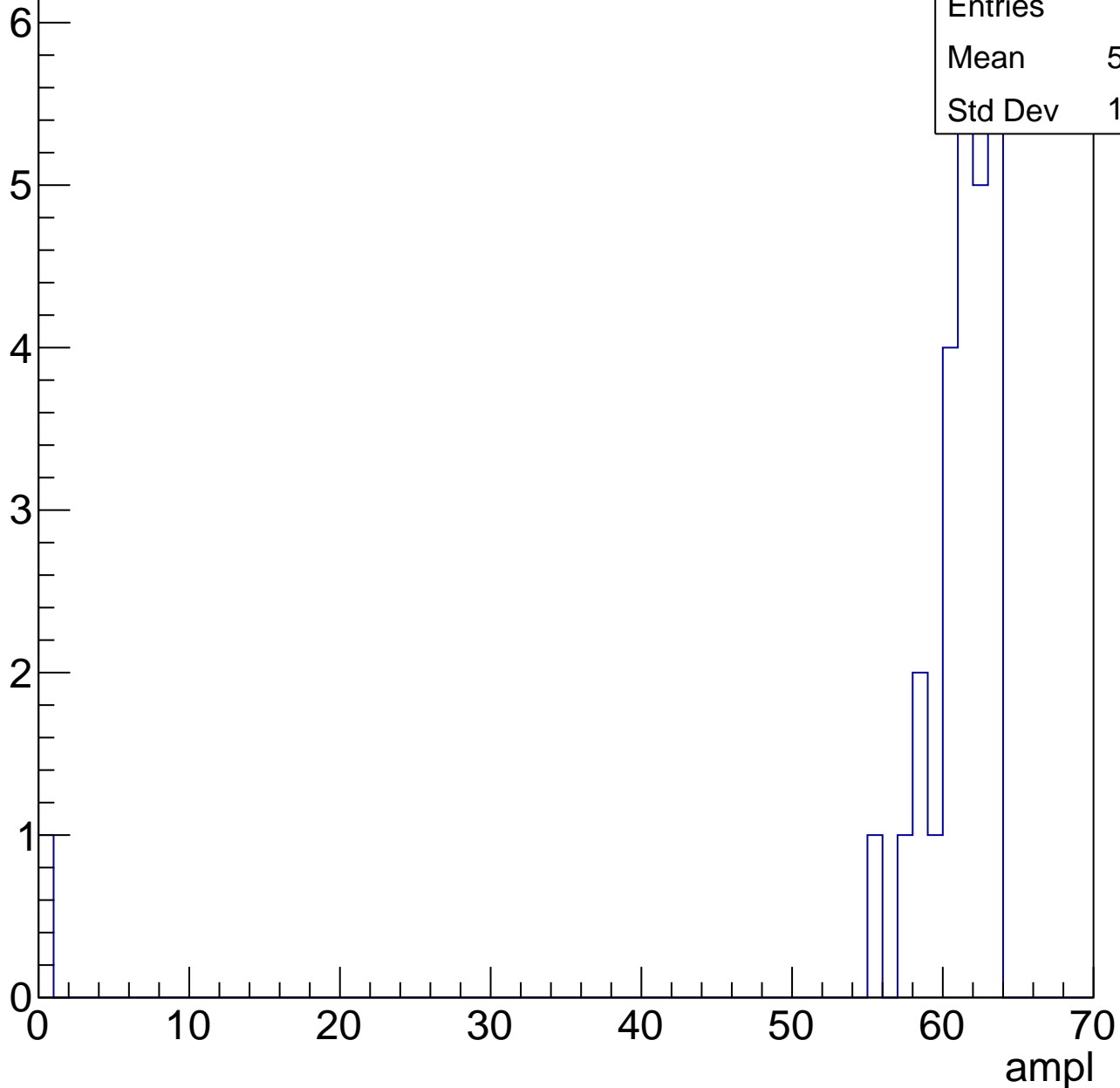


Entries	71
Mean	57.25
Std Dev	3.305

B1L103S, U17-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch52, adc0

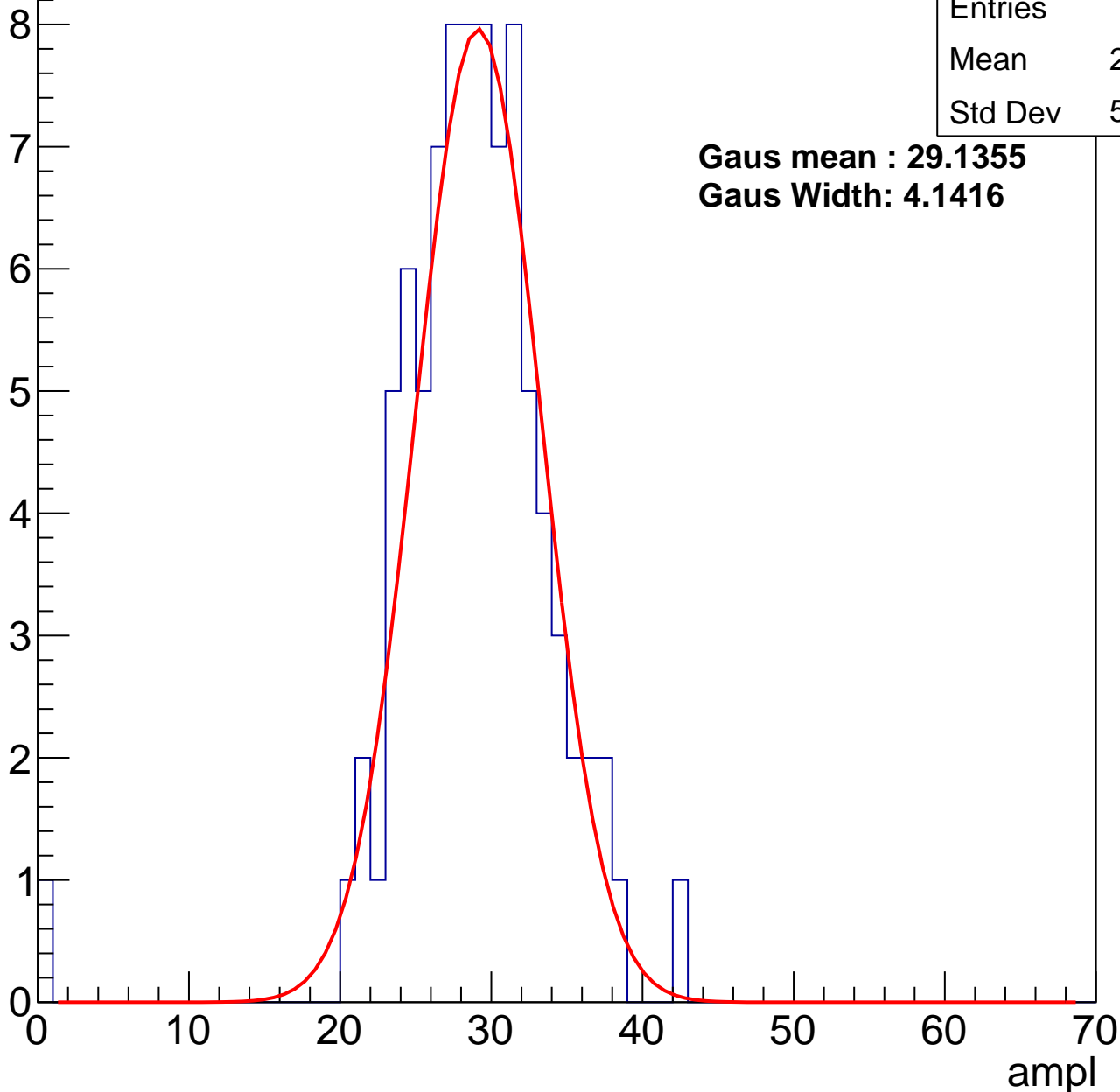
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	28.39
Std Dev	5.198

Gaus mean : 29.1355

Gaus Width: 4.1416



B1L103S, U17-ch52, adc1

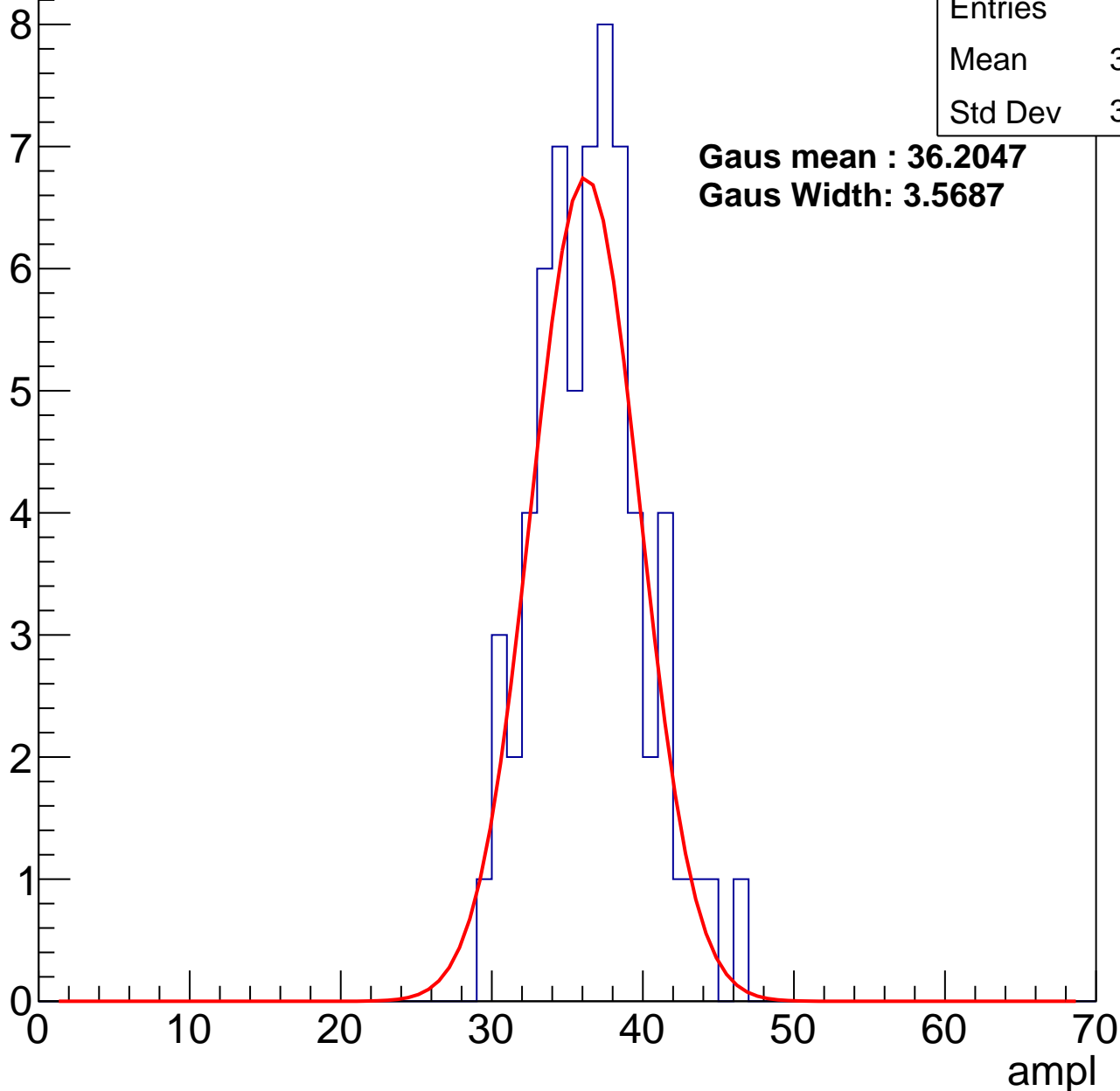
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.08
Std Dev	3.559

Gaus mean : 36.2047

Gaus Width: 3.5687



B1L103S, U17-ch52, adc2

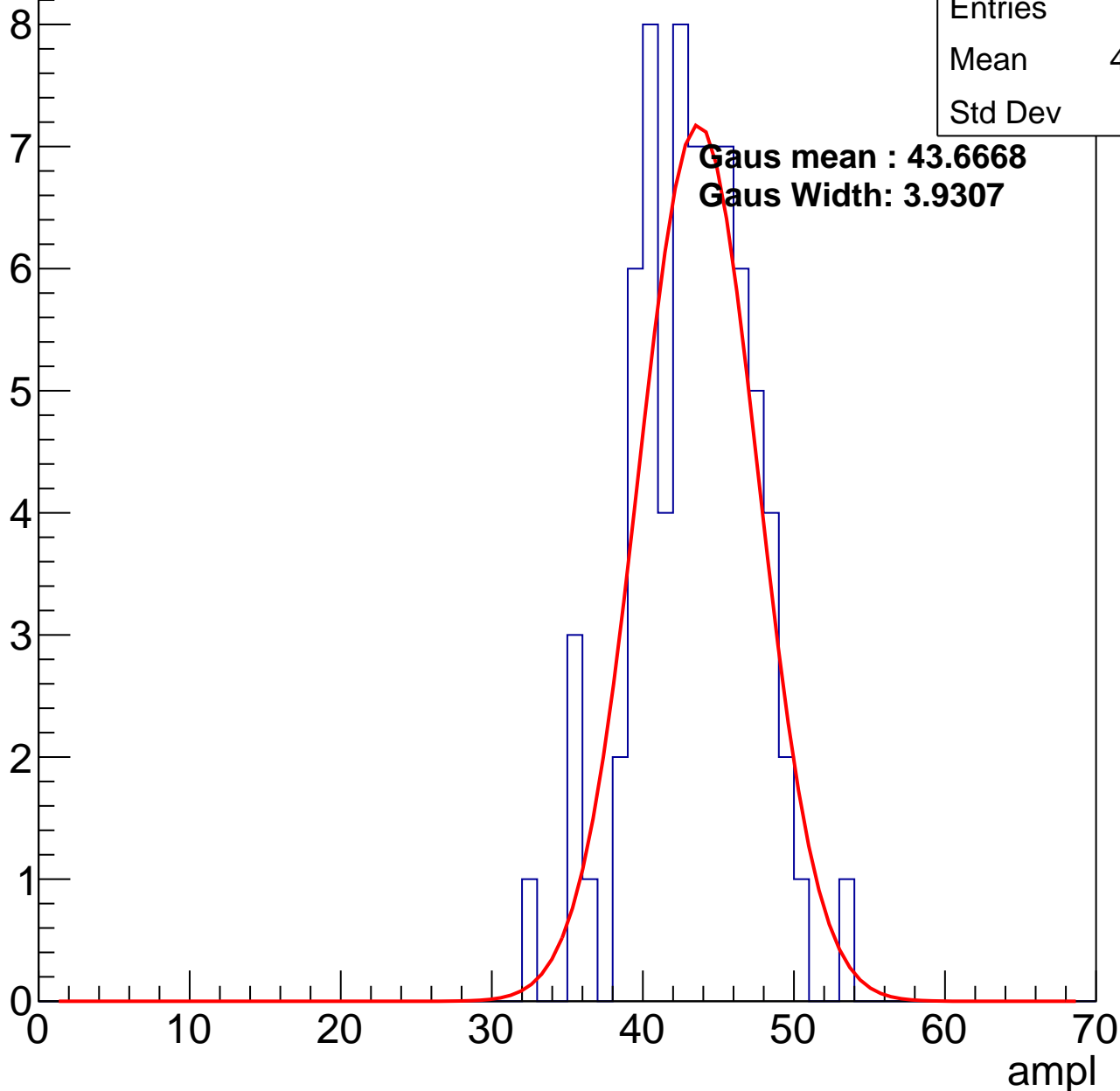
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.89
Std Dev	3.87

Gaus mean : 43.6668

Gaus Width: 3.9307

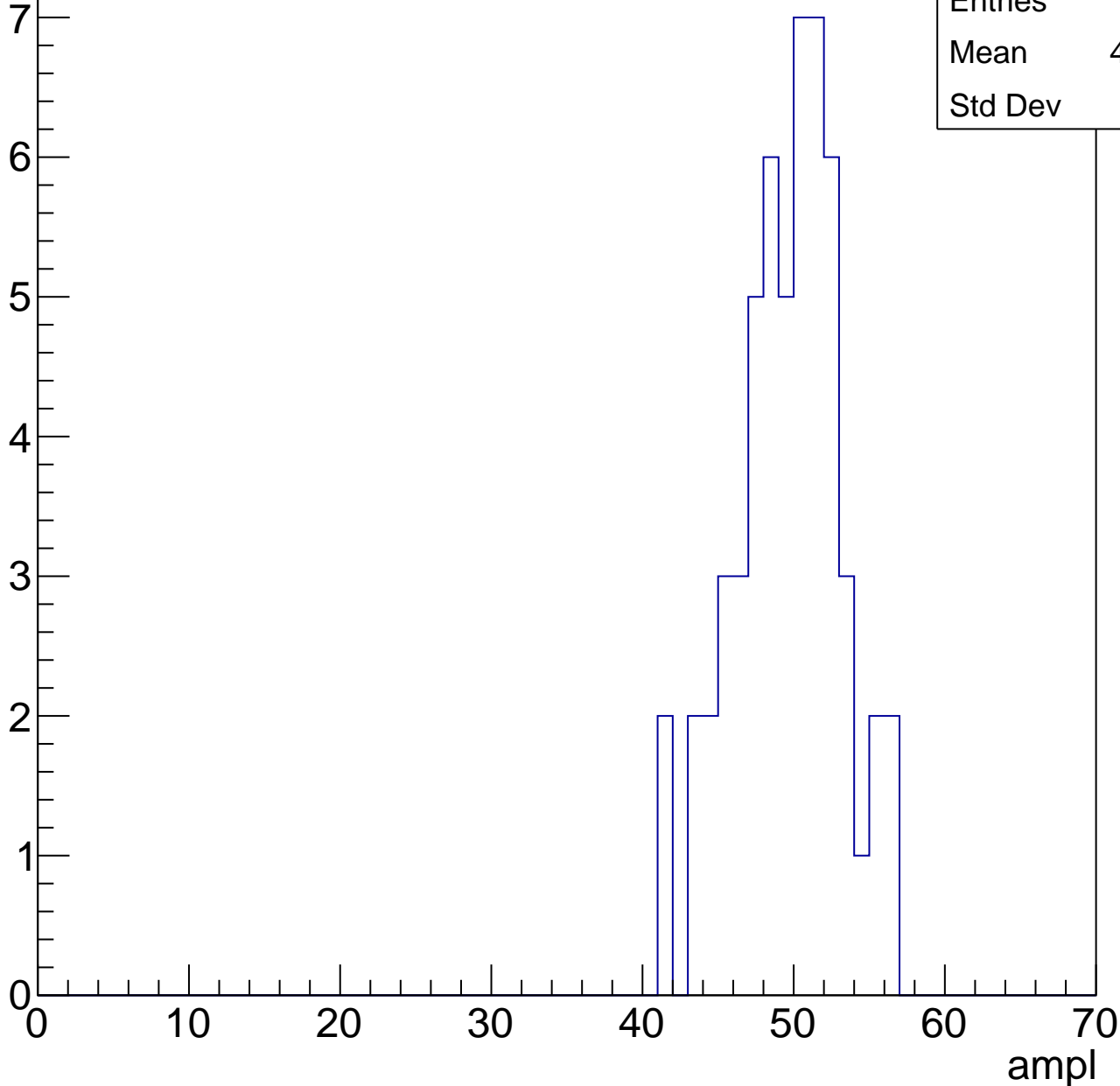


B1L103S, U17-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.12
Std Dev	3.49

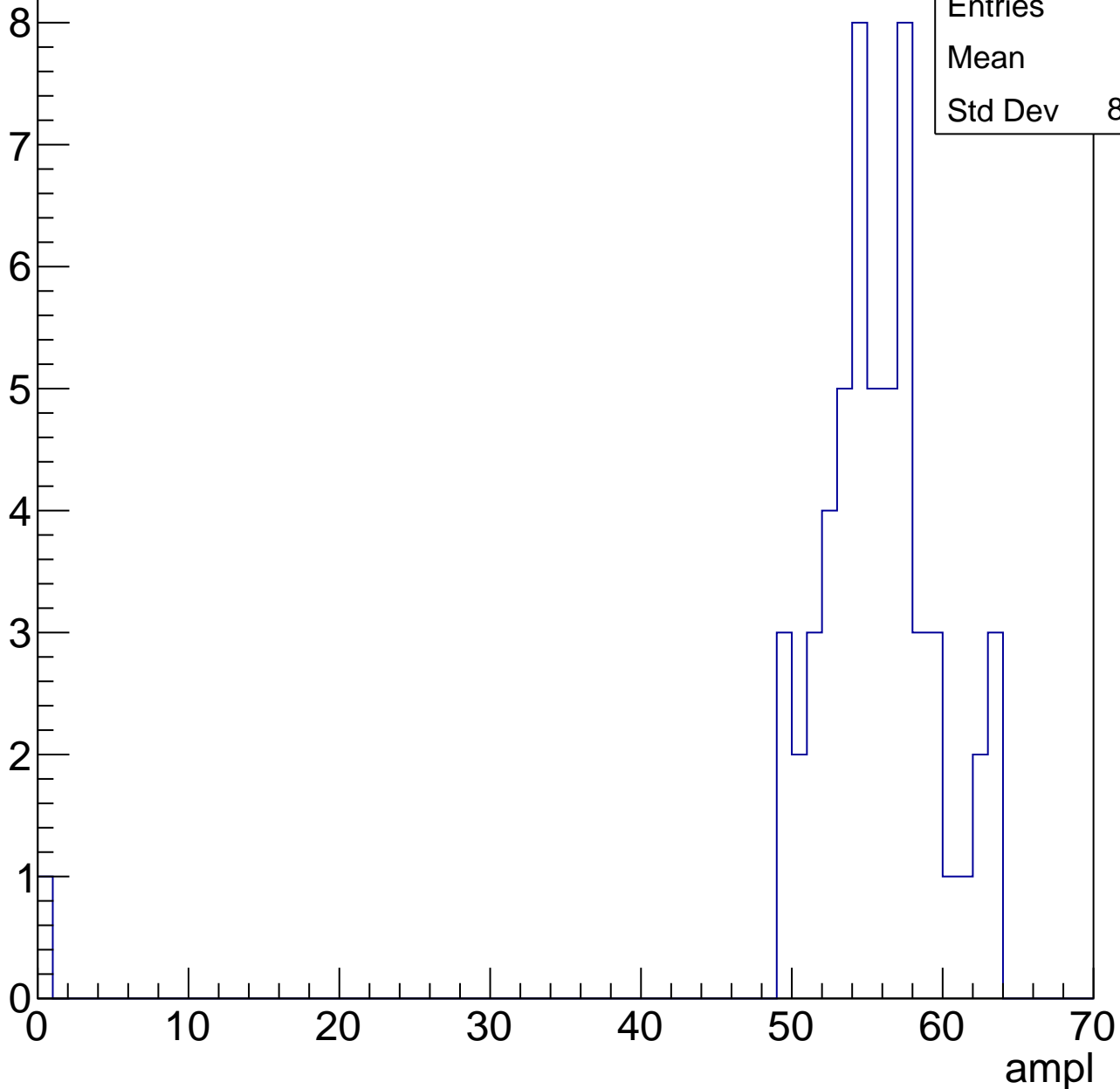


B1L103S, U17-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.4
Std Dev	8.098

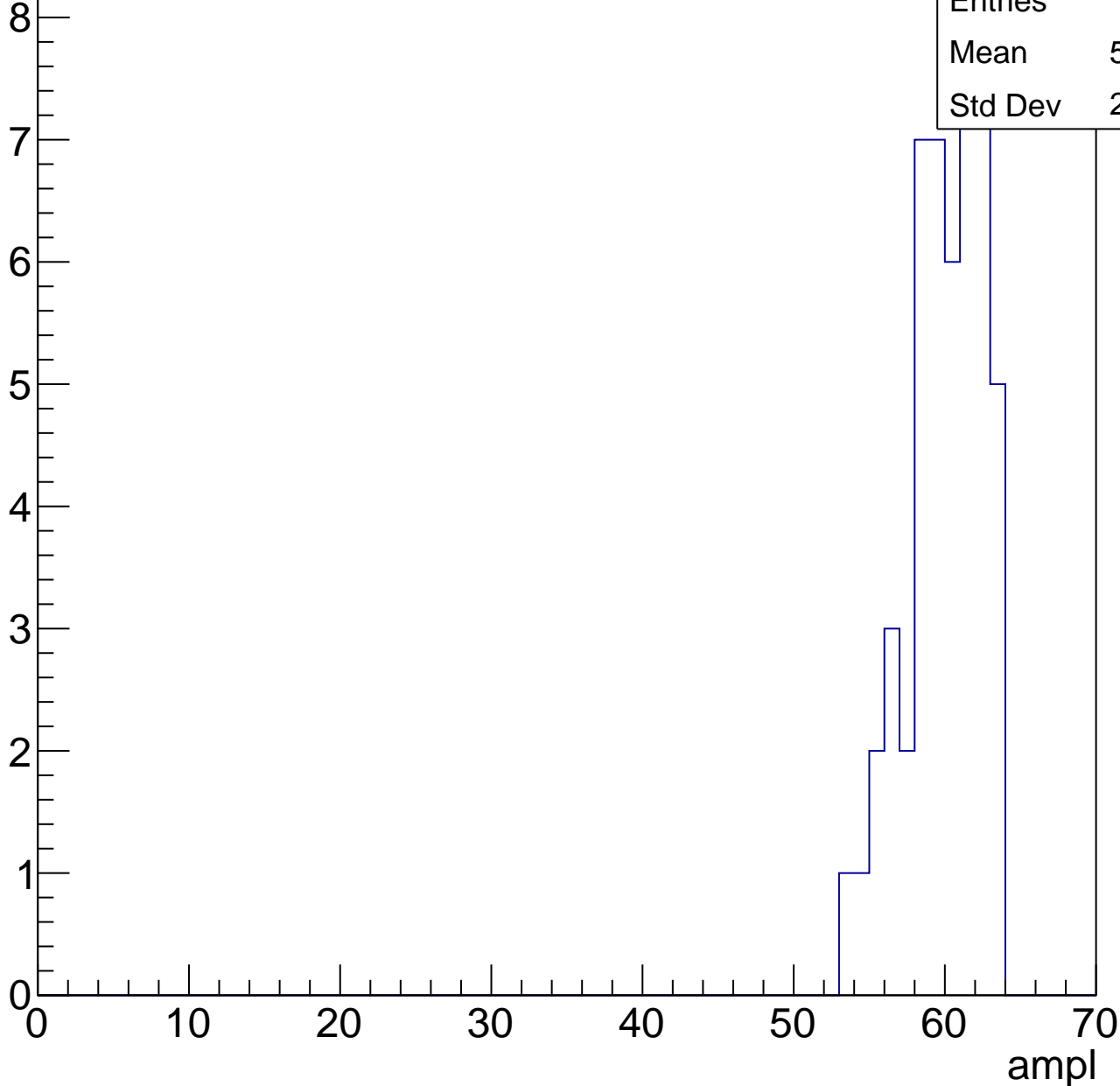


B1L103S, U17-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

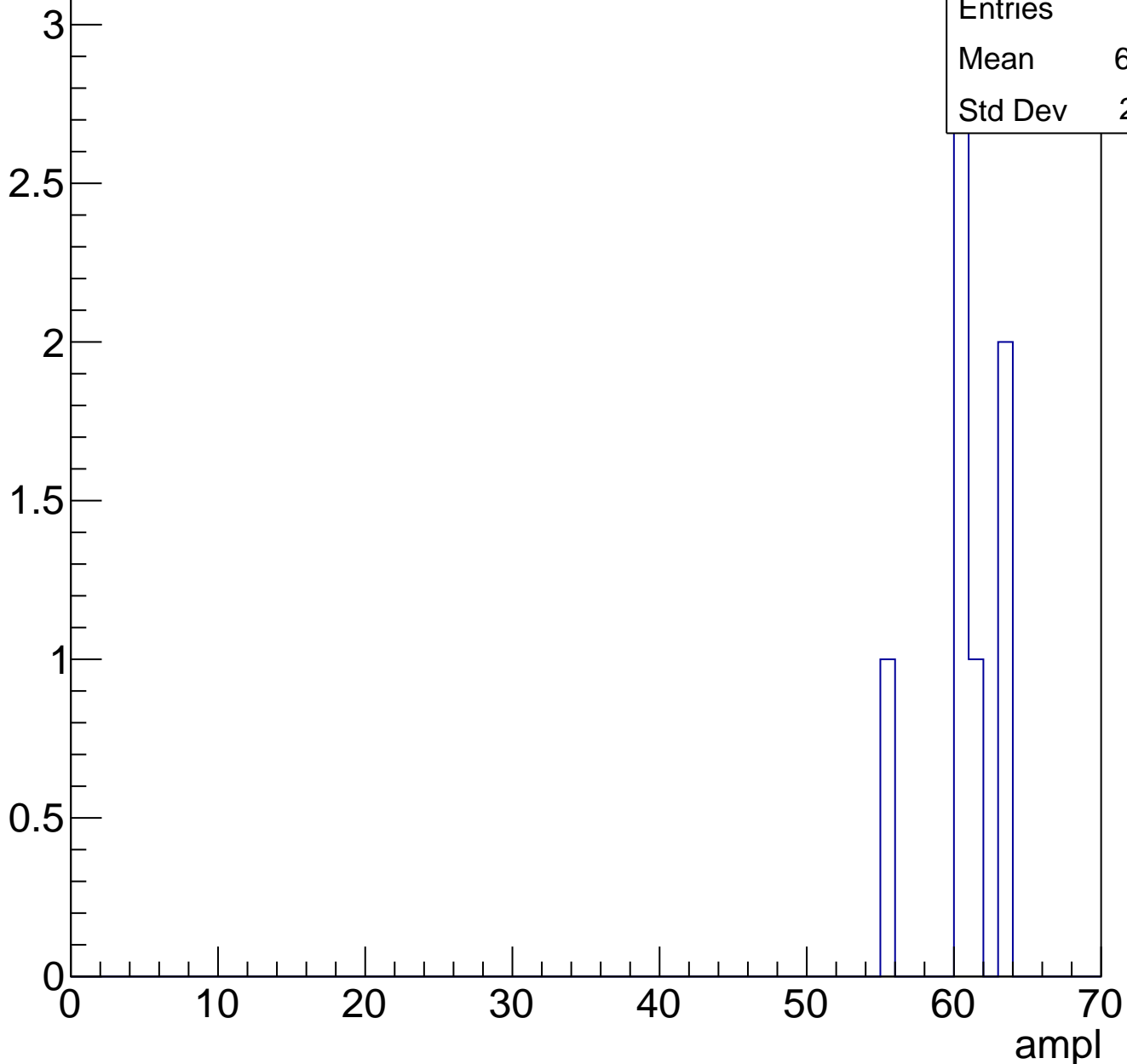
Entries	50
Mean	59.54
Std Dev	2.492



B1L103S, U17-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch53, adc0

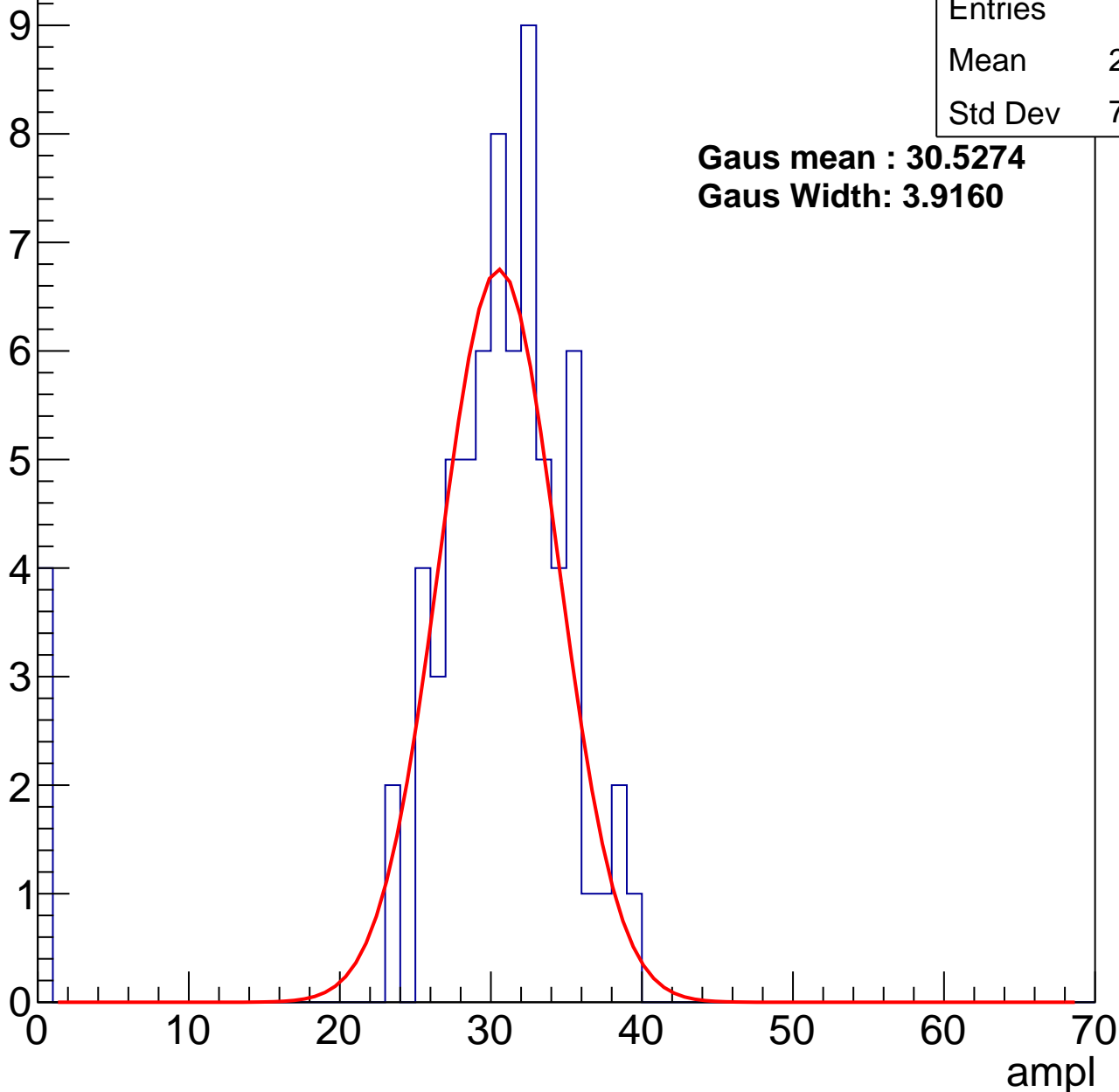
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.97
Std Dev	7.849

Gaus mean : 30.5274

Gaus Width: 3.9160



B1L103S, U17-ch53, adc1

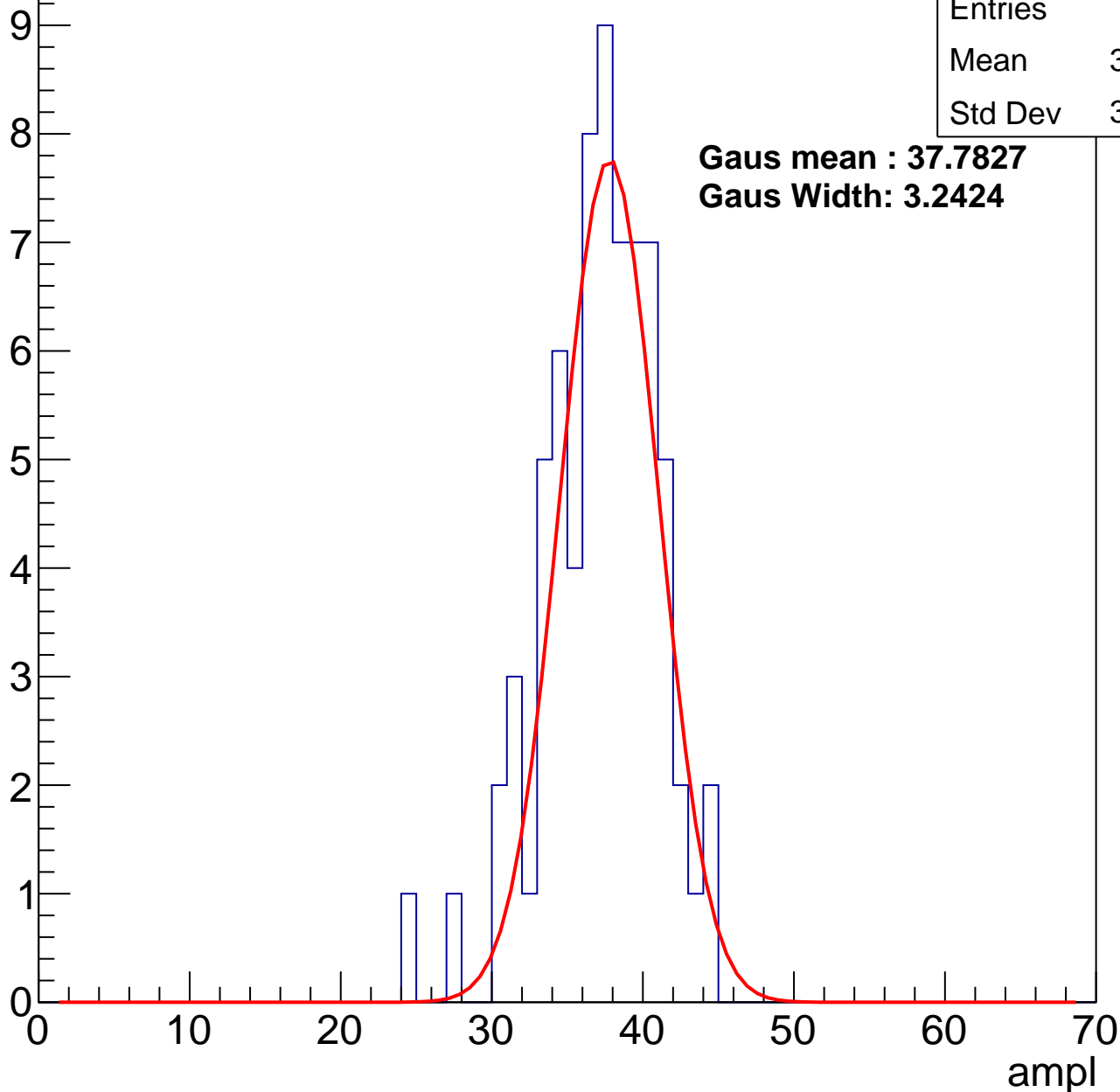
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.69
Std Dev	3.778

Gaus mean : 37.7827

Gaus Width: 3.2424



B1L103S, U17-ch53, adc2

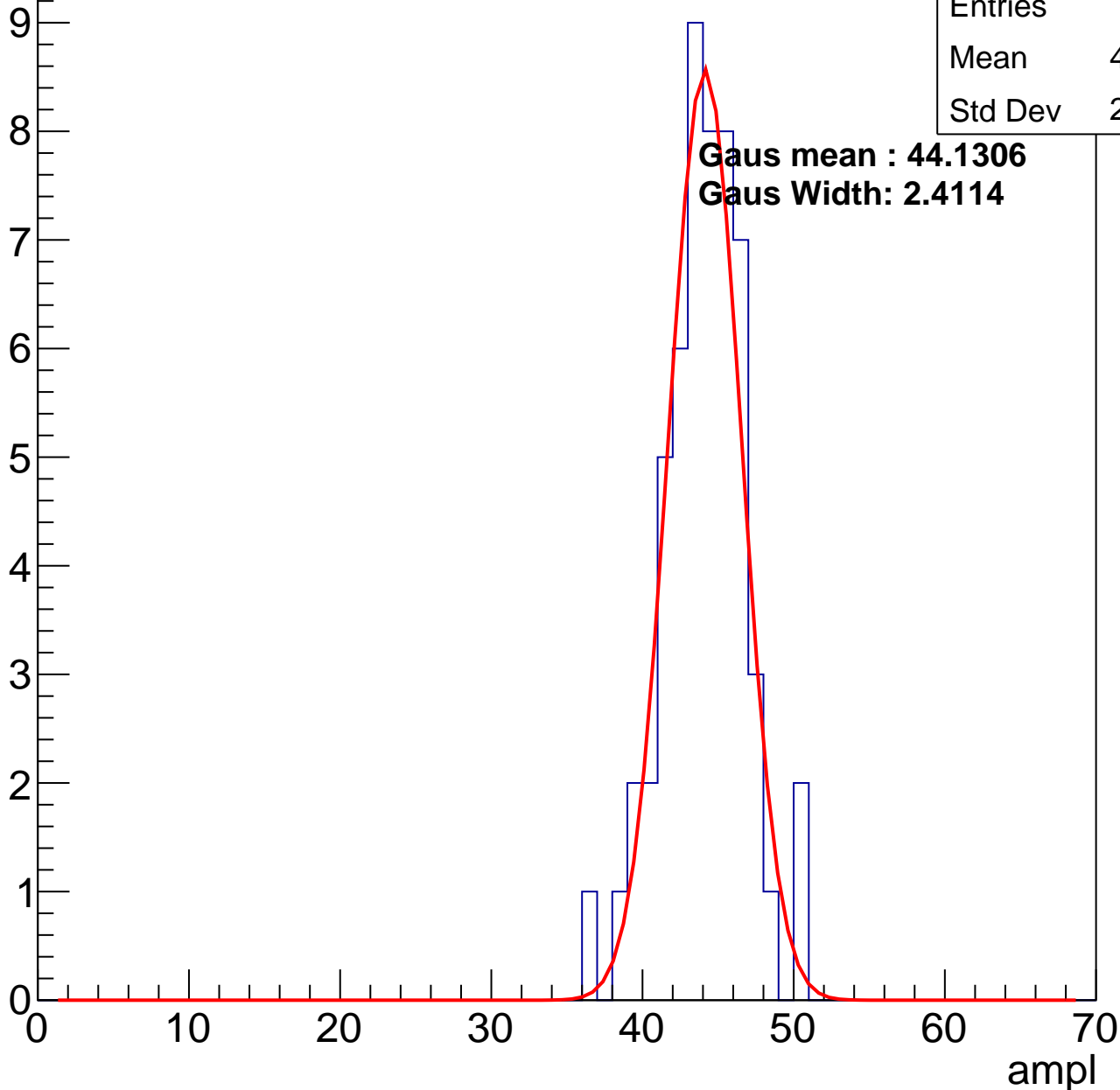
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.62
Std Dev	2.707

Gaus mean : 44.1306

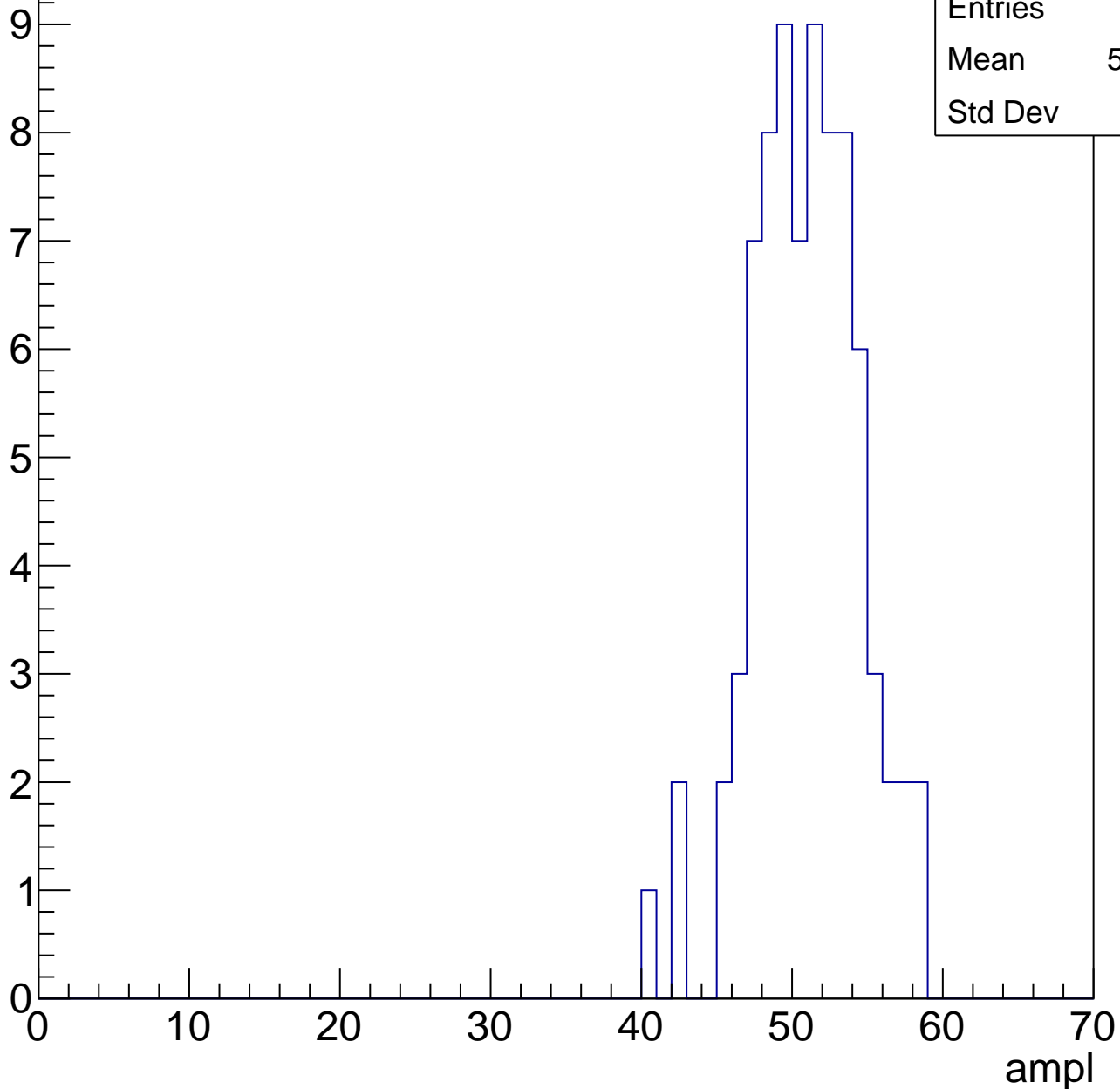
Gaus Width: 2.4114



B1L103S, U17-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

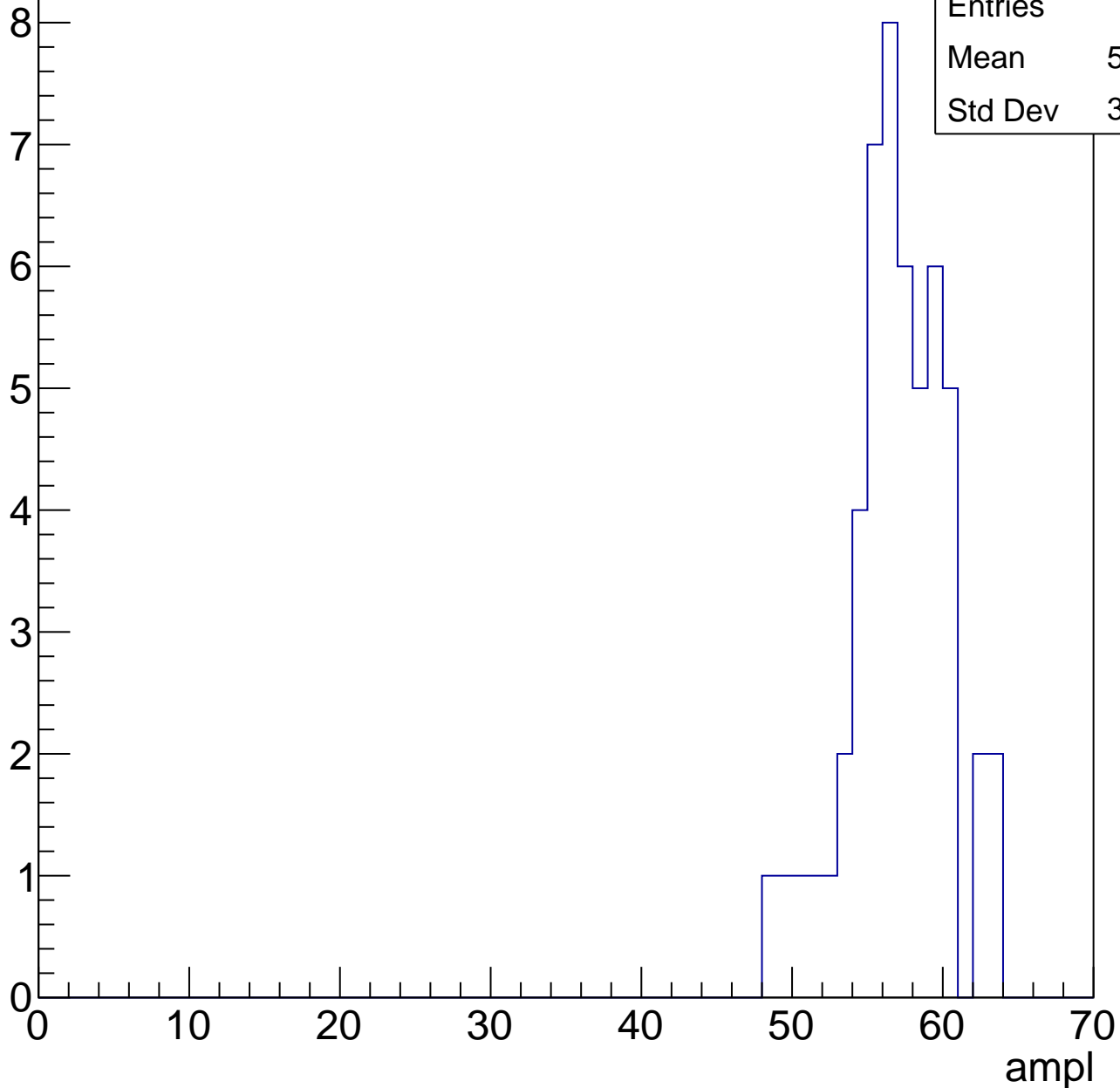
Entry



B1L103S, U17-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



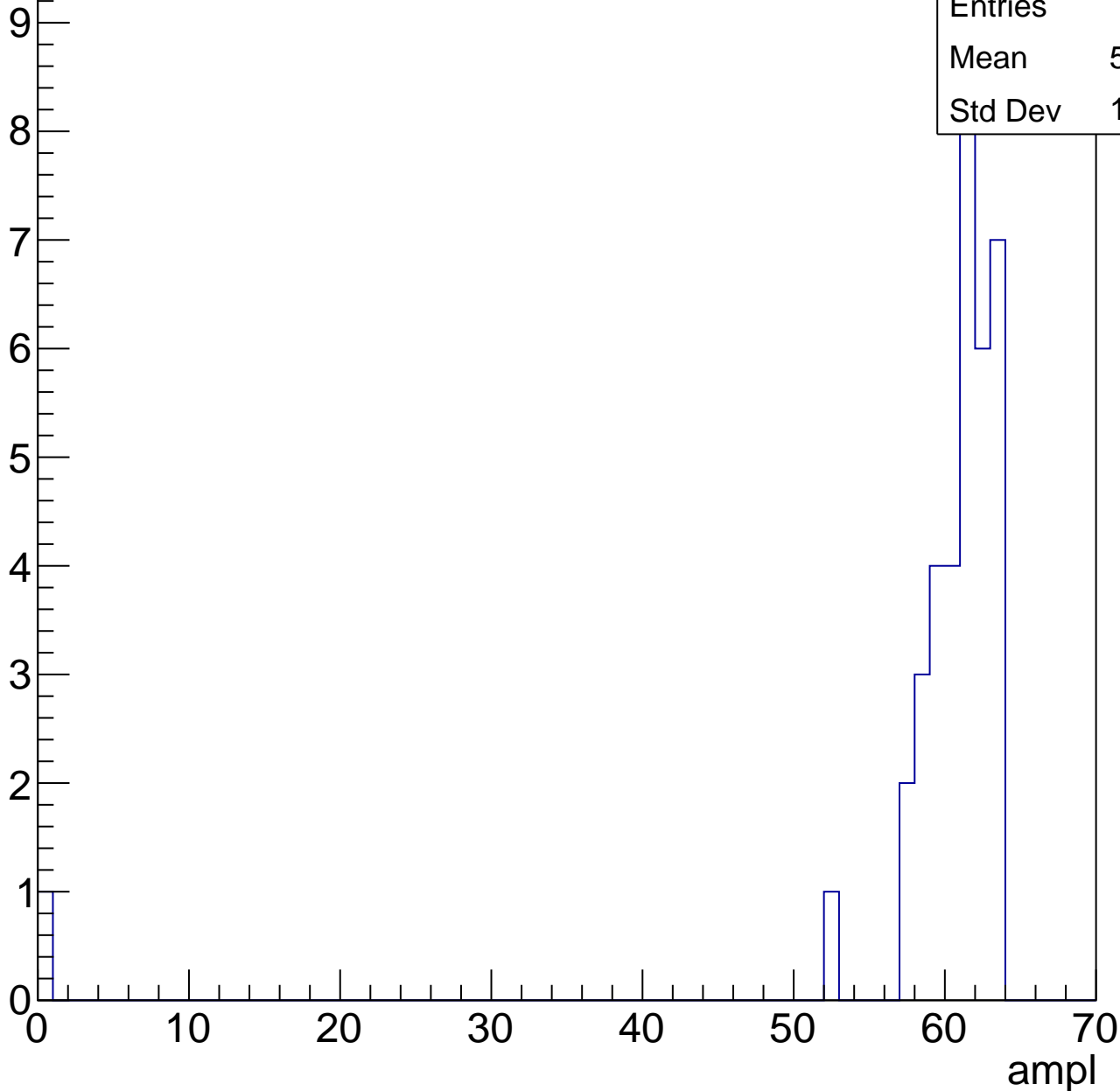
Entries	52
Mean	56.56
Std Dev	3.237

B1L103S, U17-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.86
Std Dev	10.06



B1L103S, U17-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B1L103S, U17-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch54, adc0

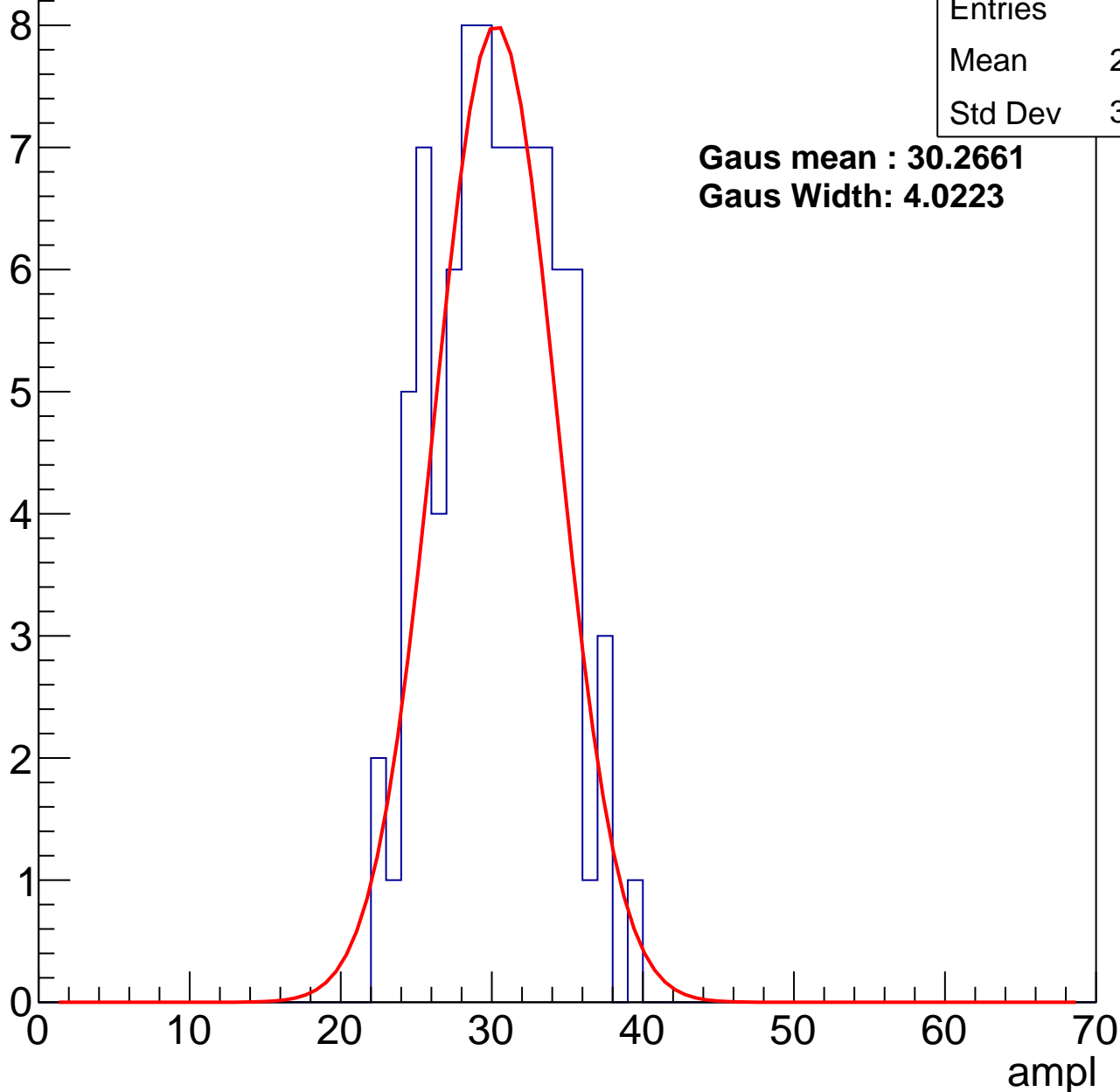
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	29.84
Std Dev	3.882

Gaus mean : 30.2661

Gaus Width: 4.0223



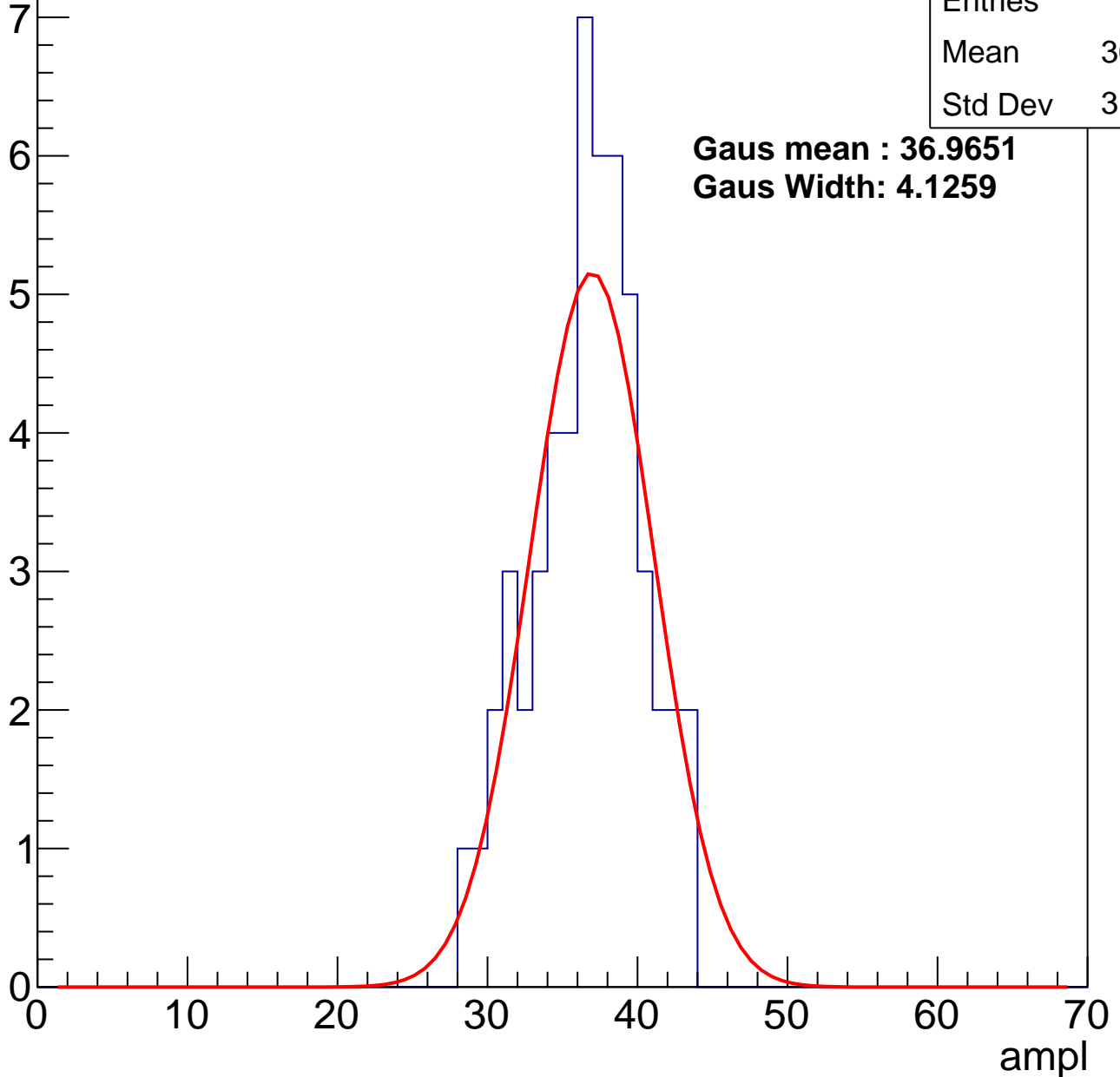
B1L103S, U17-ch54, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	36.19
Std Dev	3.582

Gaus mean : 36.9651
Gaus Width: 4.1259



B1L103S, U17-ch54, adc2

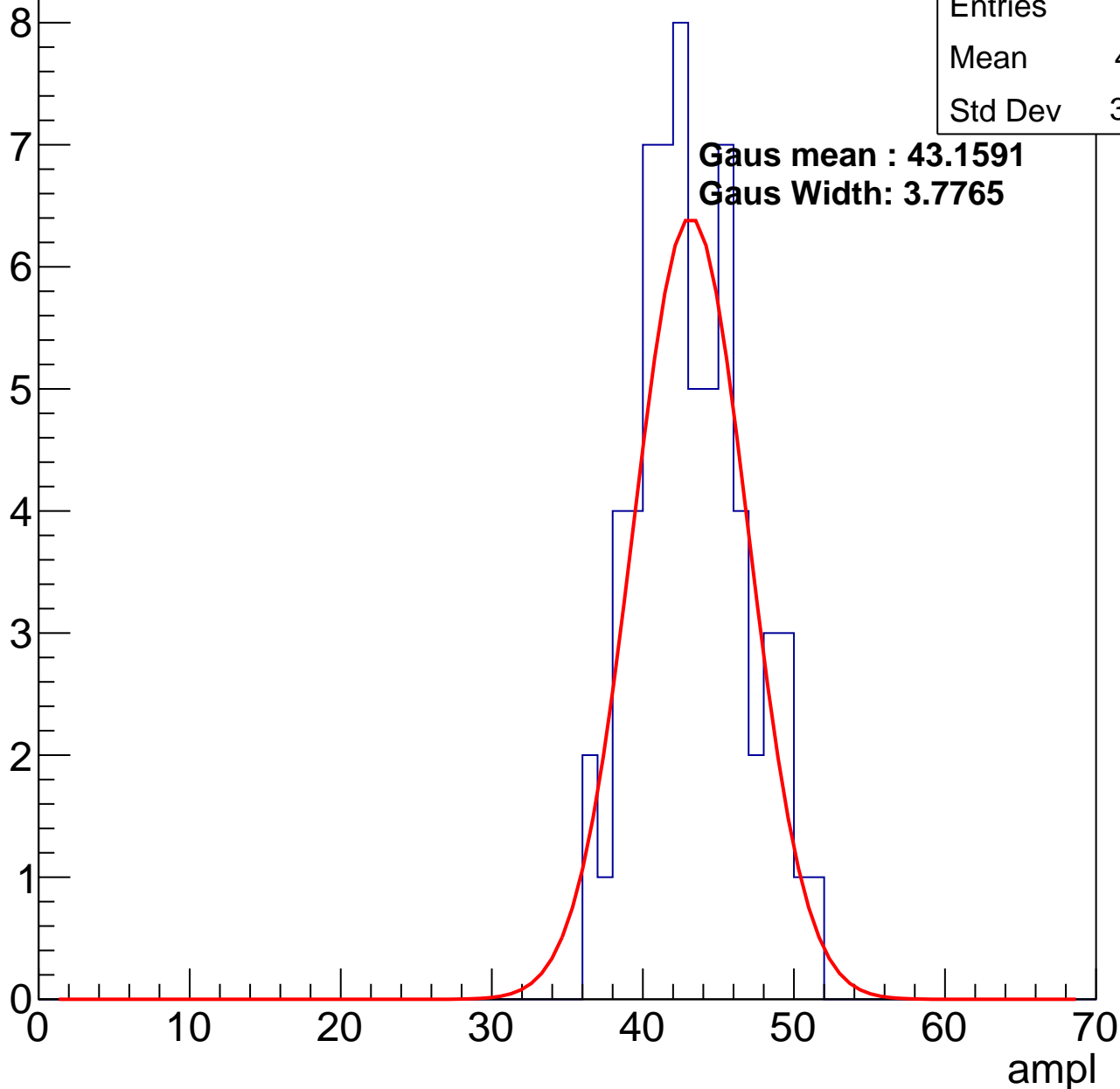
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.81
Std Dev	3.517

Gaus mean : 43.1591

Gaus Width: 3.7765

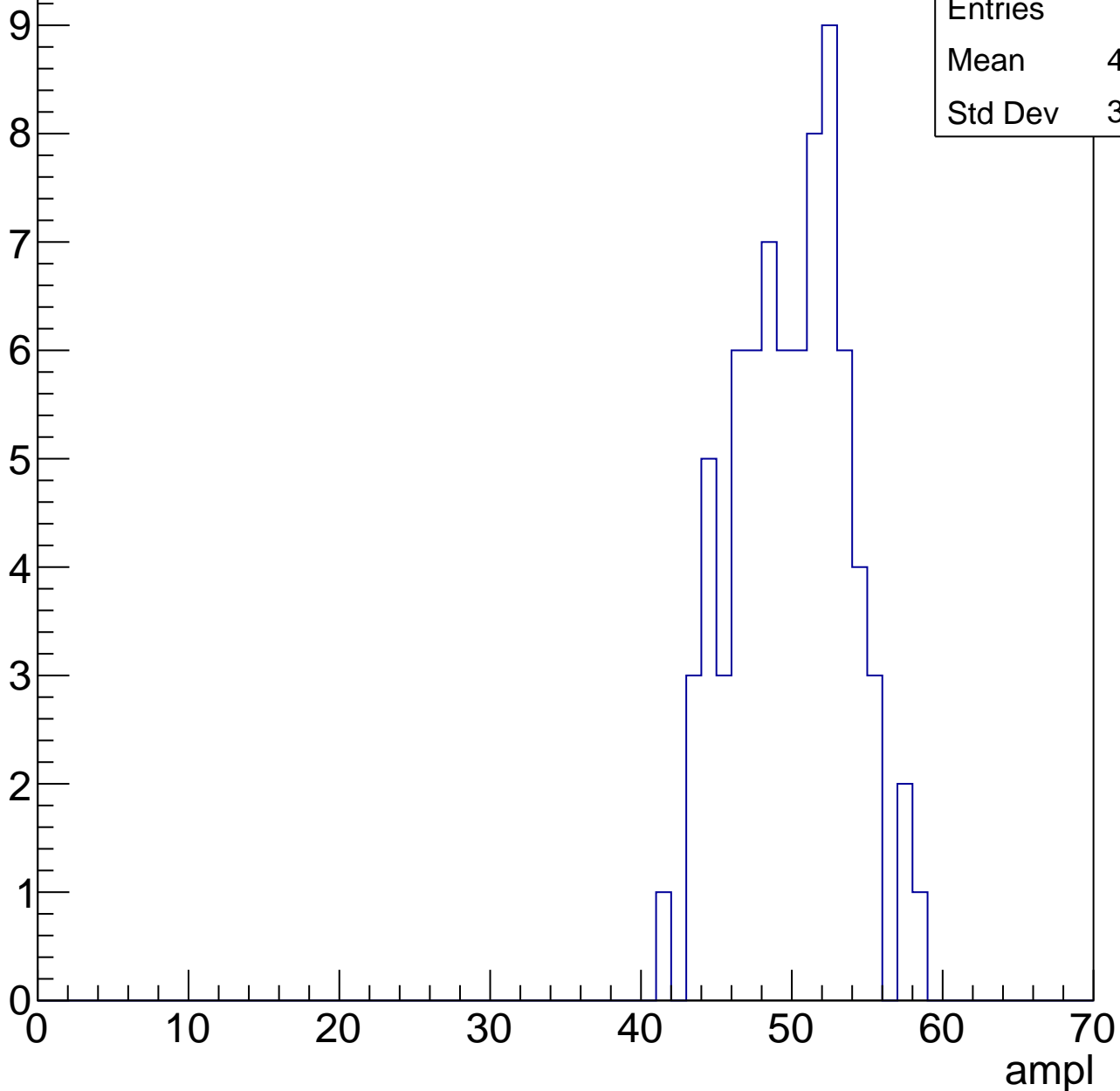


B1L103S, U17-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

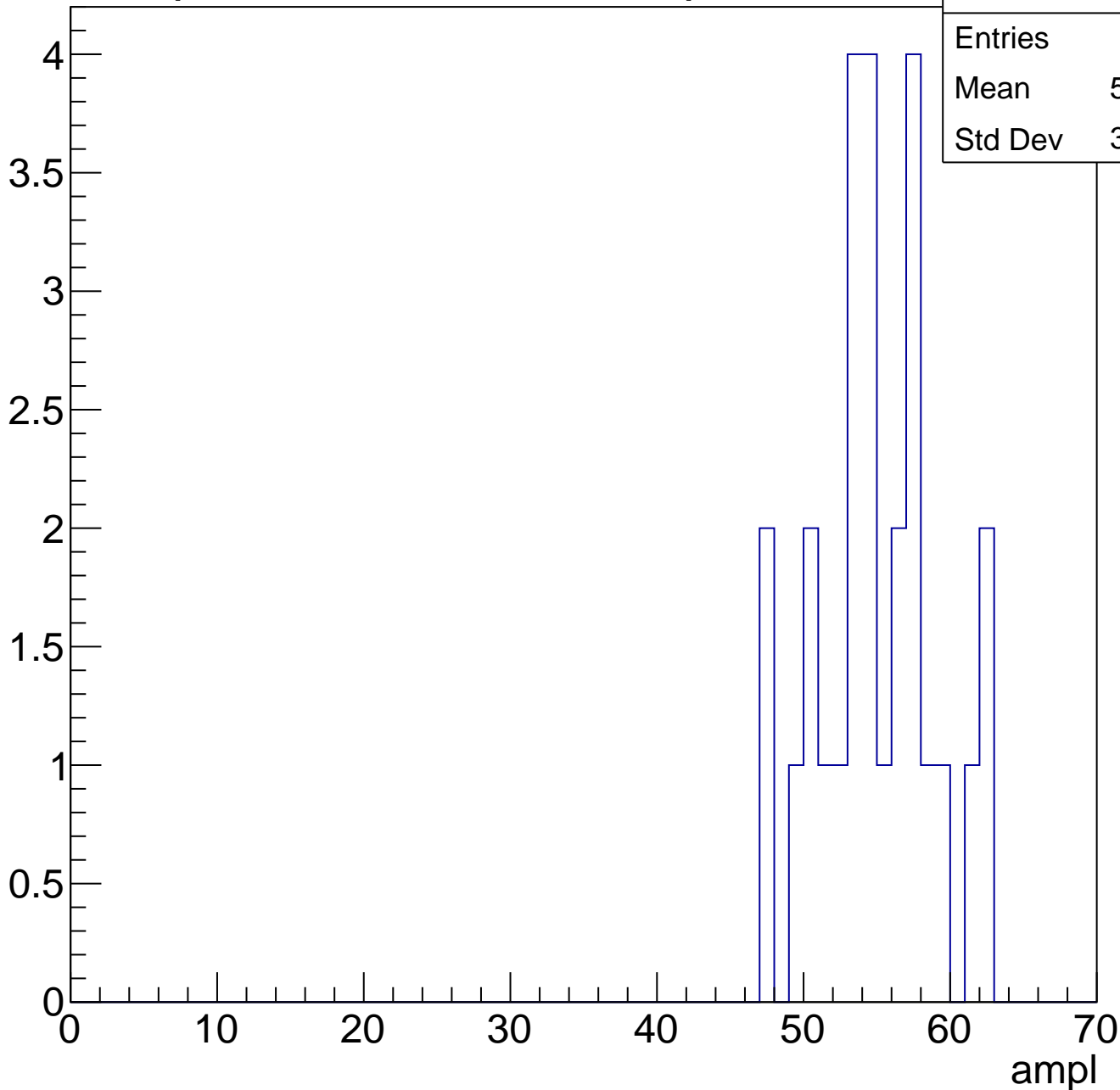
Entries	76
Mean	49.47
Std Dev	3.708



B1L103S, U17-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

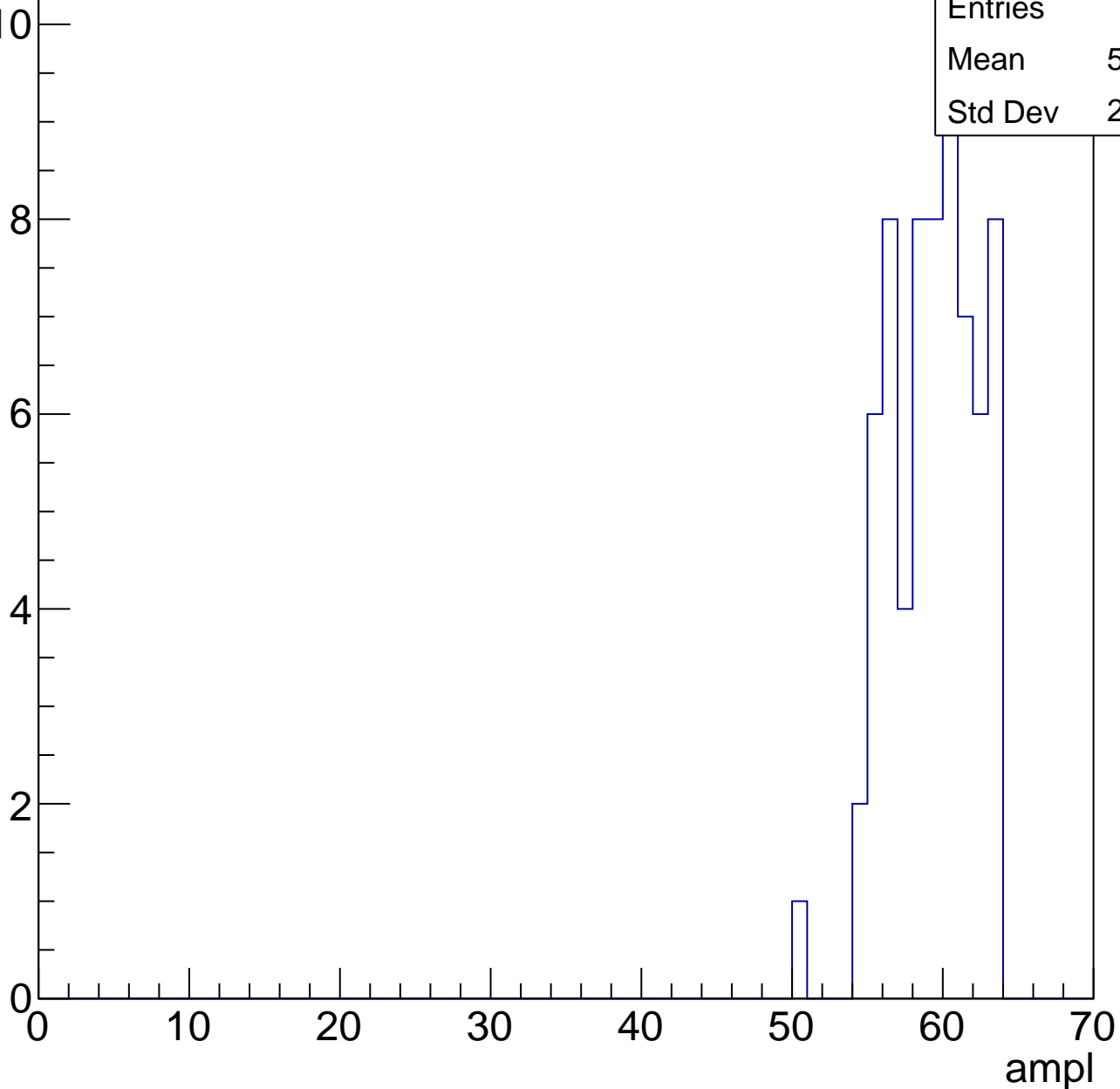


B1L103S, U17-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	58.87
Std Dev	2.823



B1L103S, U17-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

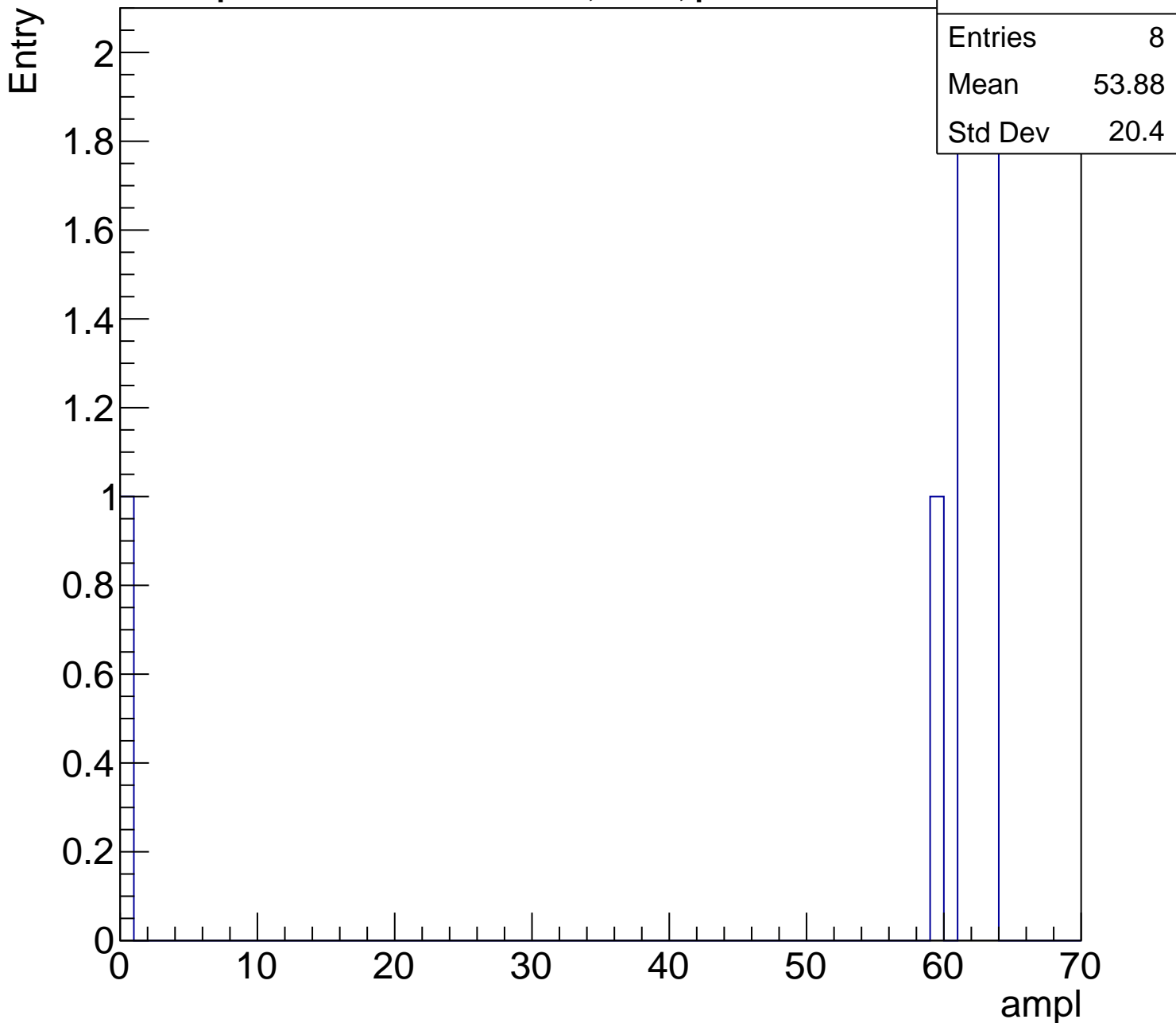
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.88
Std Dev	20.4

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch55, adc0

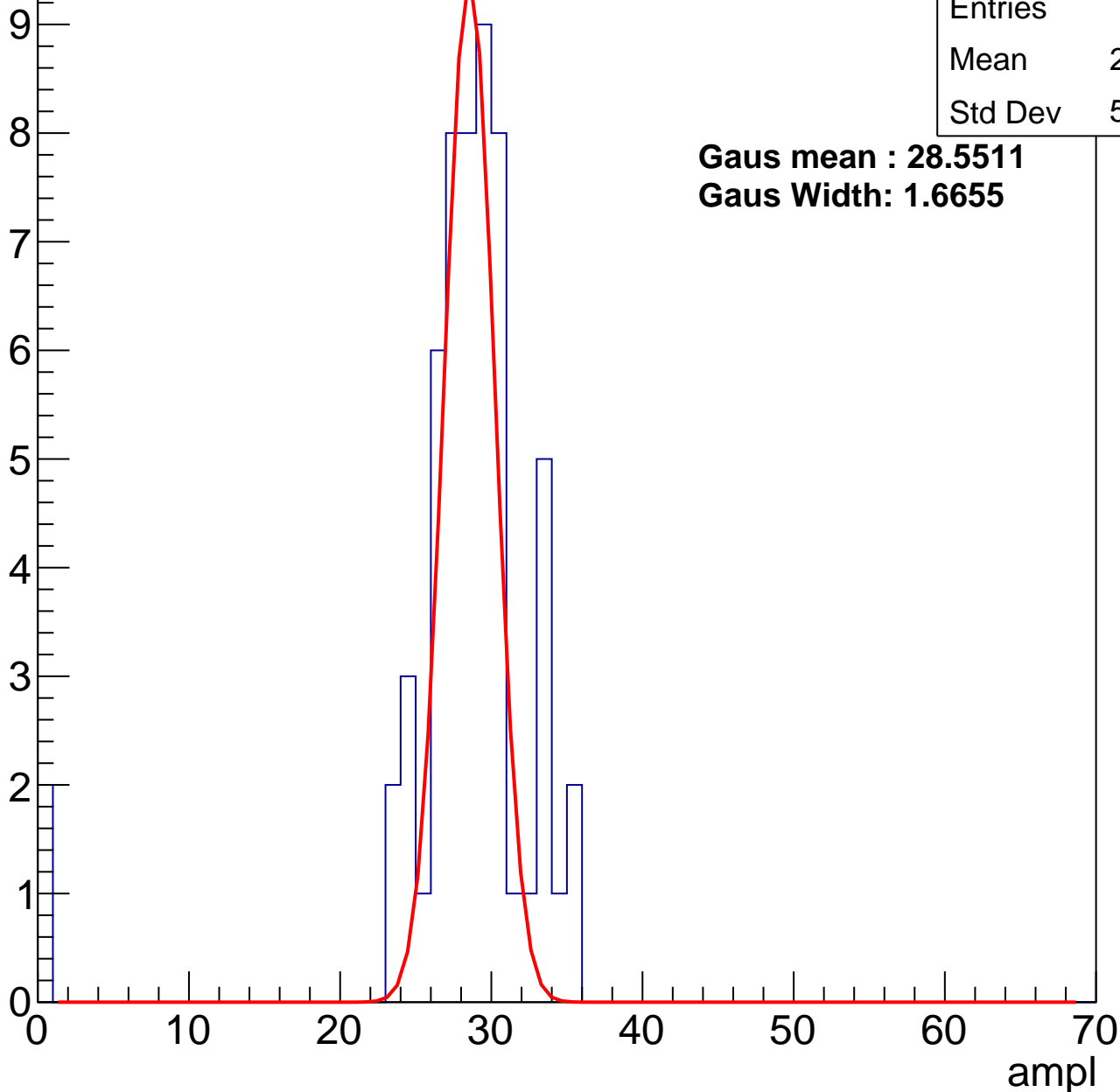
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	27.58
Std Dev	5.956

Gaus mean : 28.5511

Gaus Width: 1.6655



B1L103S, U17-ch55, adc1

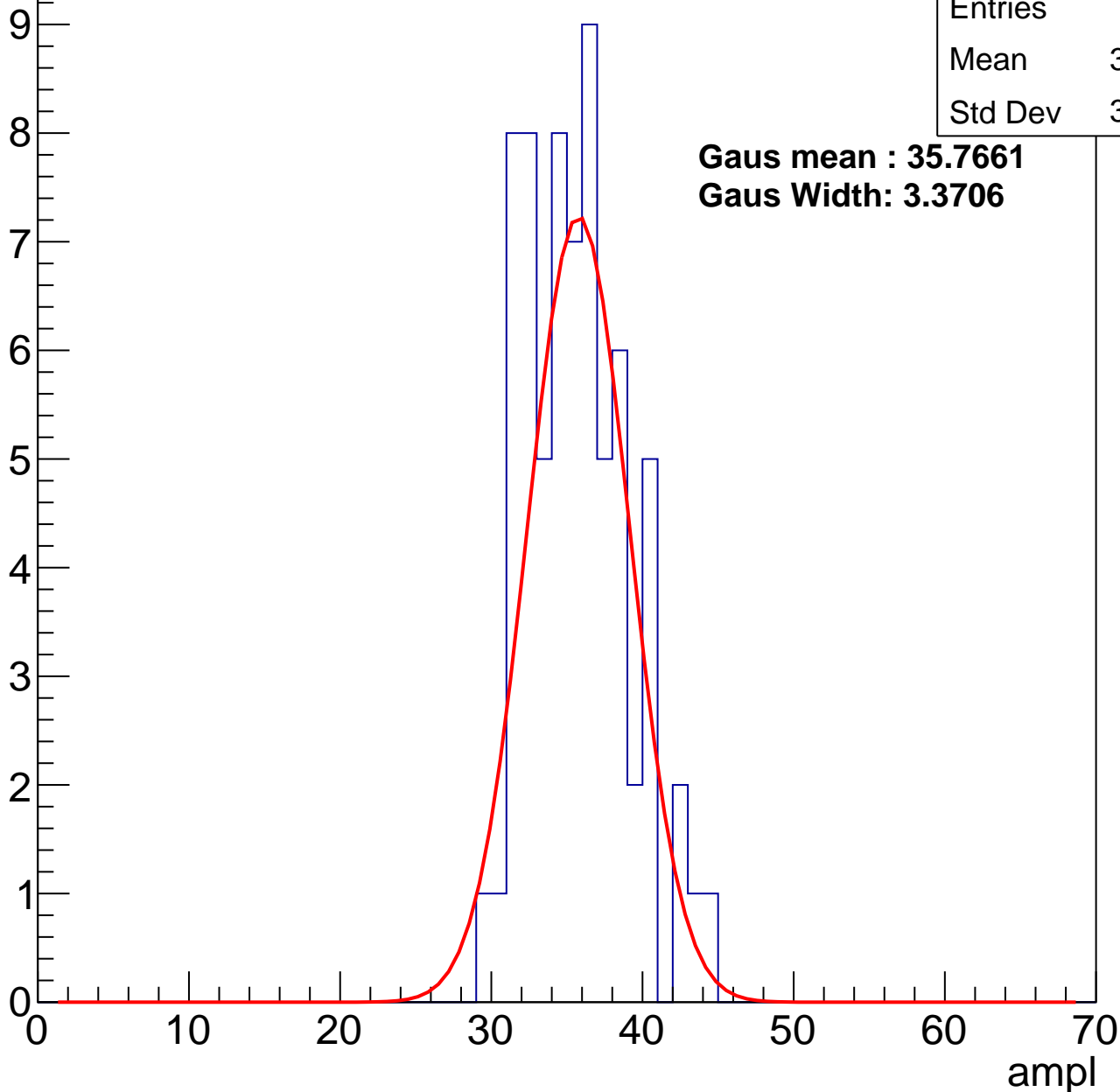
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.23
Std Dev	3.337

Gaus mean : 35.7661

Gaus Width: 3.3706



B1L103S, U17-ch55, adc2

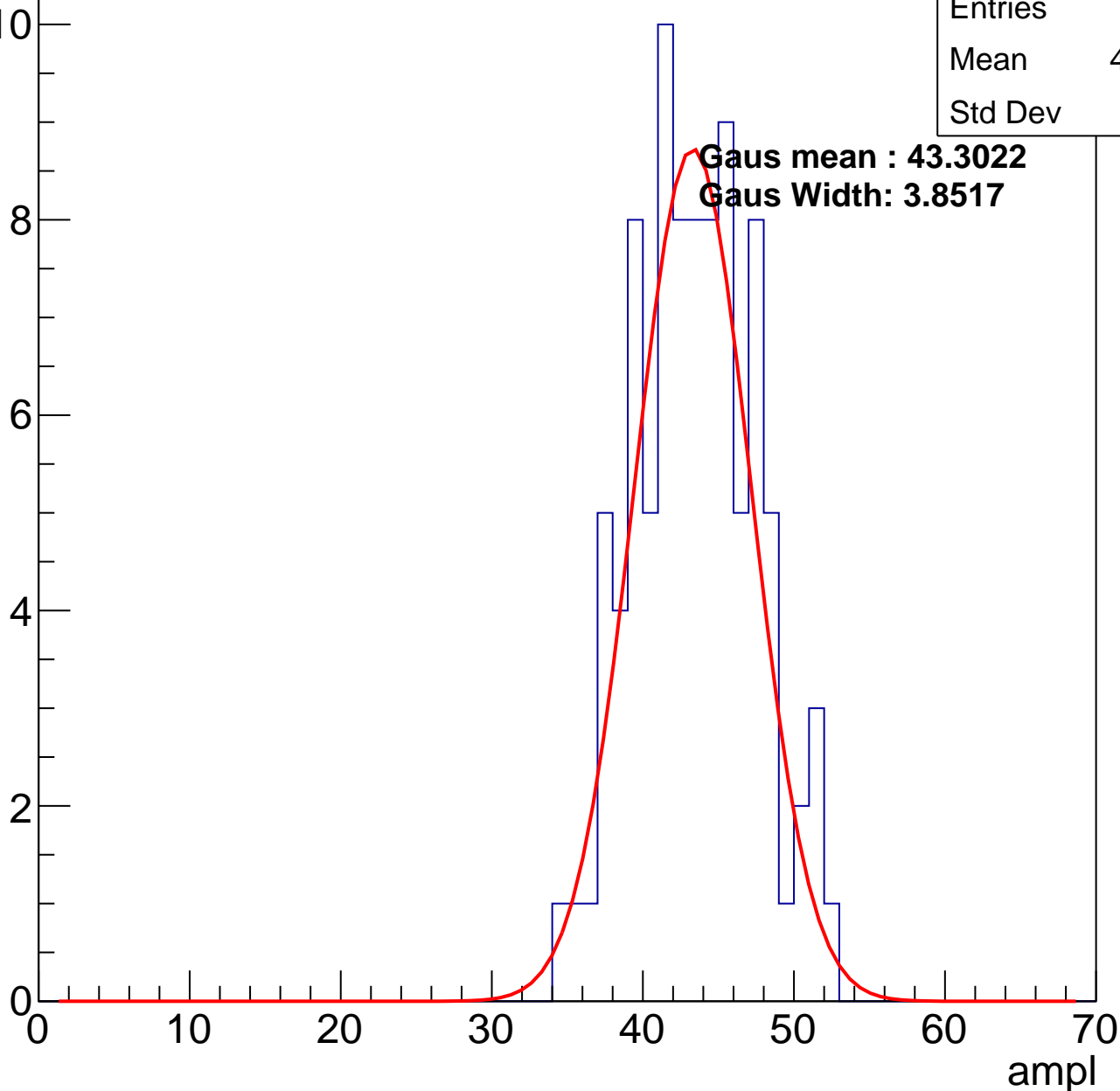
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	93
Mean	43.02
Std Dev	3.94

Gaus mean : 43.3022

Gaus Width: 3.8517

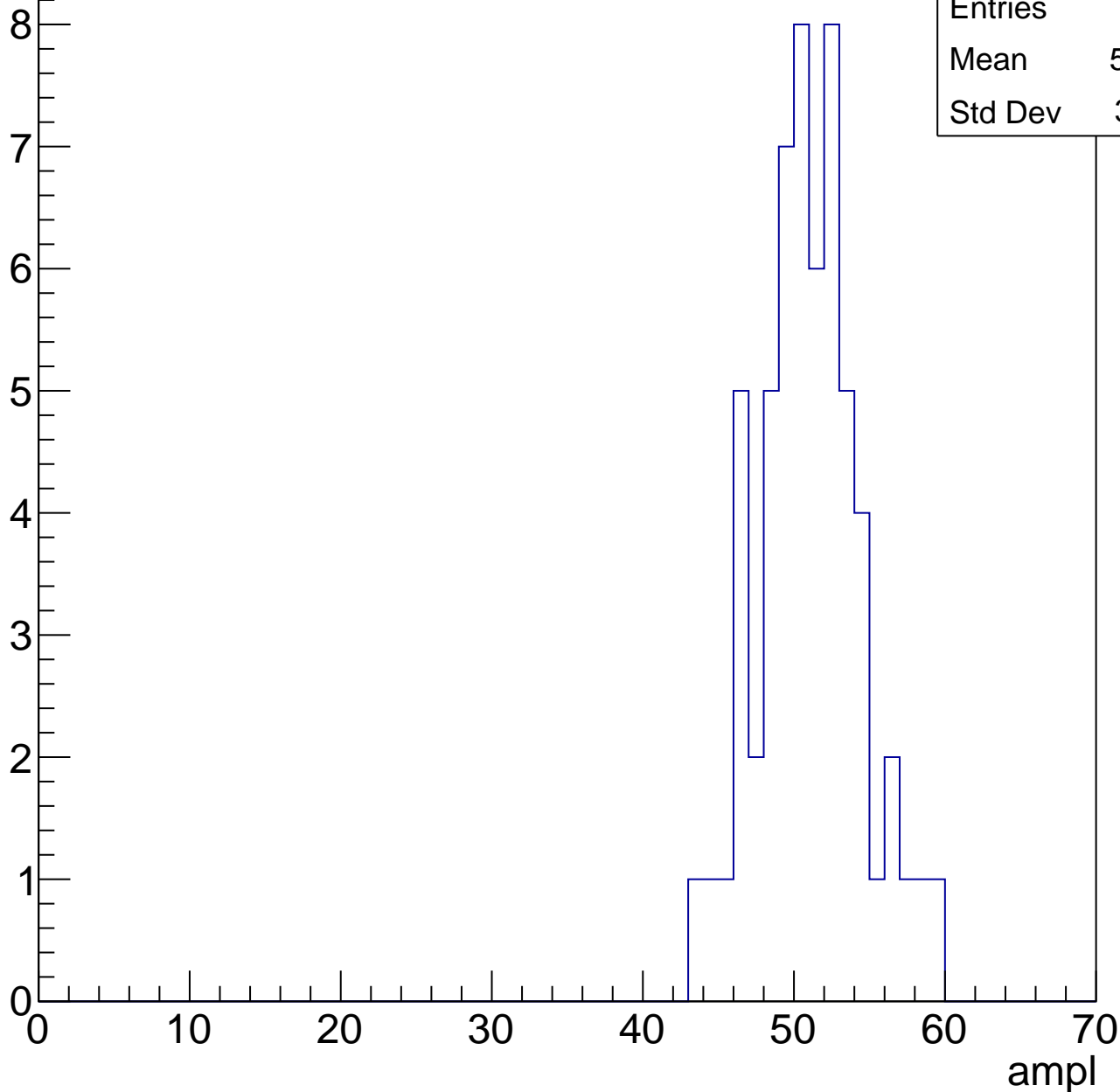


B1L103S, U17-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.56
Std Dev	3.331

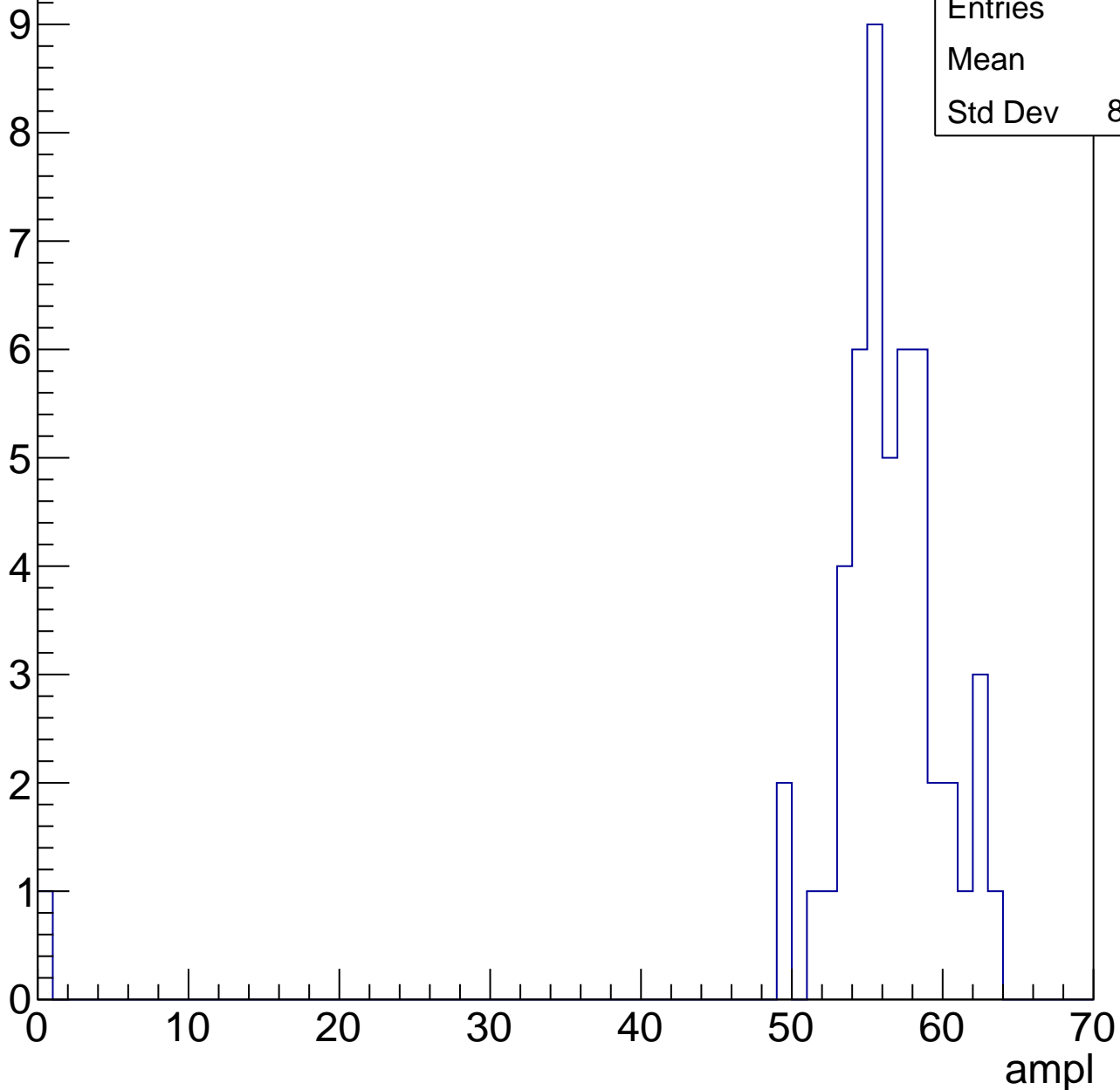


B1L103S, U17-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55
Std Dev	8.433

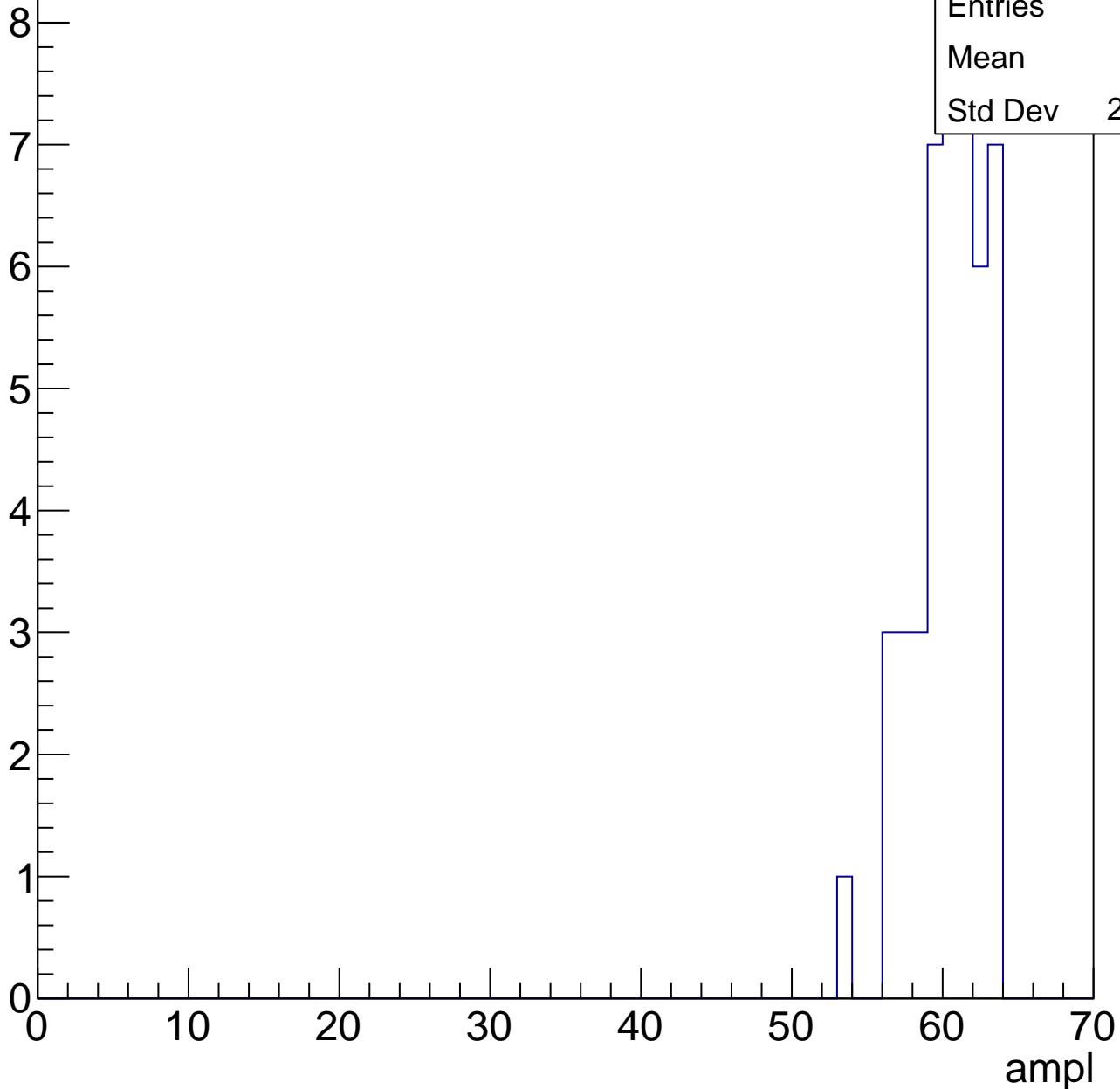


B1L103S, U17-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	60
Std Dev	2.275



B1L103S, U17-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U17-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch56, adc0

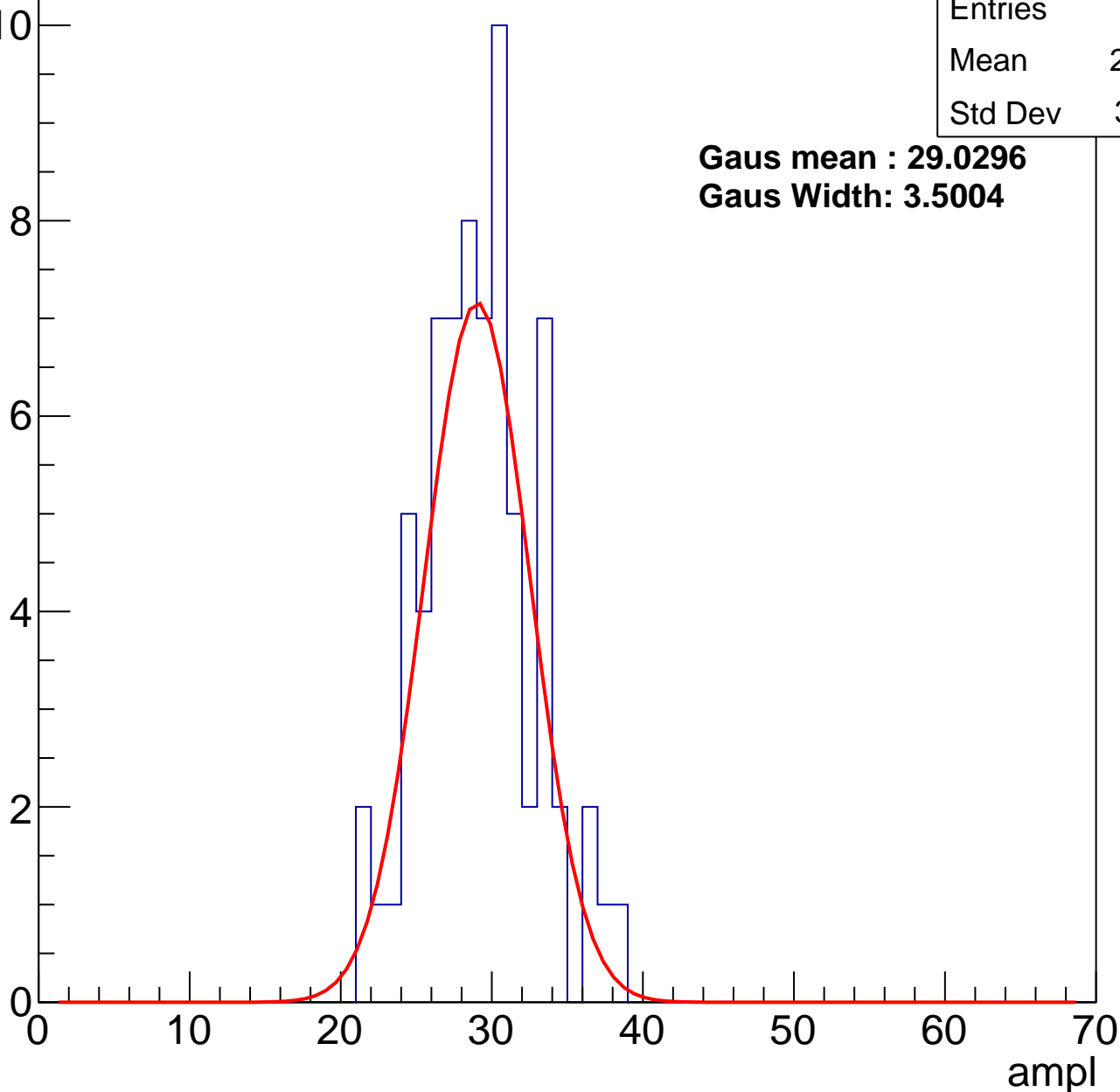
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.75
Std Dev	3.631

Gaus mean : 29.0296

Gaus Width: 3.5004



B1L103S, U17-ch56, adc1

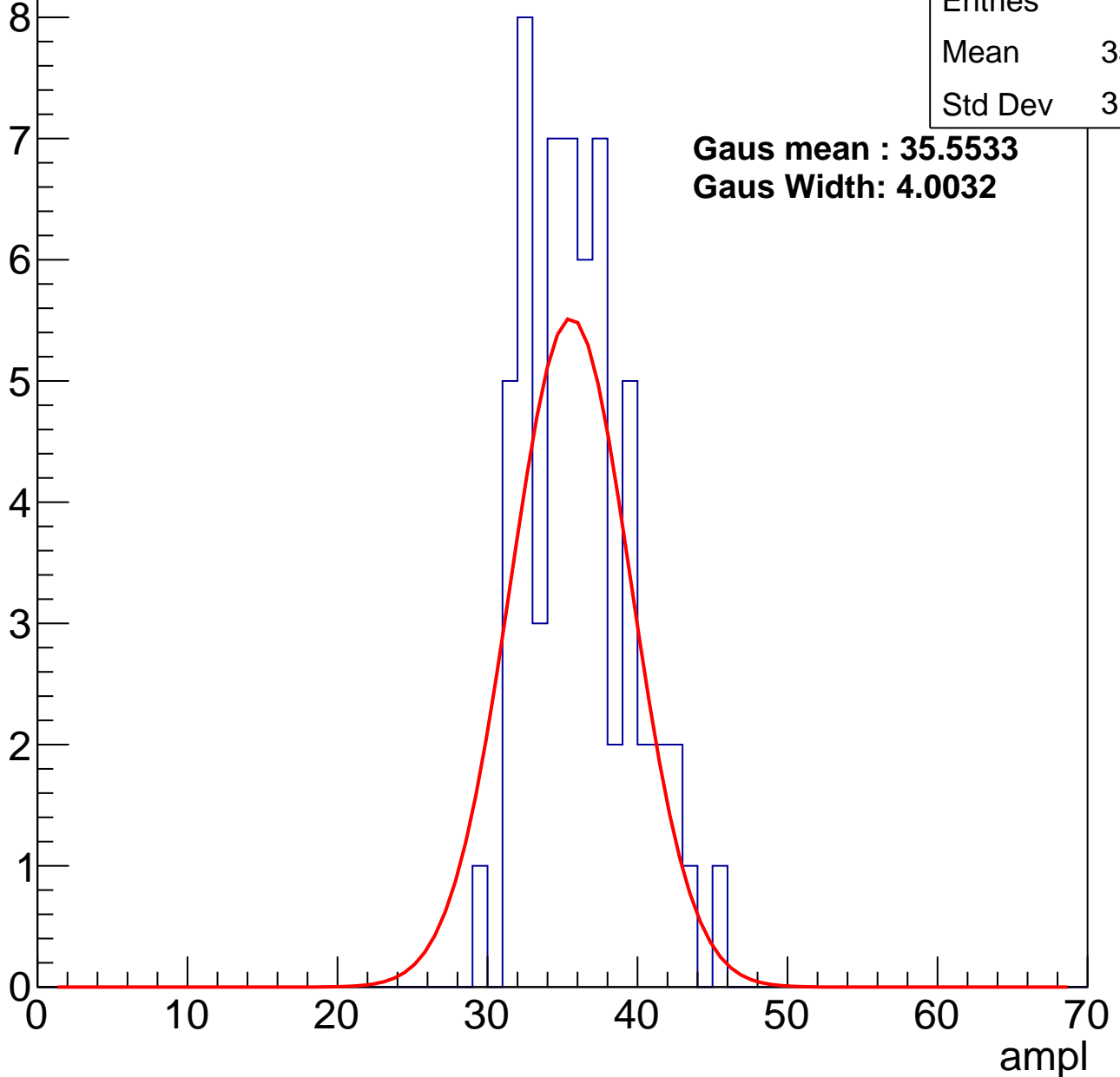
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.63
Std Dev	3.444

Gaus mean : 35.5533

Gaus Width: 4.0032



B1L103S, U17-ch56, adc2

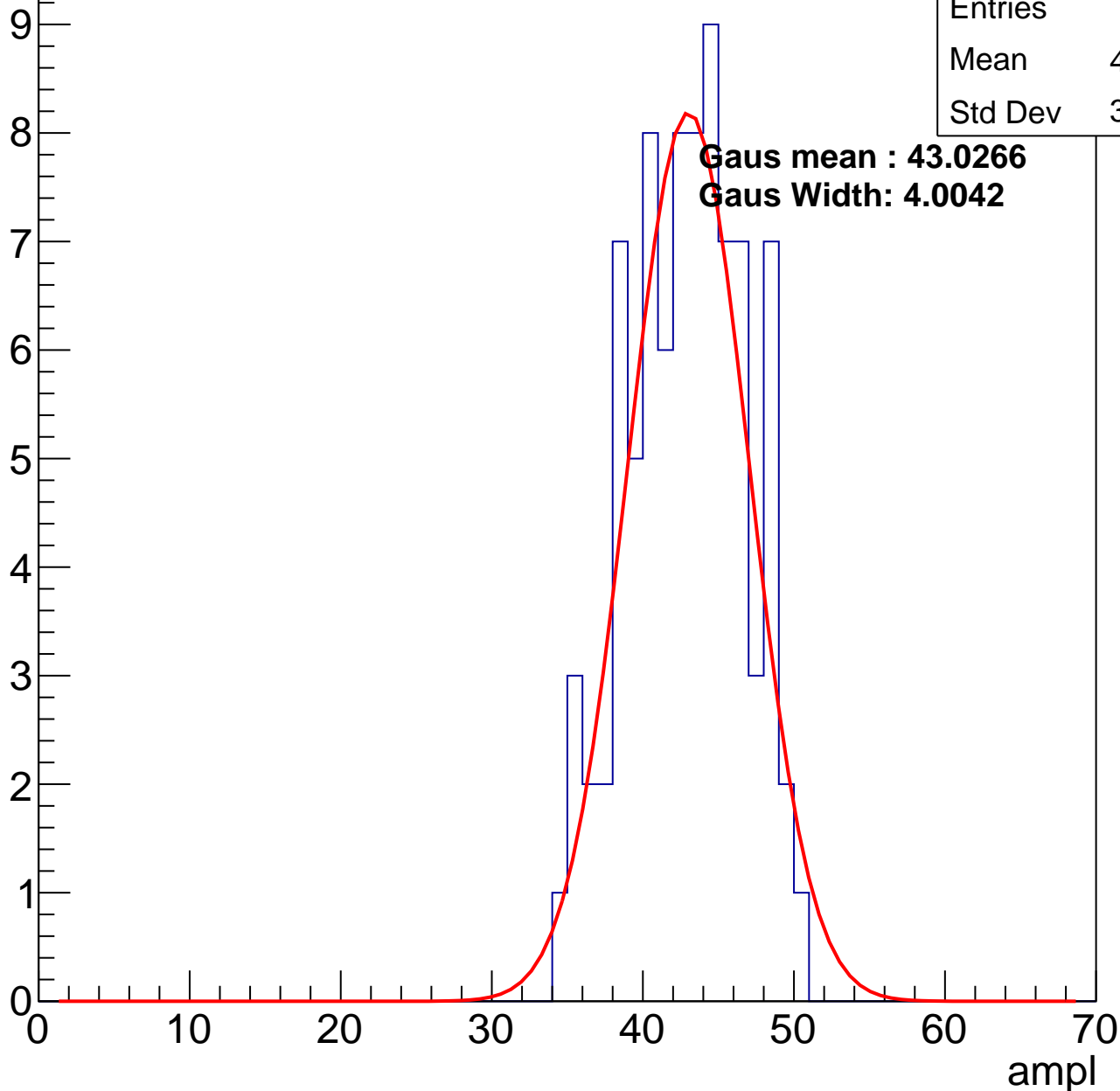
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	42.44
Std Dev	3.765

Gaus mean : 43.0266

Gaus Width: 4.0042

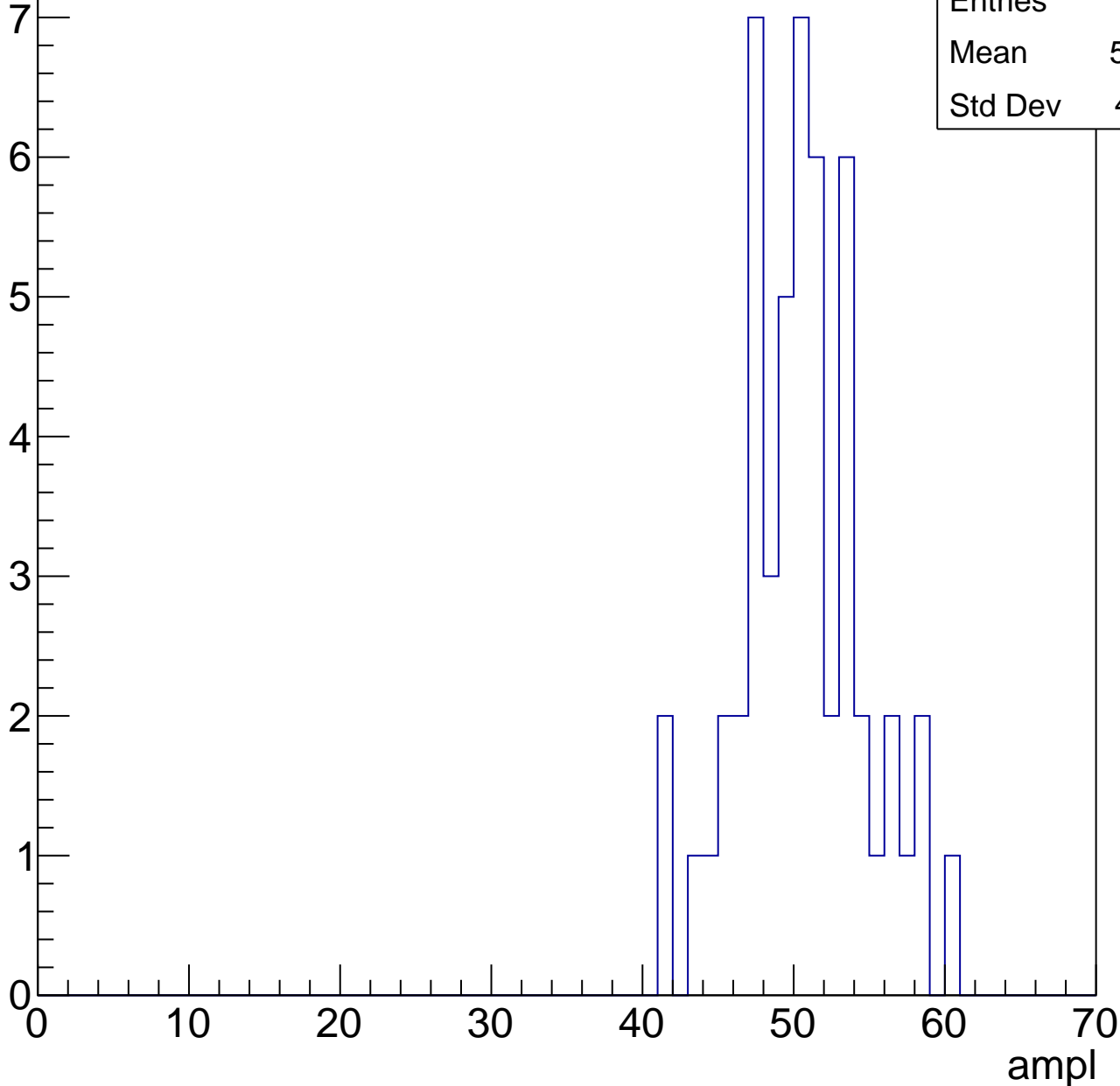


B1L103S, U17-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	50.09
Std Dev	4.081

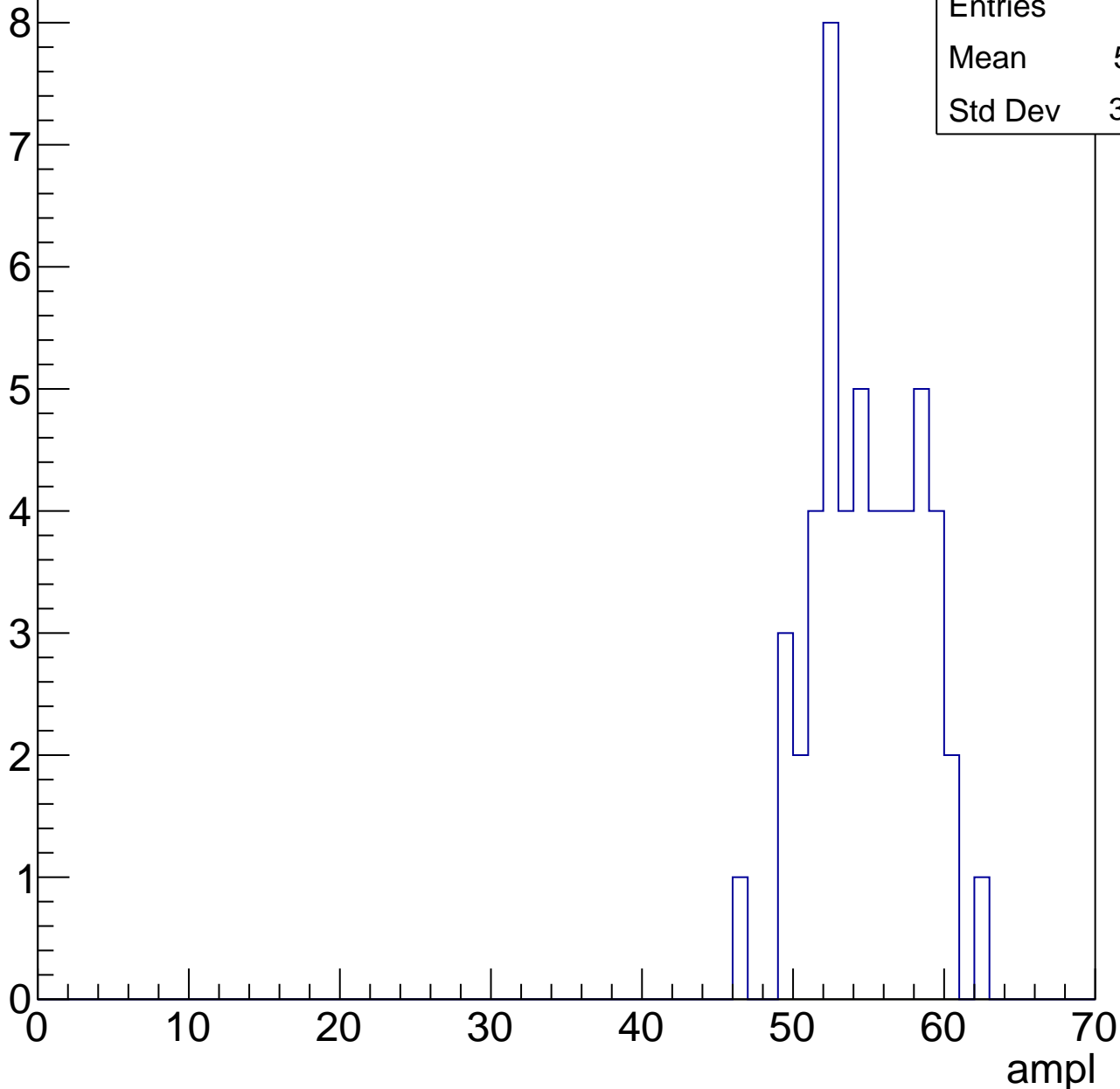


B1L103S, U17-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.41
Std Dev	3.448

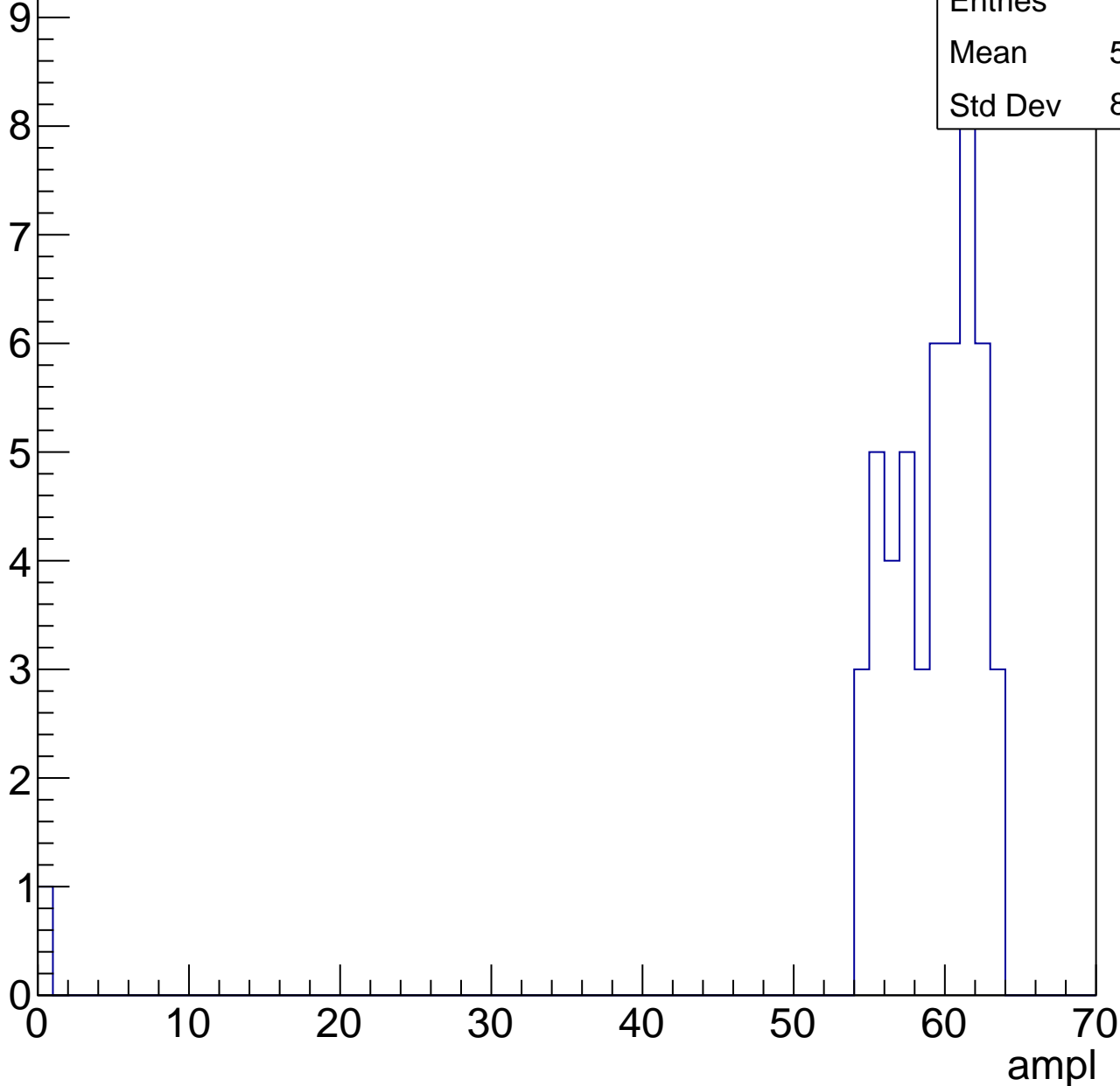


B1L103S, U17-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	57.73
Std Dev	8.582

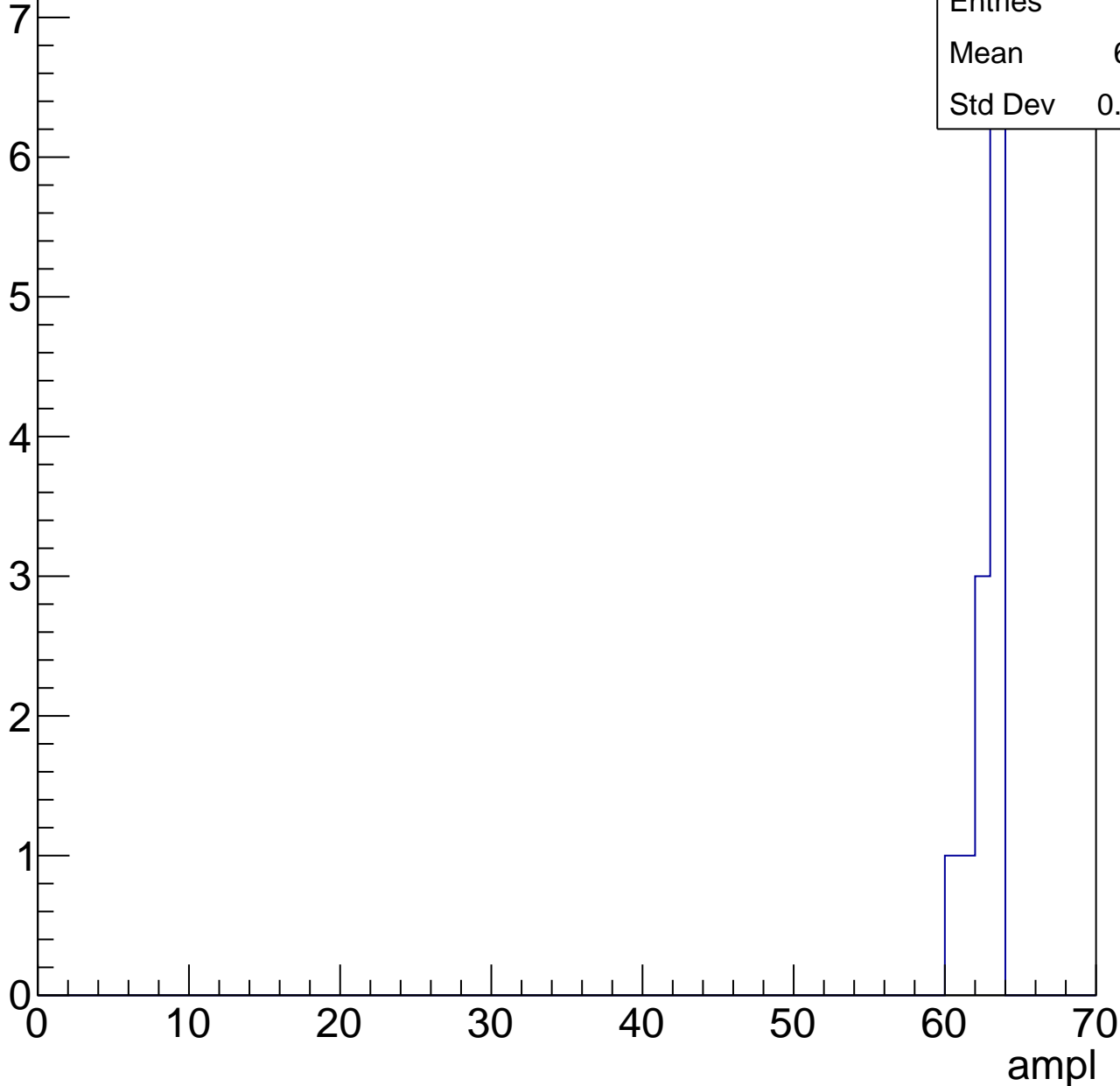


B1L103S, U17-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	62.33
Std Dev	0.9428



B1L103S, U17-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



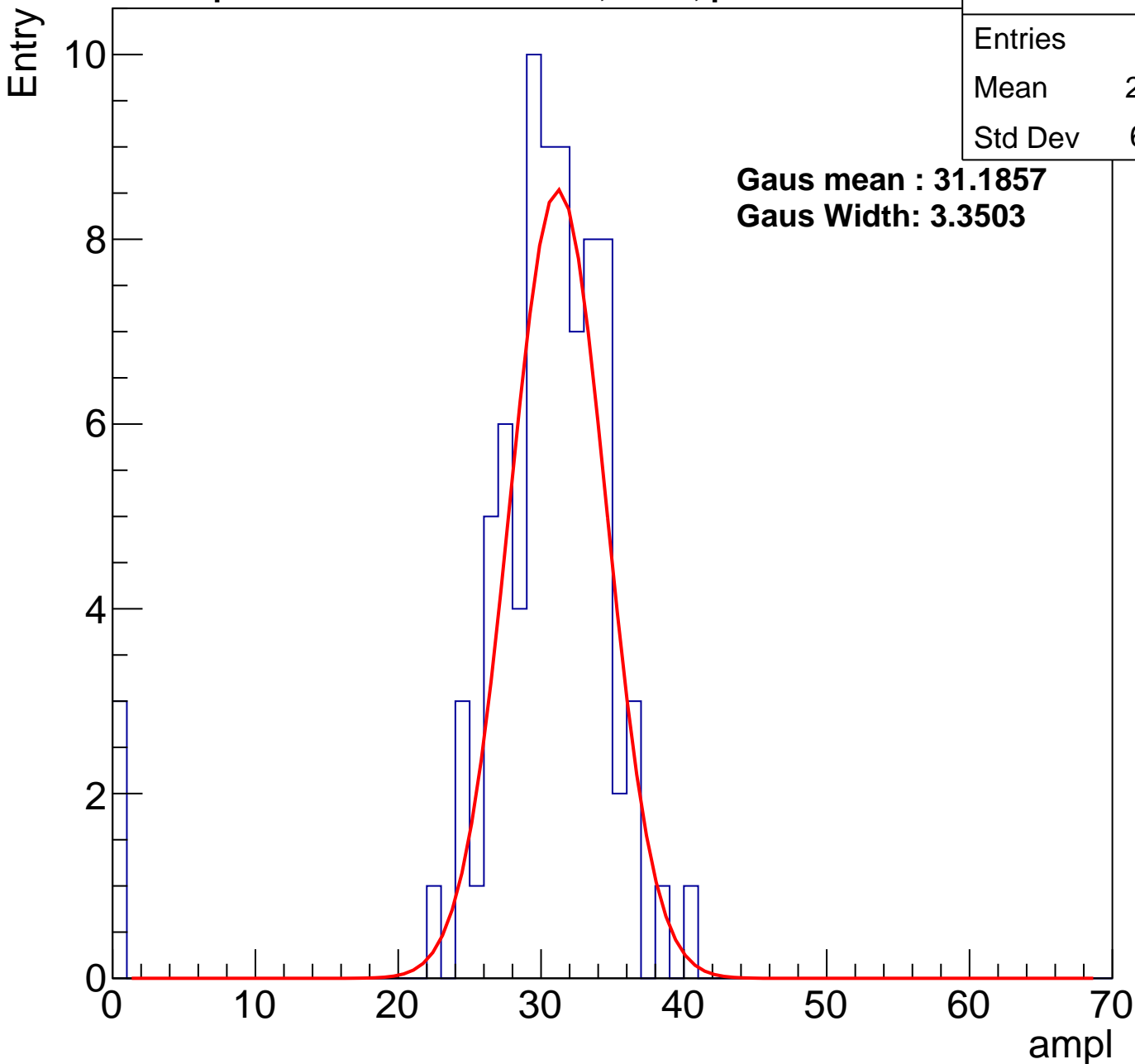
Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch57, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	29.36
Std Dev	6.651

Gaus mean : 31.1857
Gaus Width: 3.3503



B1L103S, U17-ch57, adc1

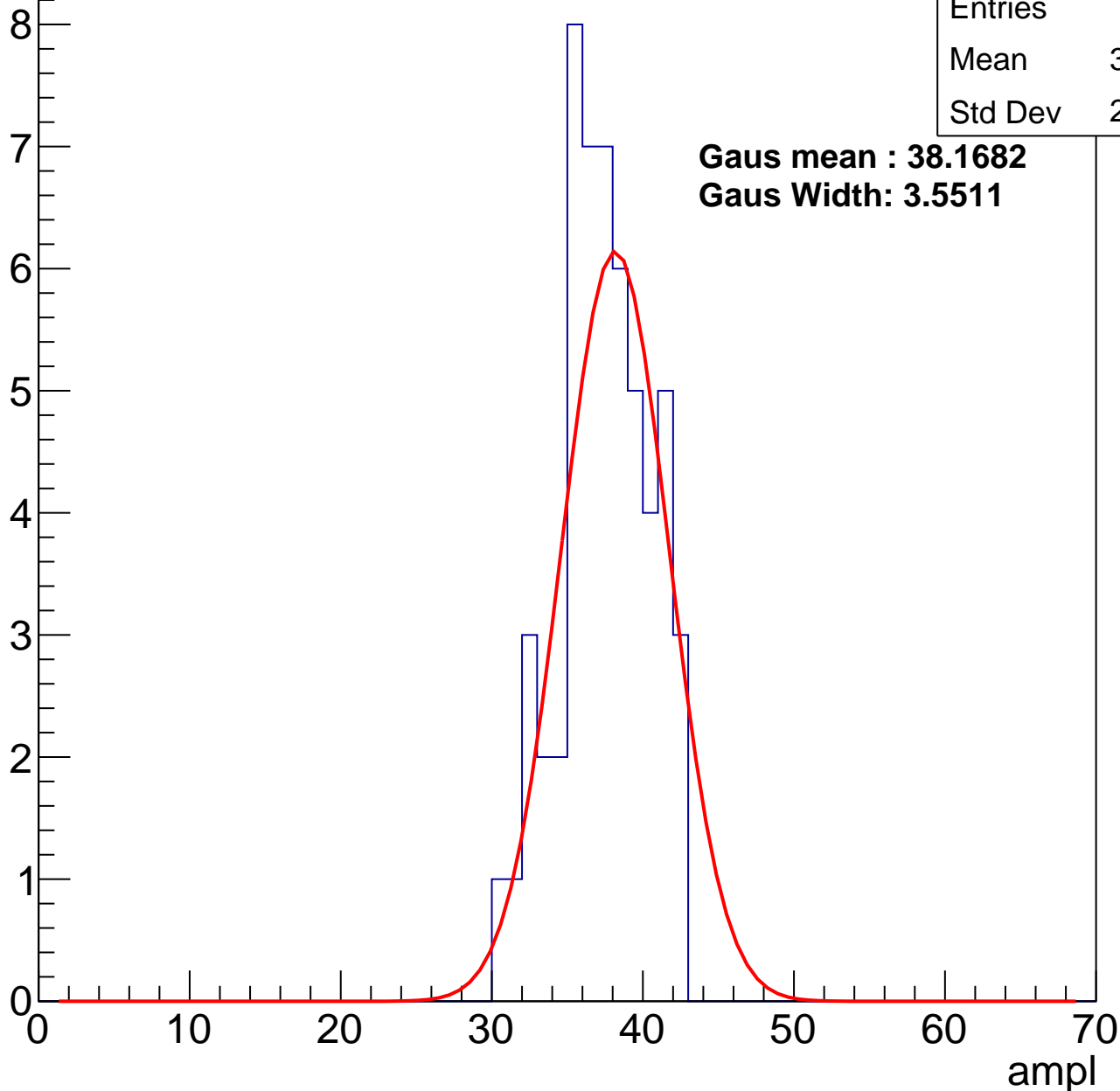
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.96
Std Dev	2.937

Gaus mean : 38.1682

Gaus Width: 3.5511



B1L103S, U17-ch57, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	43.87
Std Dev	3.588

Gaus mean : 44.0564

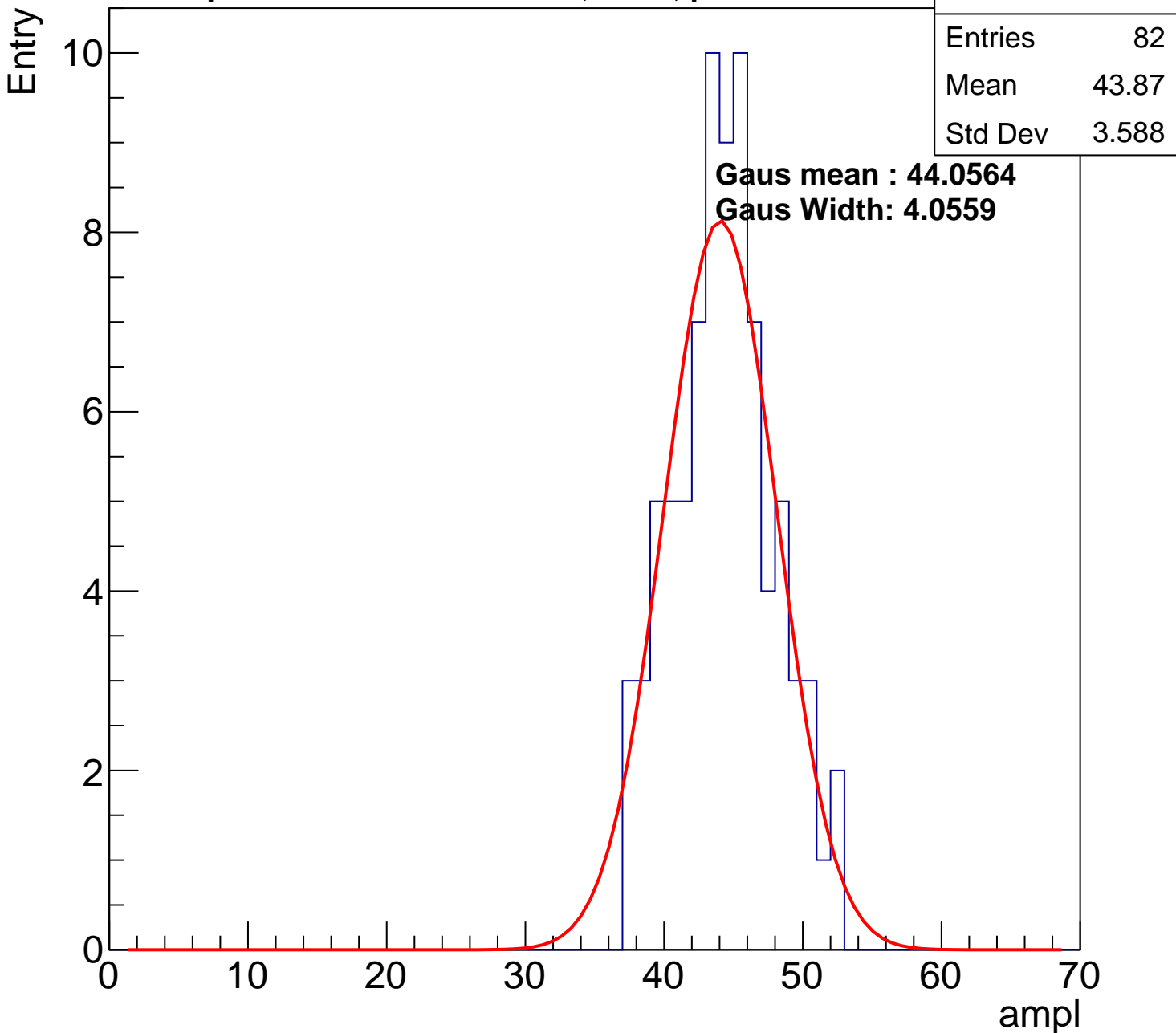
Gaus Width: 4.0559

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

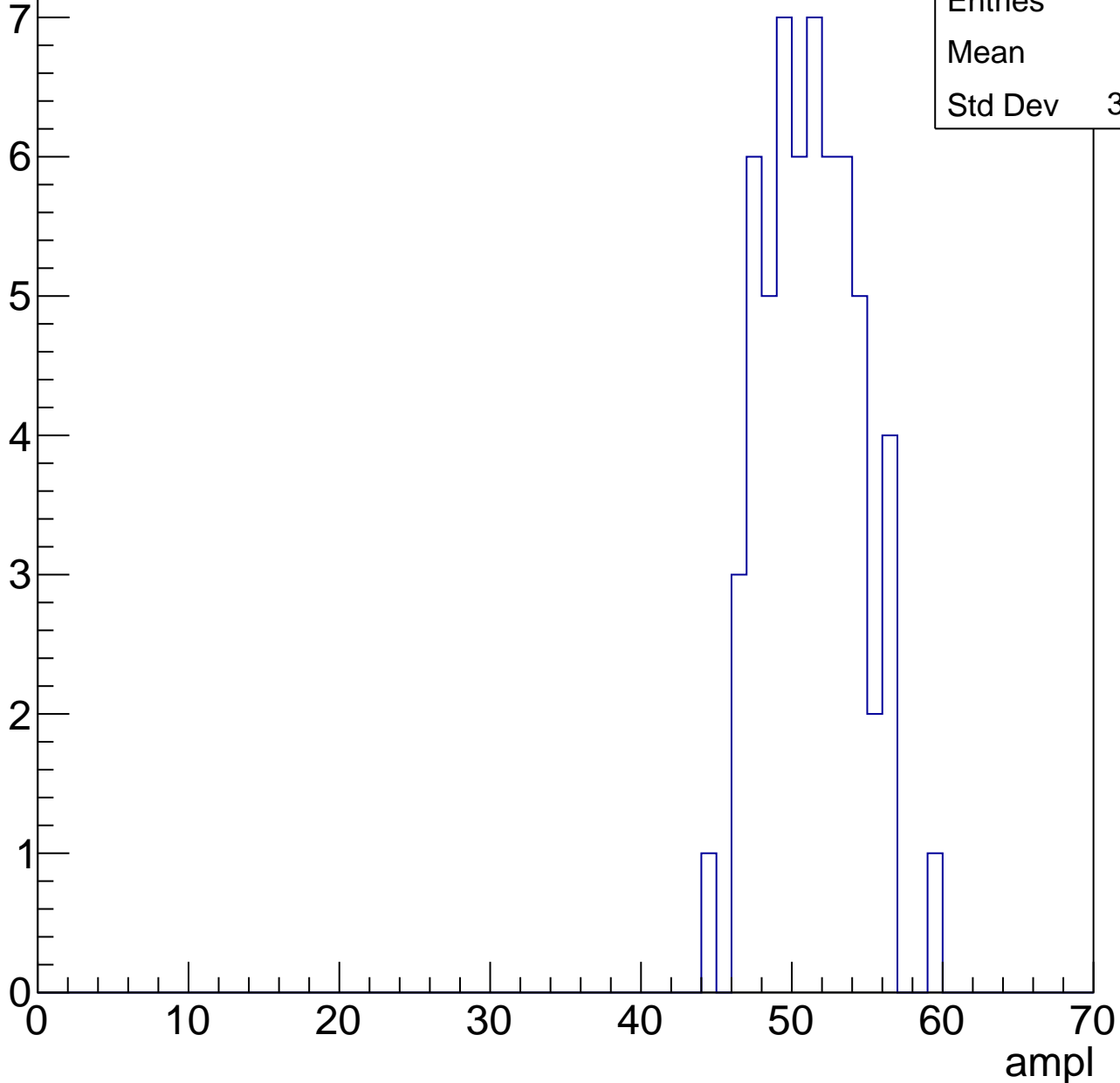


B1L103S, U17-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.8
Std Dev	3.102



B1L103S, U17-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	57.06
Std Dev	3.123

Entry

10

8

6

4

2

0

0

10

20

30

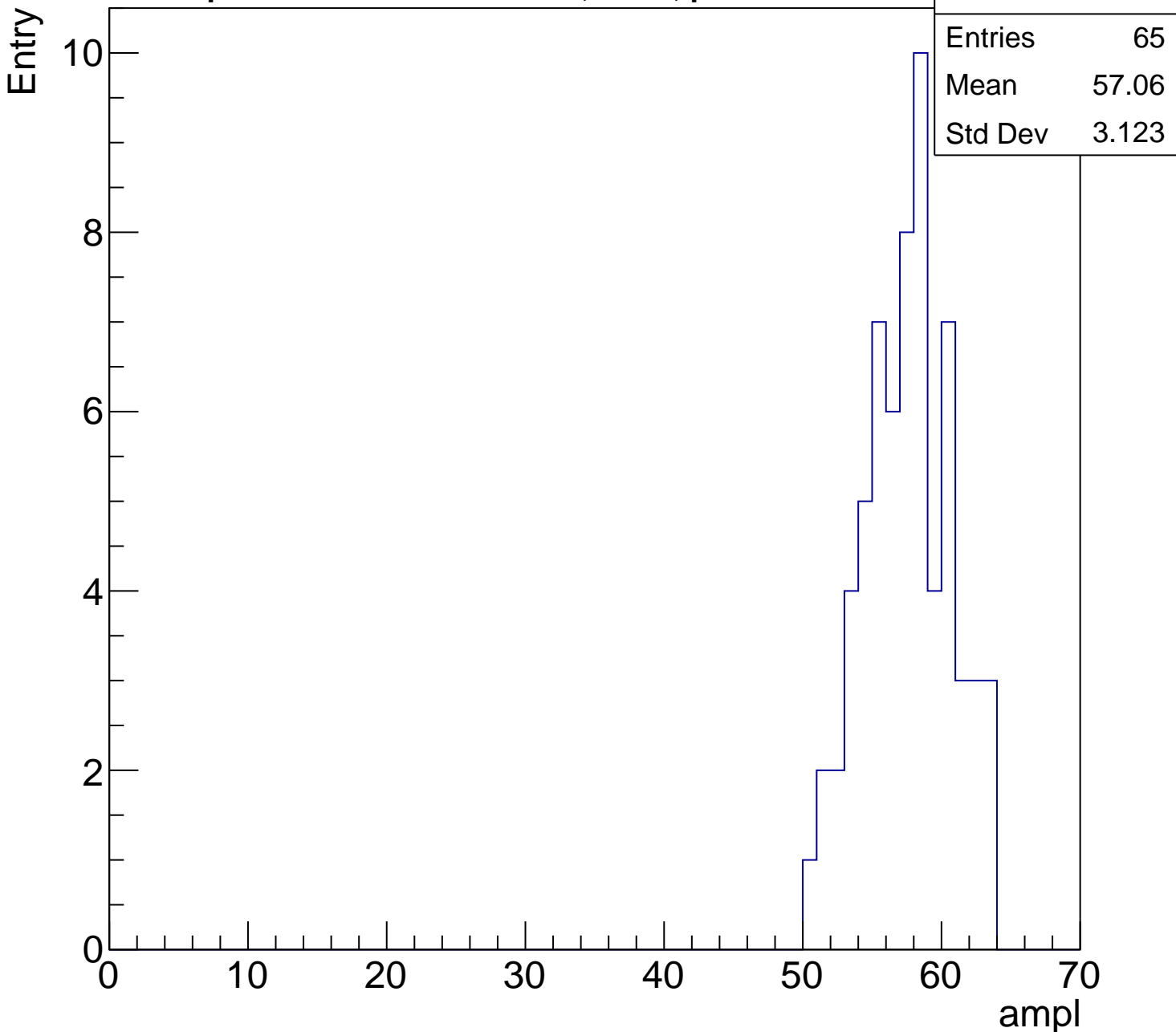
40

50

60

ampl

70

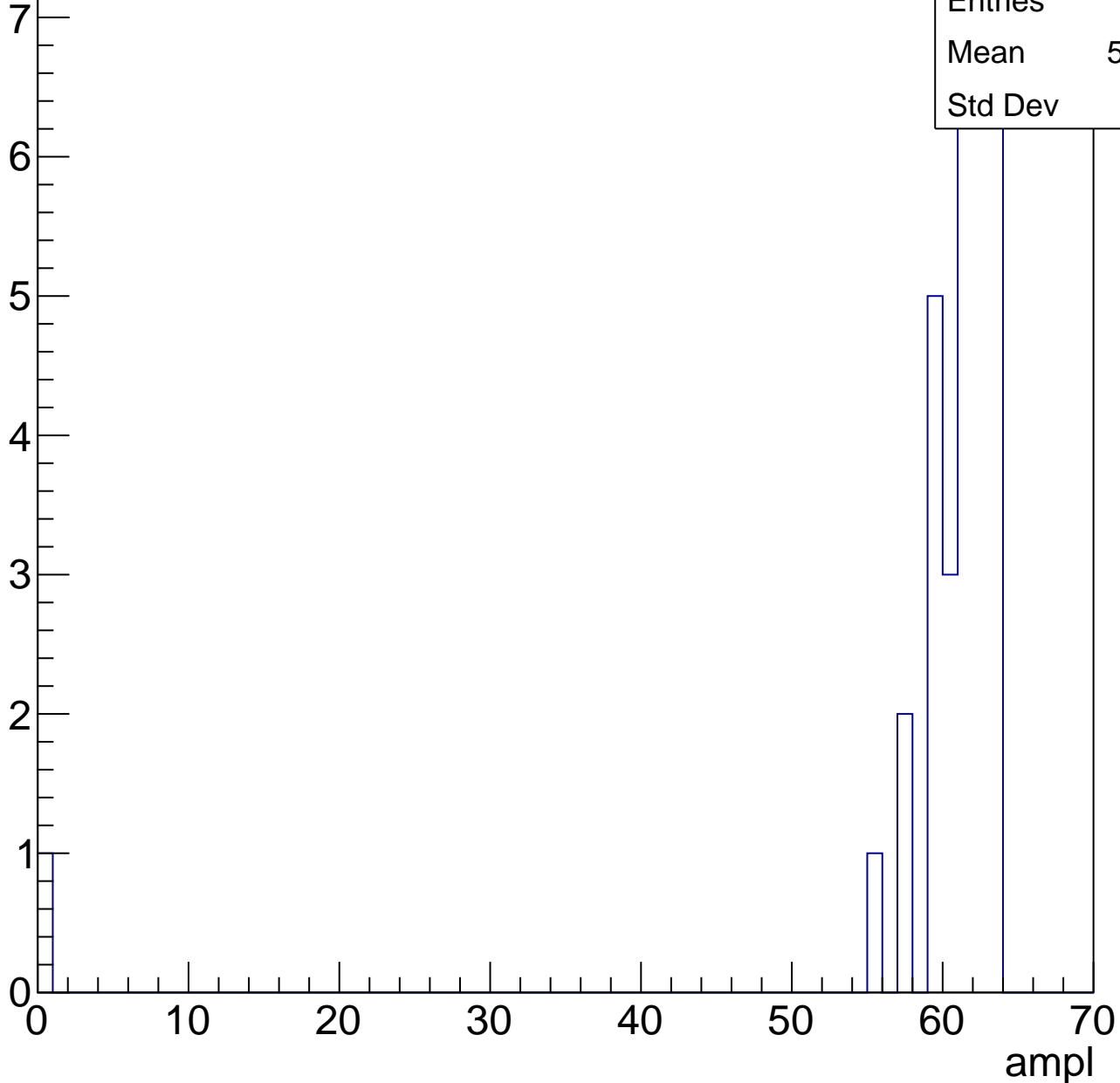


B1L103S, U17-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.97
Std Dev	10.6



B1L103S, U17-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch58, adc0

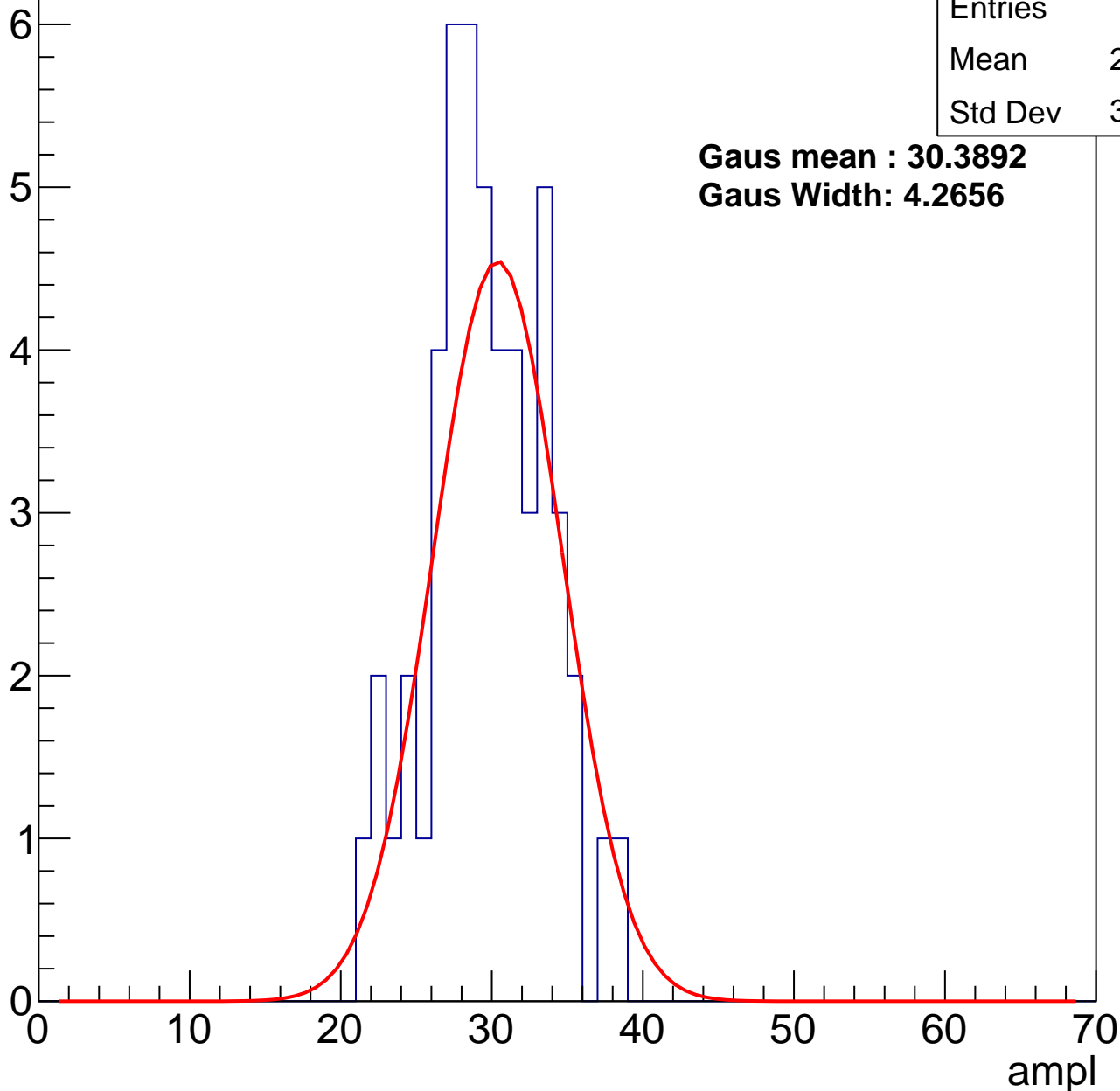
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	29.25
Std Dev	3.824

Gaus mean : 30.3892

Gaus Width: 4.2656



B1L103S, U17-ch58, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	34.99
Std Dev	4.121

Gaus mean : 35.3067

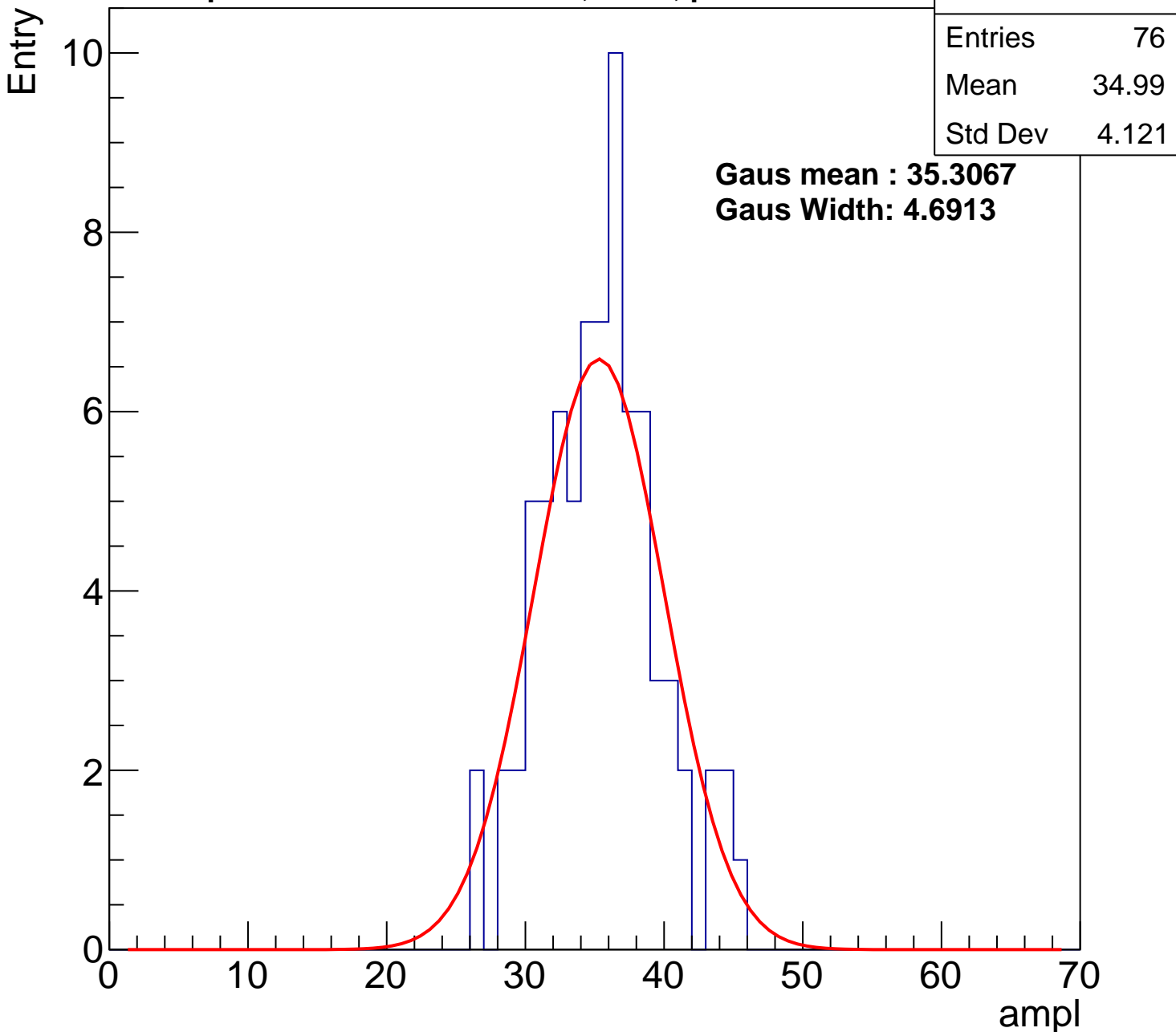
Gaus Width: 4.6913

Entry

10
8
6
4
2
0

ampl

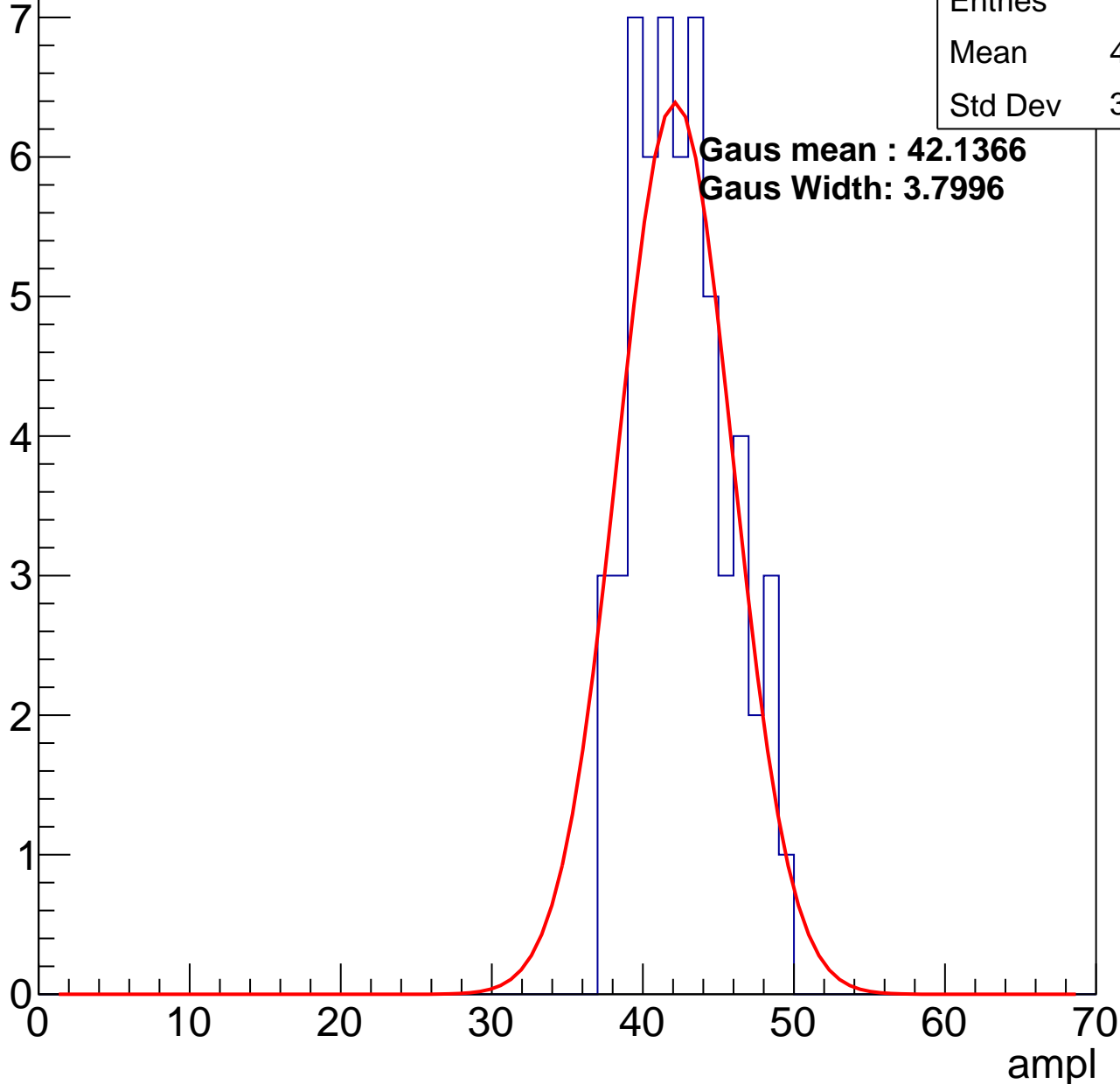
0 10 20 30 40 50 60 70



B1L103S, U17-ch58, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

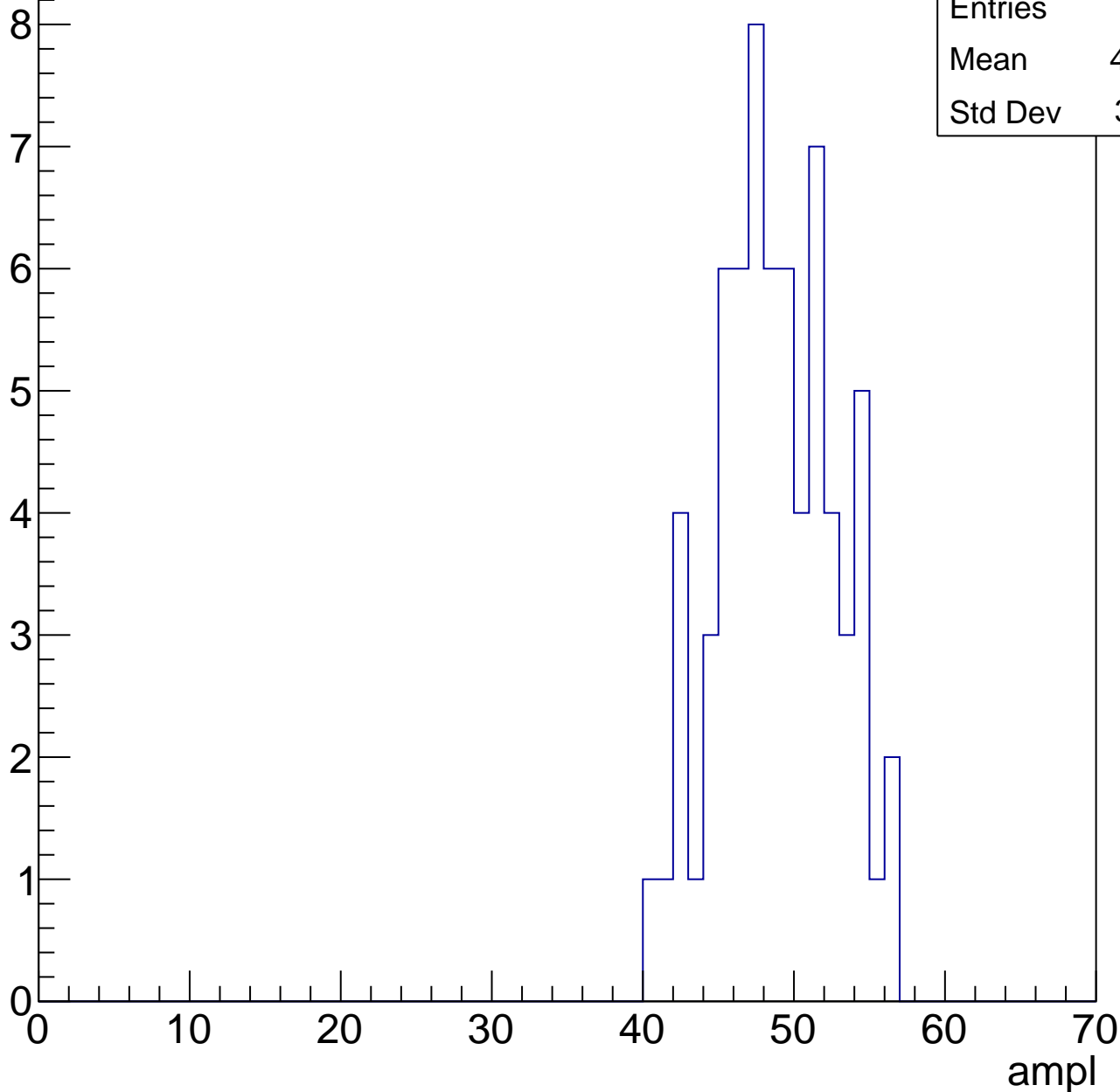


B1L103S, U17-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

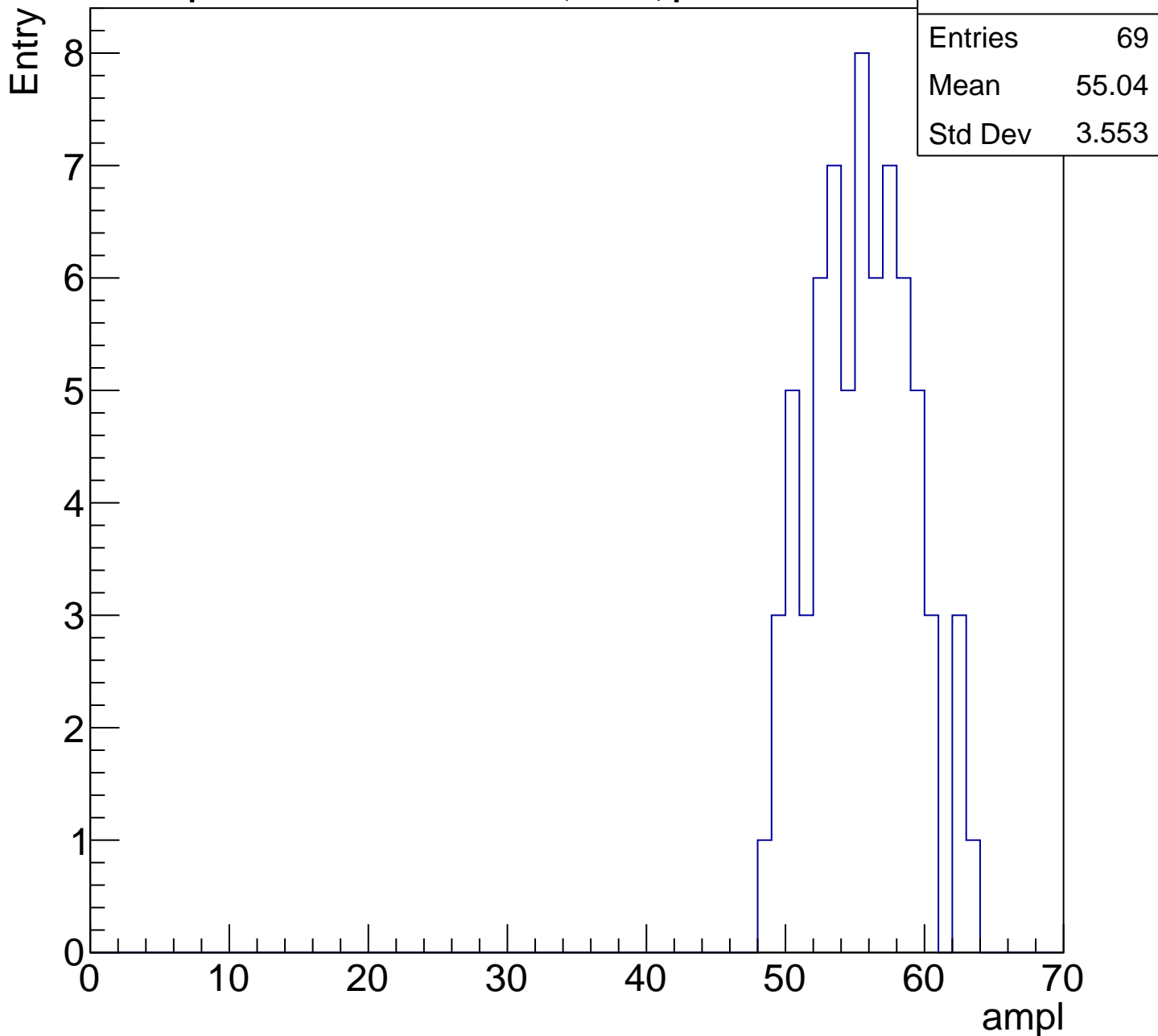
Entry

Entries	68
Mean	48.37
Std Dev	3.811



B1L103S, U17-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

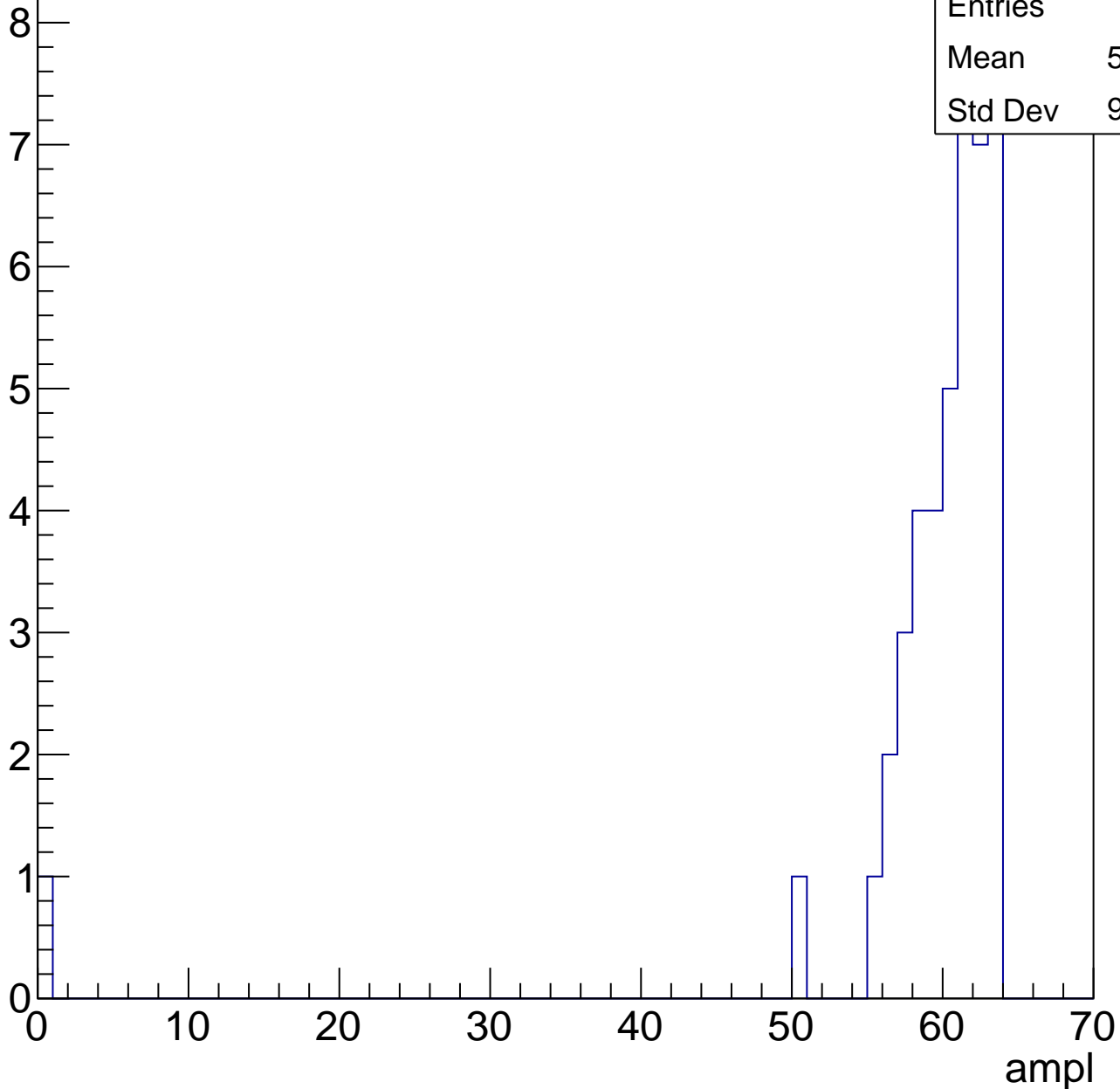


B1L103S, U17-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.68
Std Dev	9.337



B1L103S, U17-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	1.247

ampl

0 10 20 30 40 50 60 70

B1L103S, U17-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch59, adc0

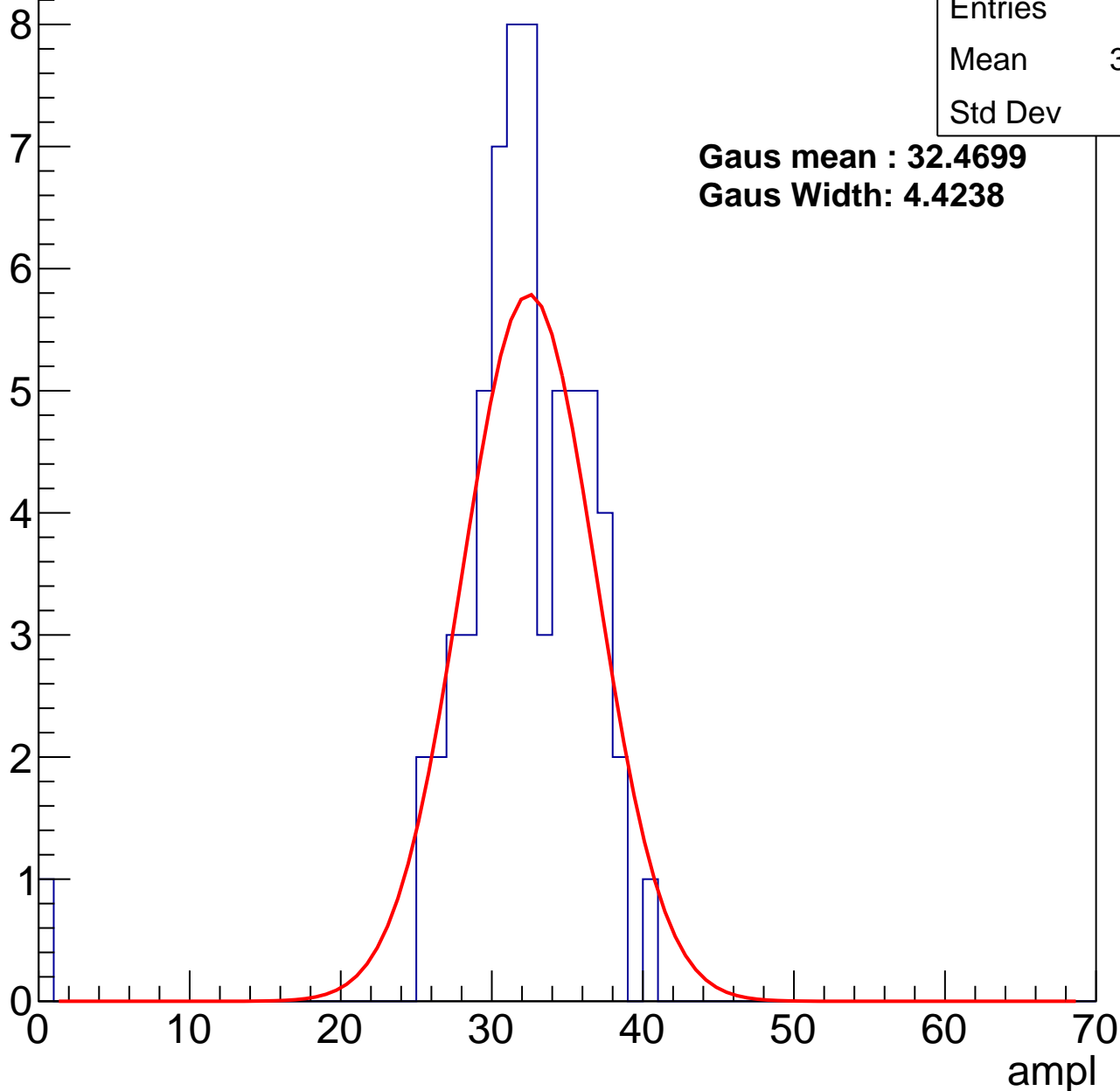
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	31.47
Std Dev	5.25

Gaus mean : 32.4699

Gaus Width: 4.4238



B1L103S, U17-ch59, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	66
Mean	38.53
Std Dev	3.281

Gaus mean : 39.1614

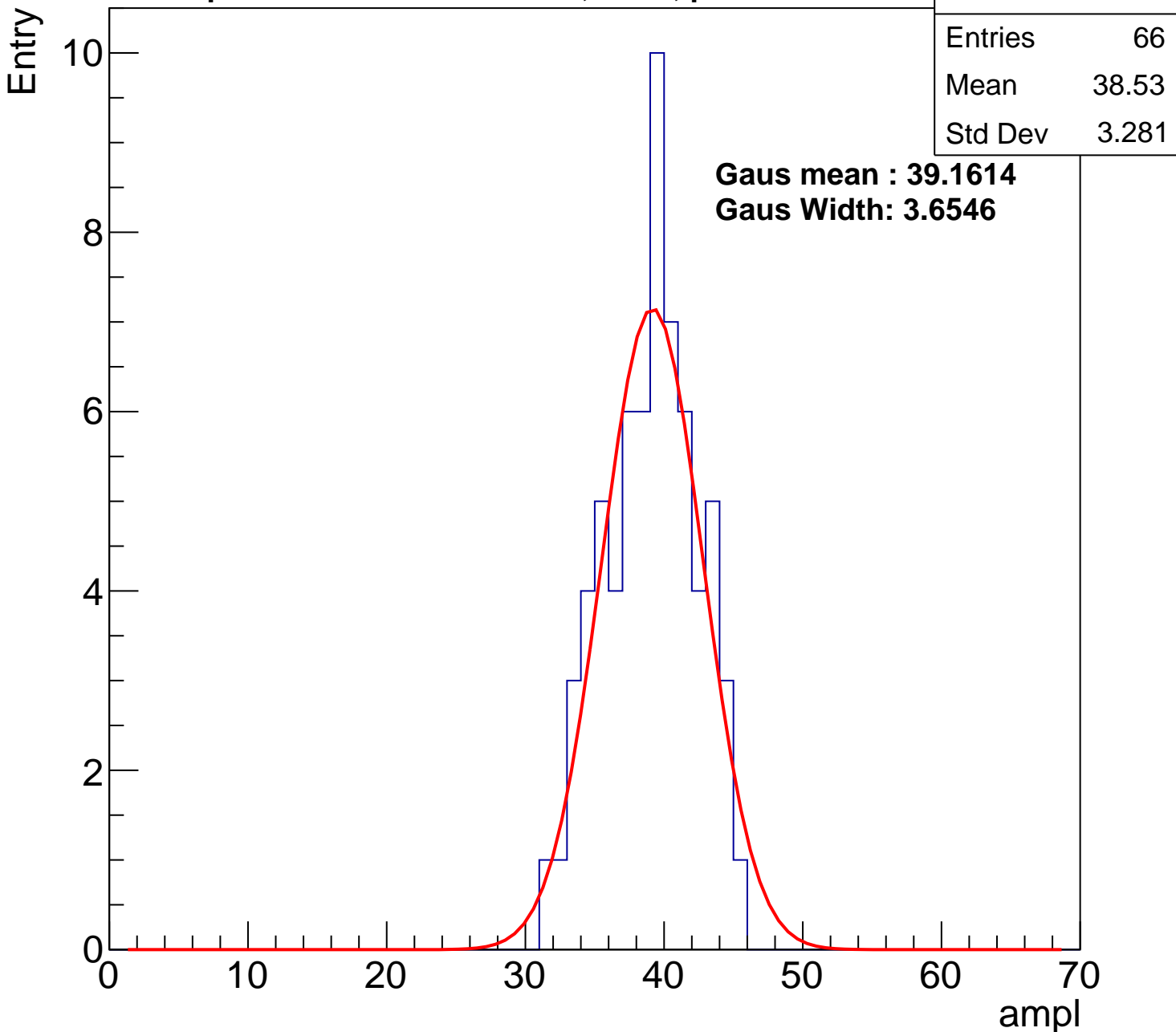
Gaus Width: 3.6546

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch59, adc2

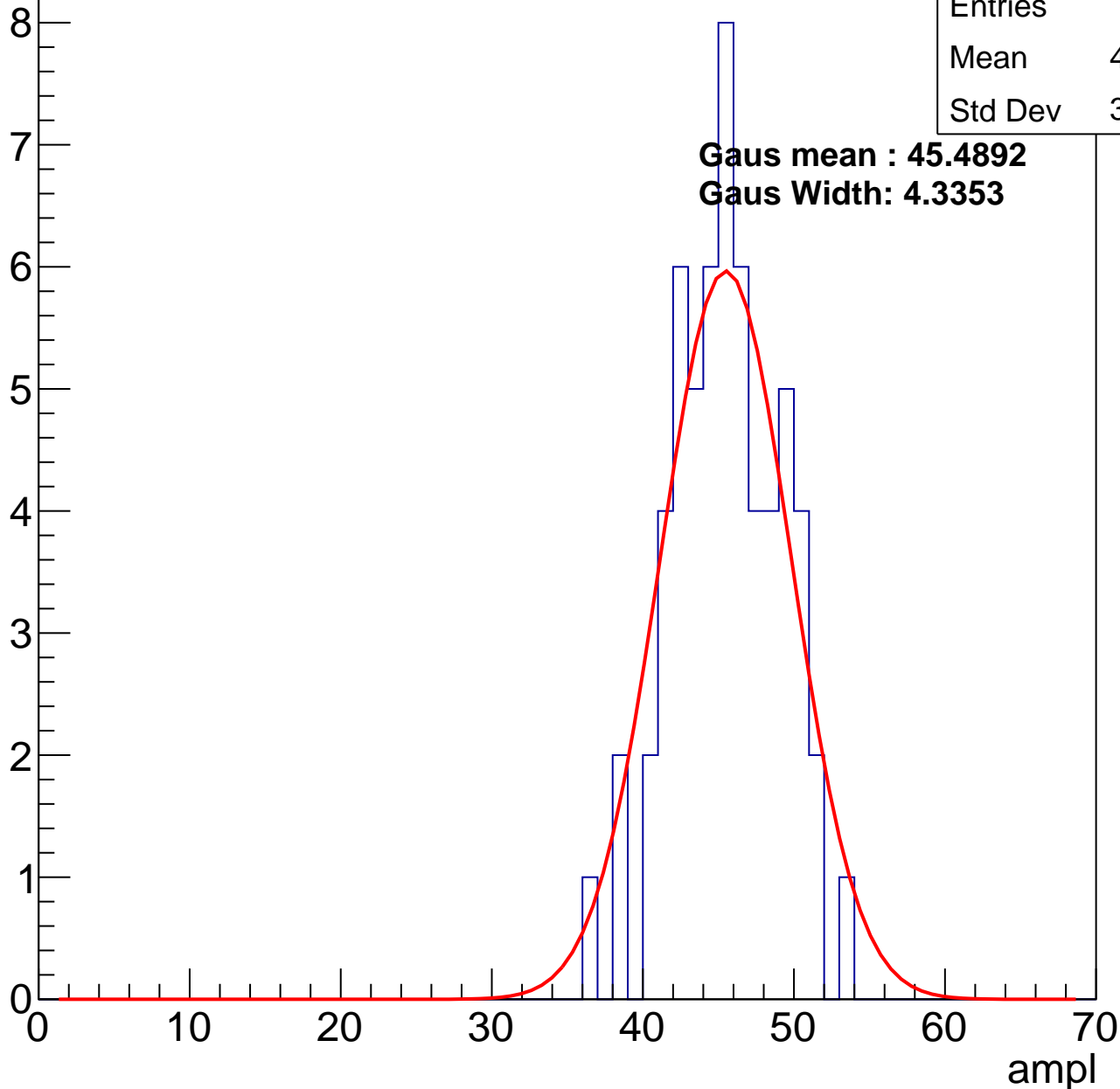
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	45.05
Std Dev	3.528

Gaus mean : 45.4892

Gaus Width: 4.3353

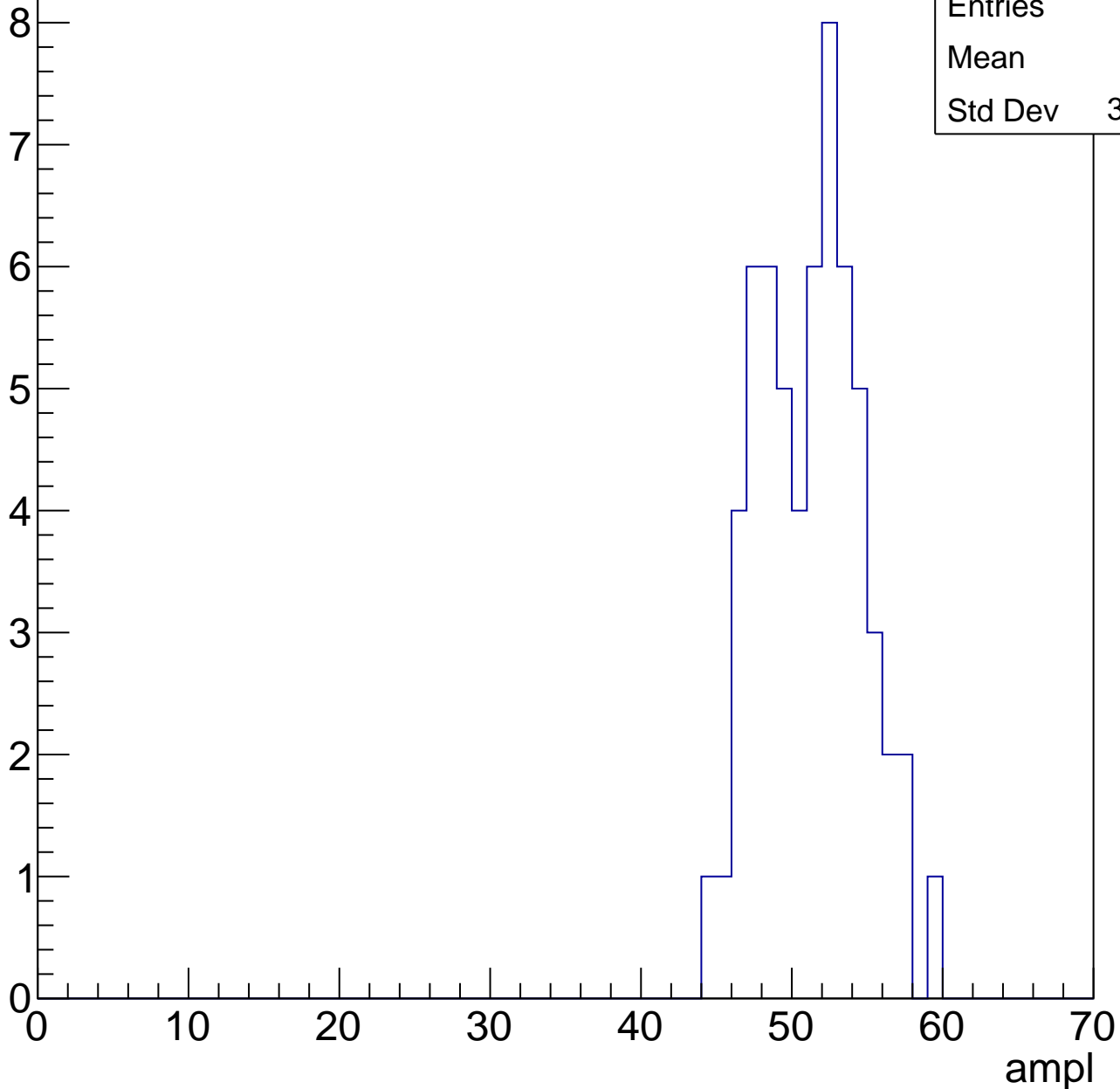


B1L103S, U17-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	50.8
Std Dev	3.336



B1L103S, U17-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

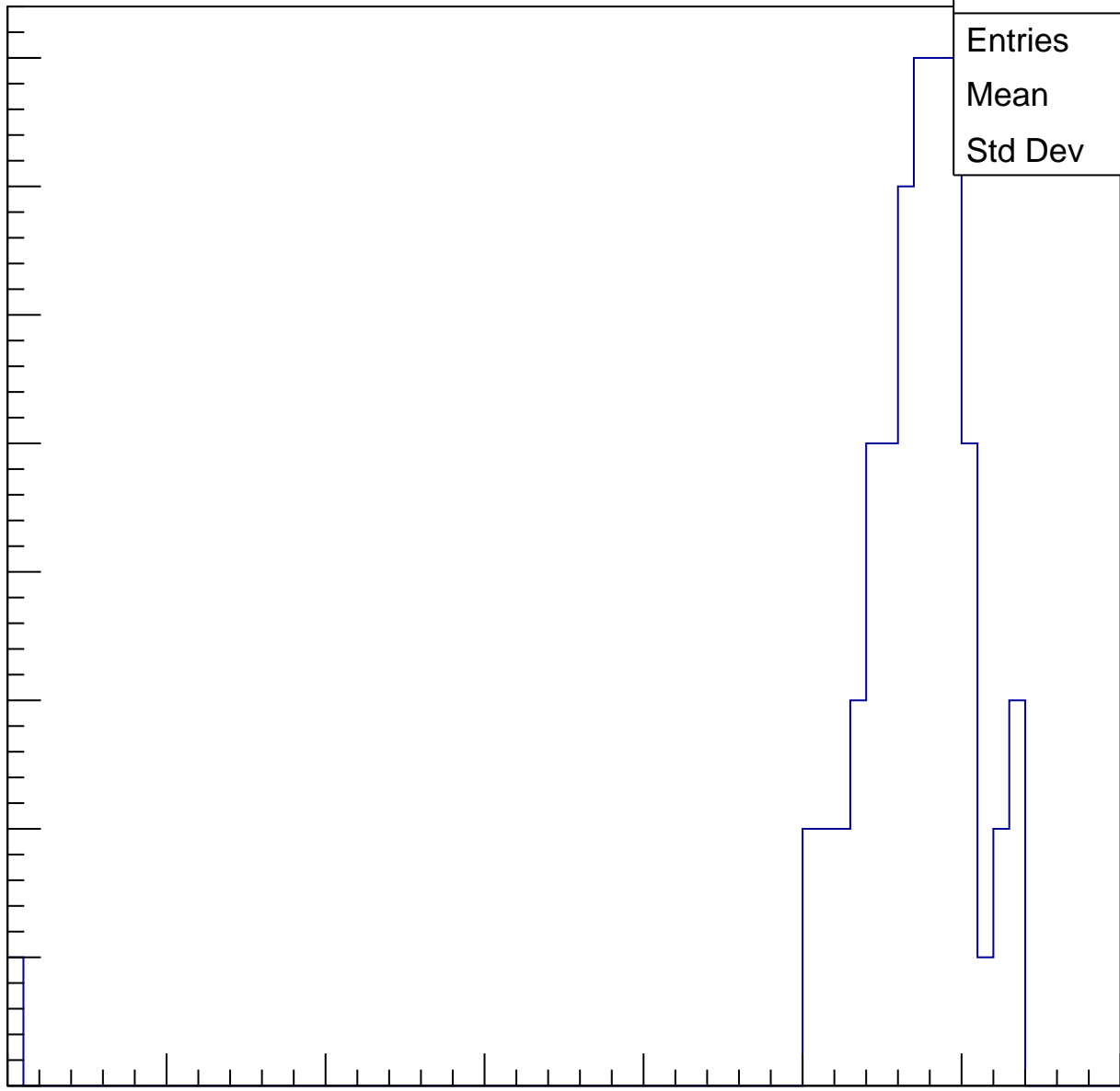
Entry

8
7
6
5
4
3
2
1
0

Entries	62
Mean	55.94
Std Dev	7.806

ampl

0 10 20 30 40 50 60 70

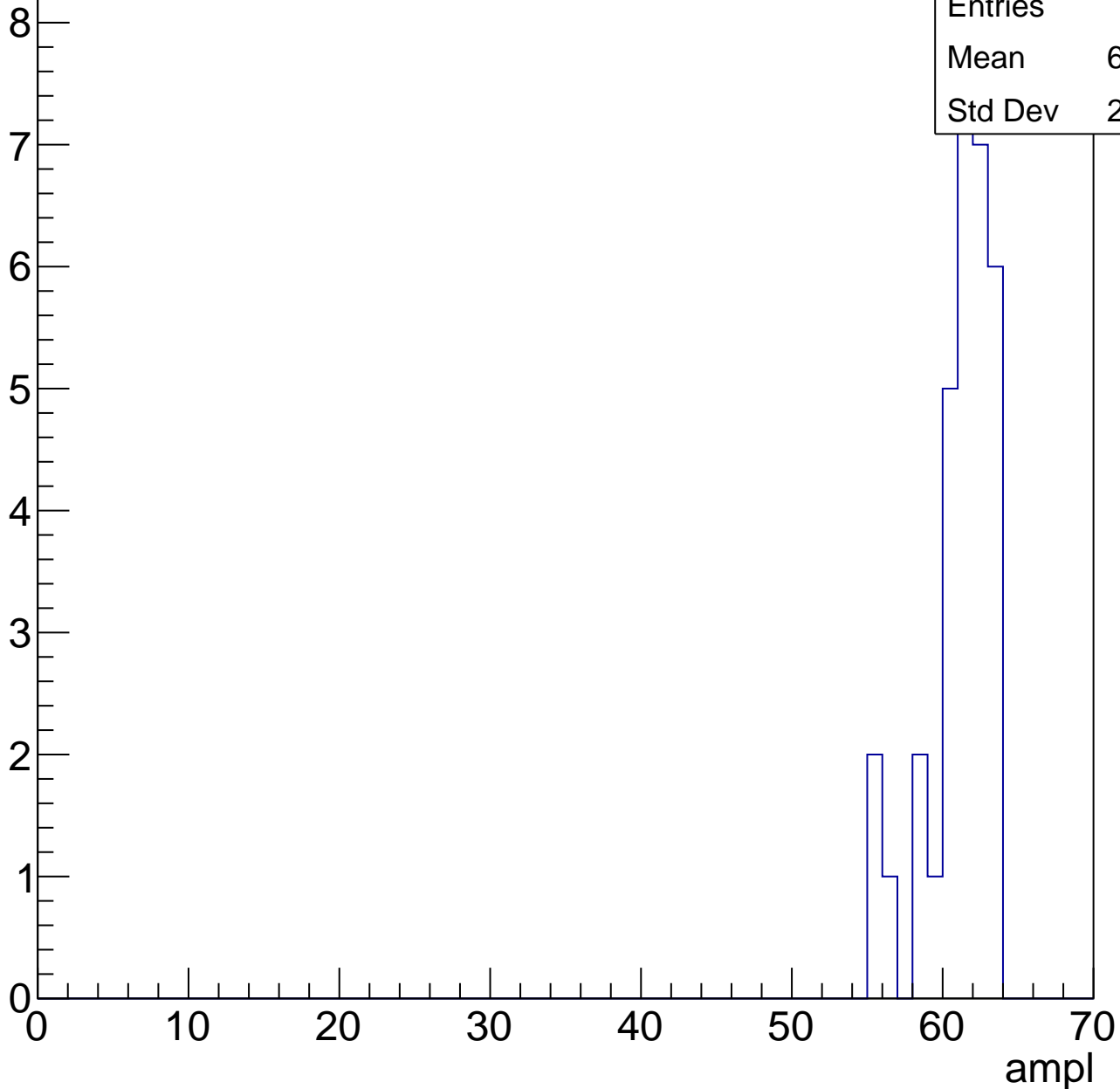


B1L103S, U17-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	60.66
Std Dev	2.174



B1L103S, U17-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch60, adc0

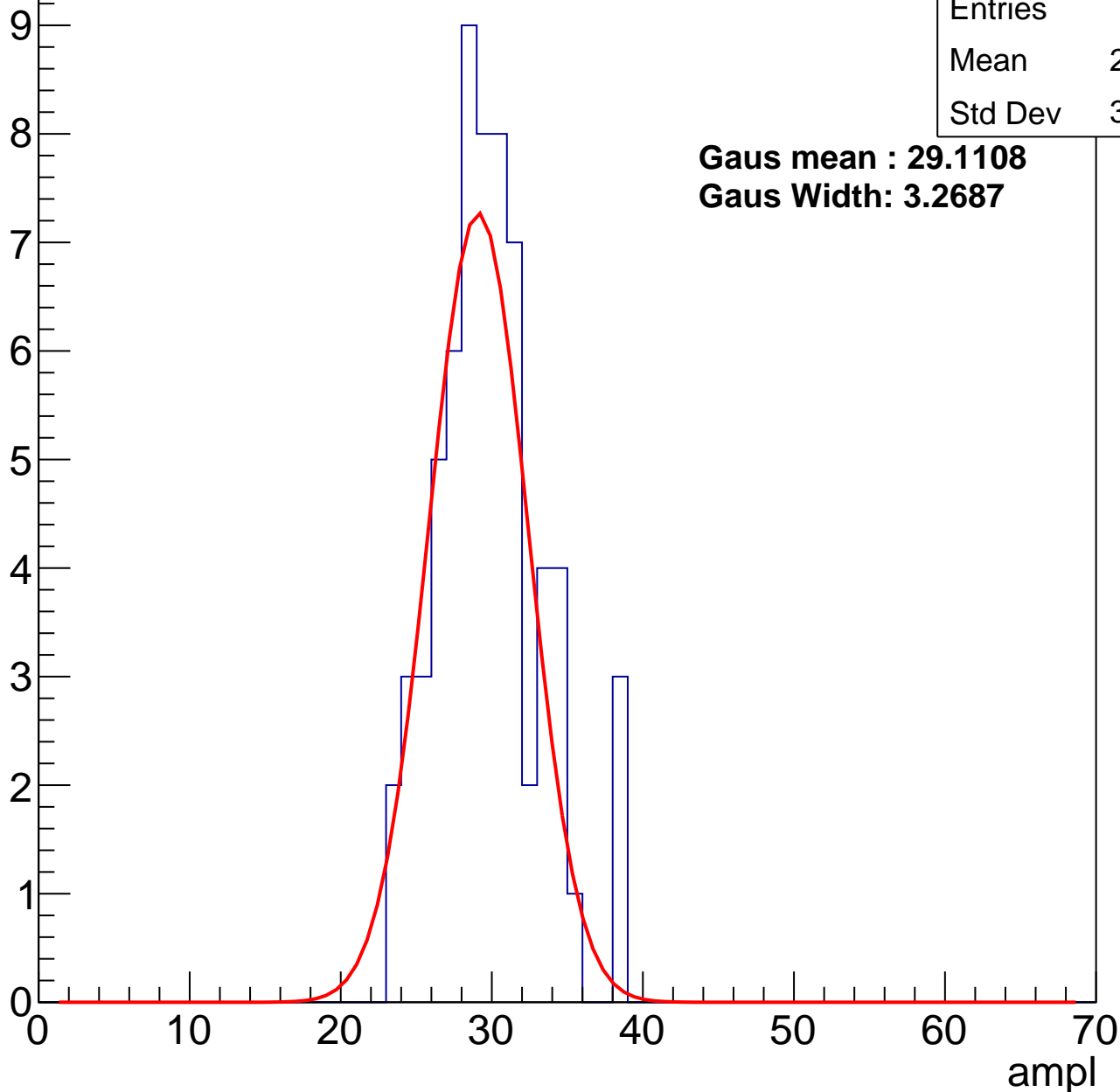
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.34
Std Dev	3.425

Gaus mean : 29.1108

Gaus Width: 3.2687



B1L103S, U17-ch60, adc1

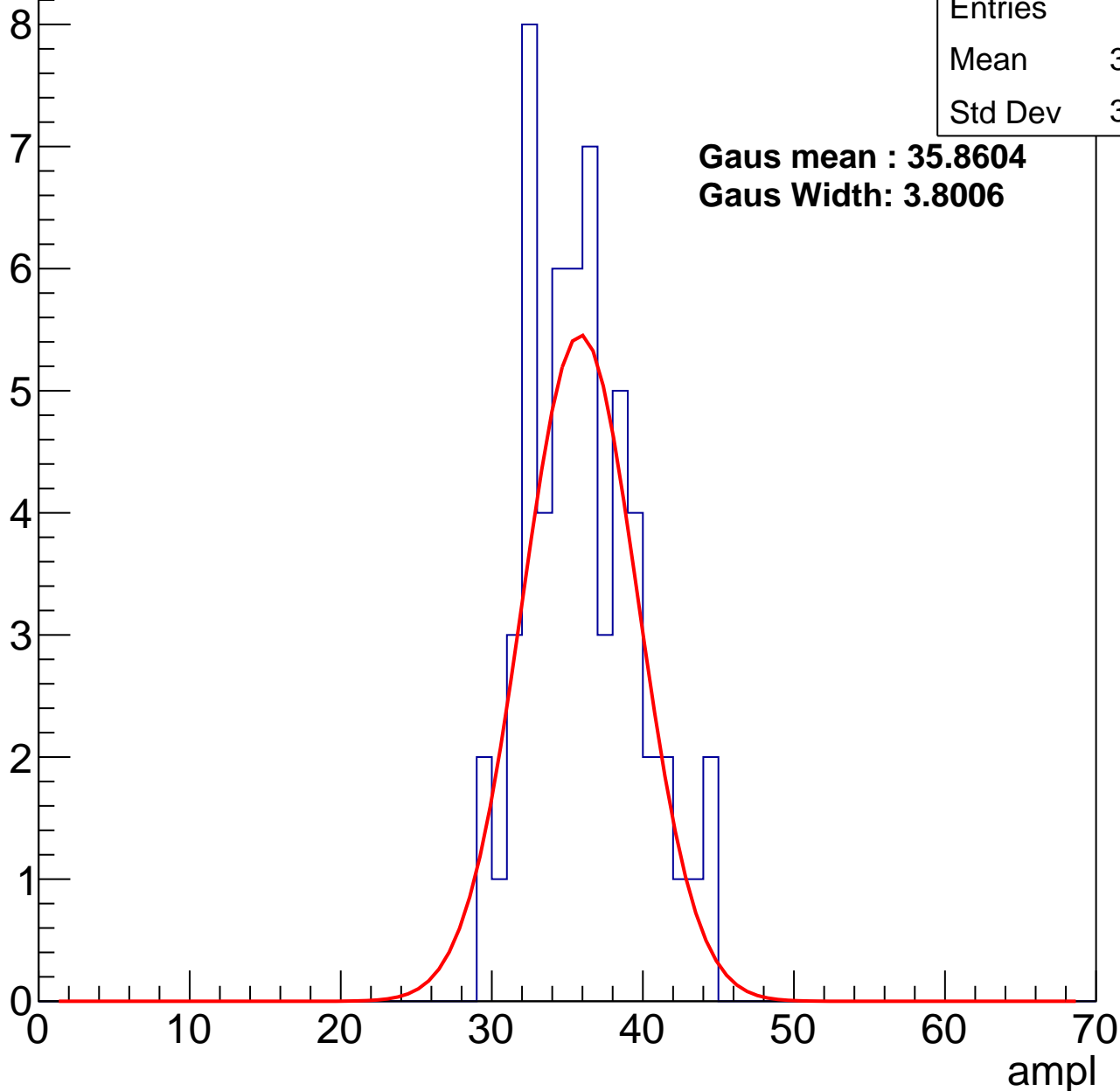
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	35.56
Std Dev	3.618

Gaus mean : 35.8604

Gaus Width: 3.8006



B1L103S, U17-ch60, adc2

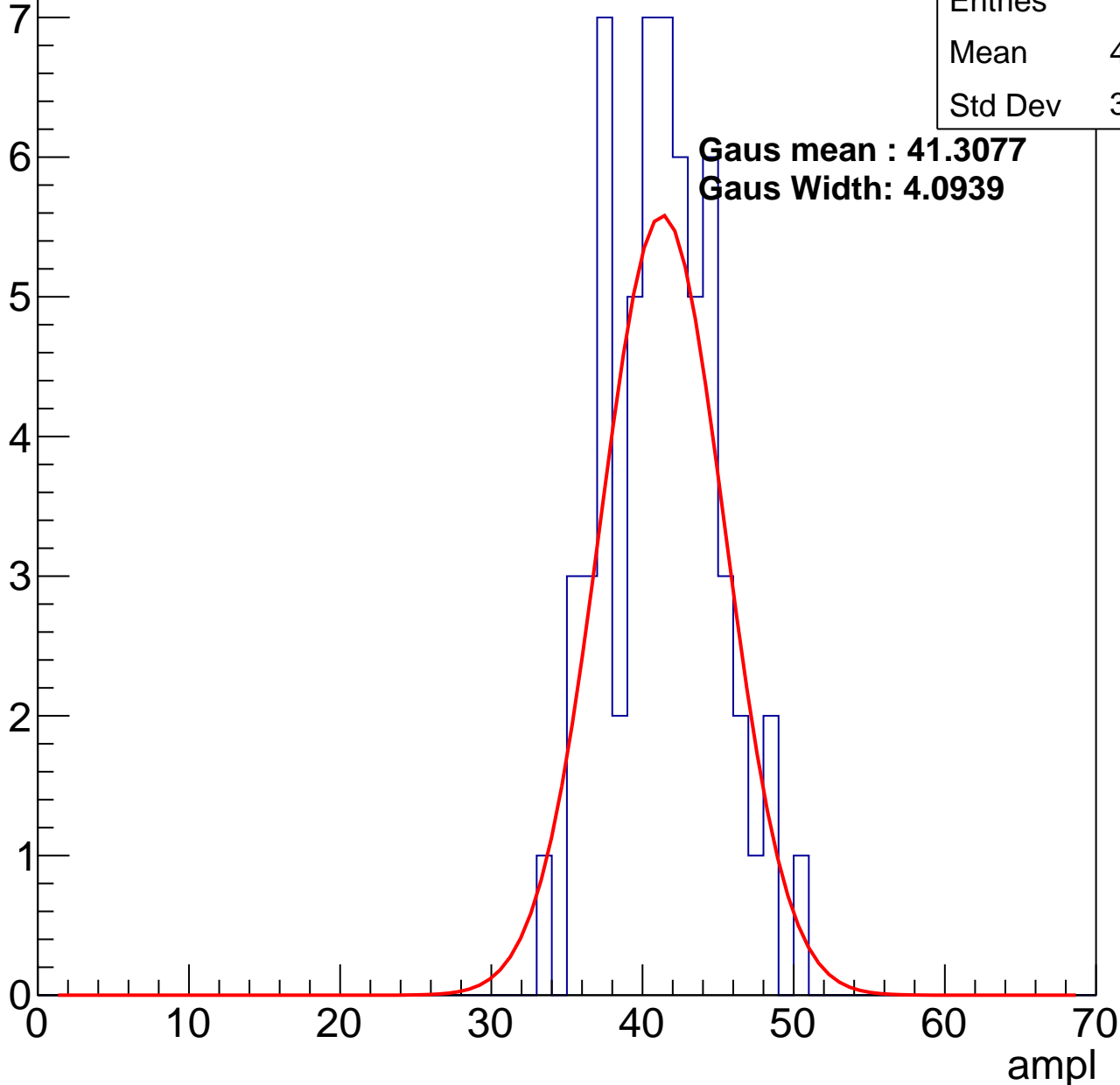
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.89
Std Dev	3.613

Gaus mean : 41.3077

Gaus Width: 4.0939

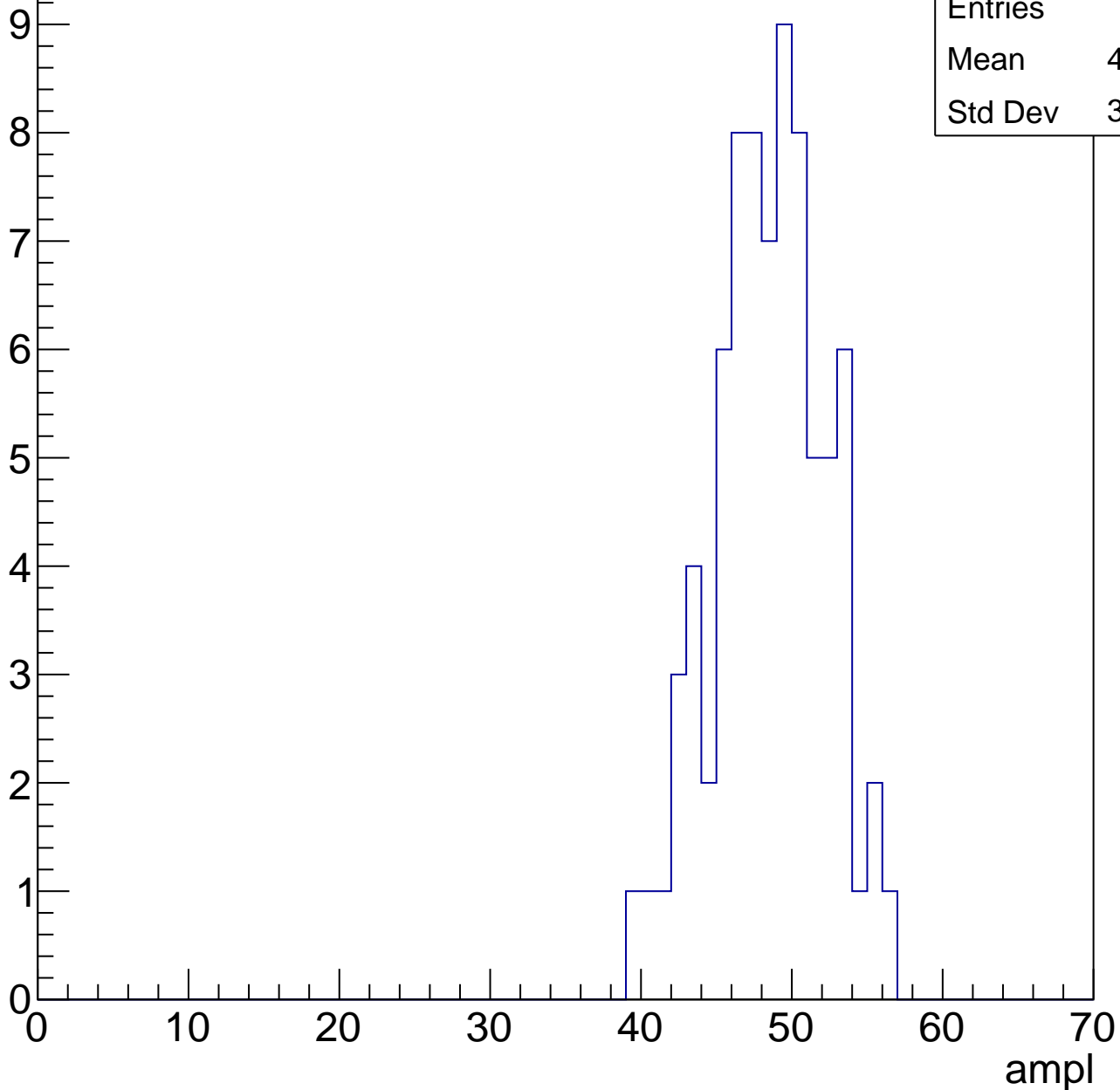


B1L103S, U17-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	48.08
Std Dev	3.654

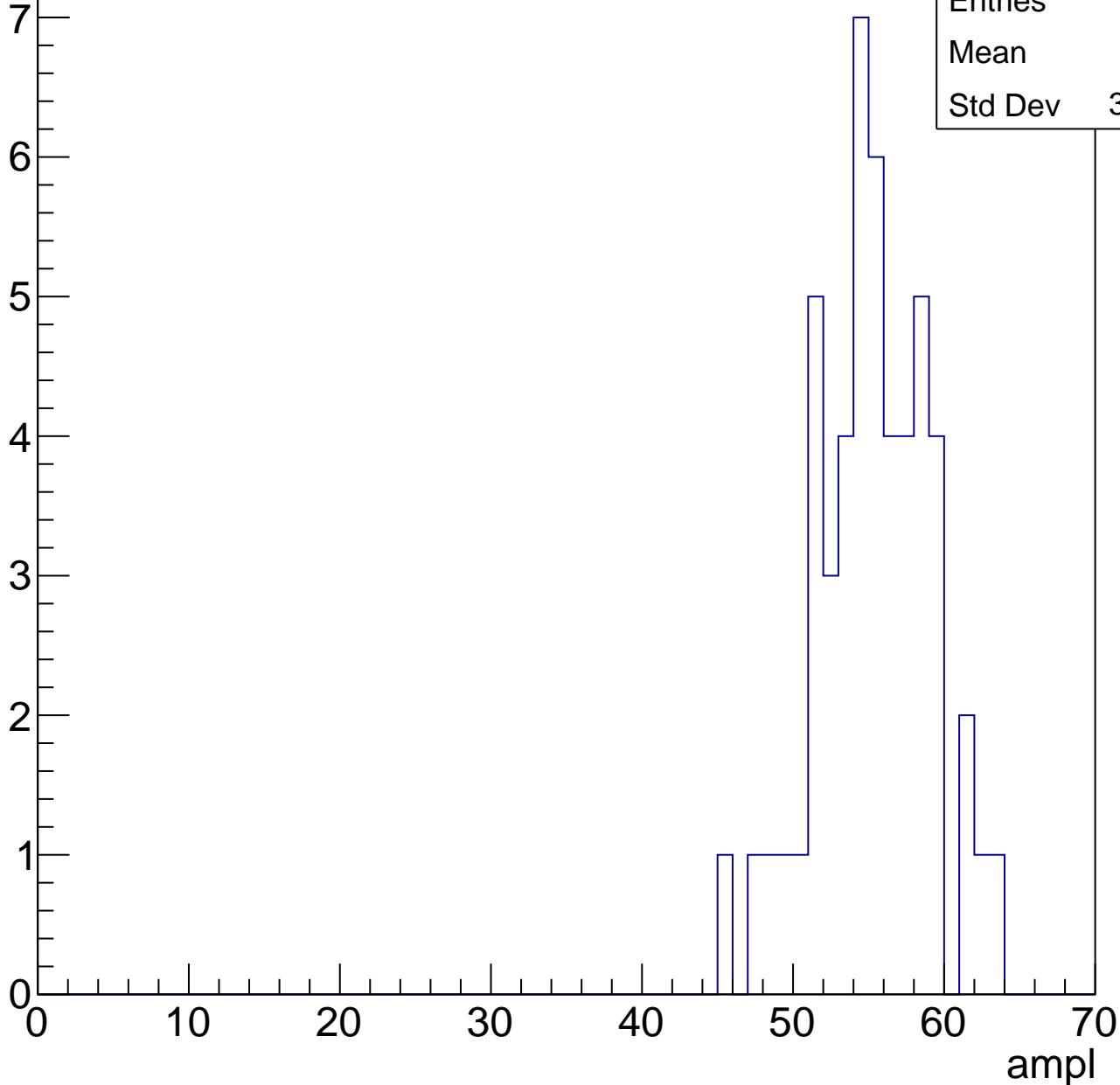


B1L103S, U17-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

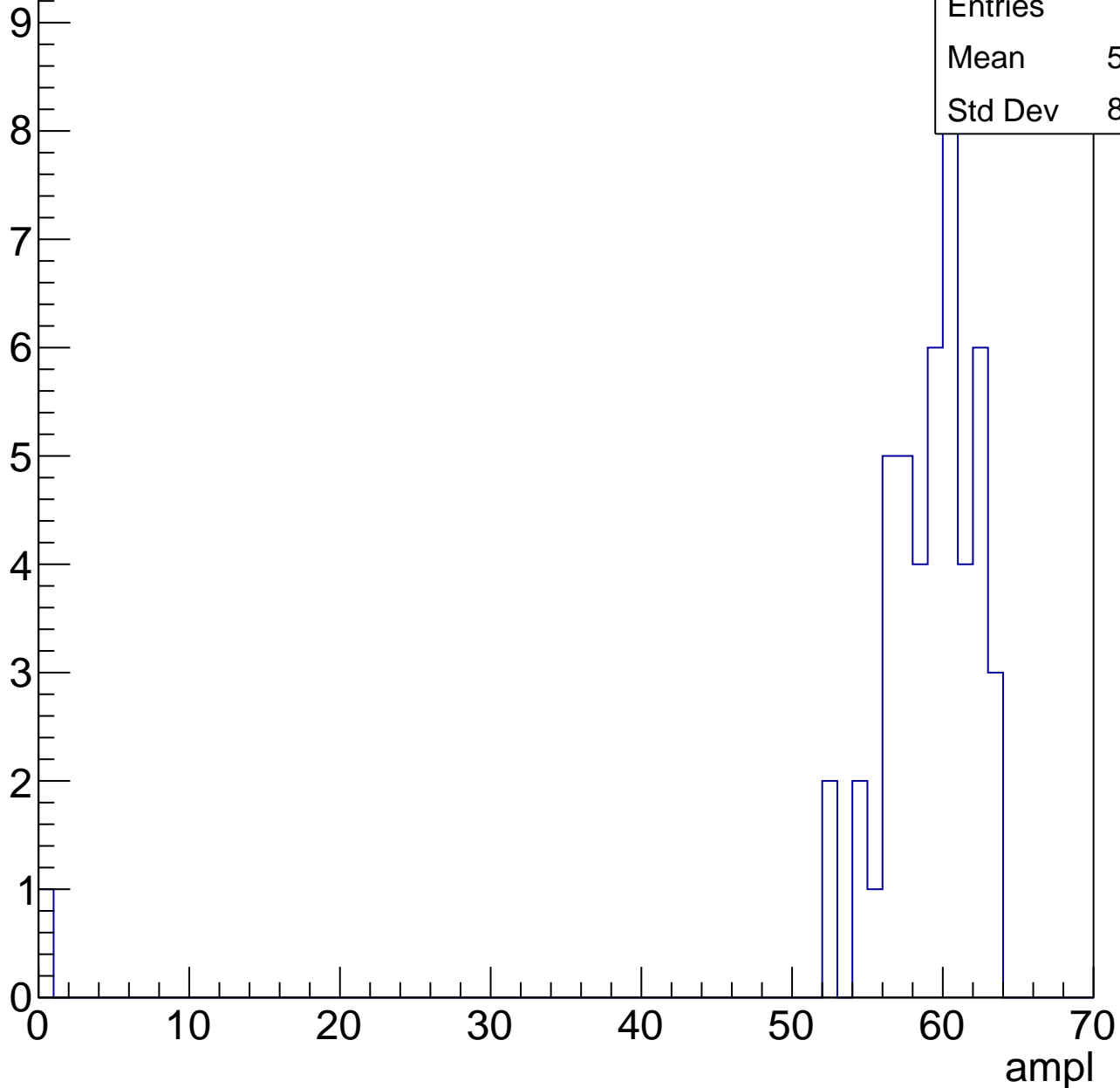
Entries	51
Mean	54.8
Std Dev	3.747



B1L103S, U17-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

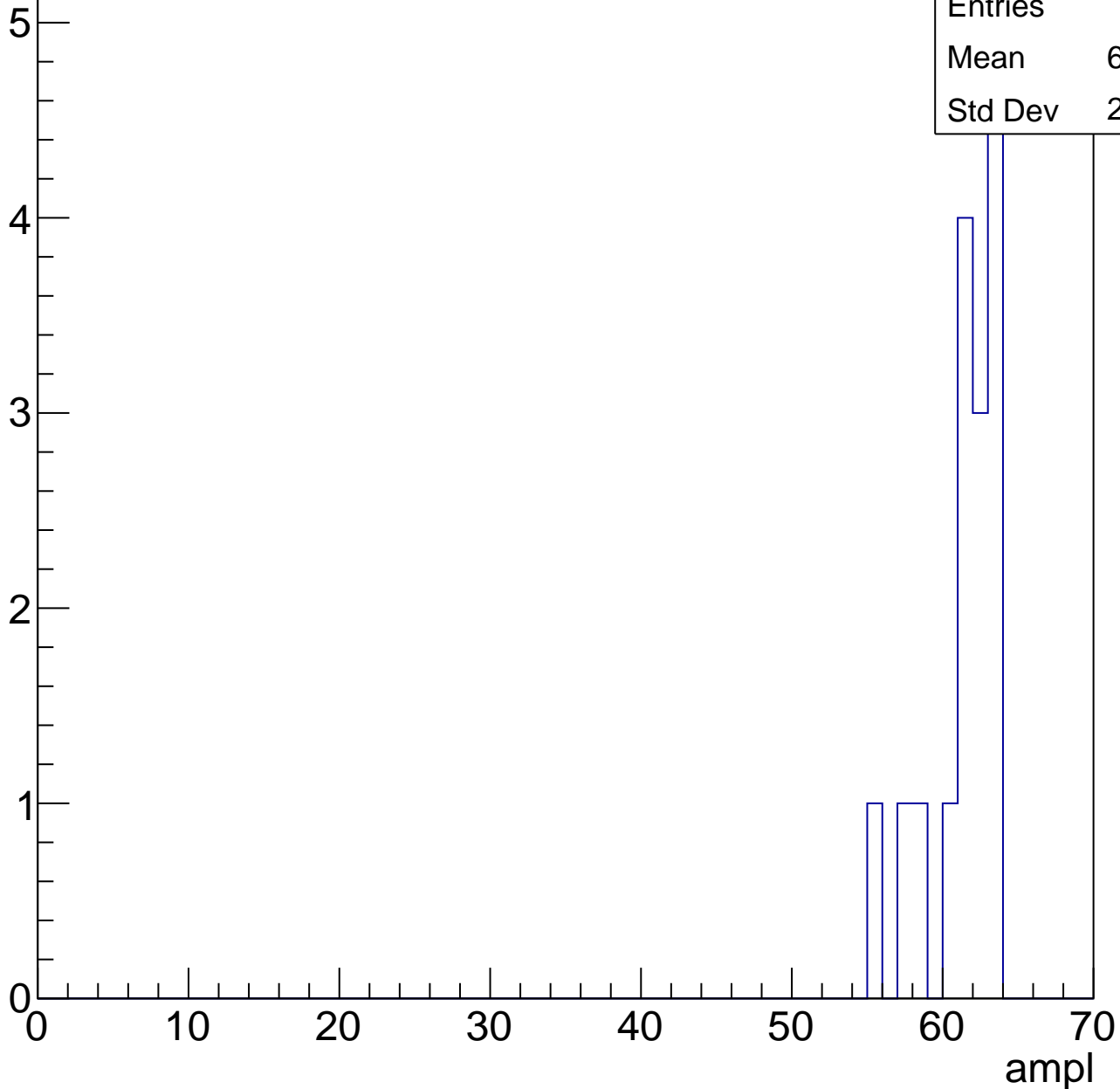


B1L103S, U17-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	60.94
Std Dev	2.304



B1L103S, U17-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L103S, U17-ch61, adc0

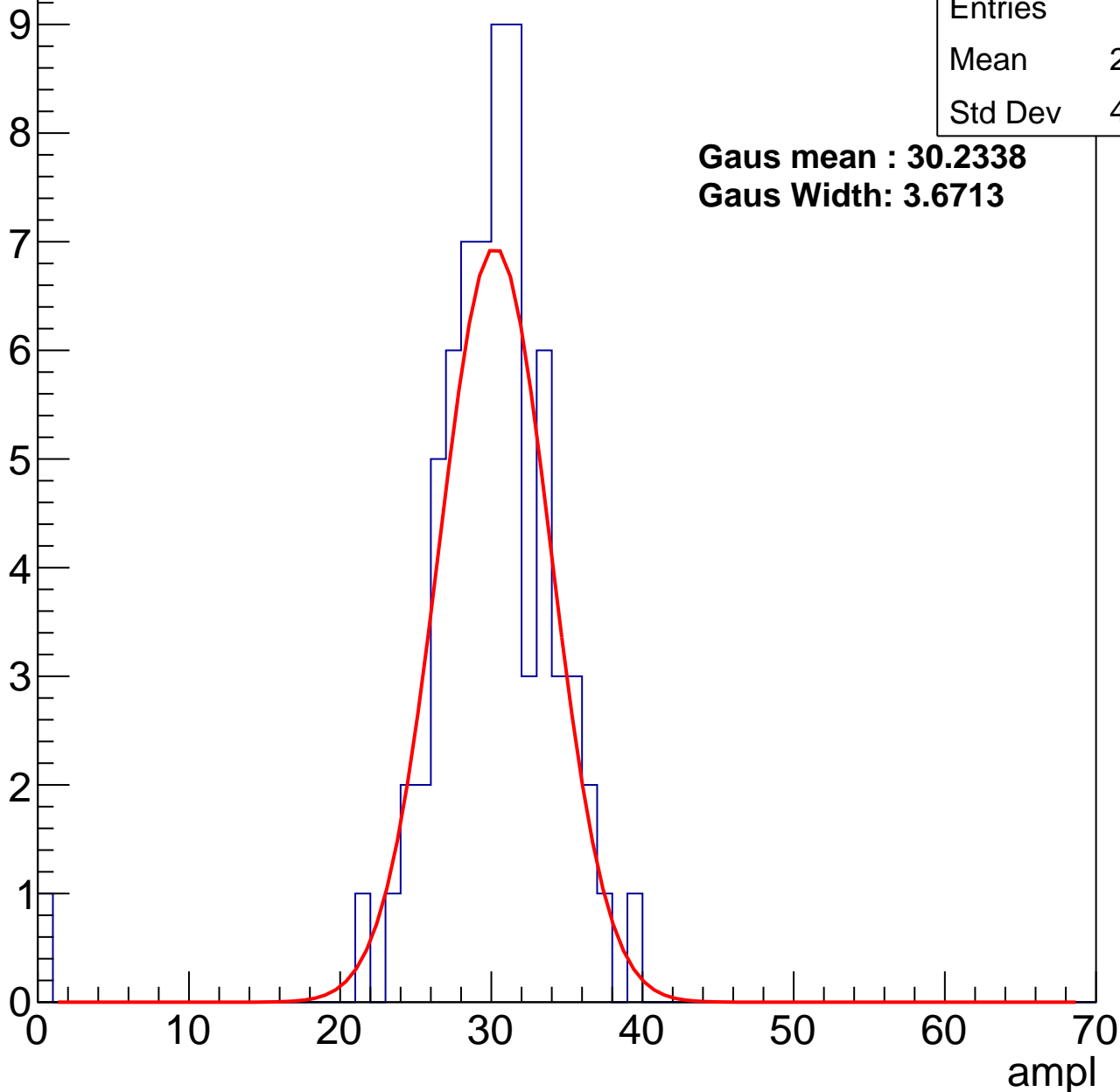
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.43
Std Dev	4.962

Gaus mean : 30.2338

Gaus Width: 3.6713



B1L103S, U17-ch61, adc1

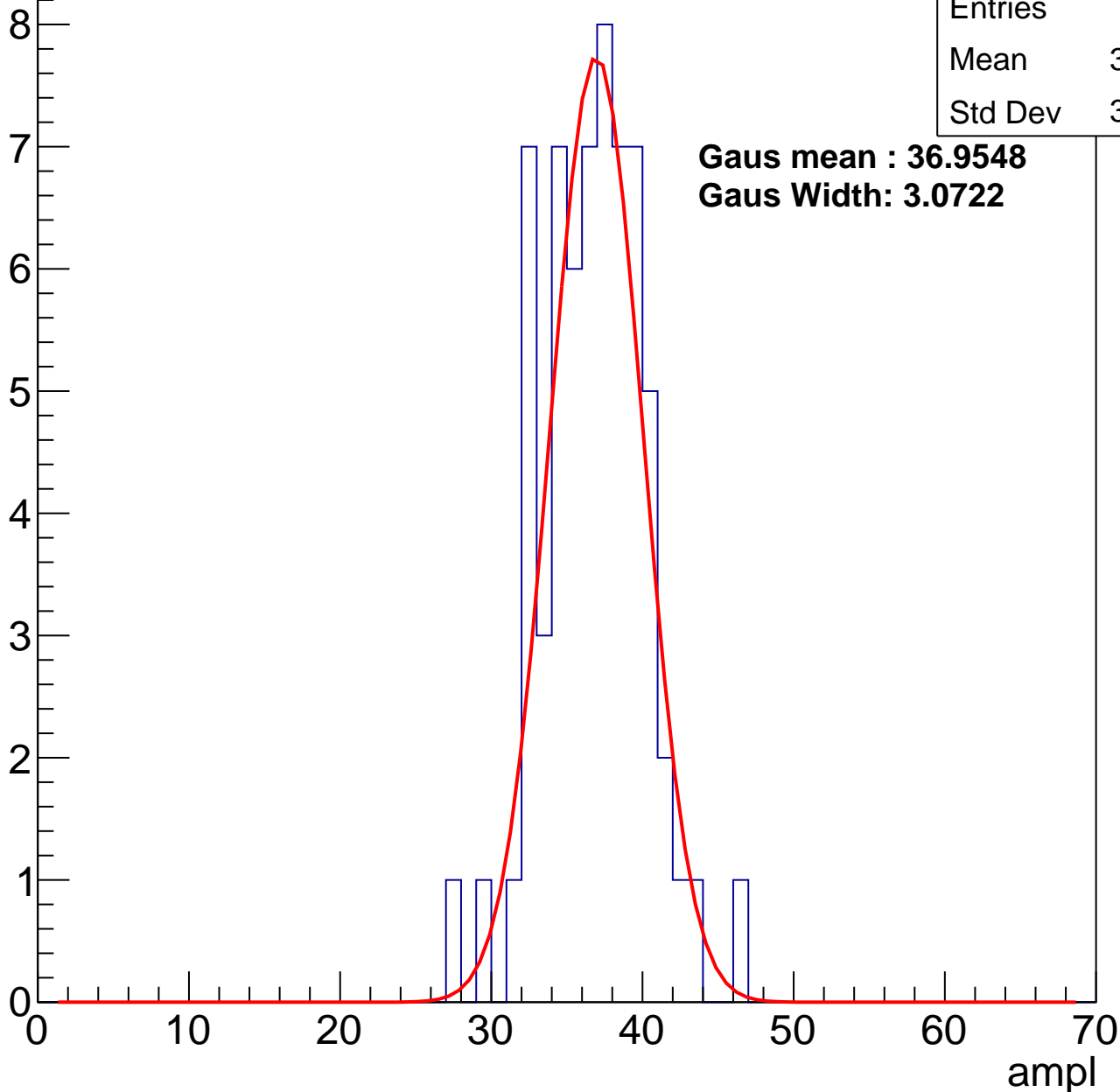
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.28
Std Dev	3.367

Gaus mean : 36.9548

Gaus Width: 3.0722



B1L103S, U17-ch61, adc2

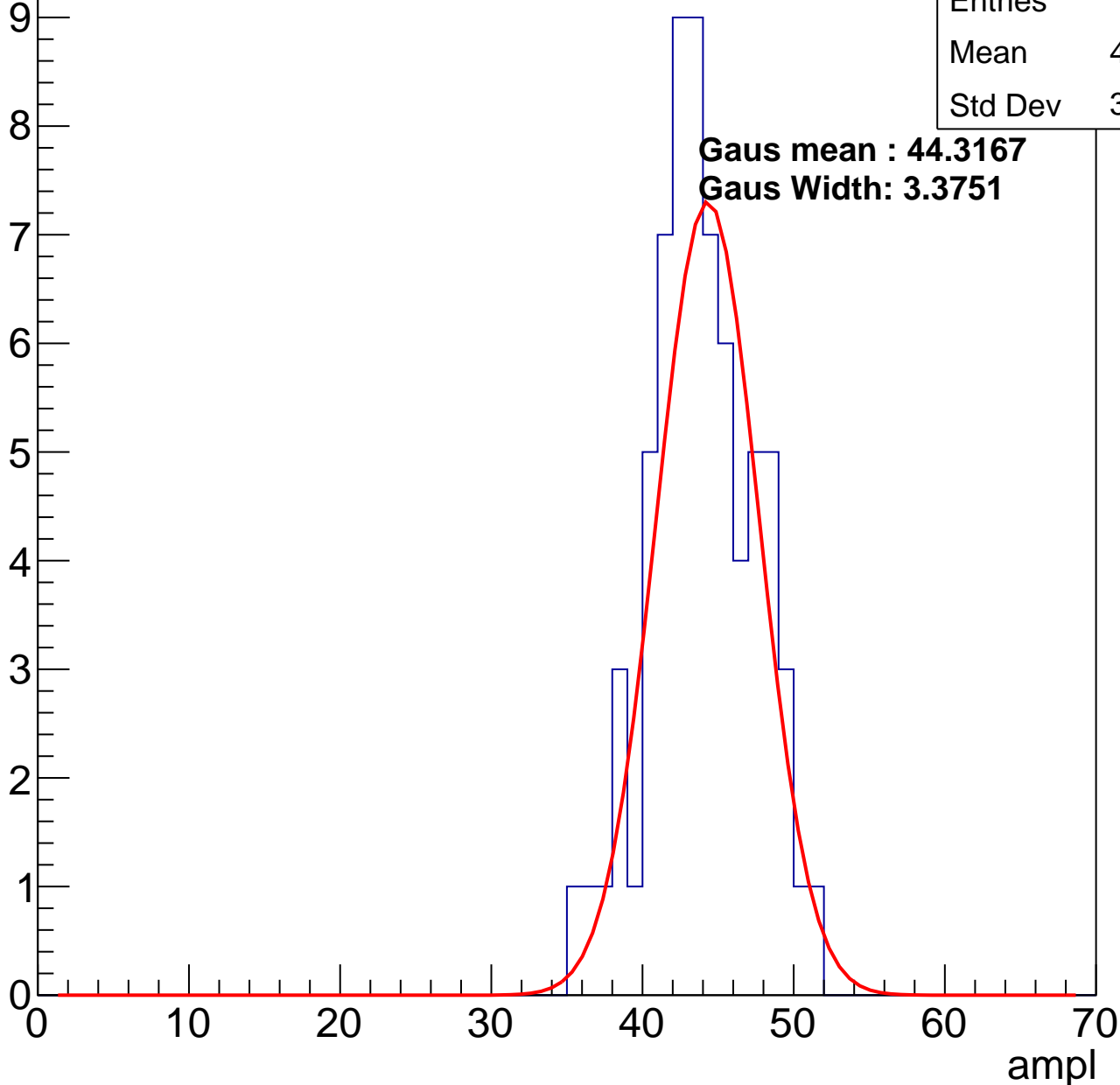
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	43.45
Std Dev	3.407

Gaus mean : 44.3167

Gaus Width: 3.3751

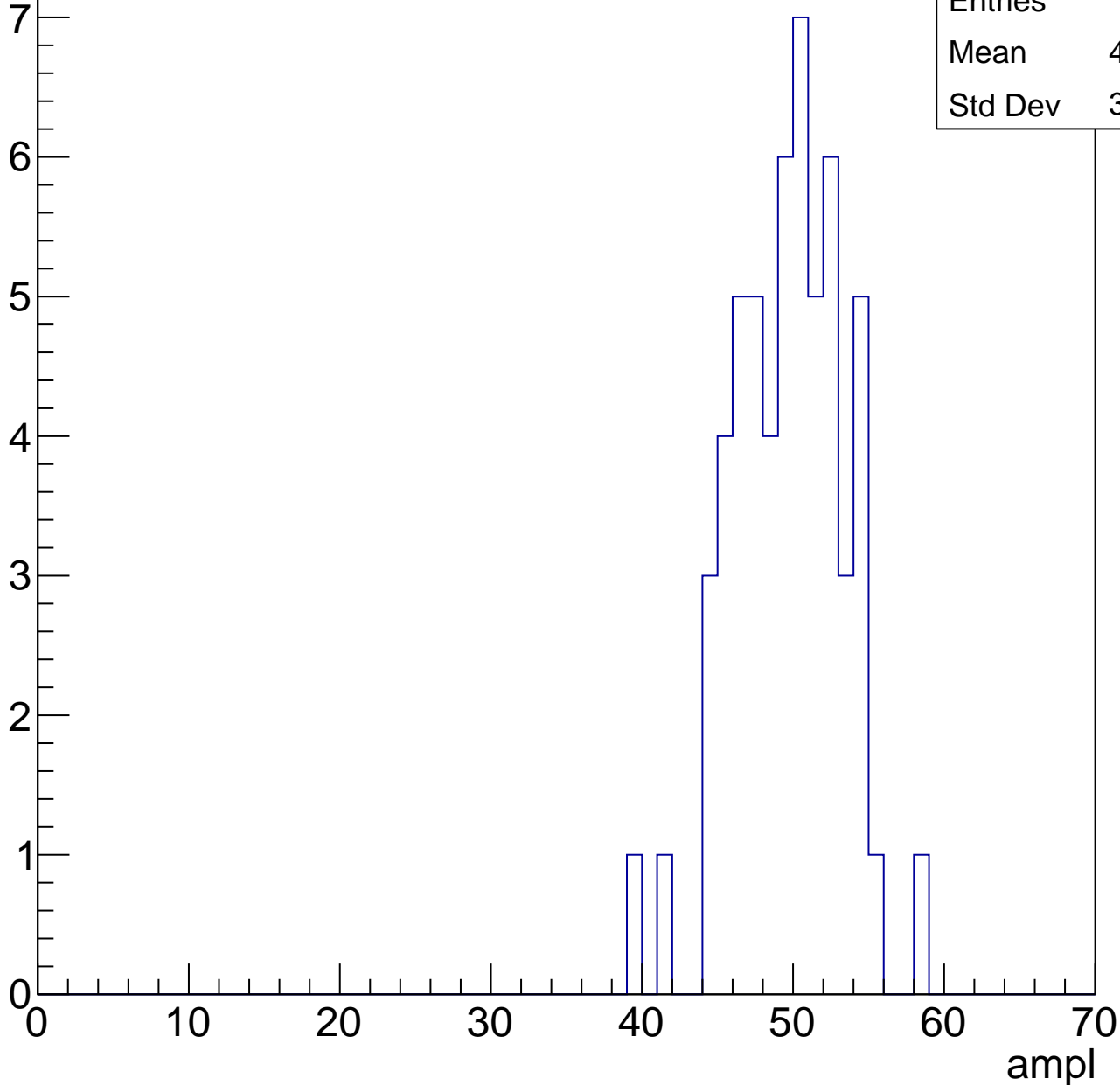


B1L103S, U17-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	49.16
Std Dev	3.607

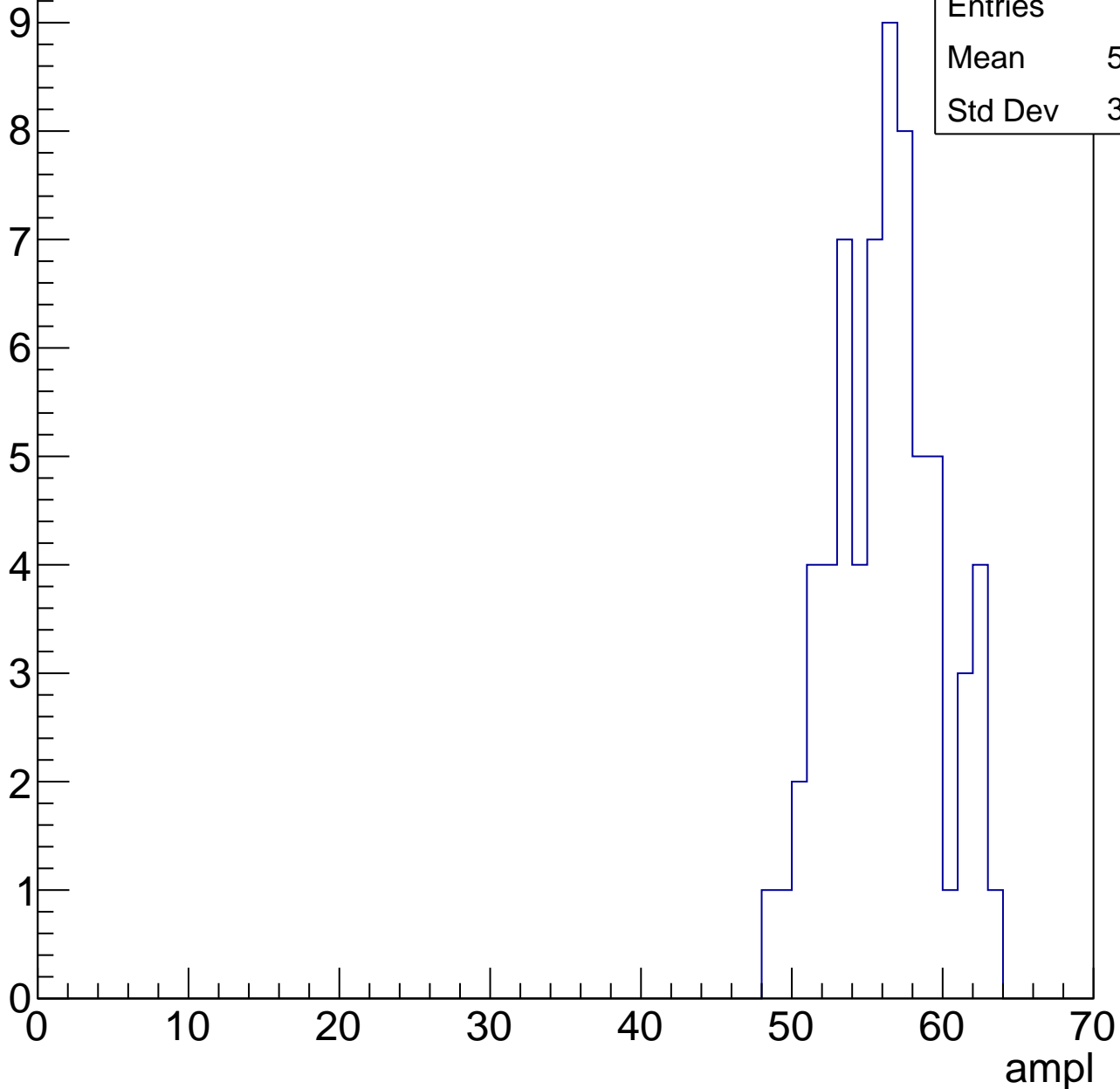


B1L103S, U17-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.76
Std Dev	3.456



B1L103S, U17-ch61, adc5

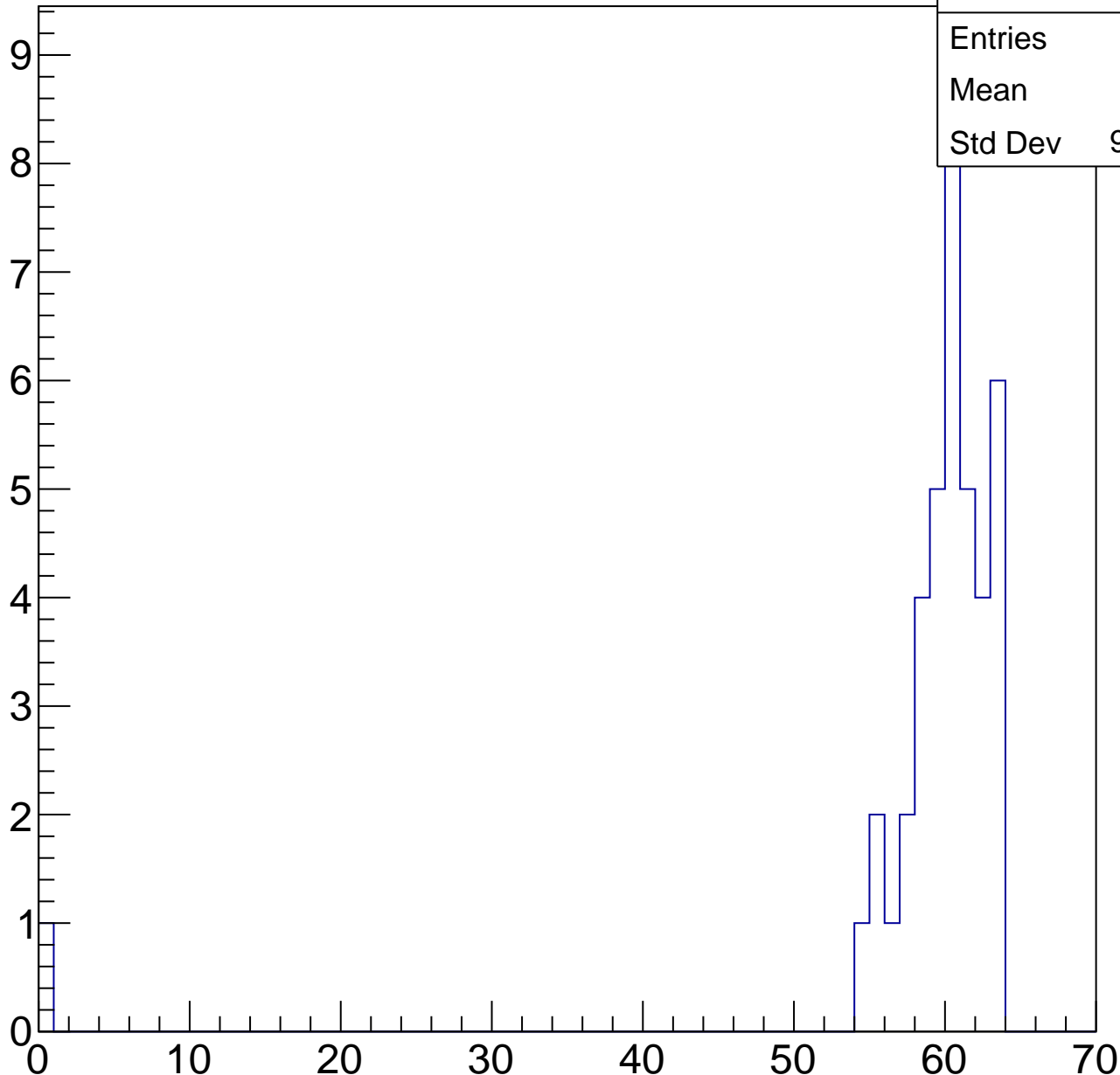
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.3
Std Dev	9.618

ampl



B1L103S, U17-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L103S, U17-ch62, adc0

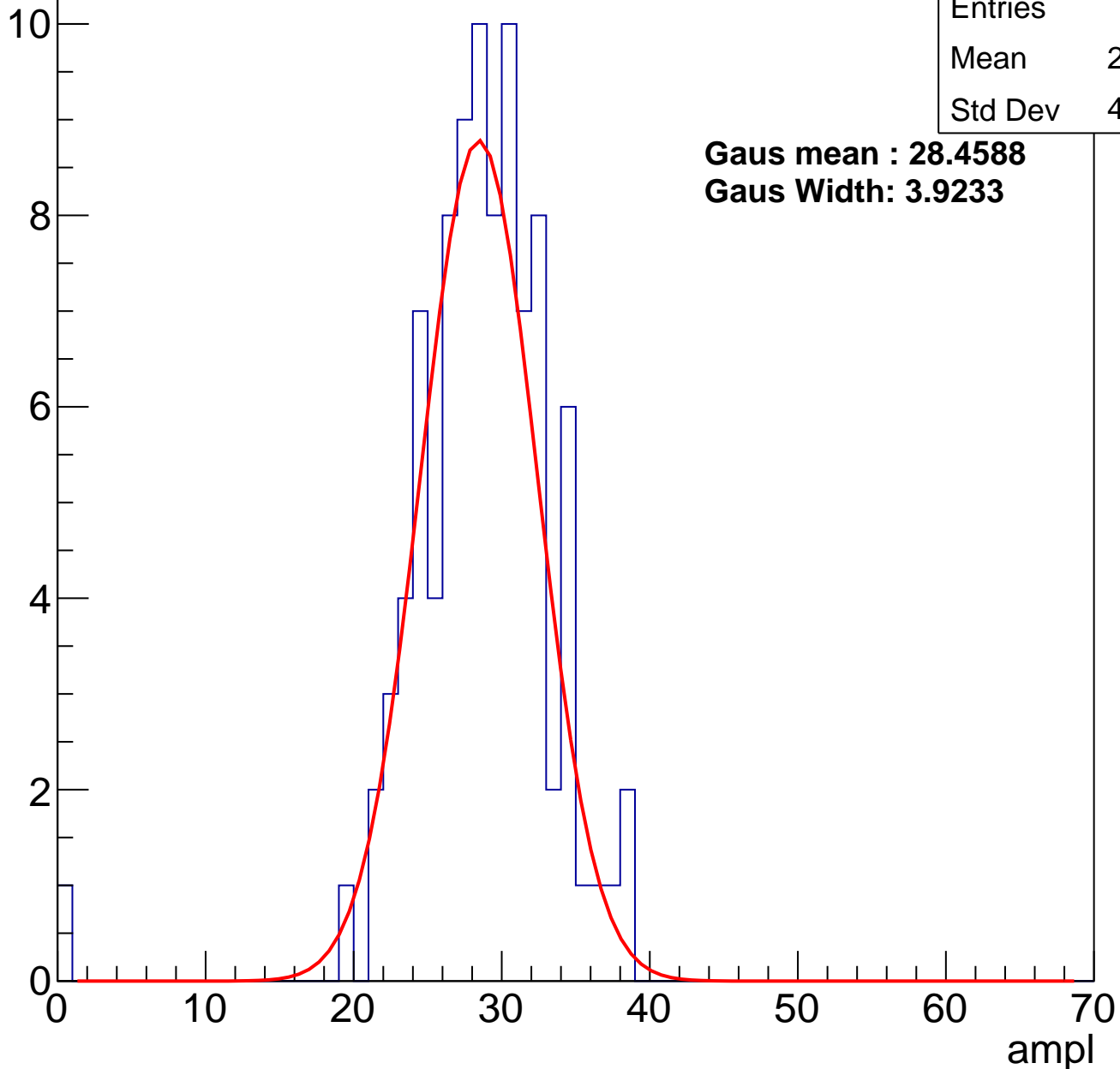
calib_packv5_042523_0143.root, FC#7, port C2

Entries	95
Mean	28.18
Std Dev	4.873

Gaus mean : 28.4588

Gaus Width: 3.9233

Entry



B1L103S, U17-ch62, adc1

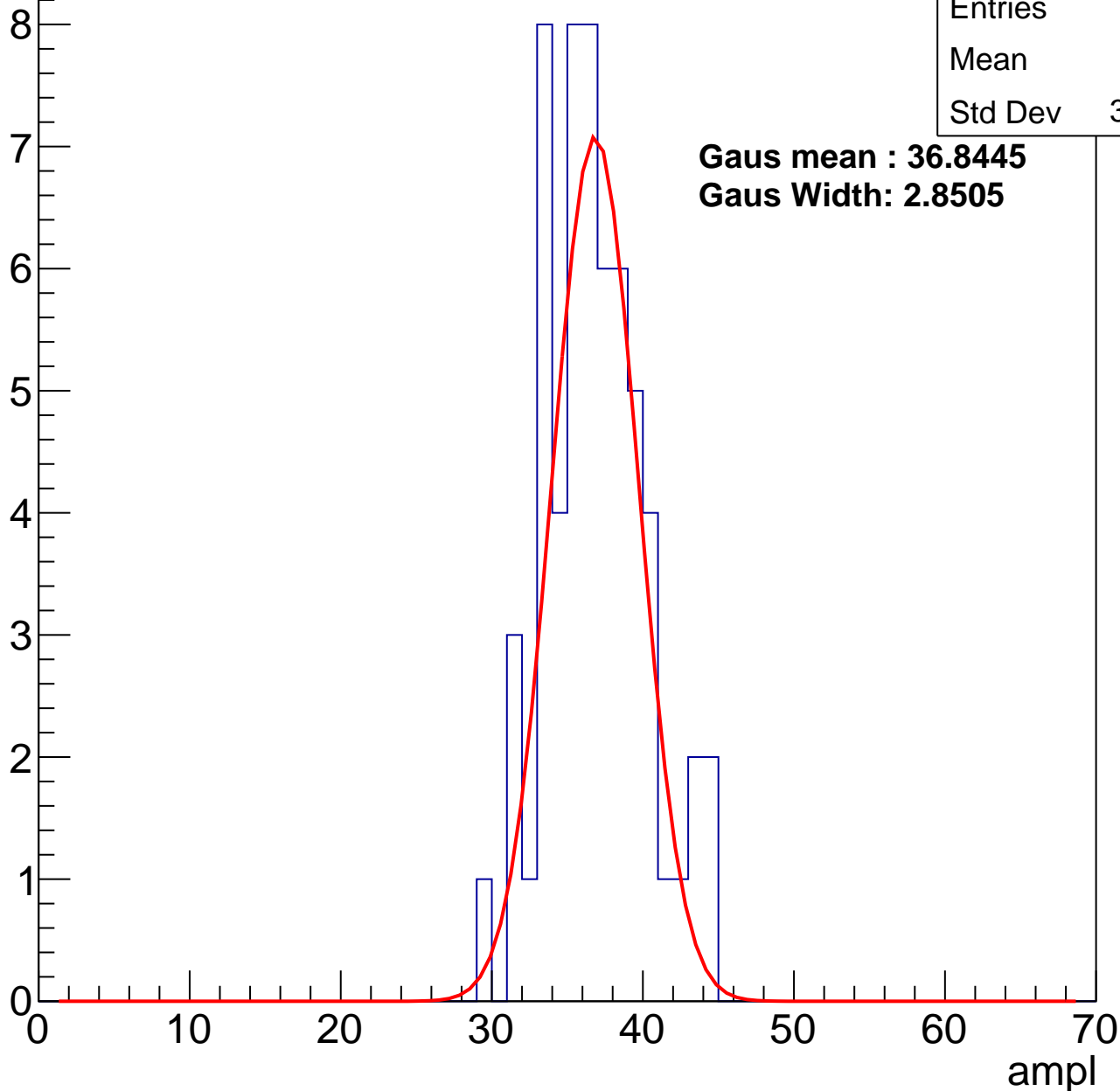
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.4
Std Dev	3.297

Gaus mean : 36.8445

Gaus Width: 2.8505



B1L103S, U17-ch62, adc2

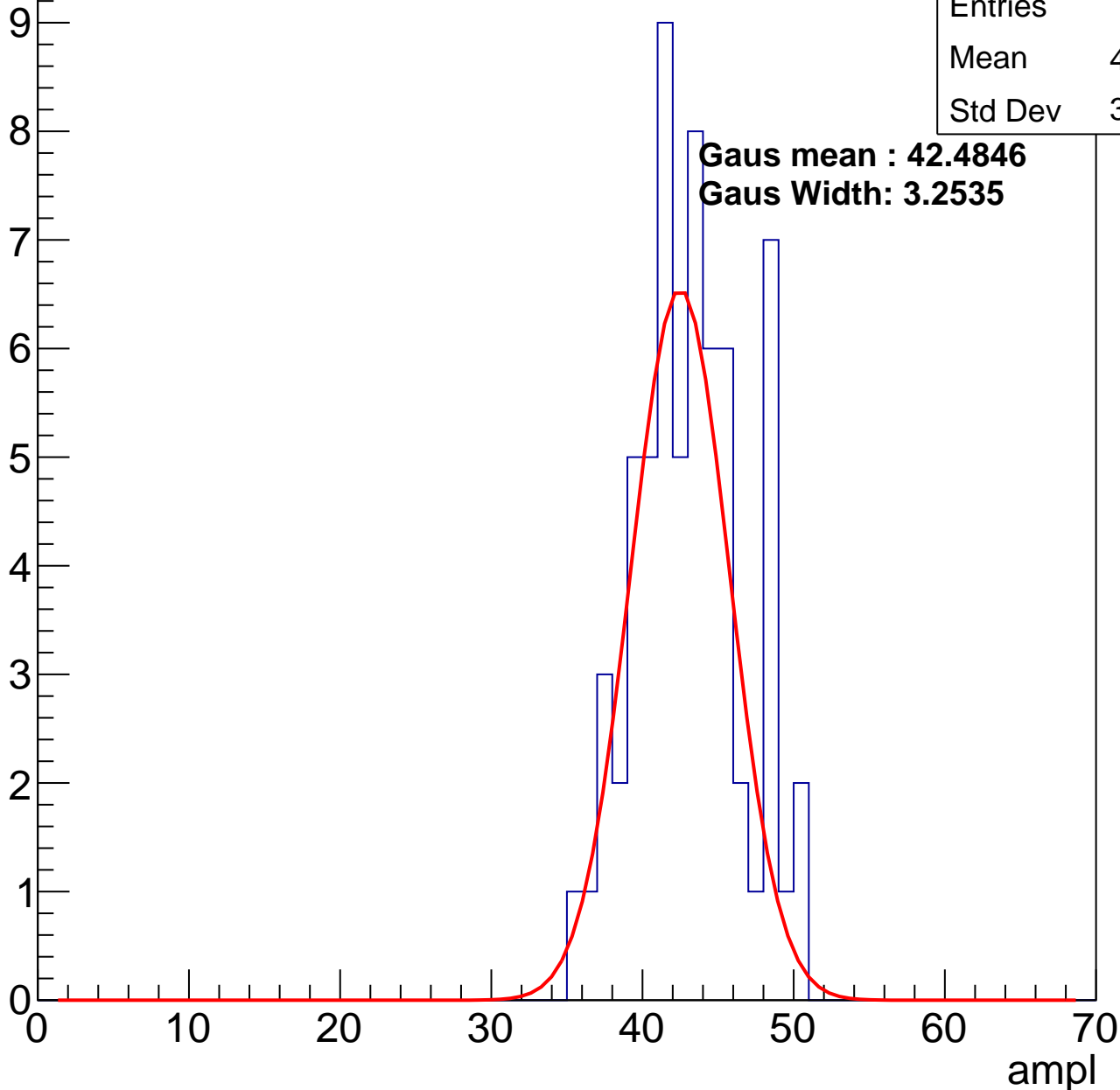
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.72
Std Dev	3.546

Gaus mean : 42.4846

Gaus Width: 3.2535

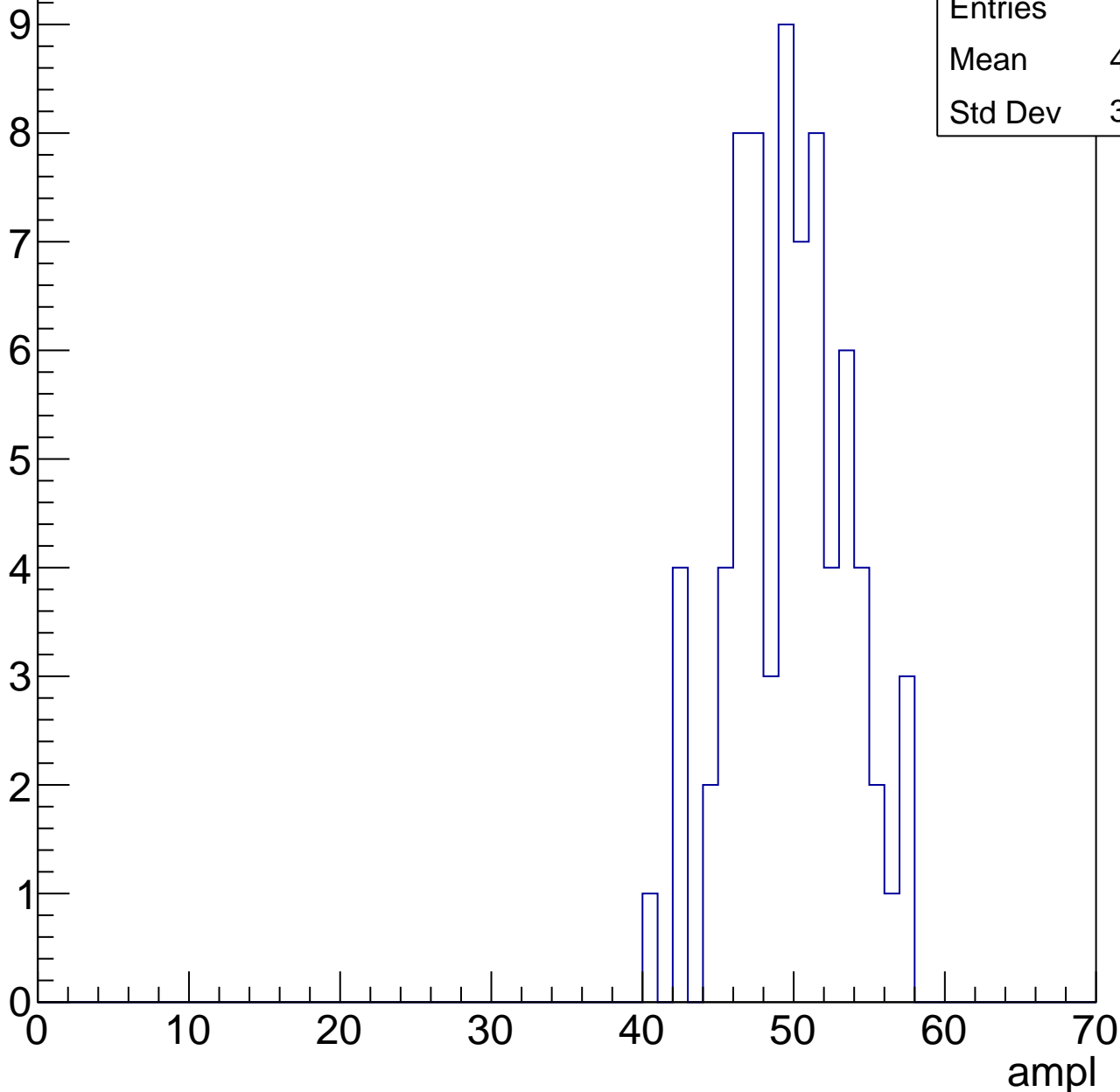


B1L103S, U17-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.22
Std Dev	3.818

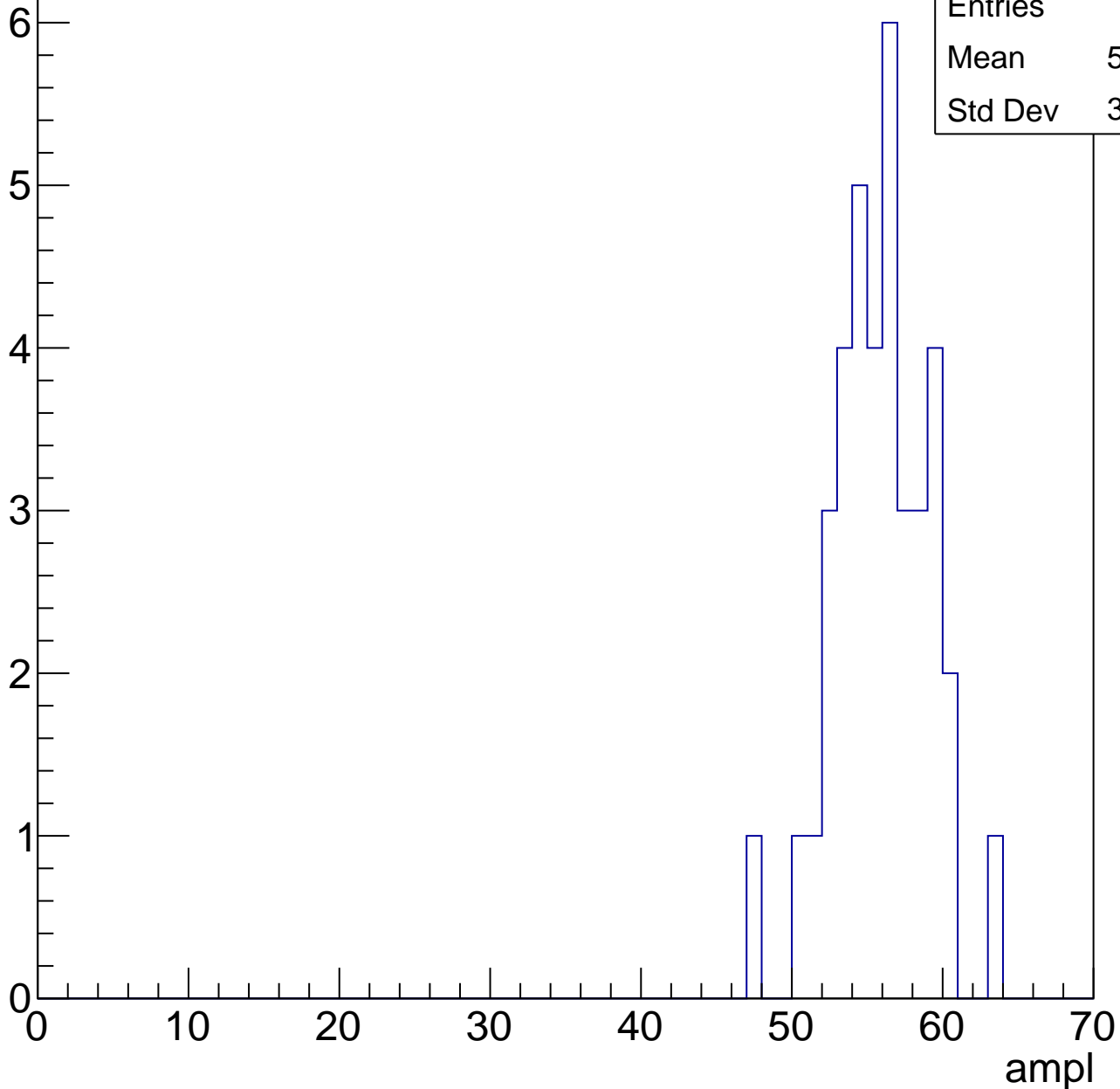


B1L103S, U17-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	55.42
Std Dev	3.117

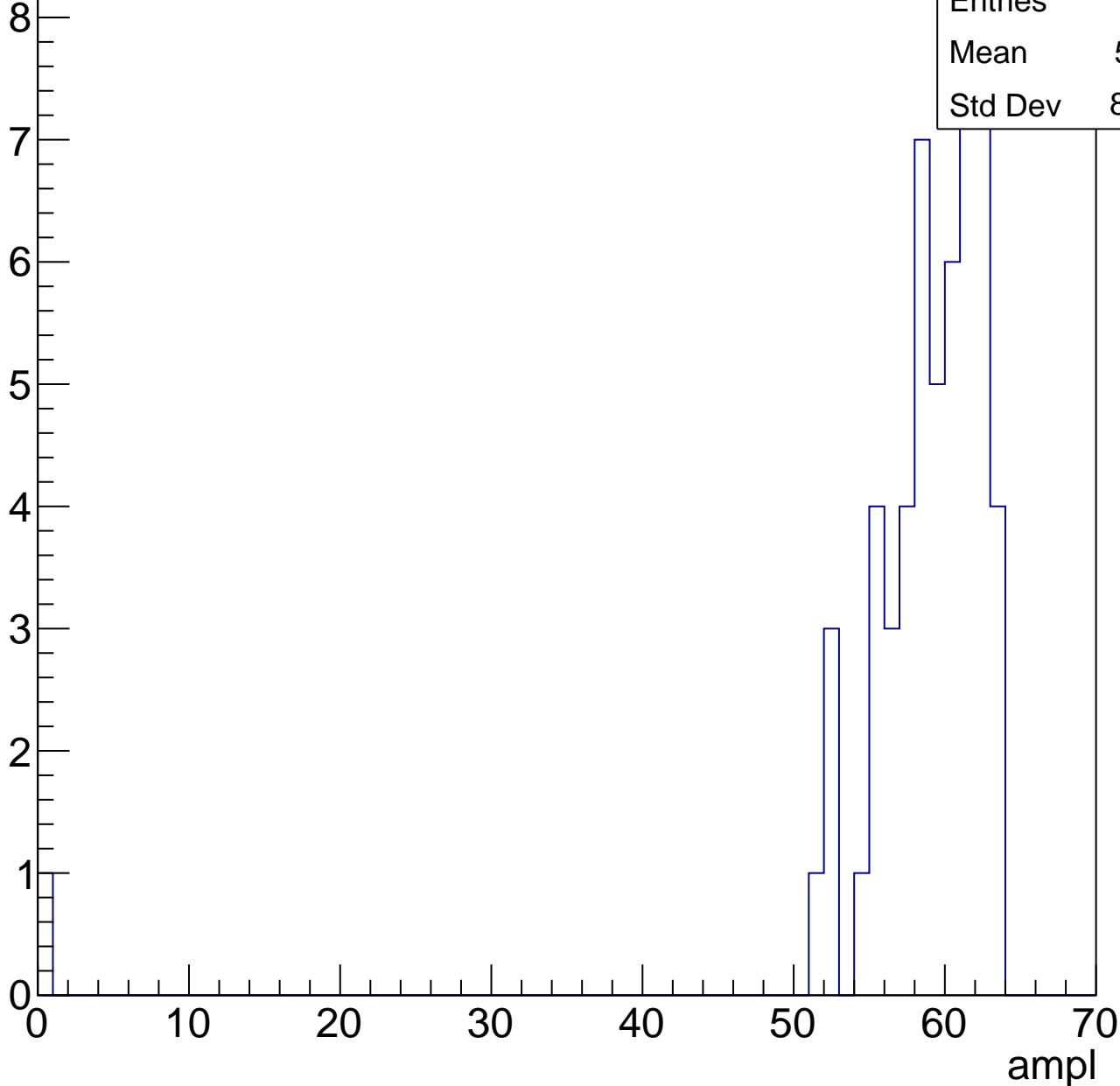


B1L103S, U17-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

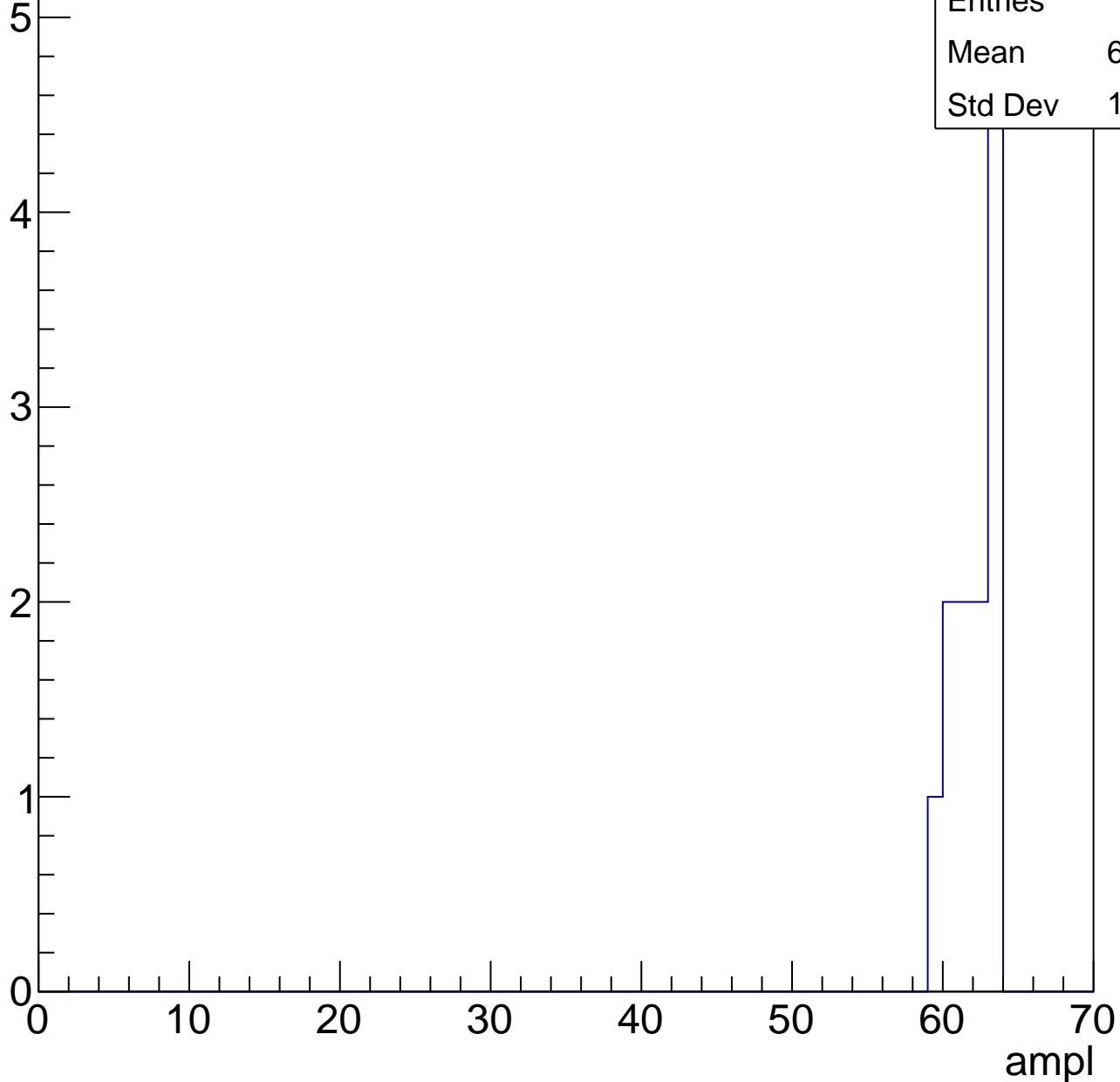
Entries	55
Mean	57.71
Std Dev	8.435



B1L103S, U17-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	61.67
Std Dev	1.374

B1L103S, U17-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch63, adc0

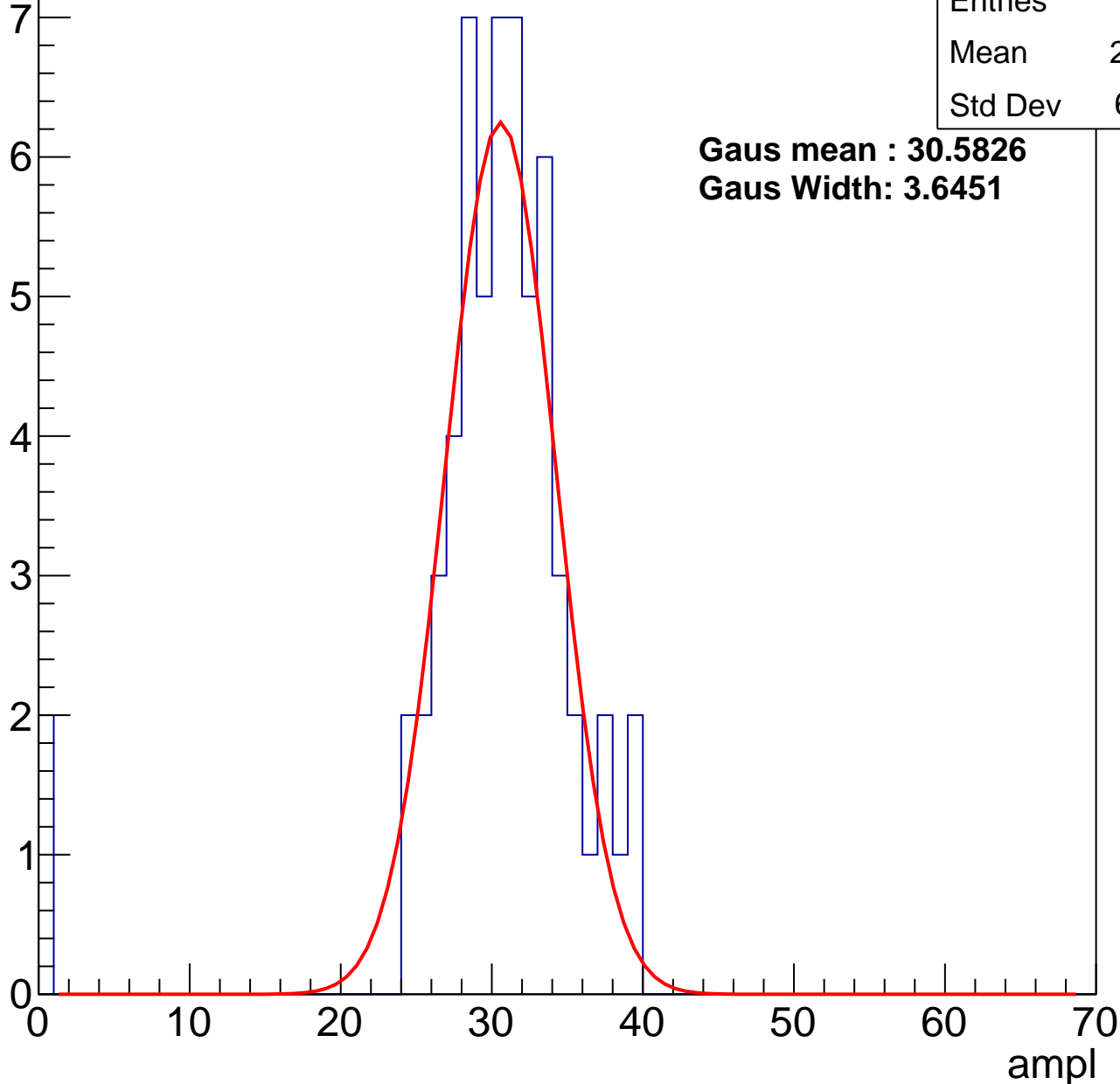
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	29.64
Std Dev	6.491

Gaus mean : 30.5826

Gaus Width: 3.6451



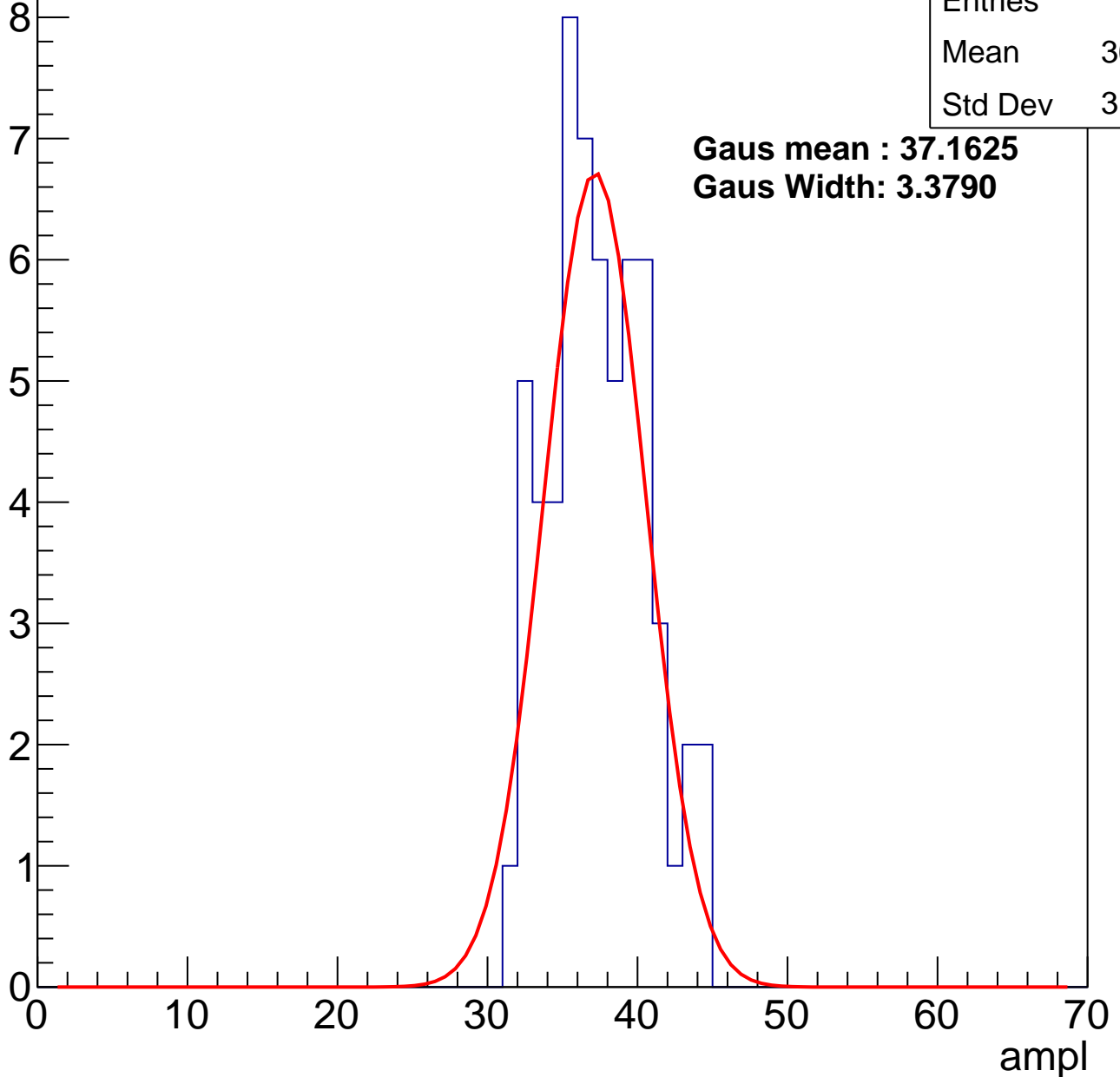
B1L103S, U17-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.93
Std Dev	3.229

Gaus mean : 37.1625
Gaus Width: 3.3790



B1L103S, U17-ch63, adc2

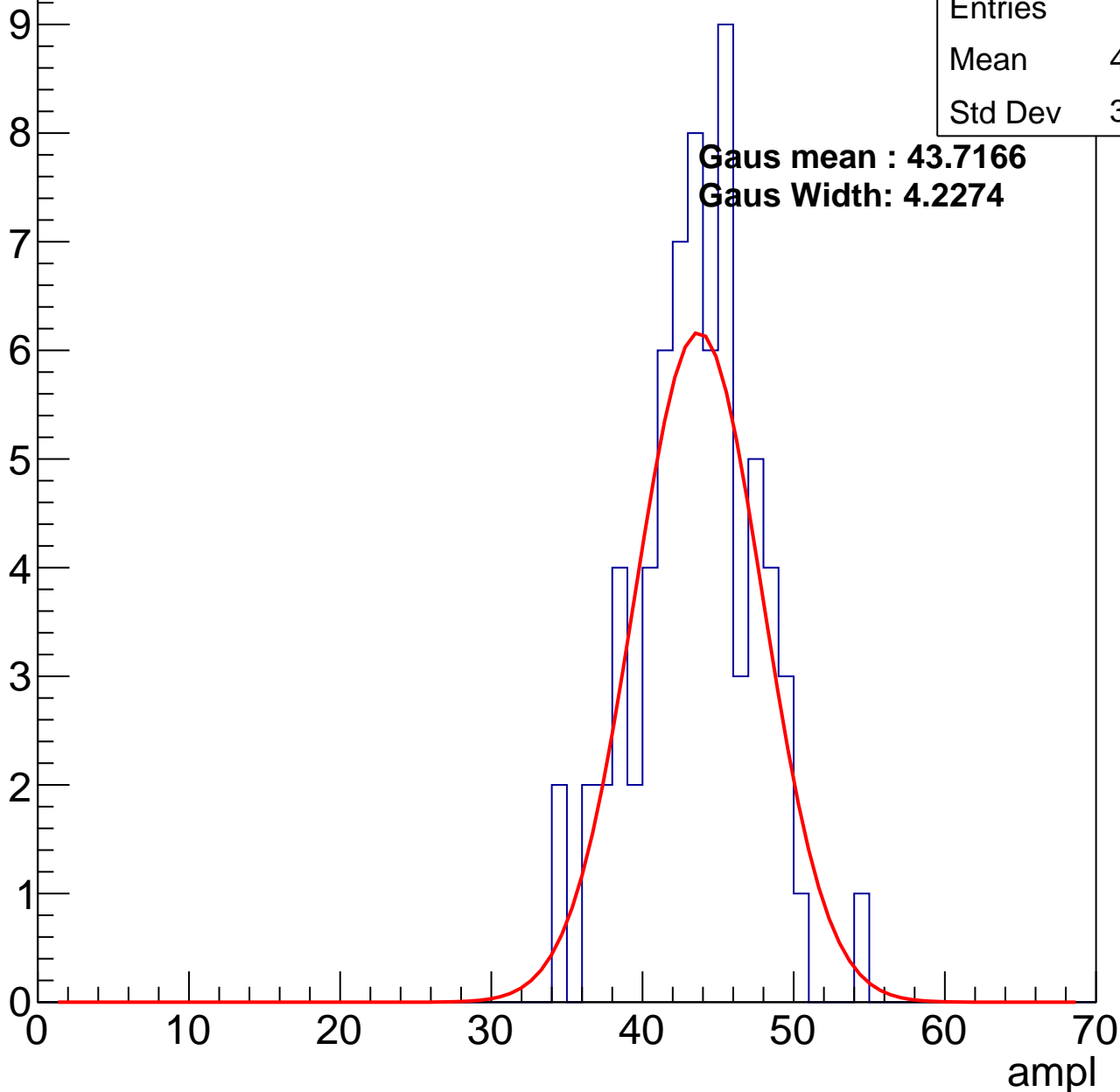
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	43.09
Std Dev	3.915

Gaus mean : 43.7166

Gaus Width: 4.2274

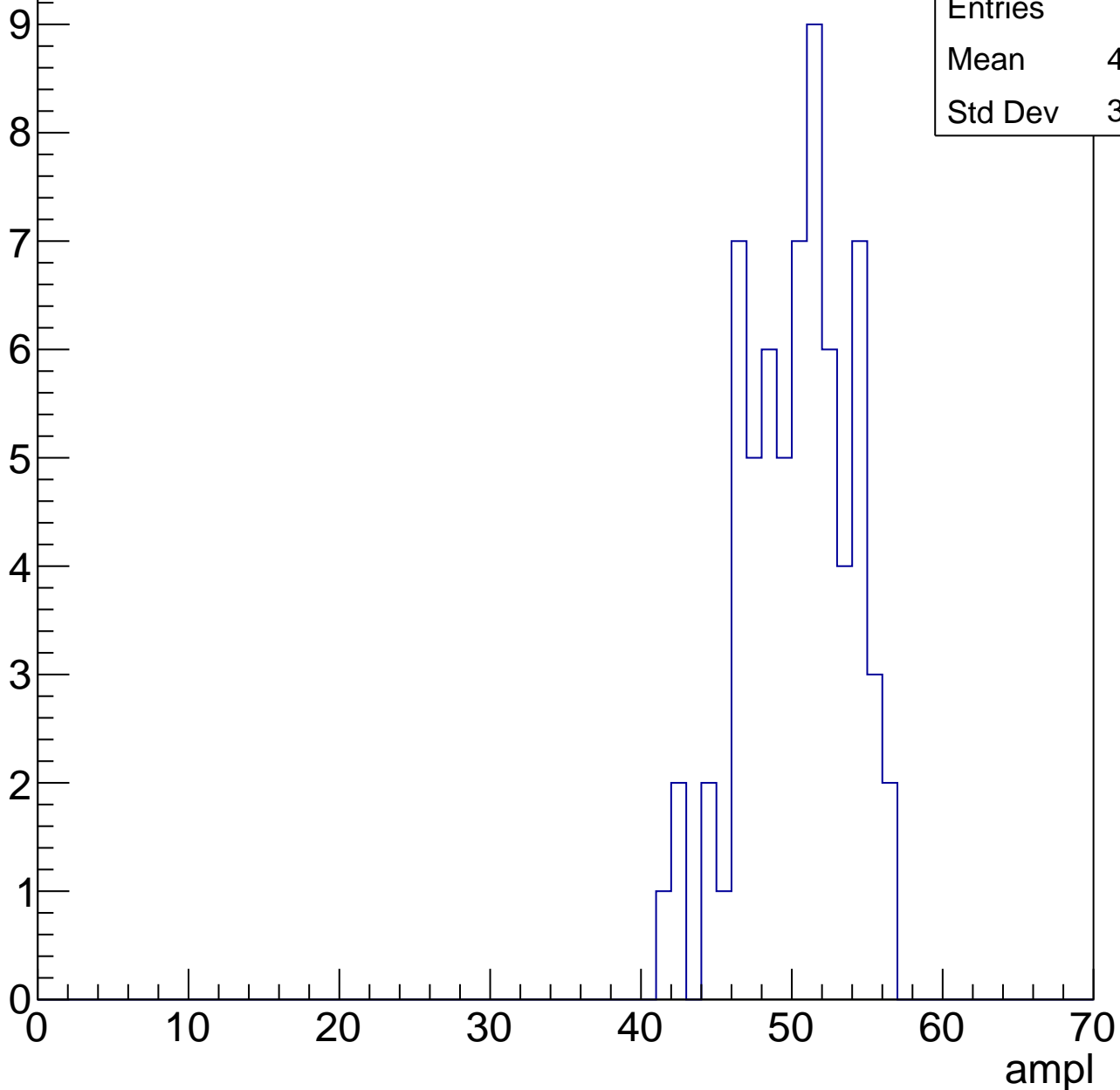


B1L103S, U17-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.79
Std Dev	3.492

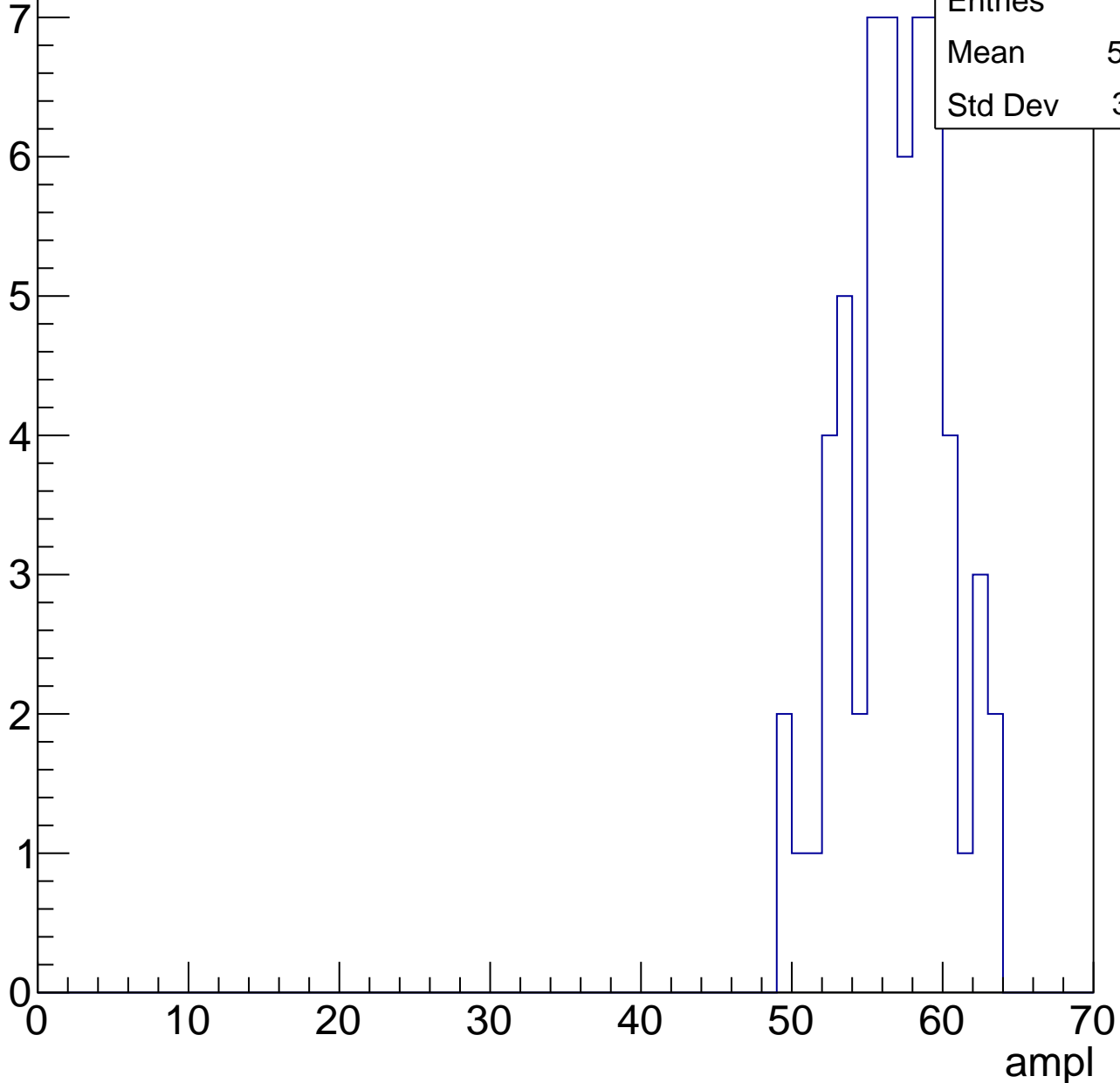


B1L103S, U17-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

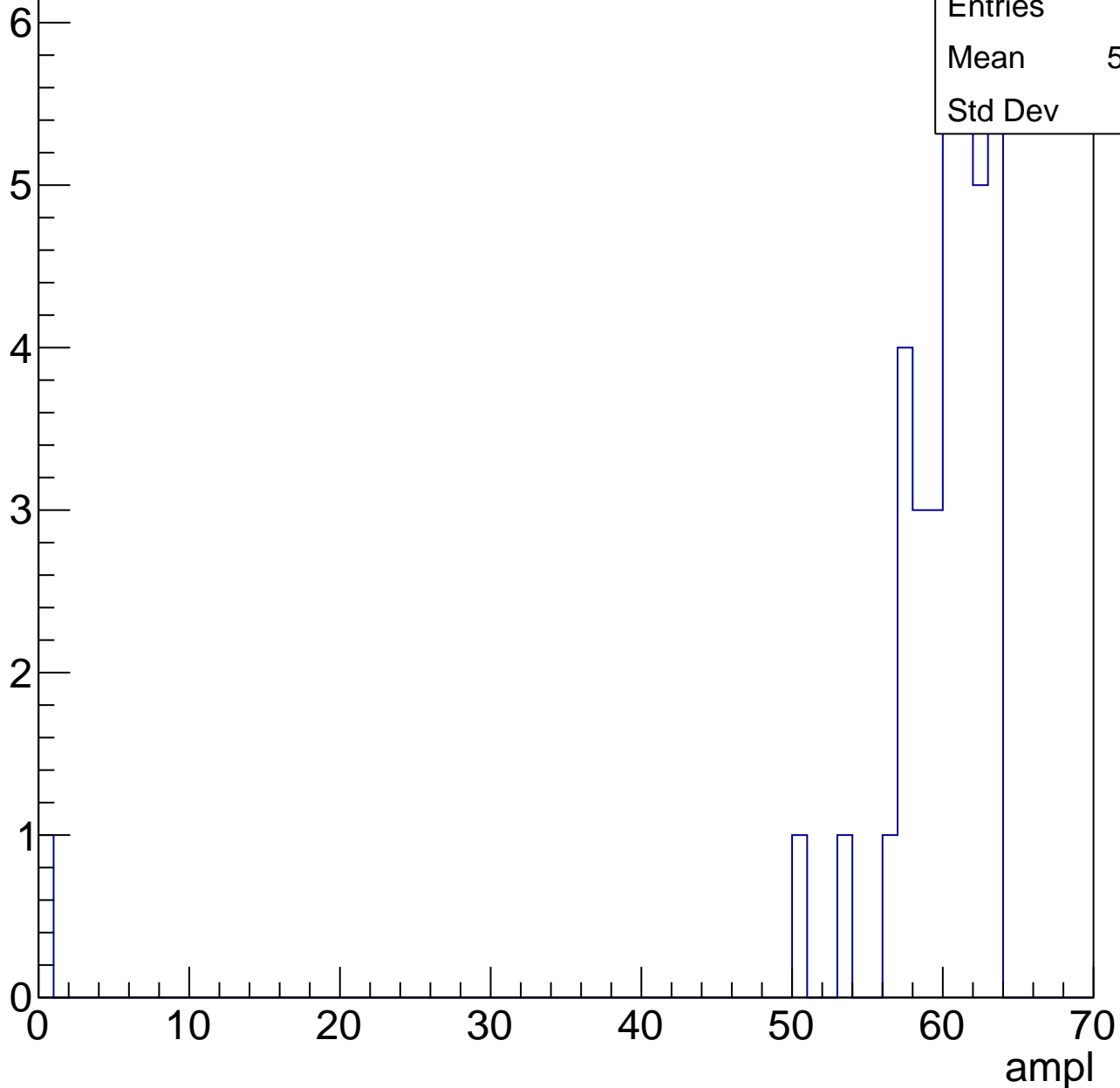
Entries	59
Mean	56.46
Std Dev	3.351



B1L103S, U17-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch64, adc0

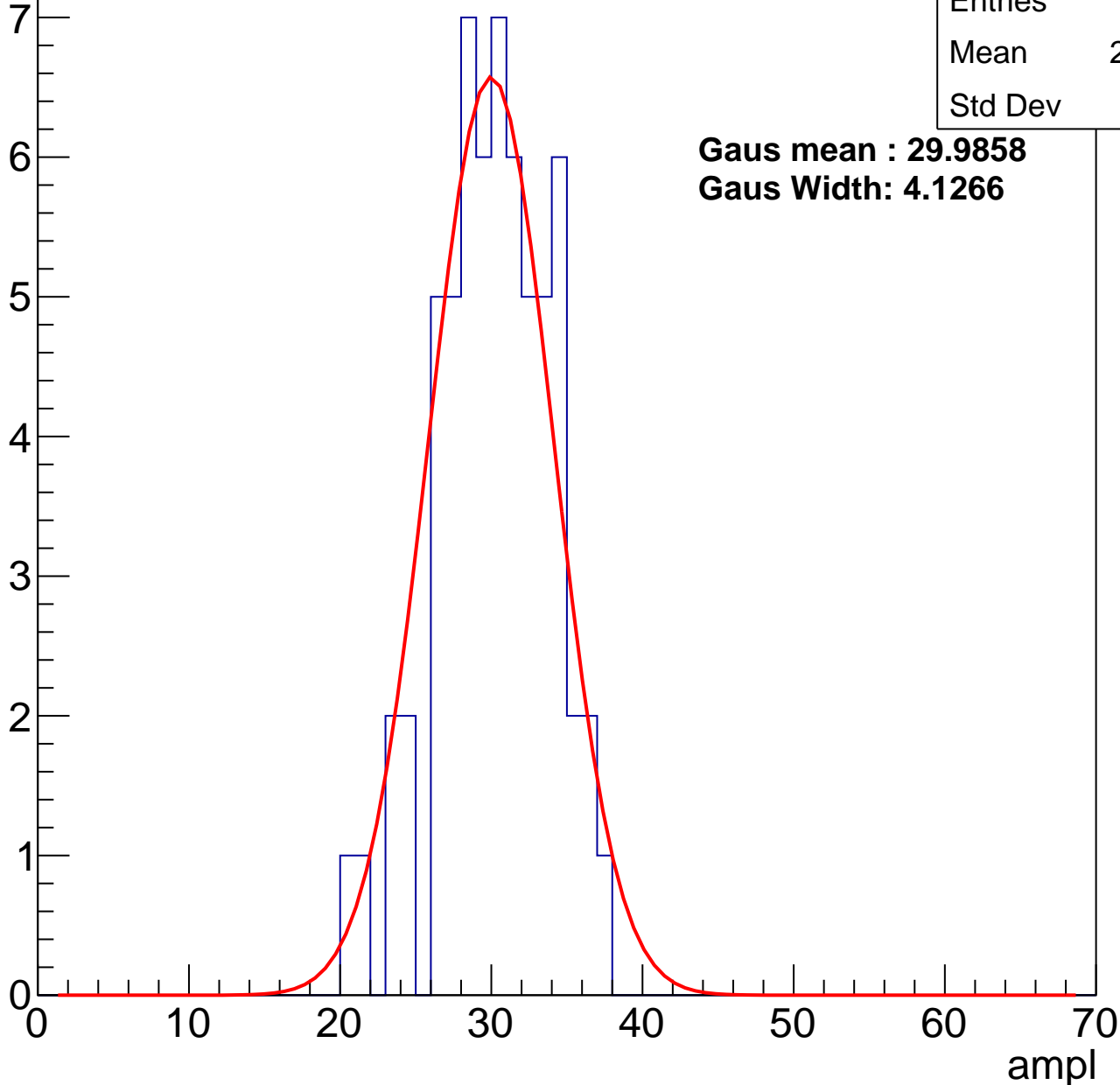
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.75
Std Dev	3.66

Gaus mean : 29.9858

Gaus Width: 4.1266



B1L103S, U17-ch64, adc1

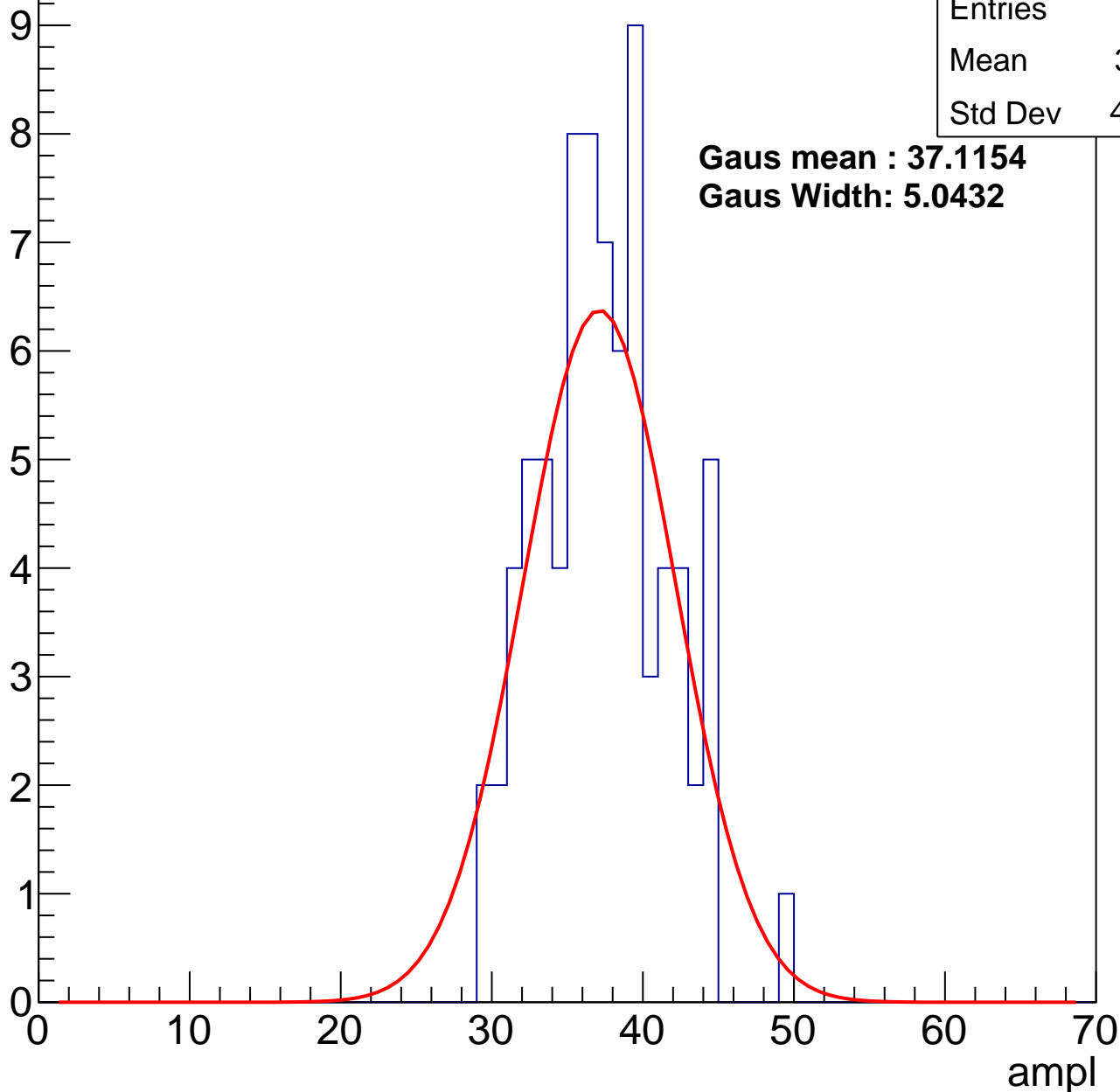
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.91
Std Dev	4.125

Gaus mean : 37.1154

Gaus Width: 5.0432



B1L103S, U17-ch64, adc2

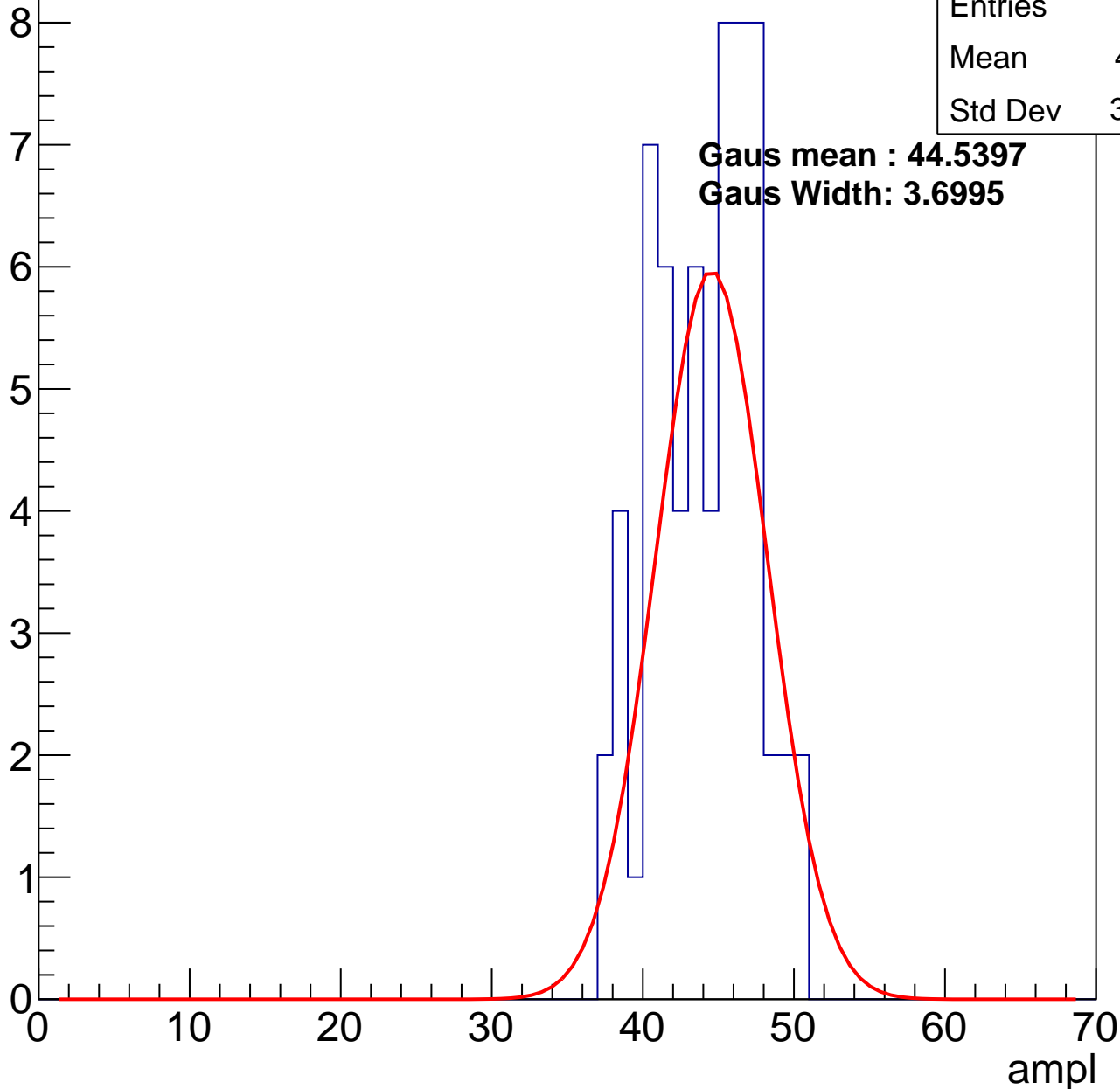
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.61
Std Dev	3.334

Gaus mean : 44.5397

Gaus Width: 3.6995

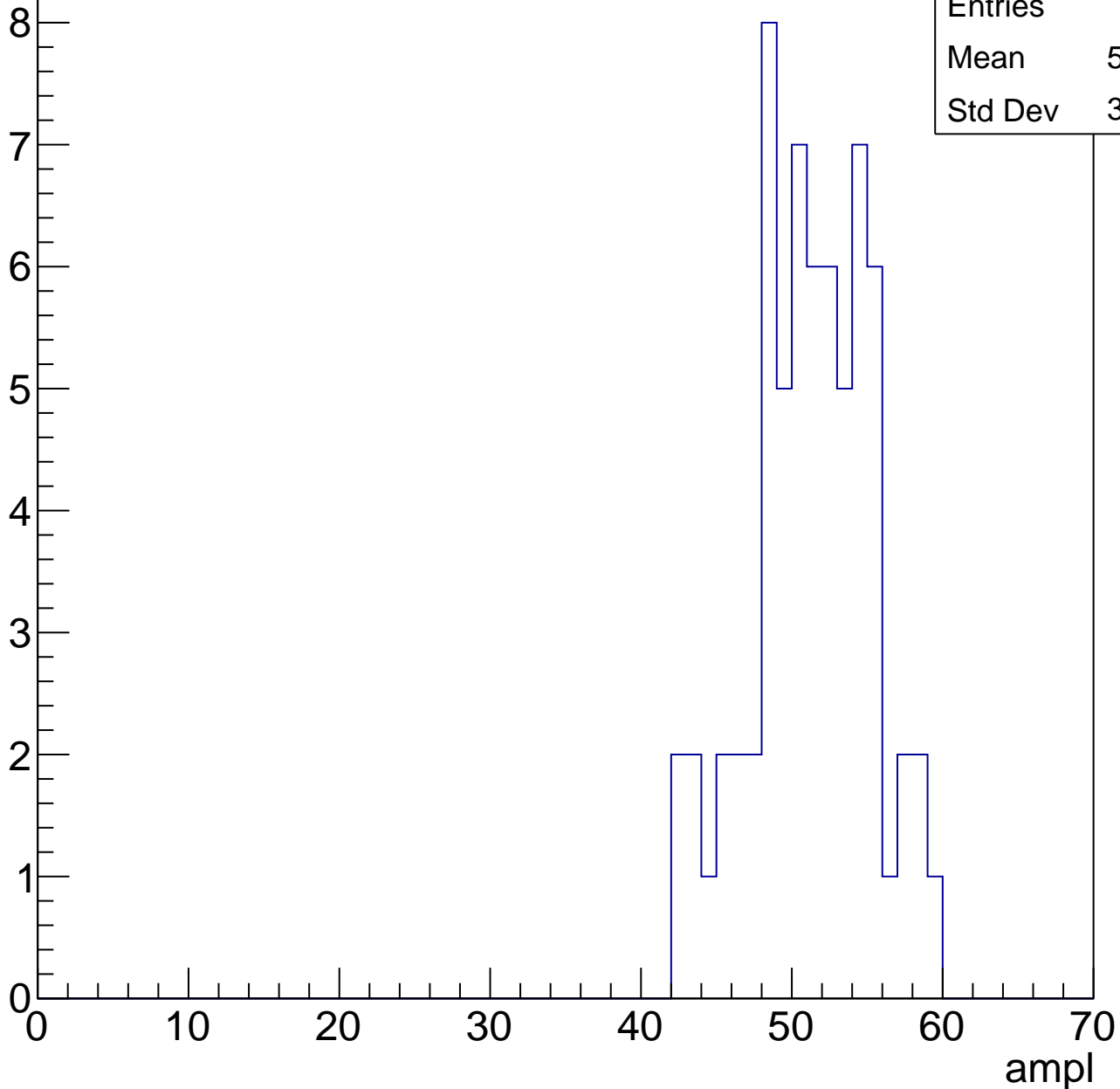


B1L103S, U17-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

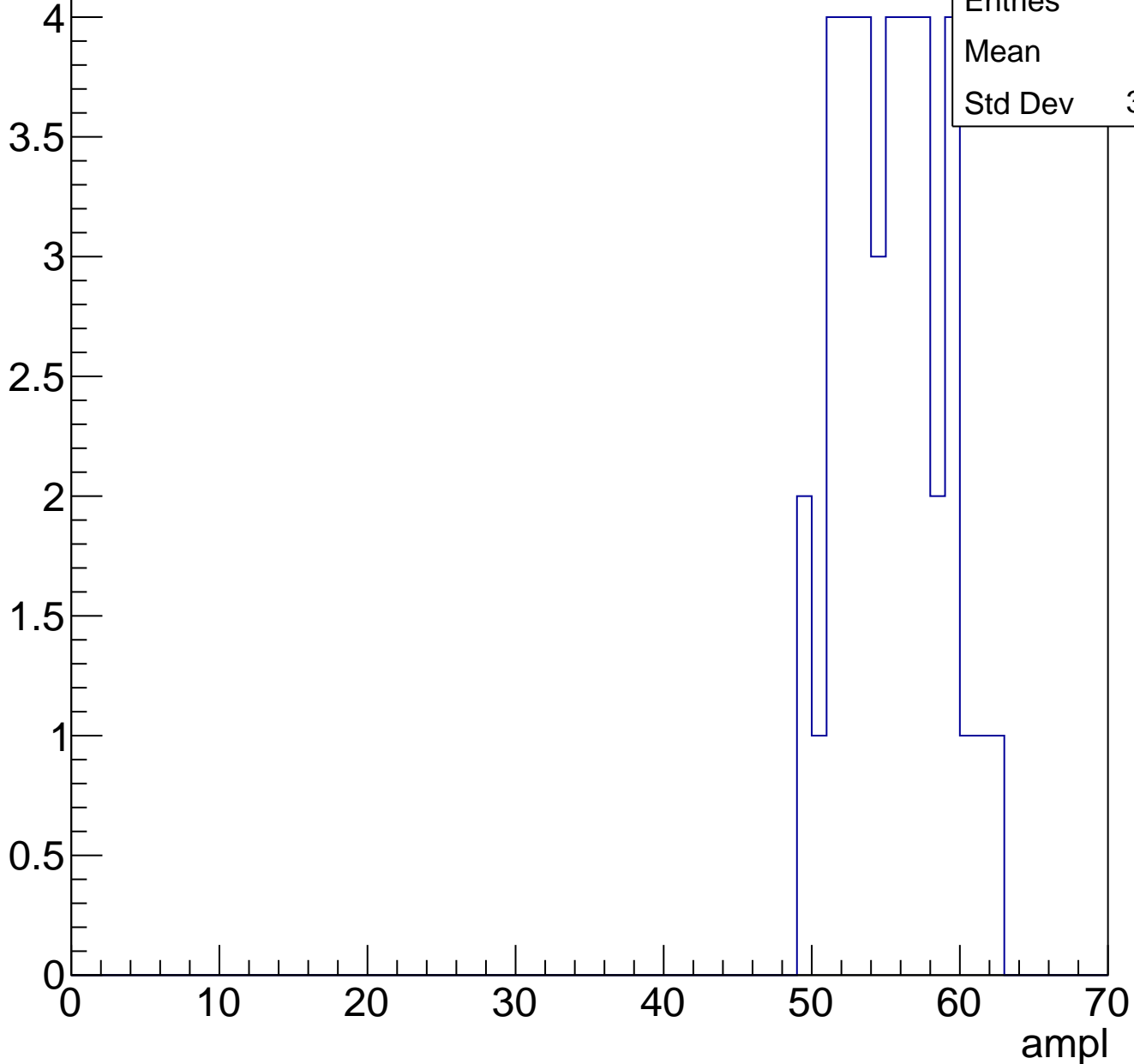
Entries	67
Mean	50.82
Std Dev	3.928



B1L103S, U17-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 54

Mean 58.81

Std Dev 8.433

8

6

4

2

0

0

10

20

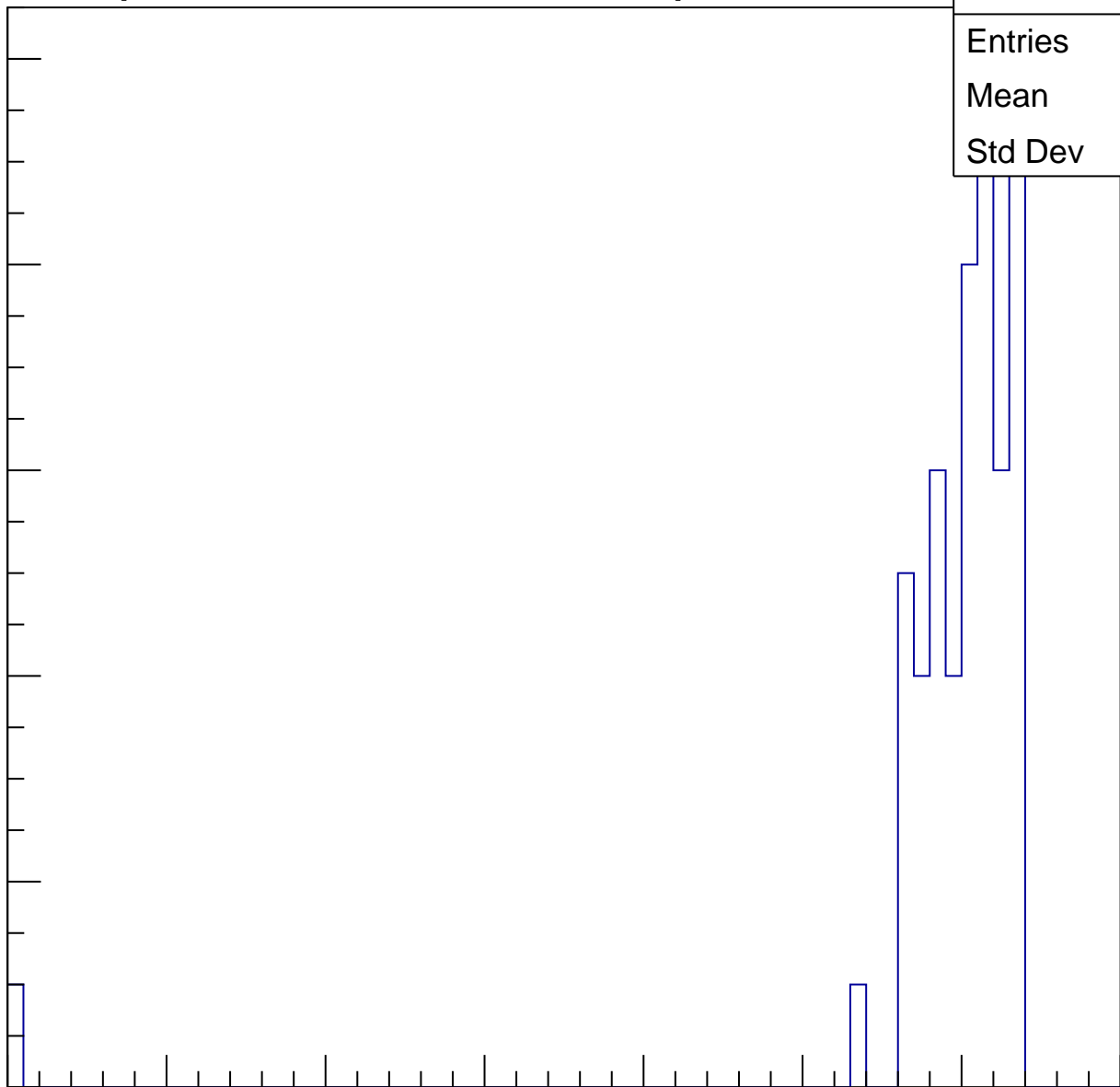
30

40

50

60

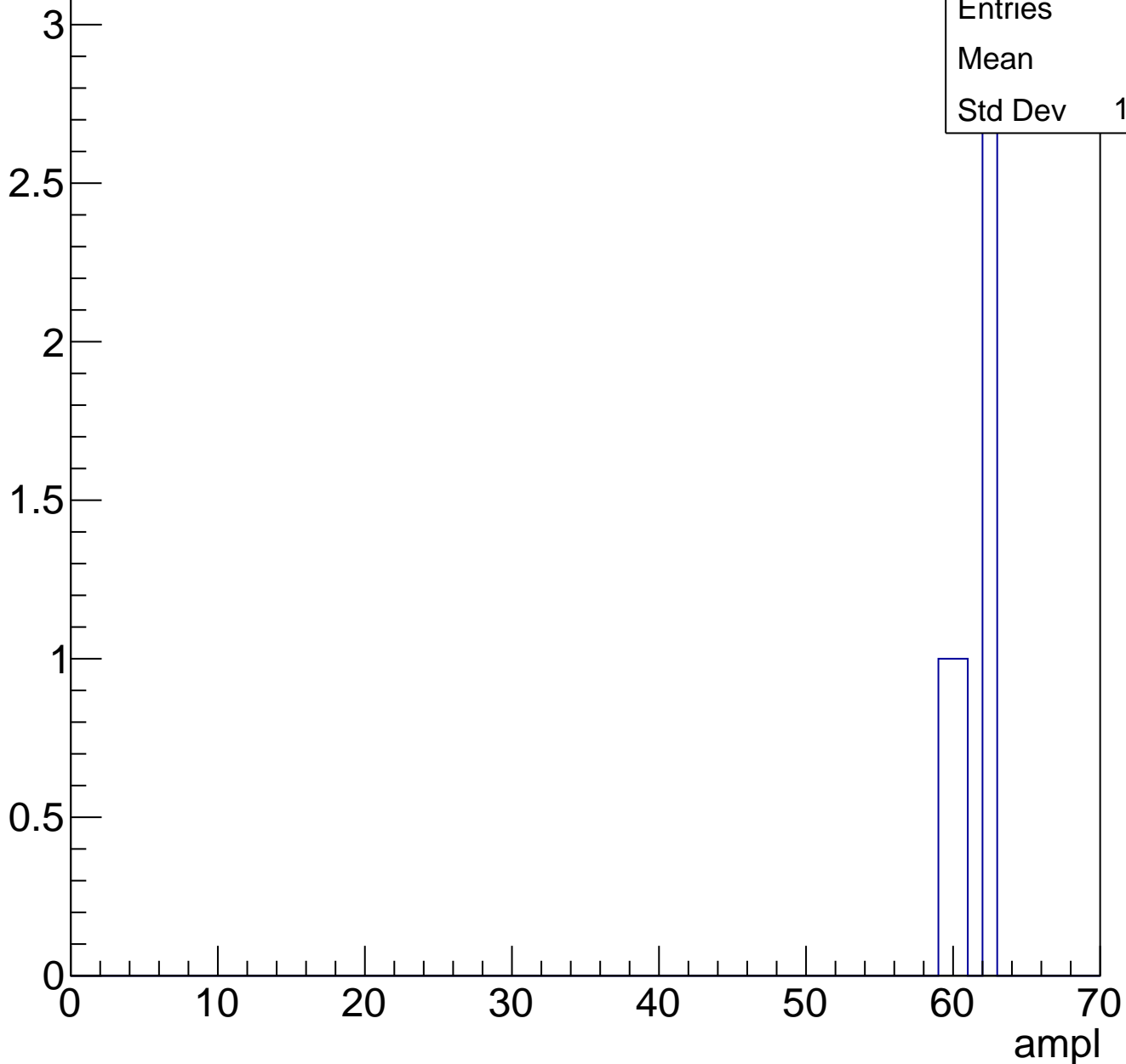
ampl



B1L103S, U17-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch65, adc0

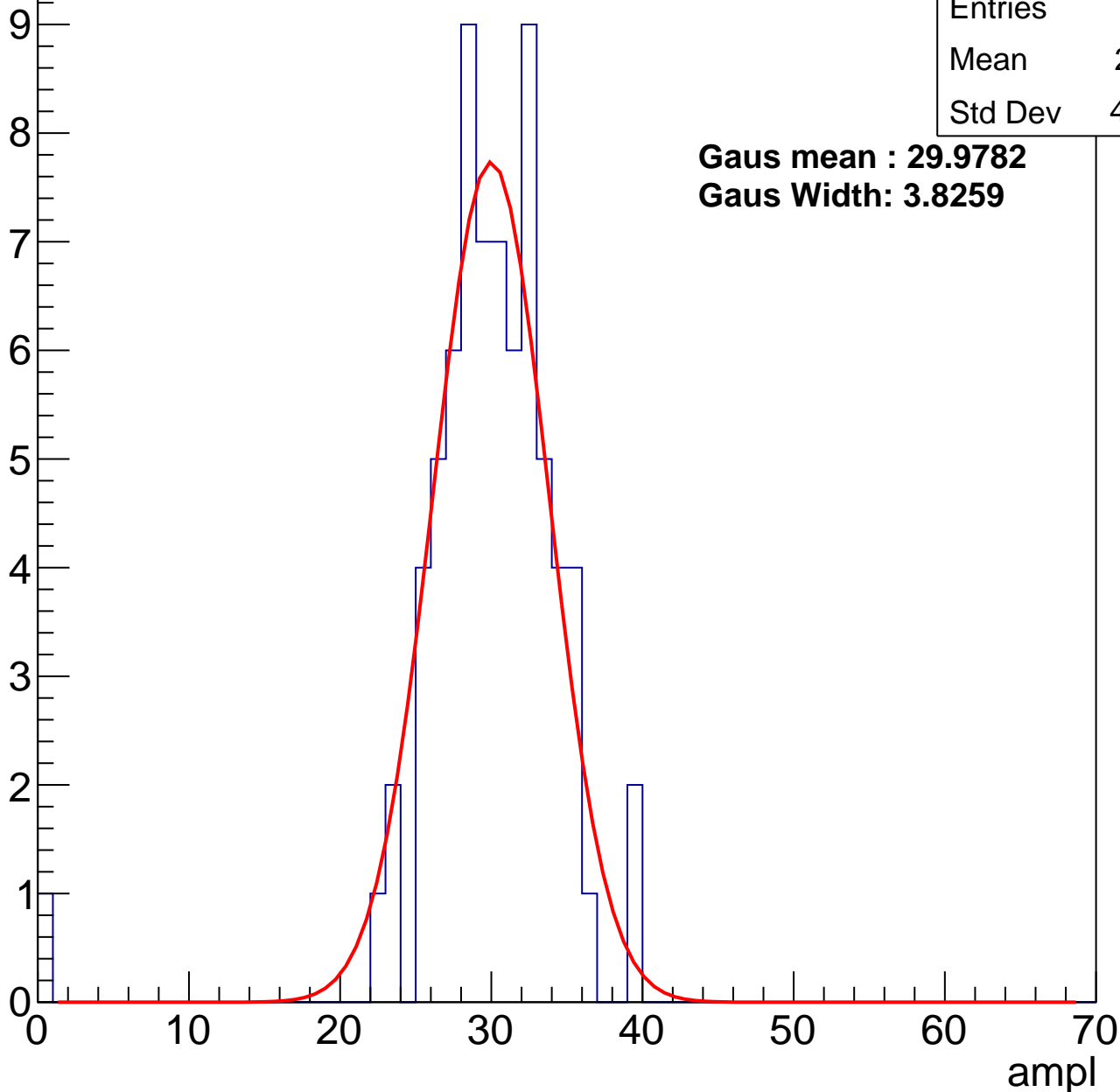
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.51
Std Dev	4.924

Gaus mean : 29.9782

Gaus Width: 3.8259



B1L103S, U17-ch65, adc1

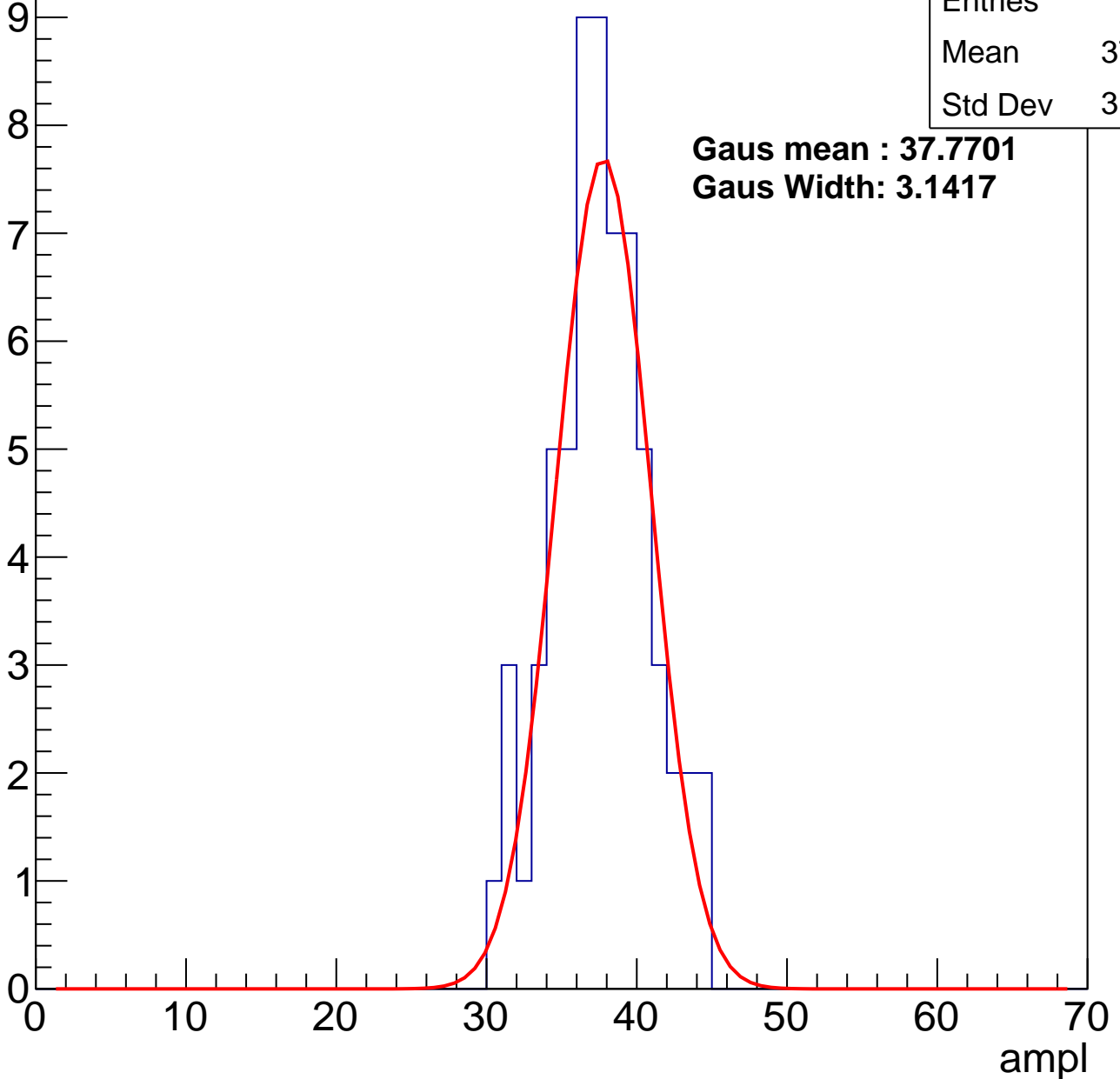
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.12
Std Dev	3.189

Gaus mean : 37.7701

Gaus Width: 3.1417



B1L103S, U17-ch65, adc2

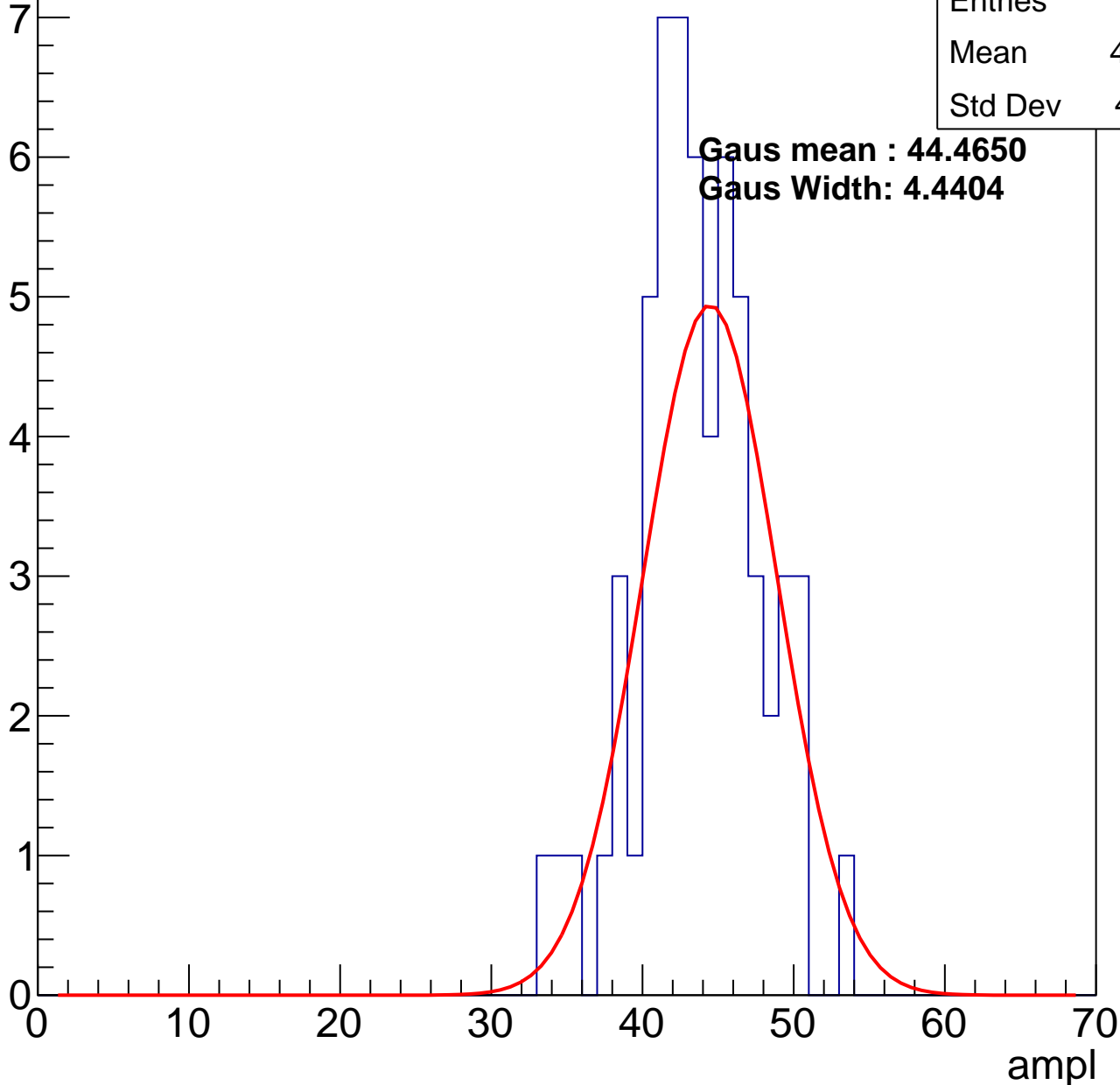
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.23
Std Dev	4.031

Gaus mean : 44.4650

Gaus Width: 4.4404

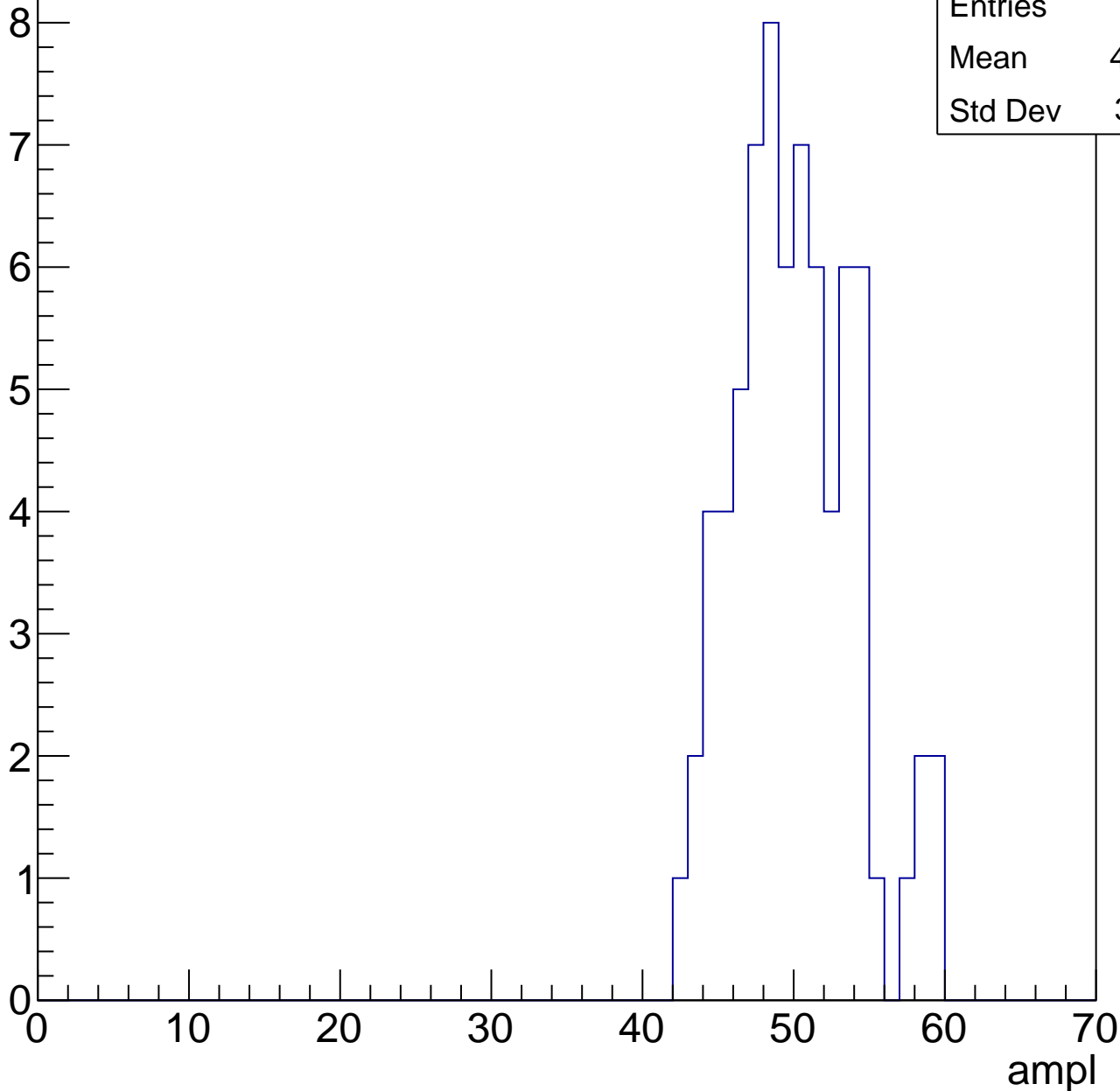


B1L103S, U17-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.62
Std Dev	3.931

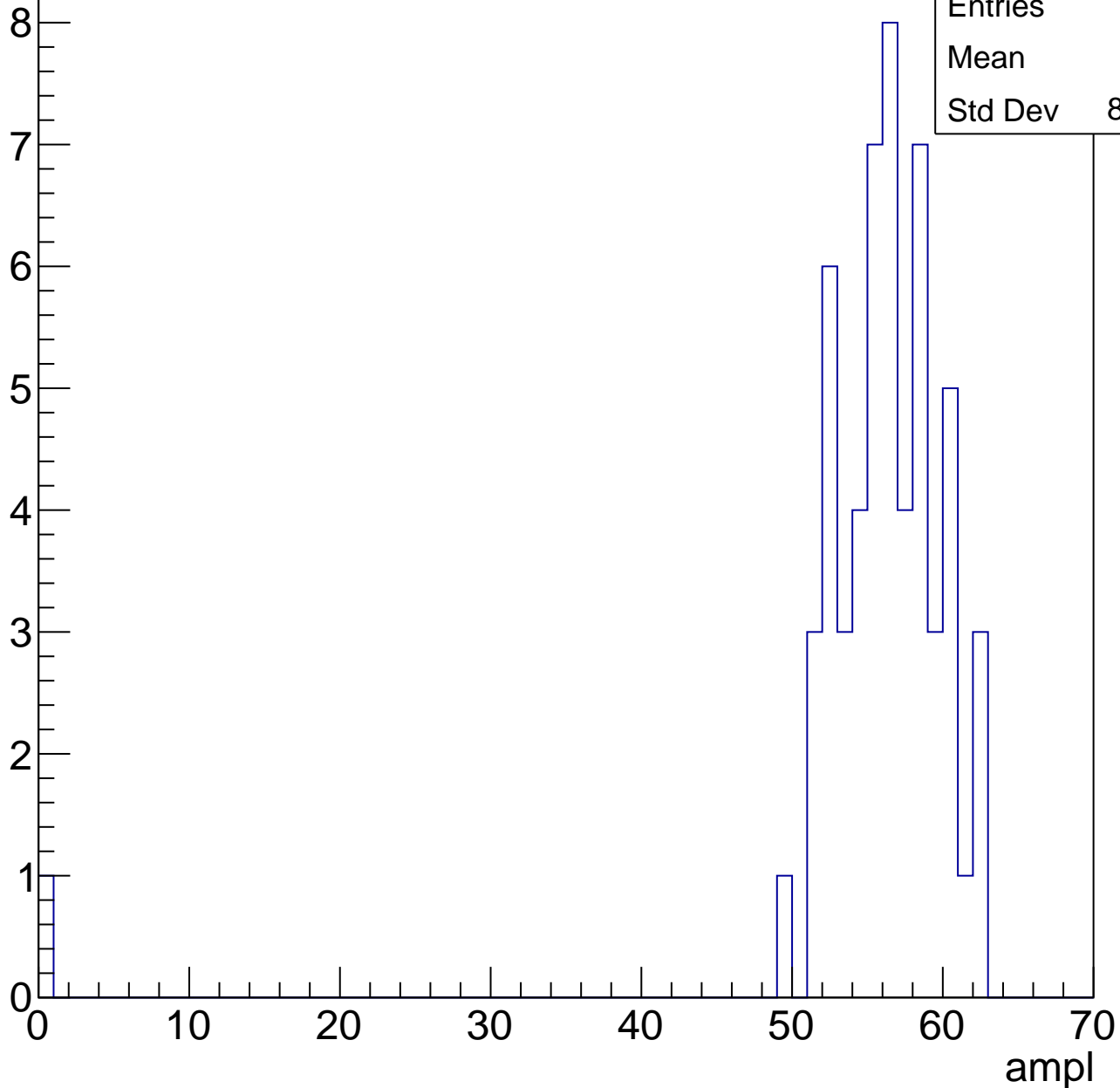


B1L103S, U17-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55
Std Dev	8.042

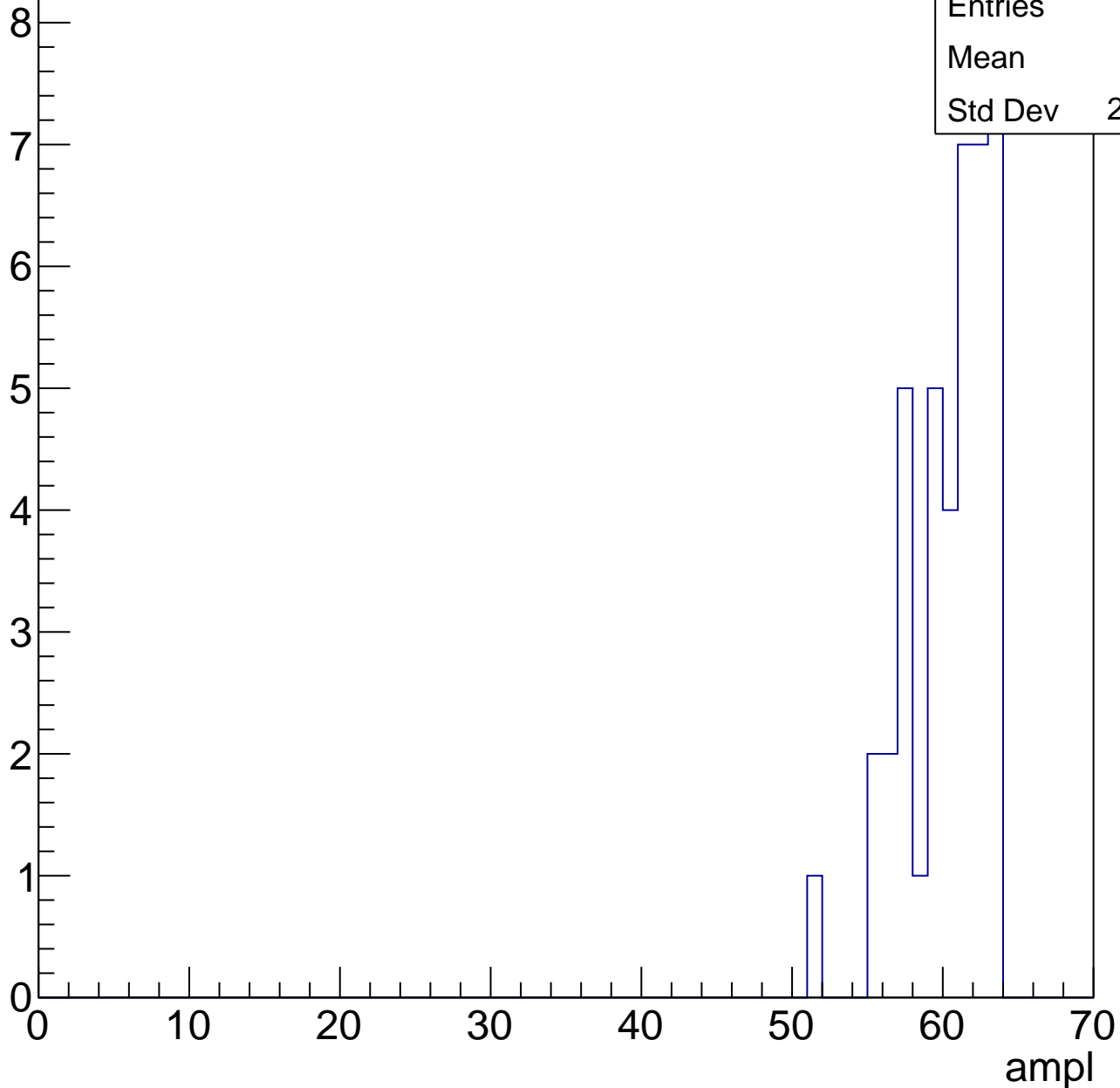


B1L103S, U17-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

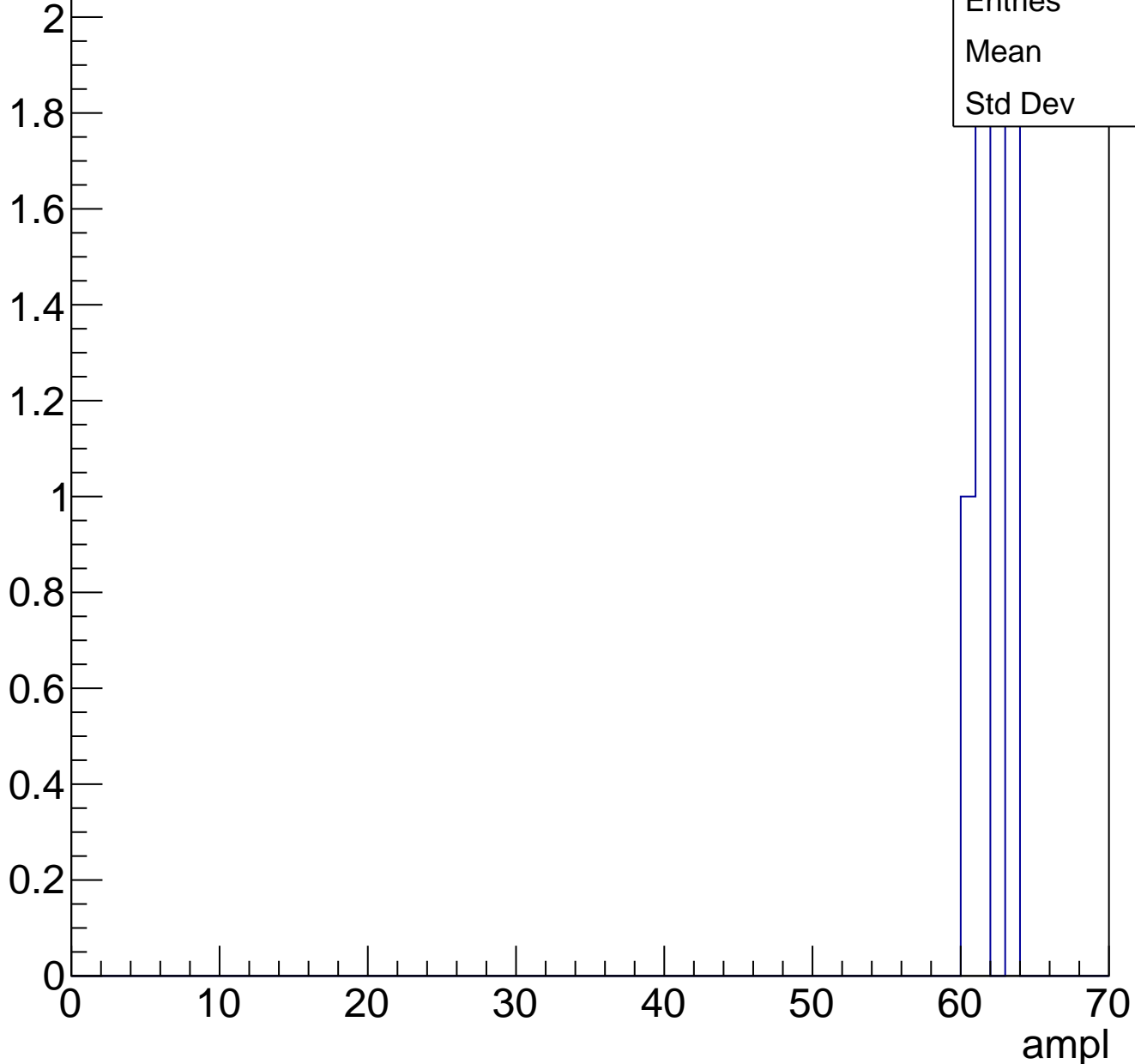
Entries	42
Mean	59.9
Std Dev	2.776



B1L103S, U17-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch66, adc0

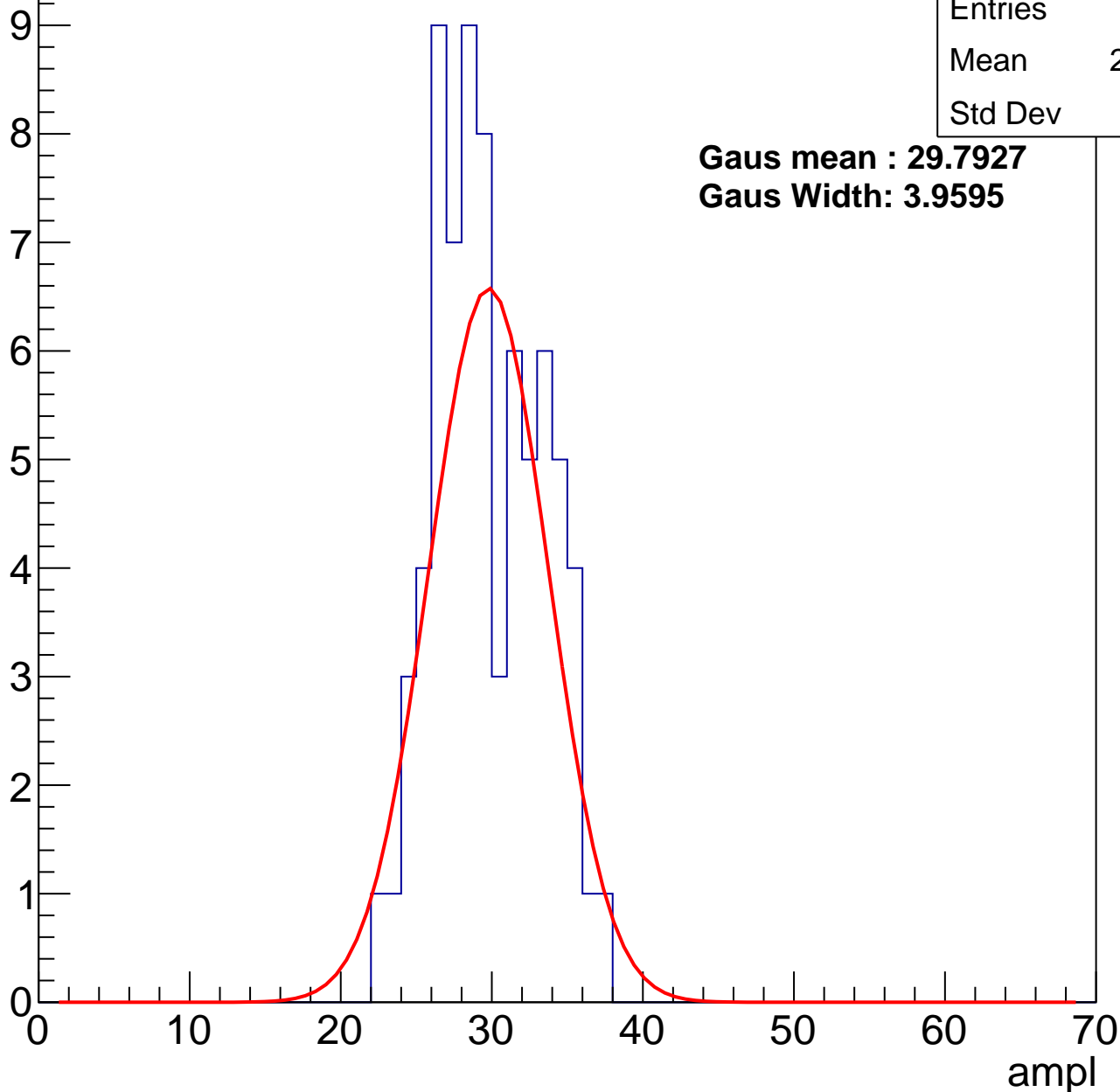
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.33
Std Dev	3.48

Gaus mean : 29.7927

Gaus Width: 3.9595



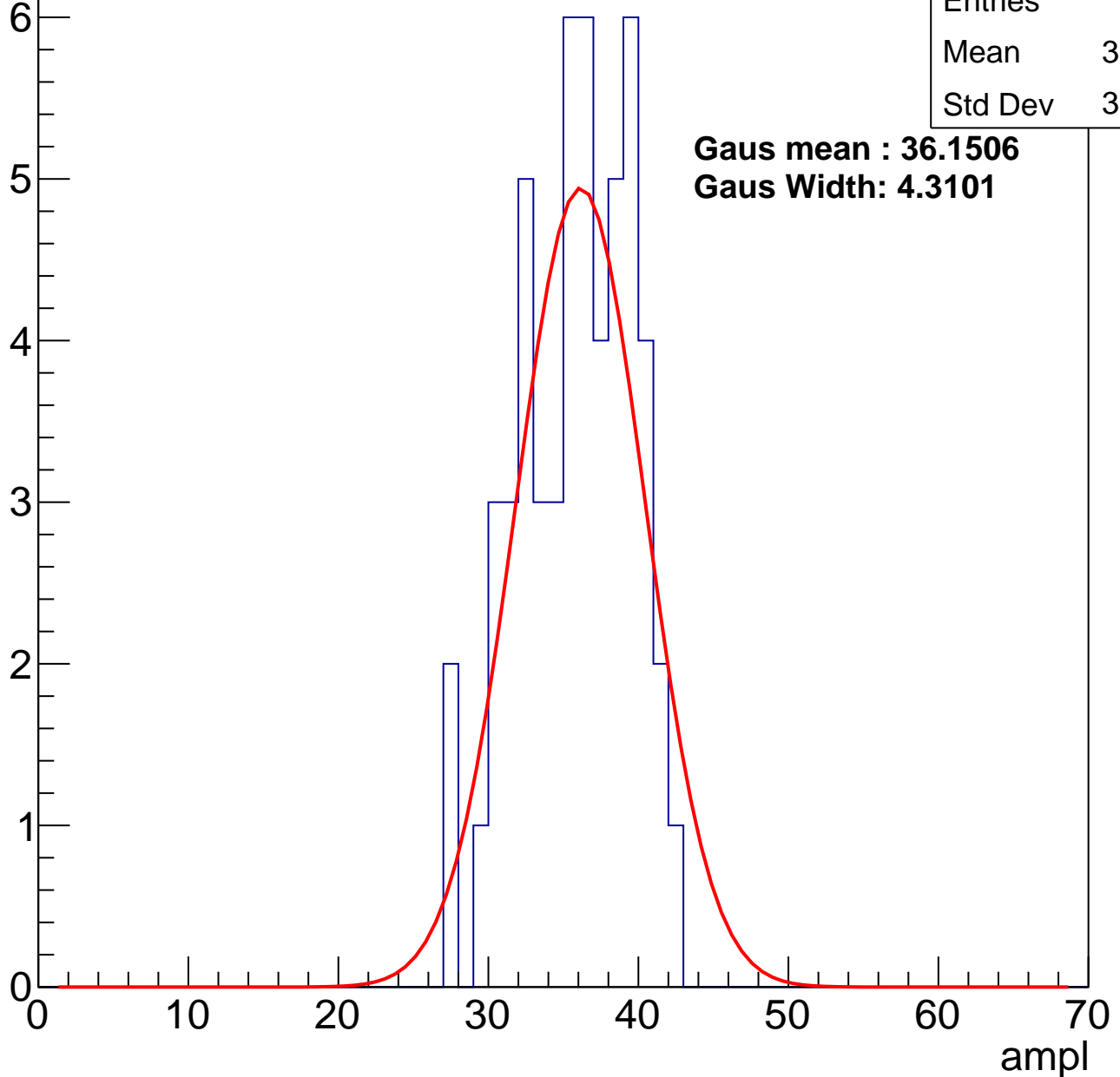
B1L103S, U17-ch66, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	35.35
Std Dev	3.662

Gaus mean : 36.1506
Gaus Width: 4.3101



B1L103S, U17-ch66, adc2

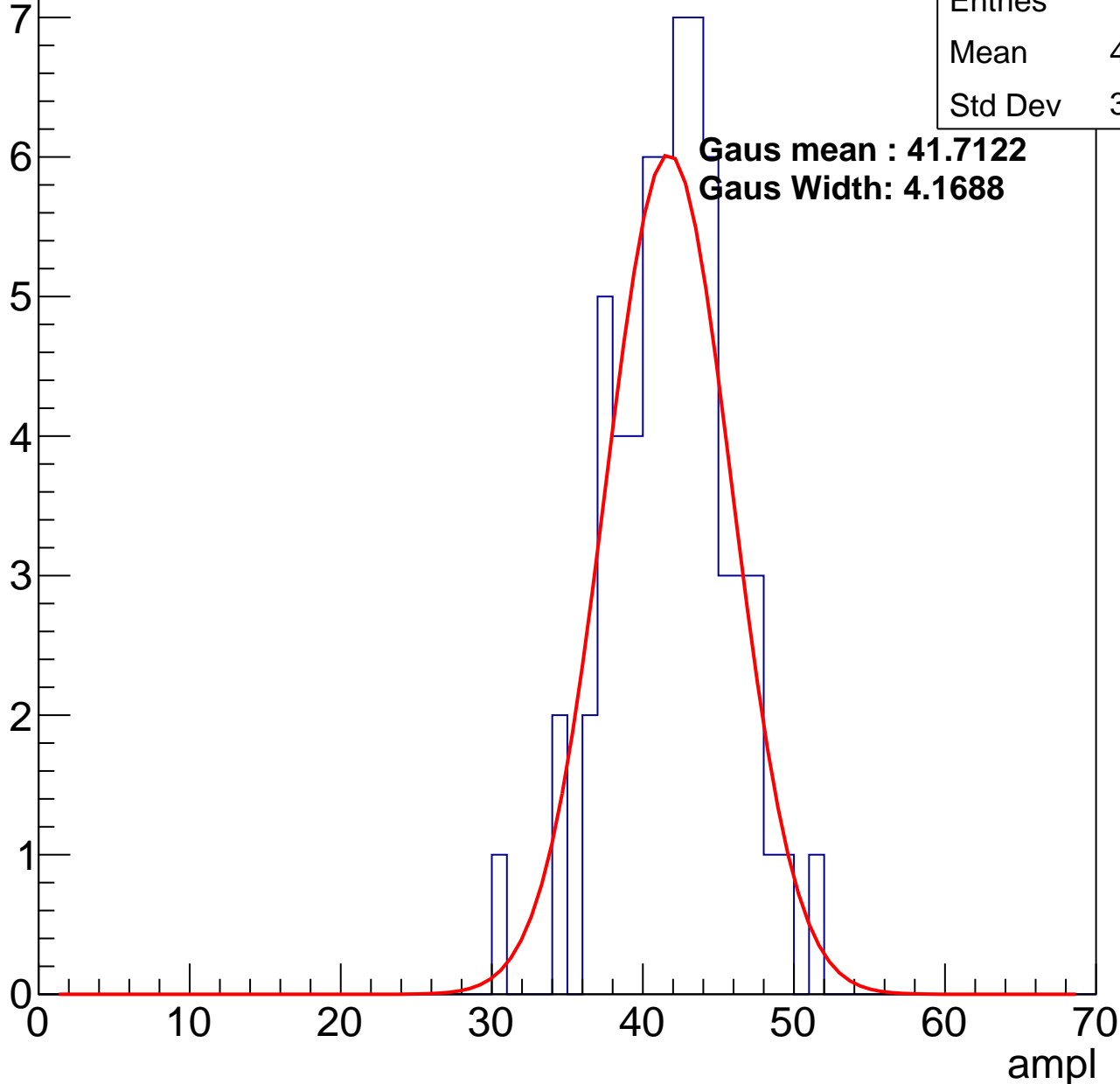
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.45
Std Dev	3.867

Gaus mean : 41.7122

Gaus Width: 4.1688

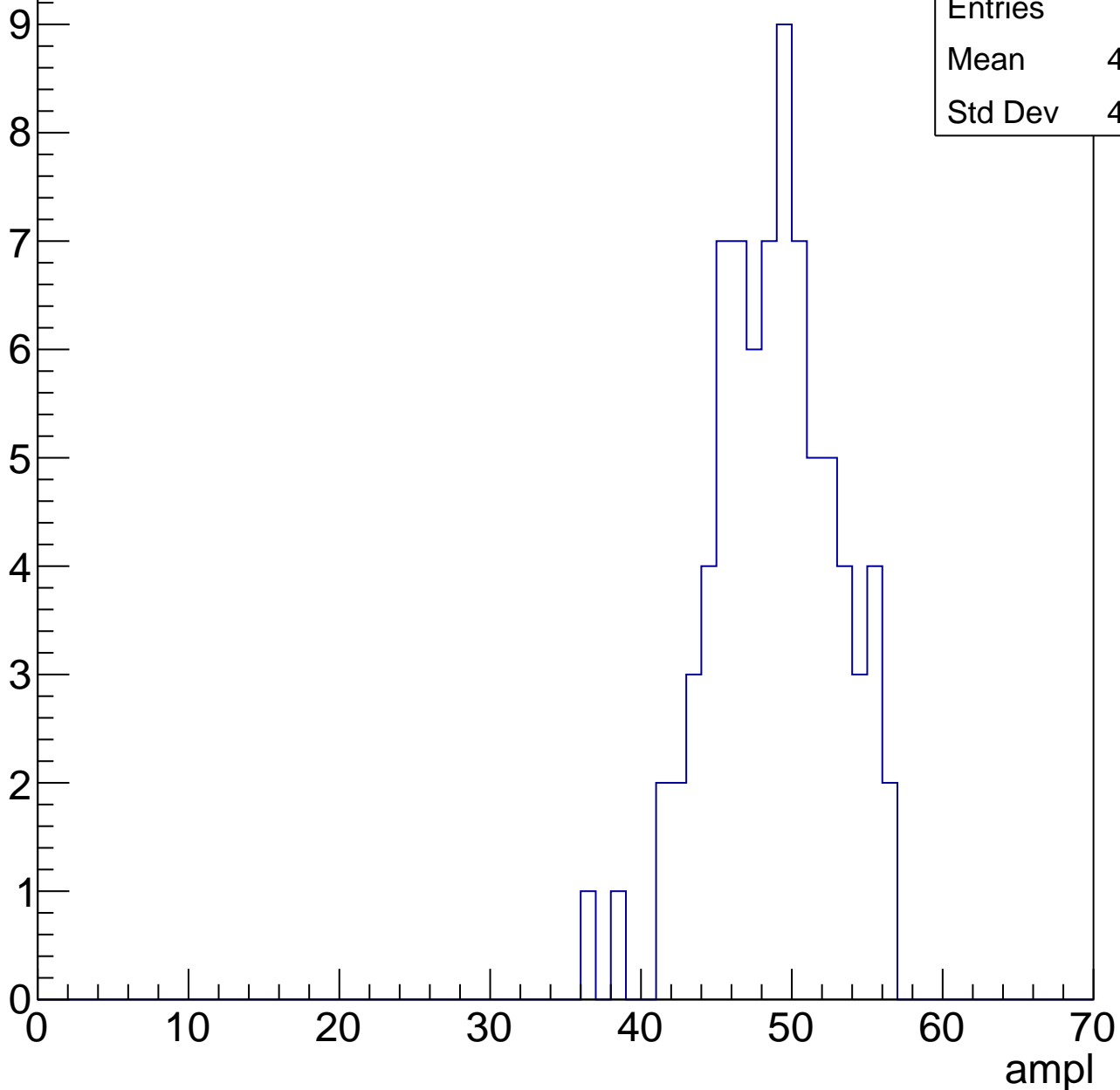


B1L103S, U17-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	48.25
Std Dev	4.117

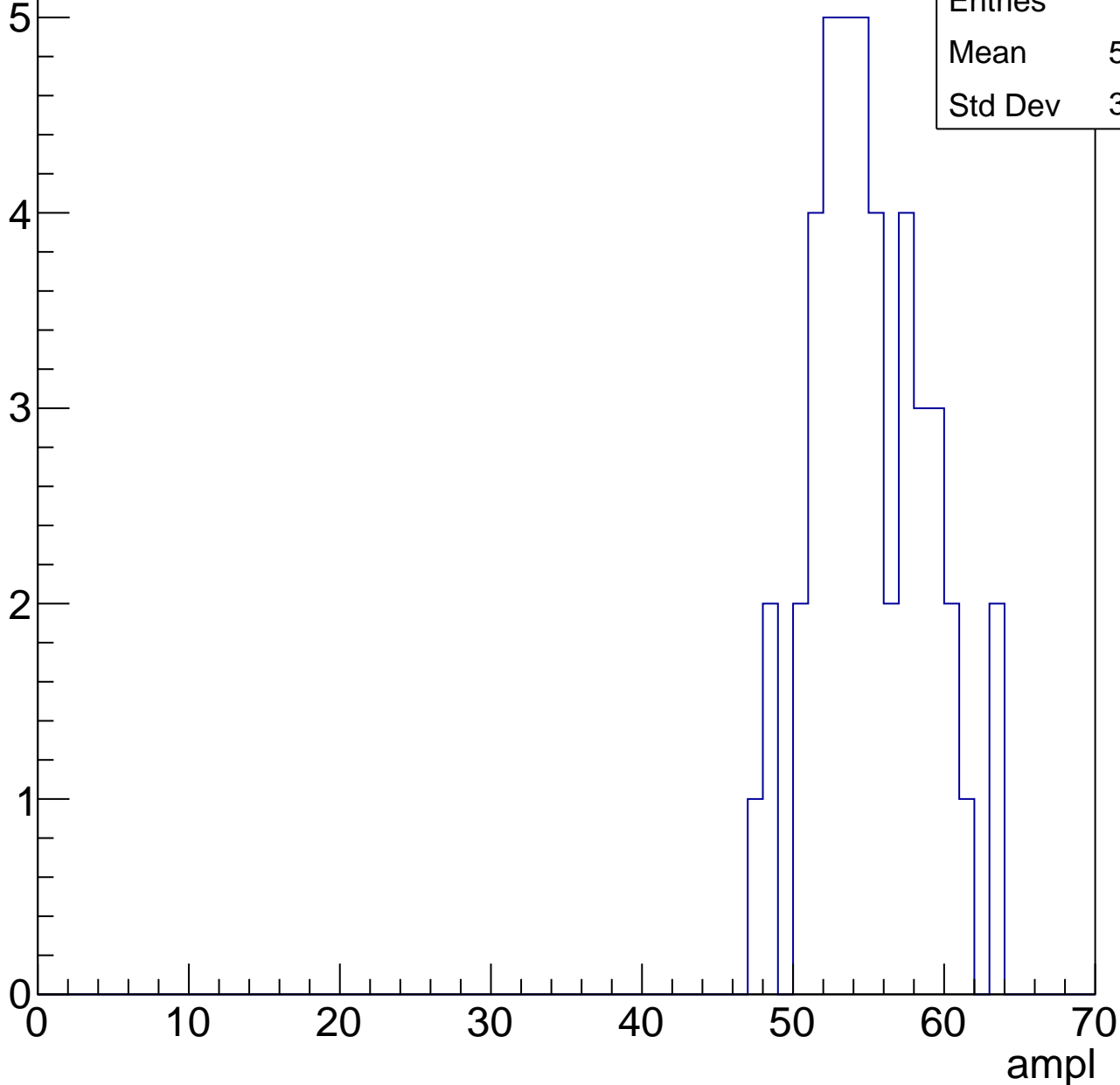


B1L103S, U17-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	54.67
Std Dev	3.795

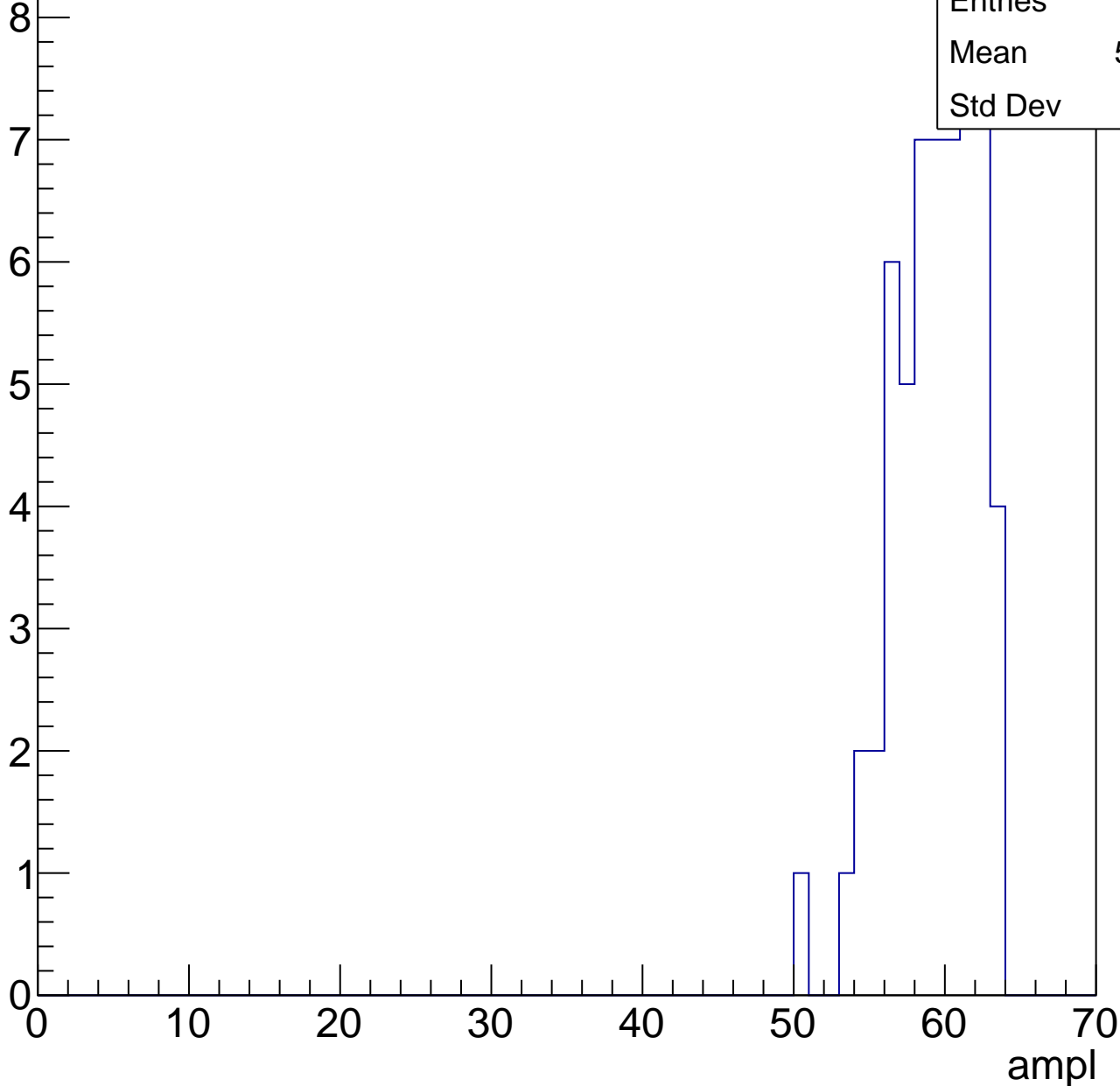


B1L103S, U17-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

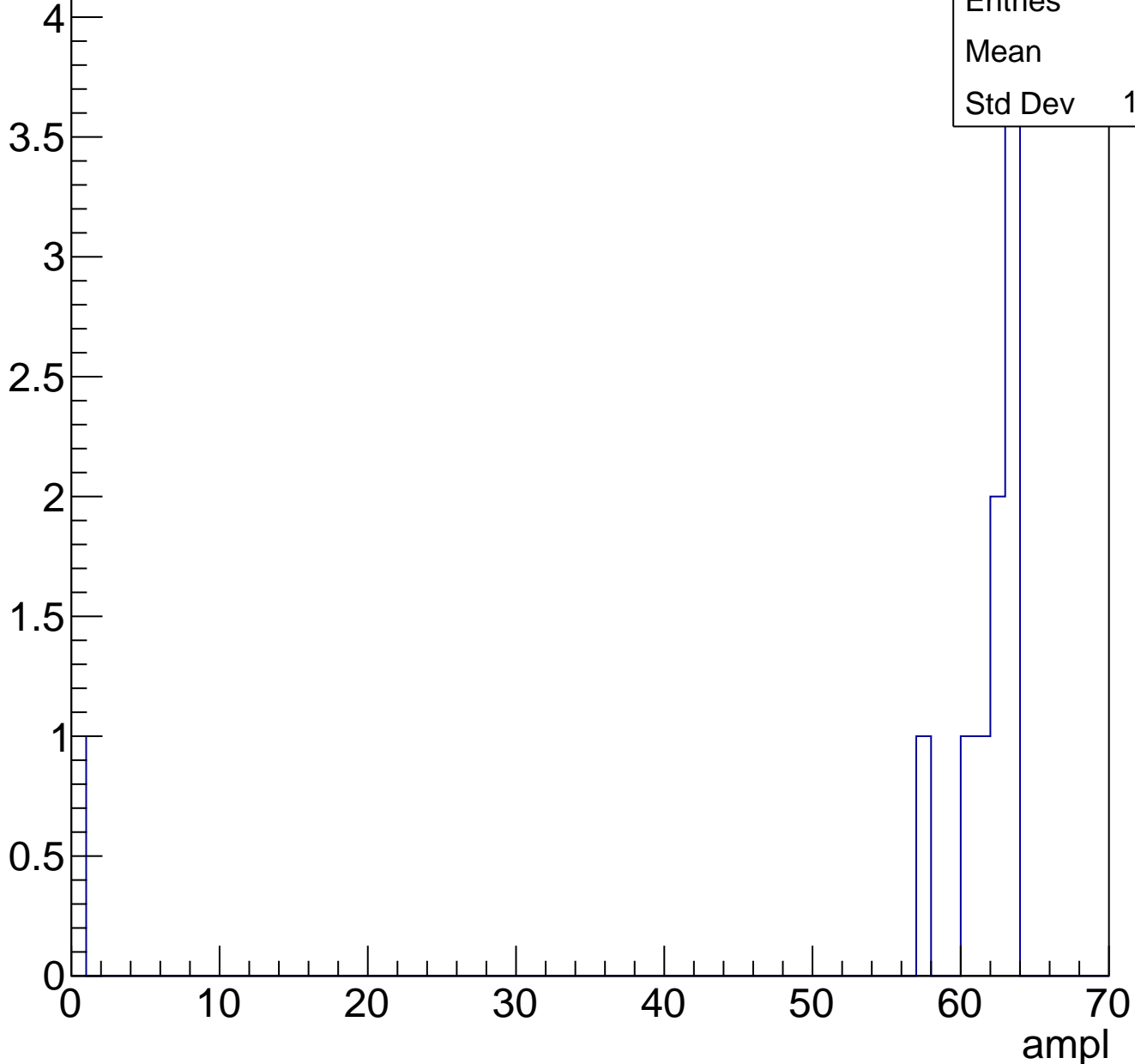
Entries	58
Mean	58.91
Std Dev	2.8



B1L103S, U17-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	55.4
Std Dev	18.55

B1L103S, U17-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch67, adc0

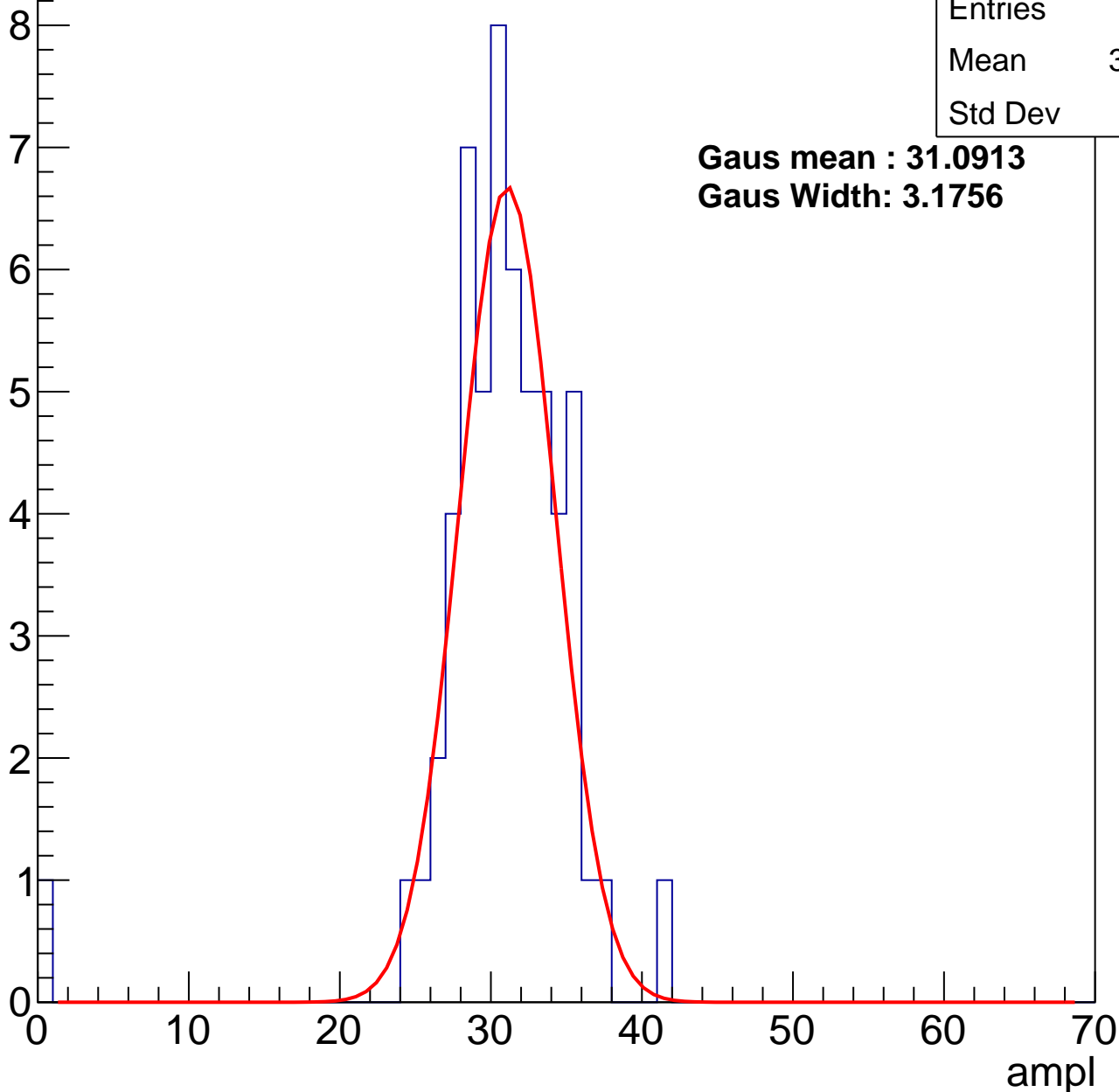
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	30.28
Std Dev	5.17

Gaus mean : 31.0913

Gaus Width: 3.1756



B1L103S, U17-ch67, adc1

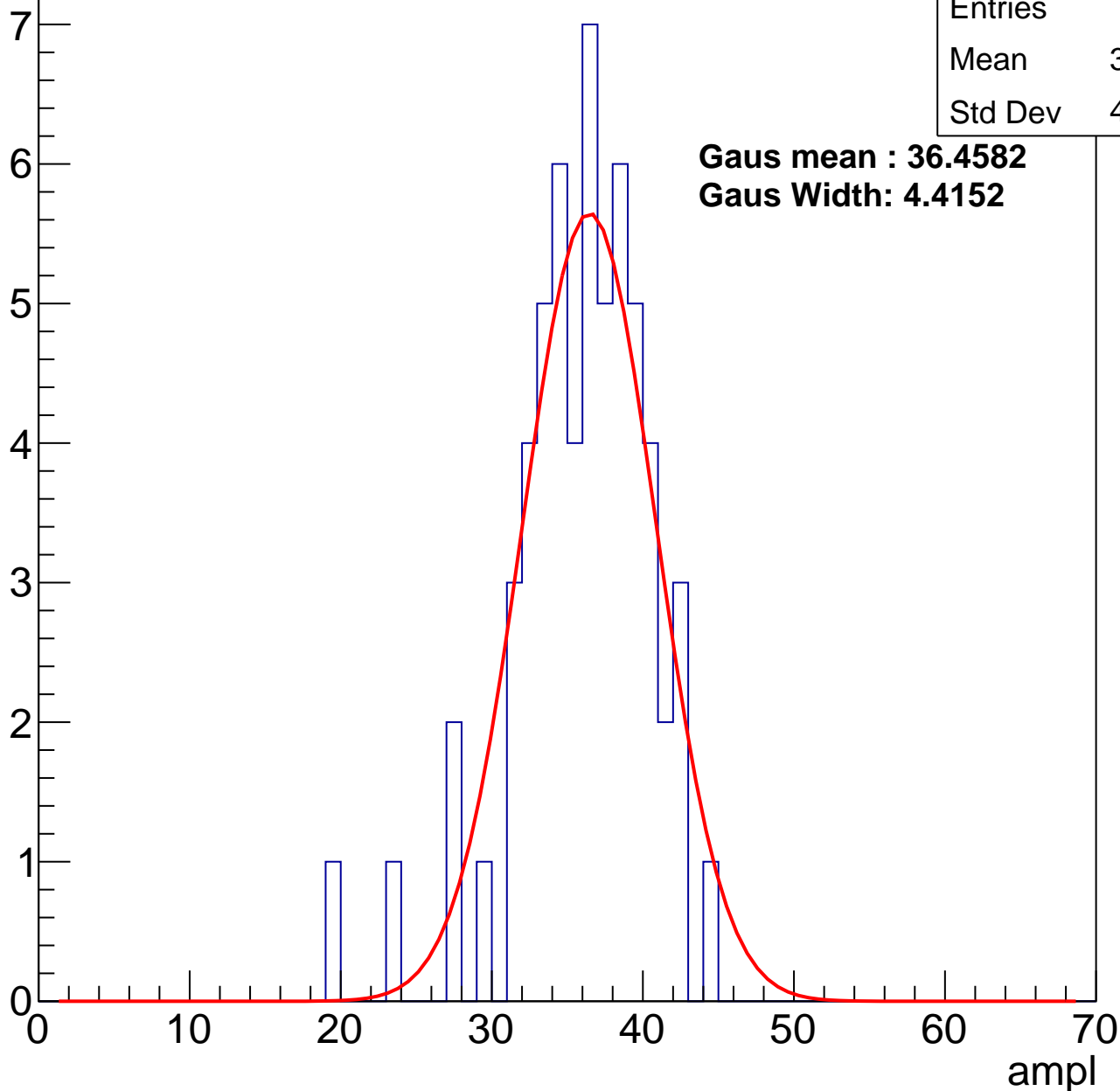
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.45
Std Dev	4.515

Gaus mean : 36.4582

Gaus Width: 4.4152



B1L103S, U17-ch67, adc2

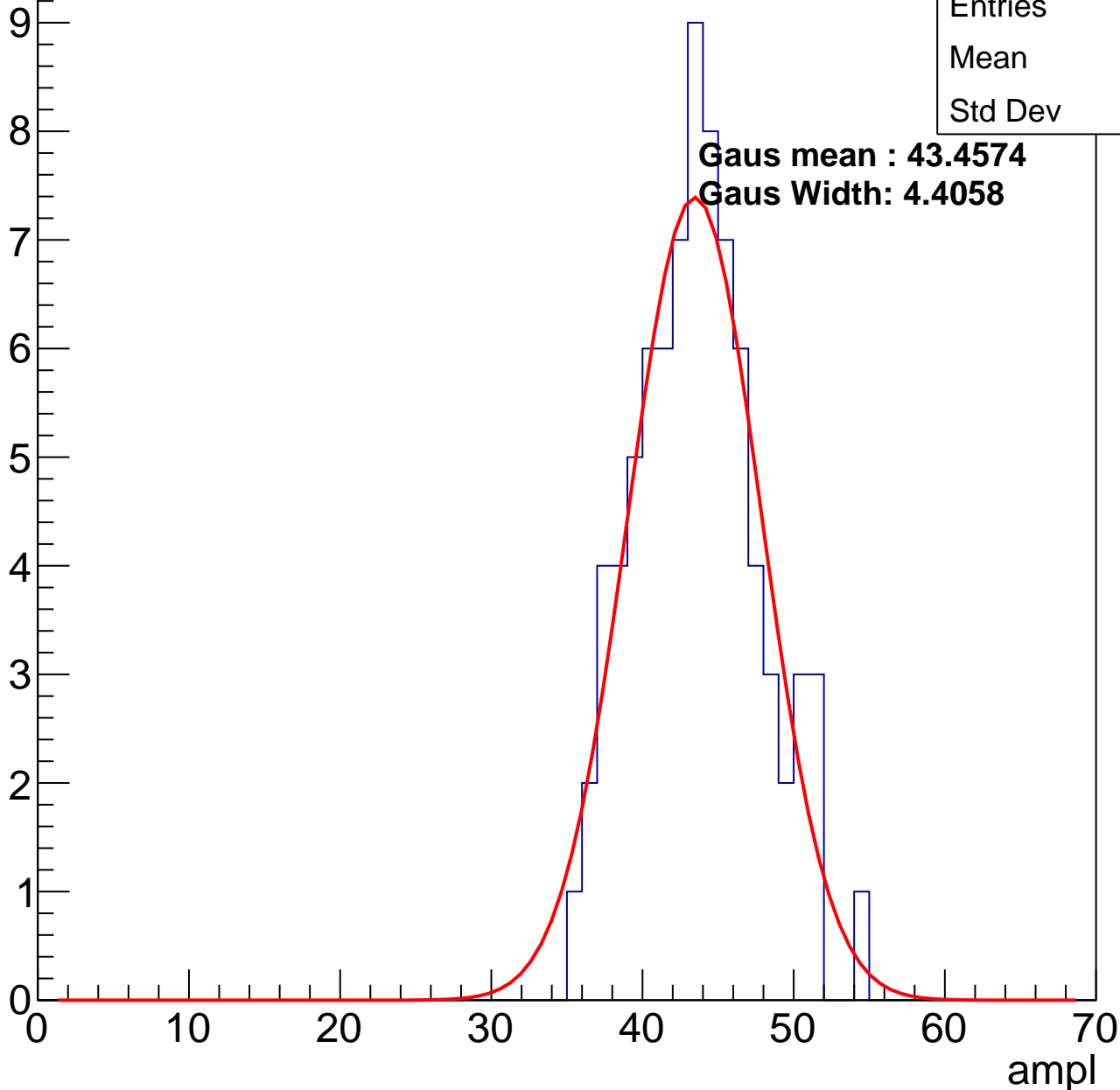
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	43.2
Std Dev	4.05

Gaus mean : 43.4574

Gaus Width: 4.4058

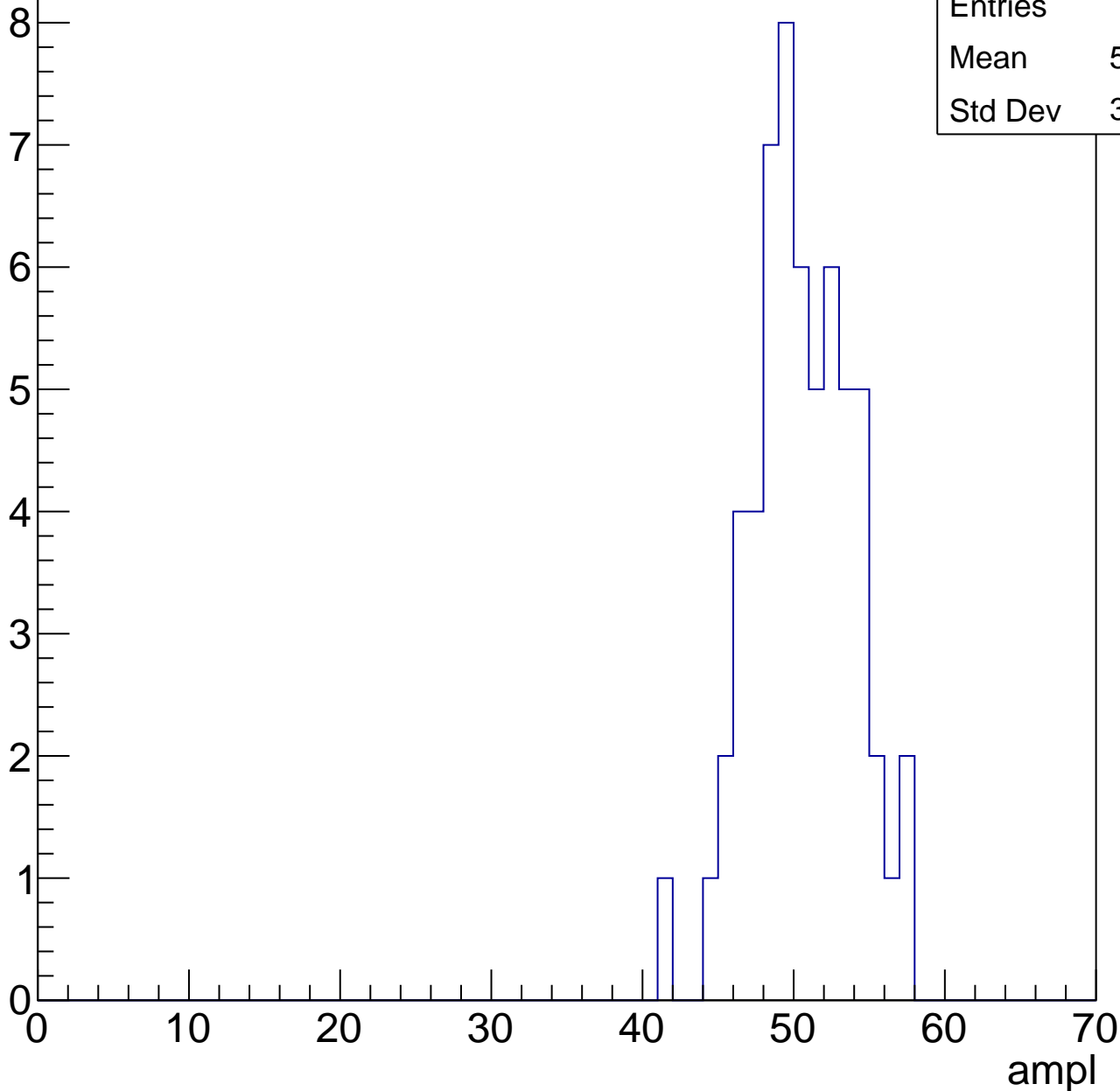


B1L103S, U17-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.12
Std Dev	3.294

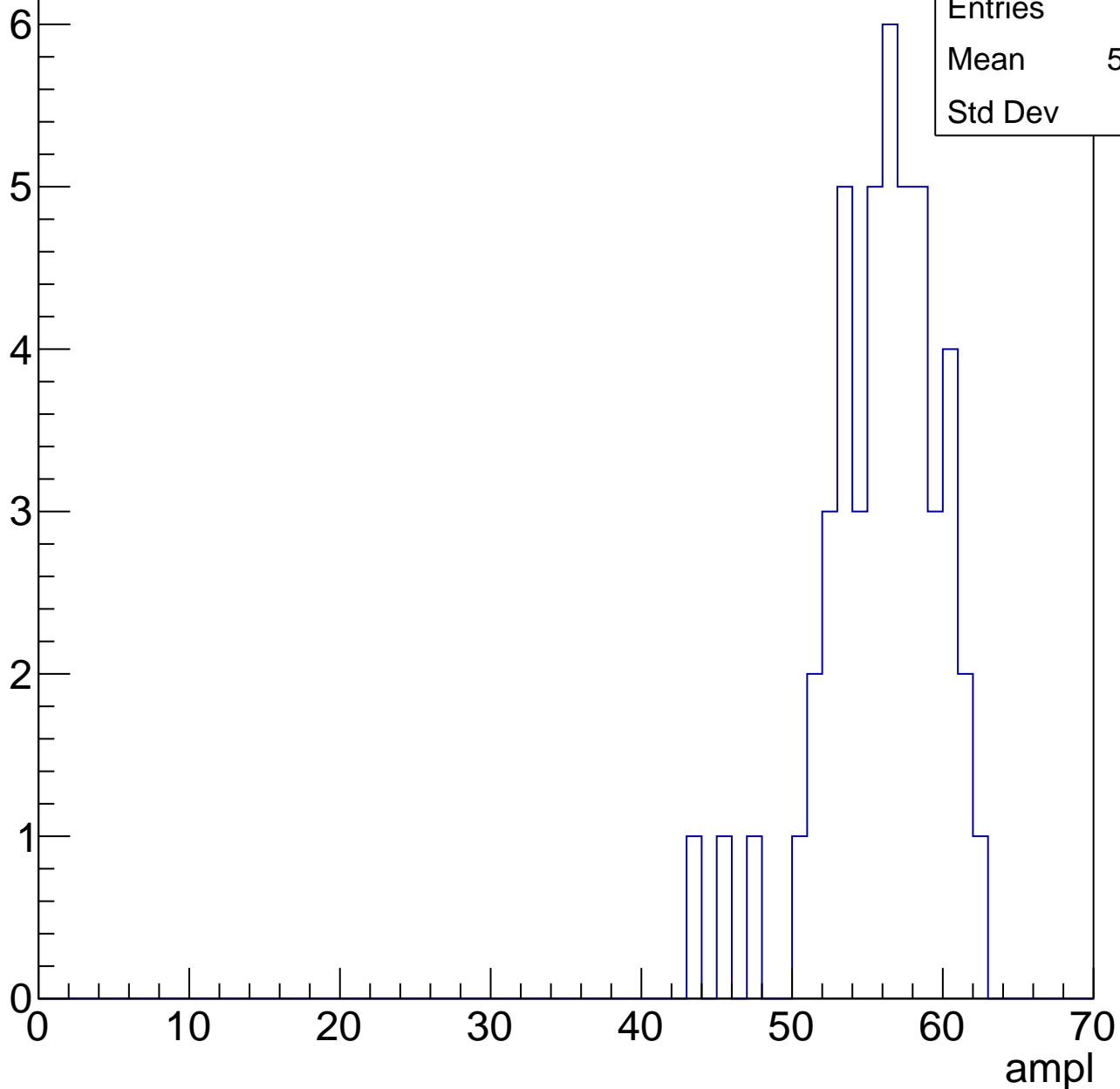


B1L103S, U17-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	55.35
Std Dev	3.95

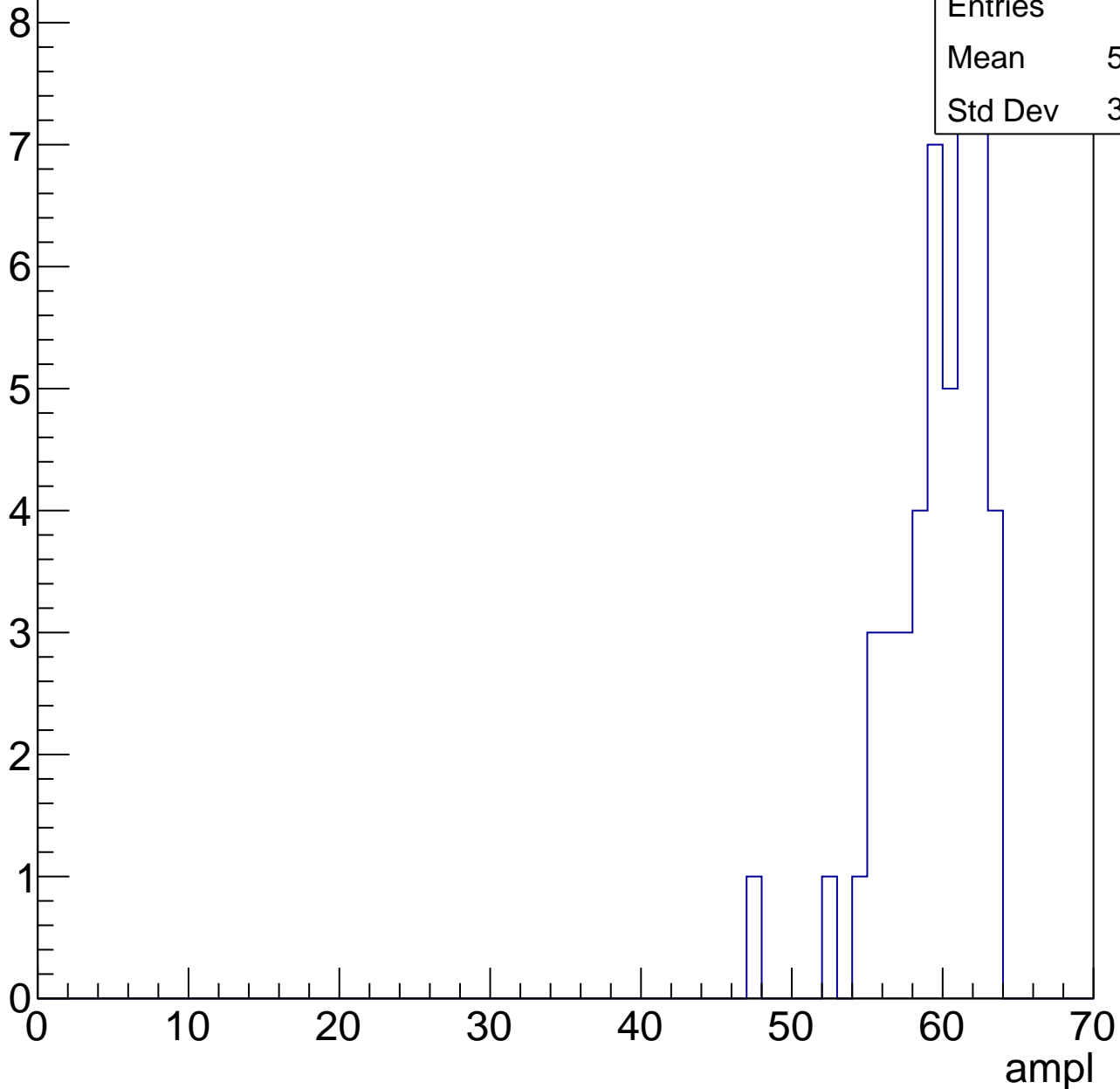


B1L103S, U17-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	59.12
Std Dev	3.166

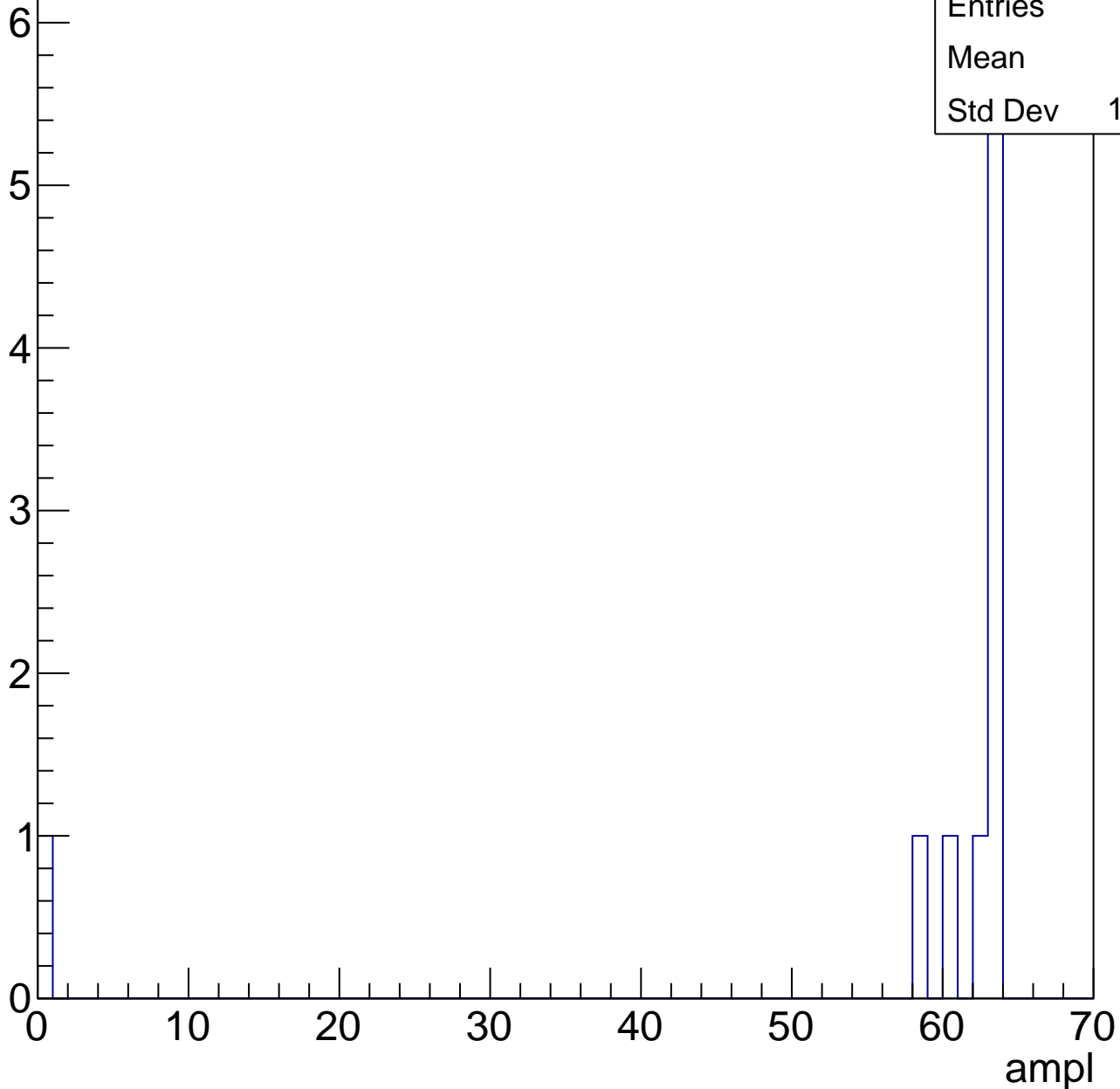


B1L103S, U17-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	55.8
Std Dev	18.67



B1L103S, U17-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch68, adc0

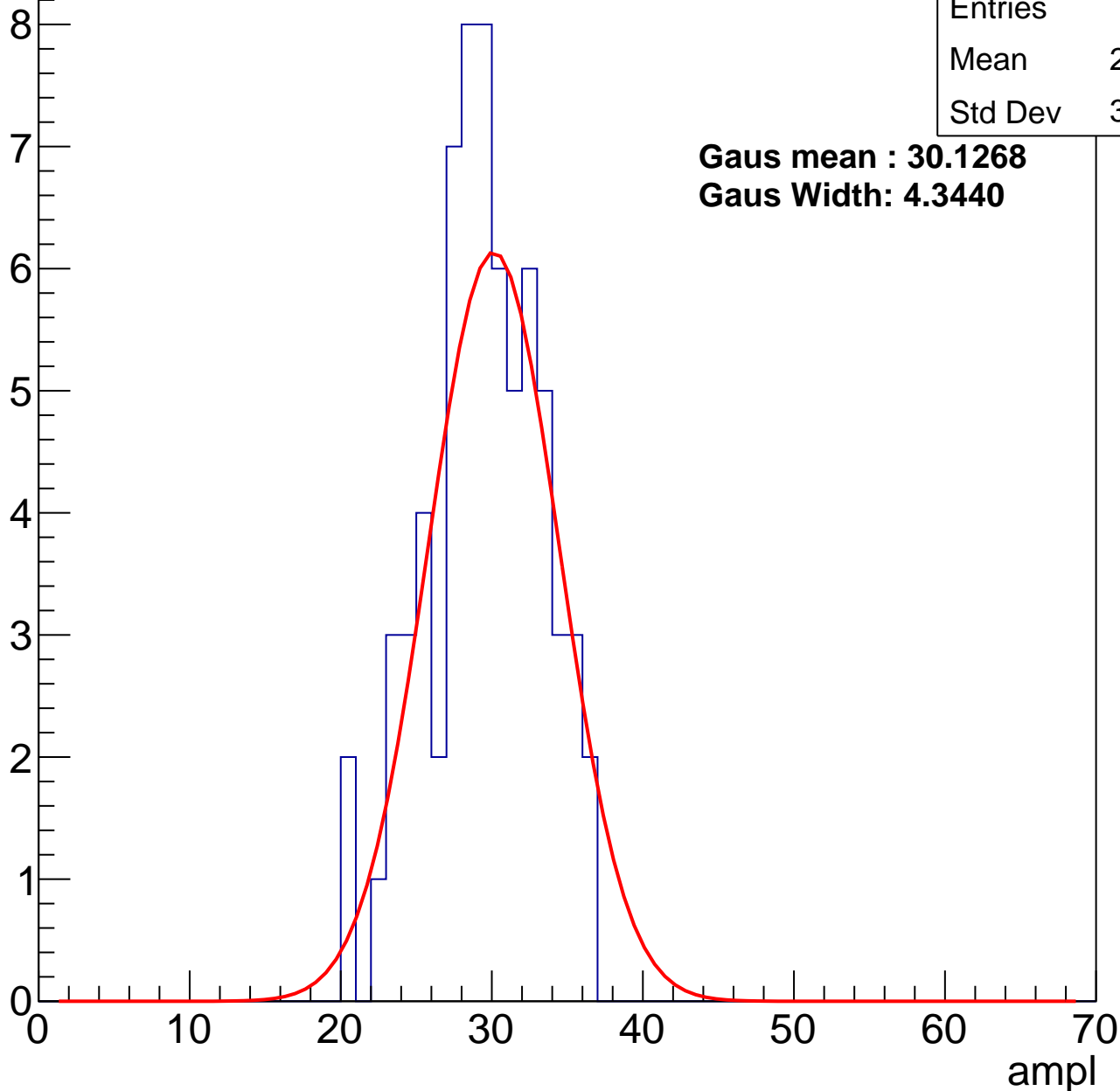
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.99
Std Dev	3.744

Gaus mean : 30.1268

Gaus Width: 4.3440



B1L103S, U17-ch68, adc1

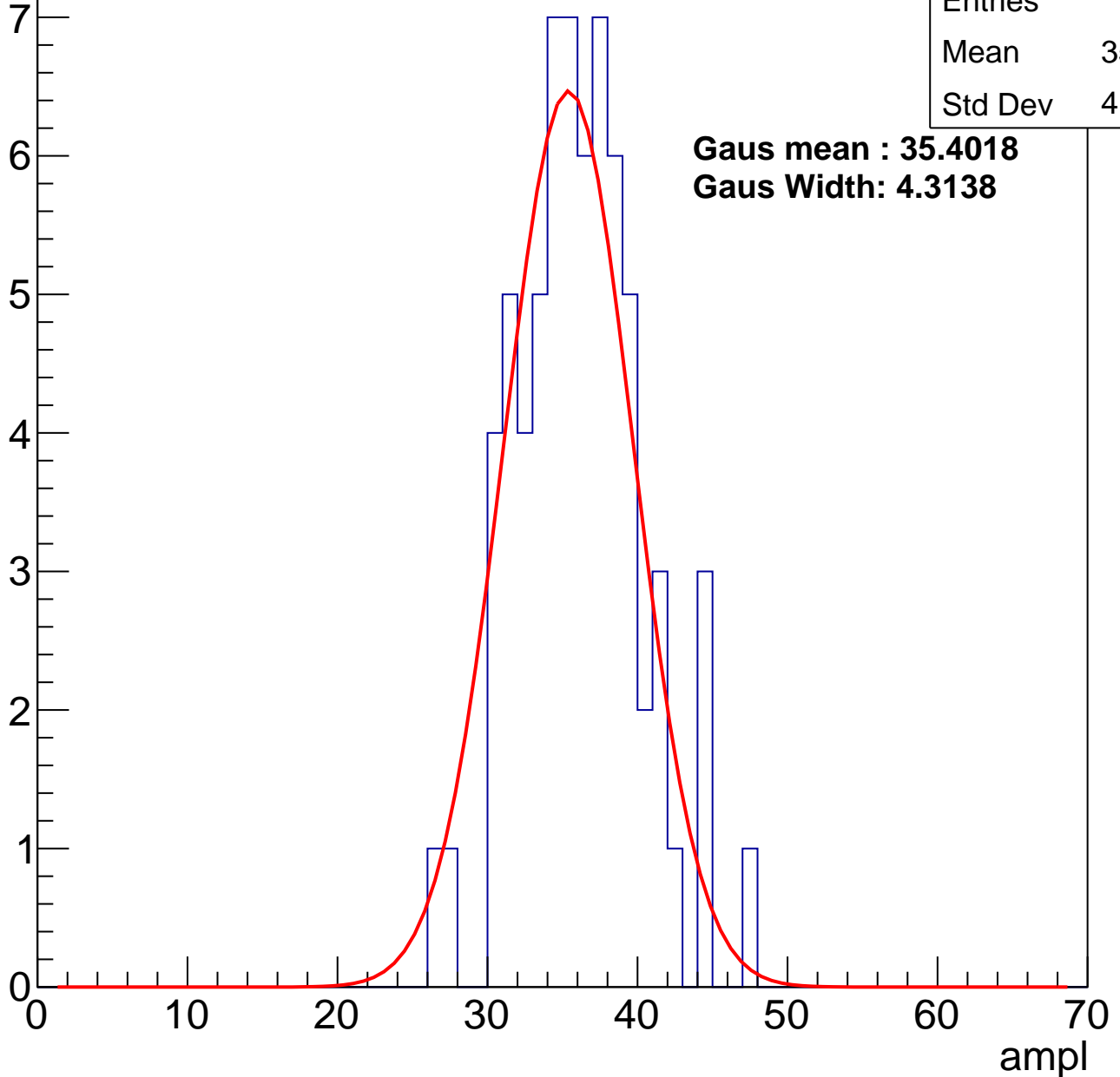
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.68
Std Dev	4.056

Gaus mean : 35.4018

Gaus Width: 4.3138

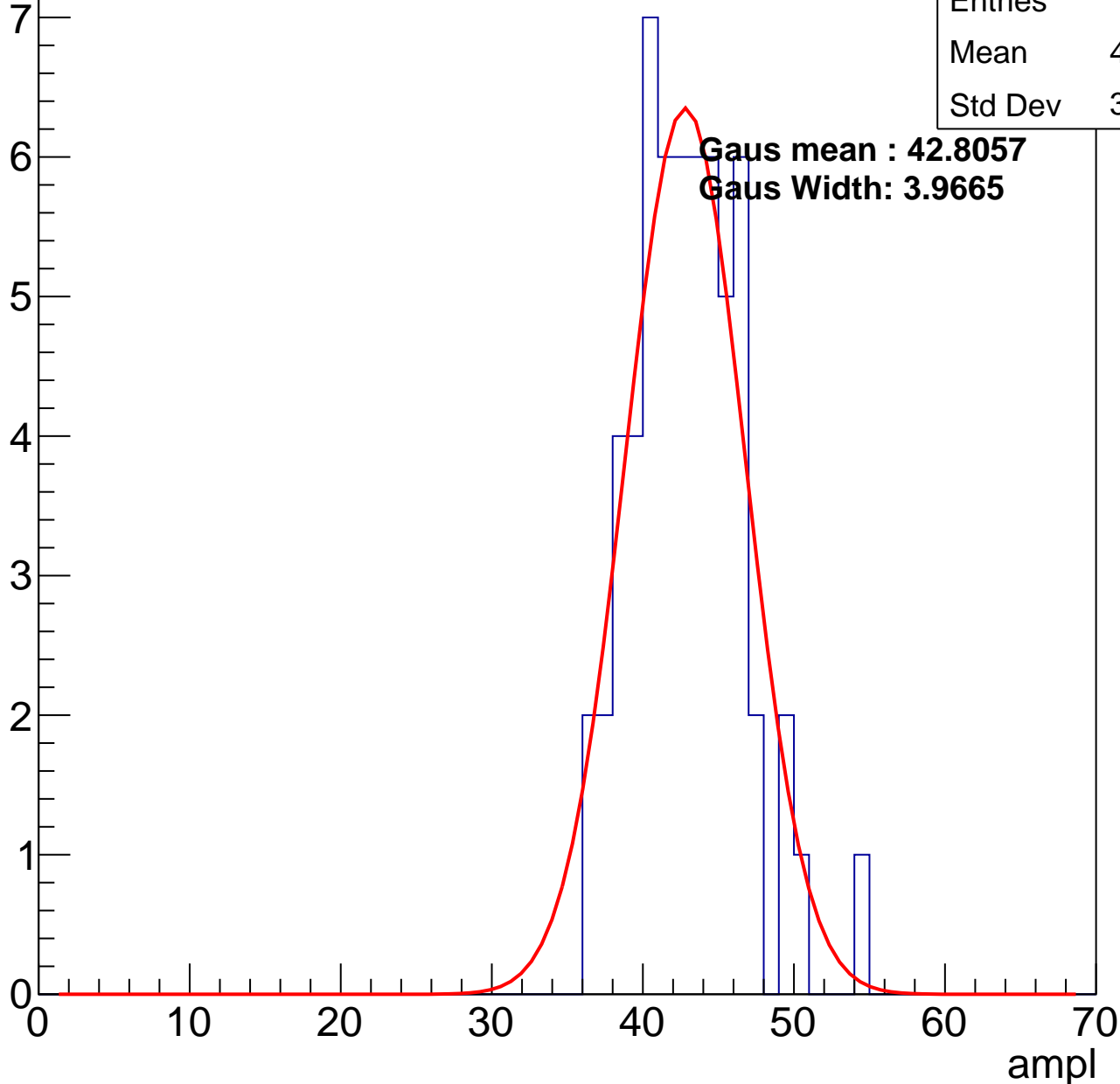


B1L103S, U17-ch68, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.52
Std Dev	3.575



B1L103S, U17-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

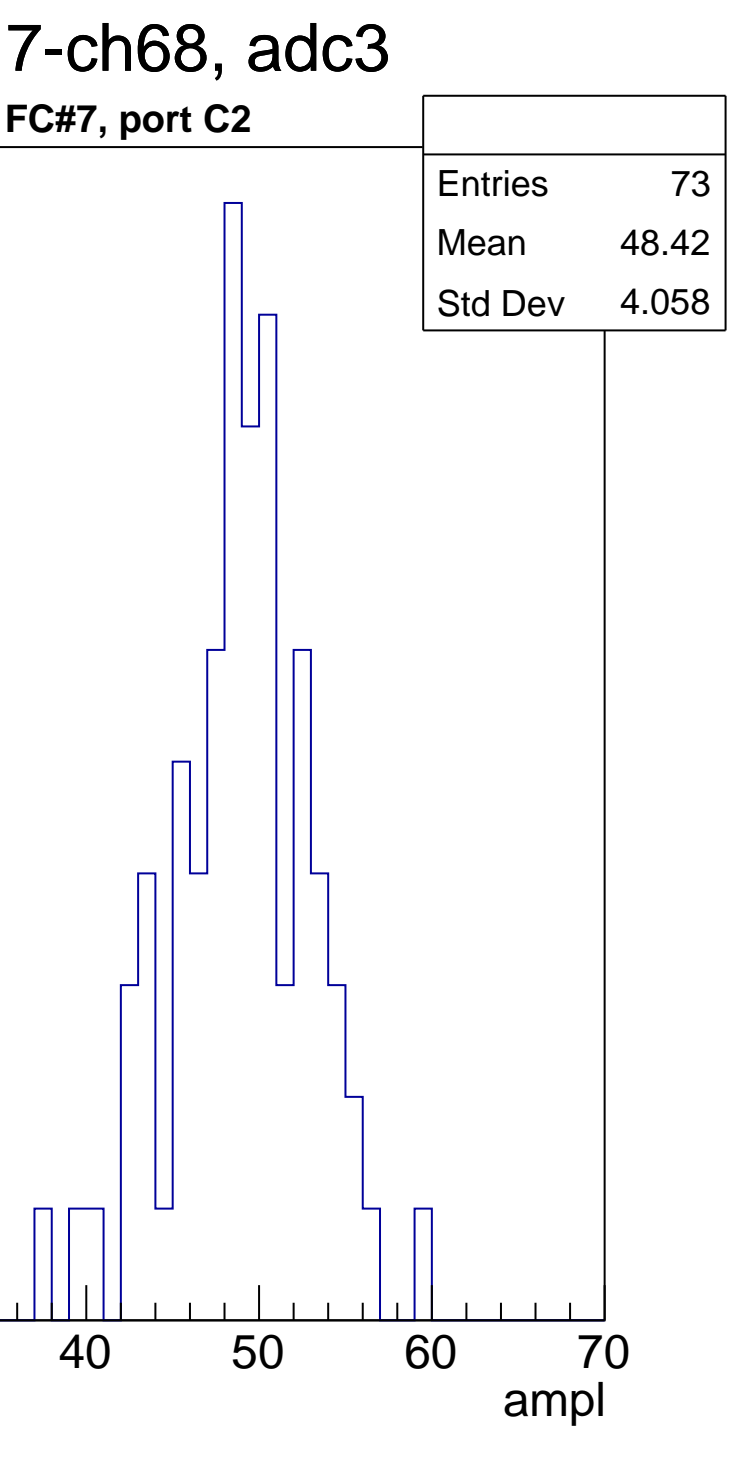
Entries	73
Mean	48.42
Std Dev	4.058

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

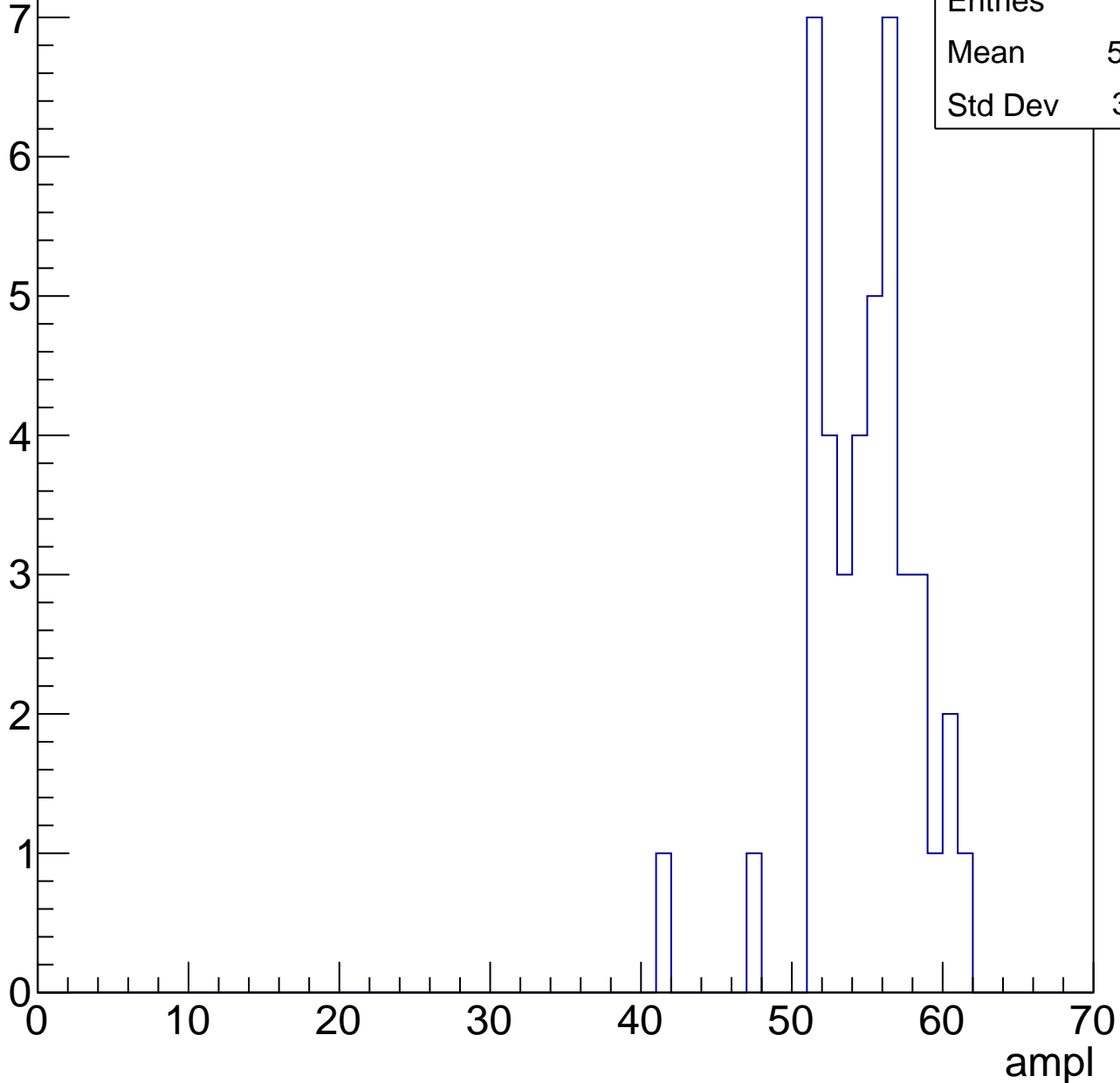


B1L103S, U17-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	54.29
Std Dev	3.621

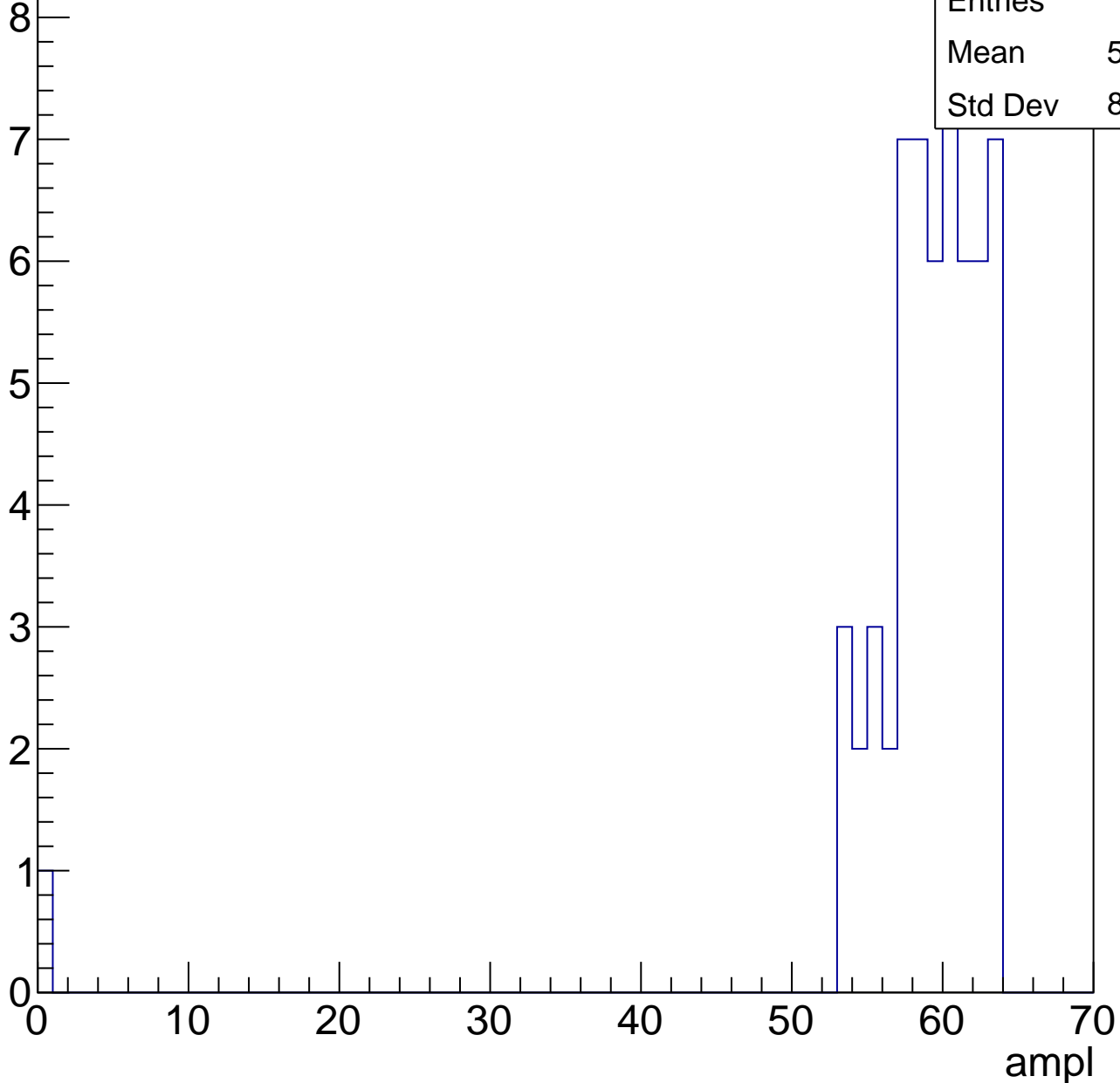


B1L103S, U17-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

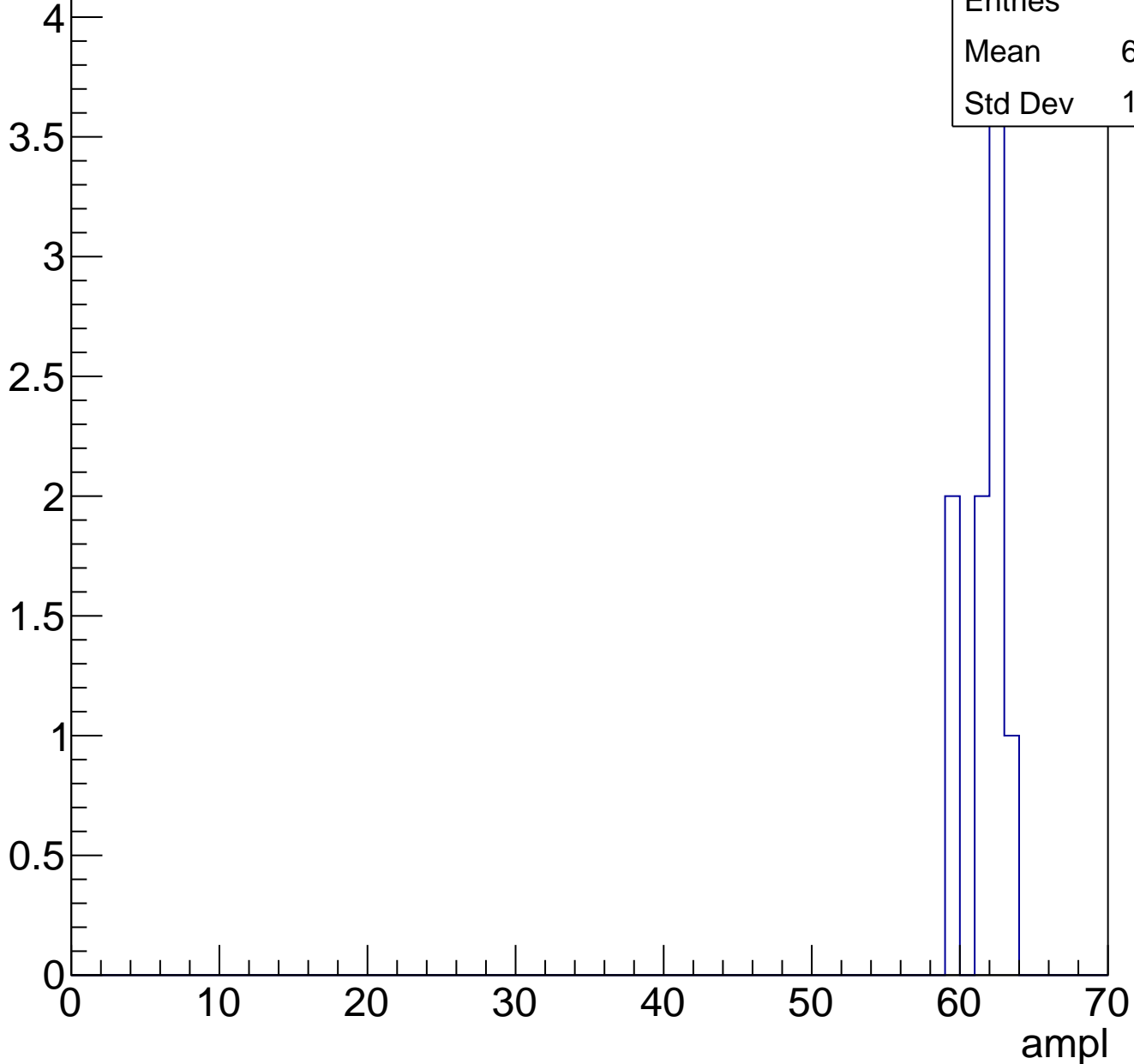
Entries	58
Mean	57.97
Std Dev	8.175



B1L103S, U17-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

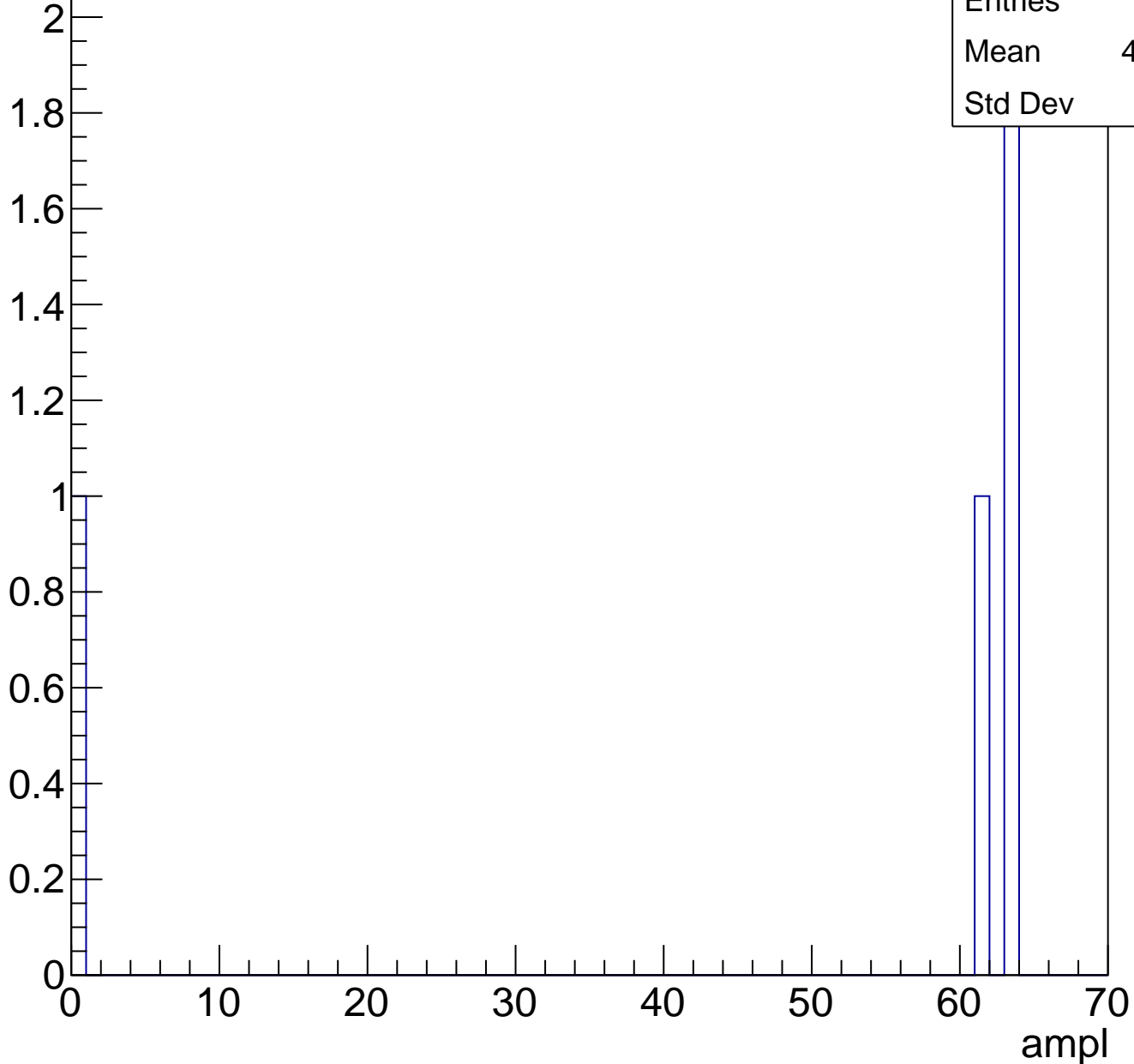
Entry



B1L103S, U17-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch69, adc0

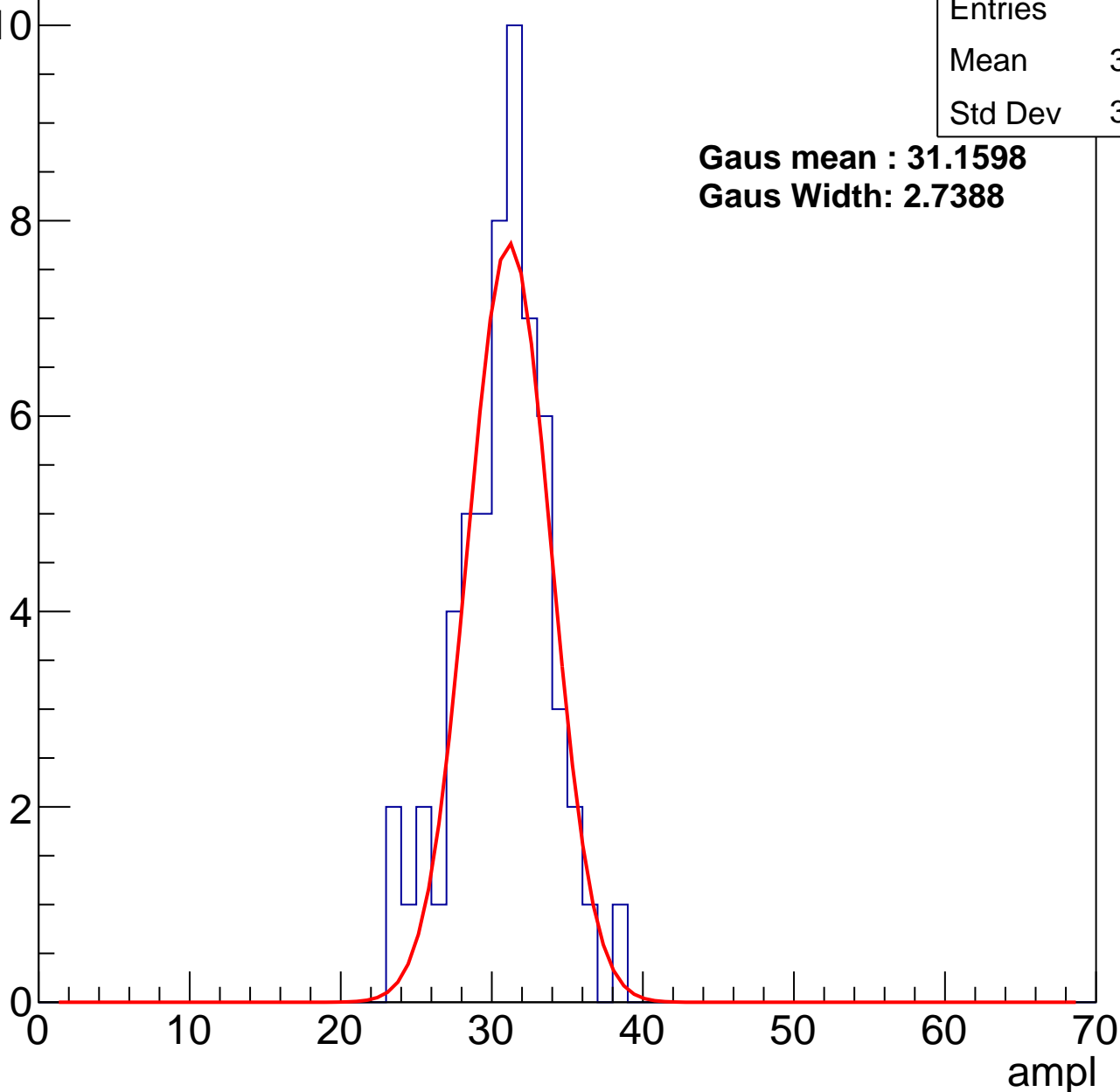
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	30.29
Std Dev	3.068

Gaus mean : 31.1598

Gaus Width: 2.7388



B1L103S, U17-ch69, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	36.5
Std Dev	3.242

Gaus mean : 36.9838

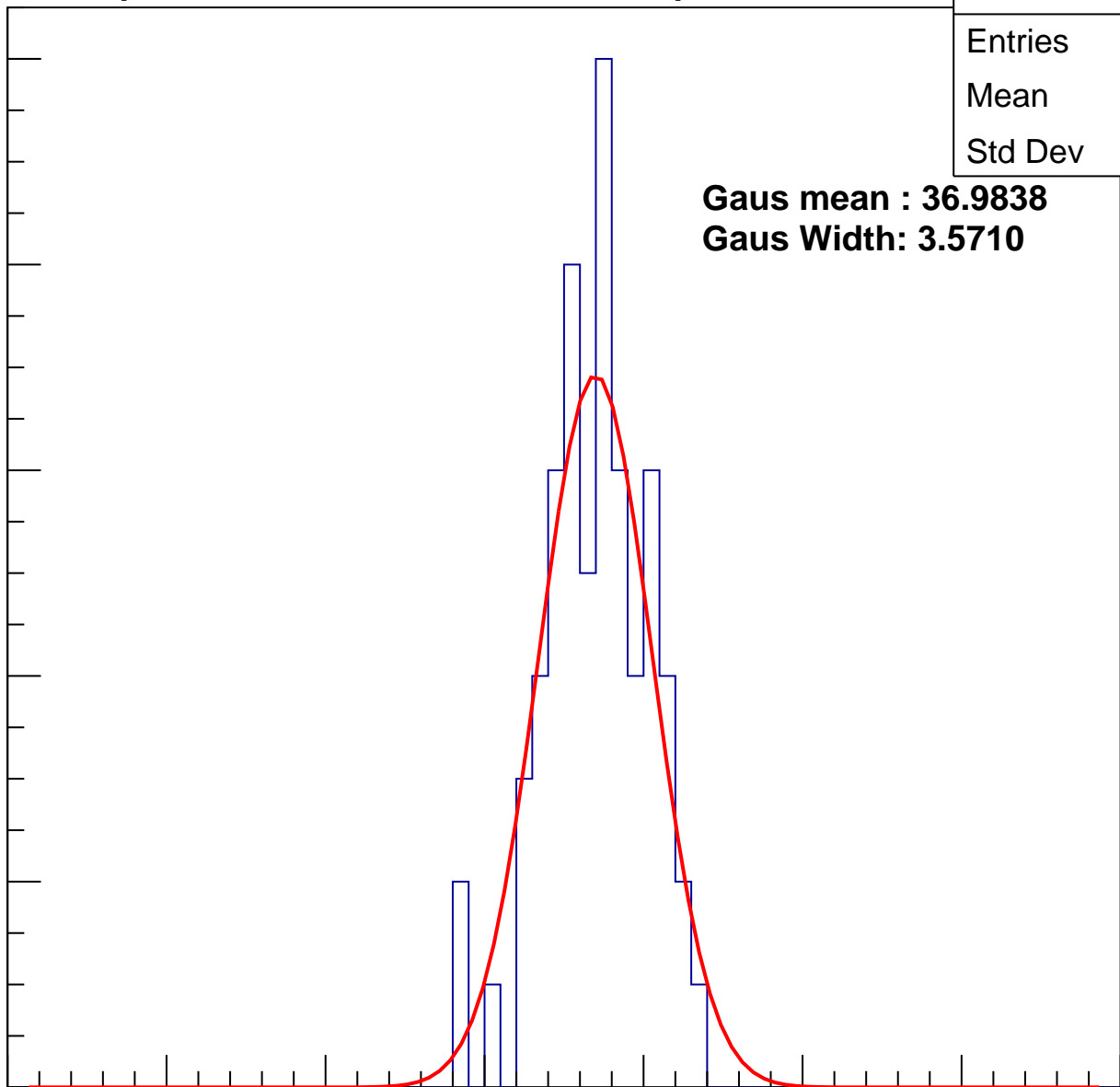
Gaus Width: 3.5710

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch69, adc2

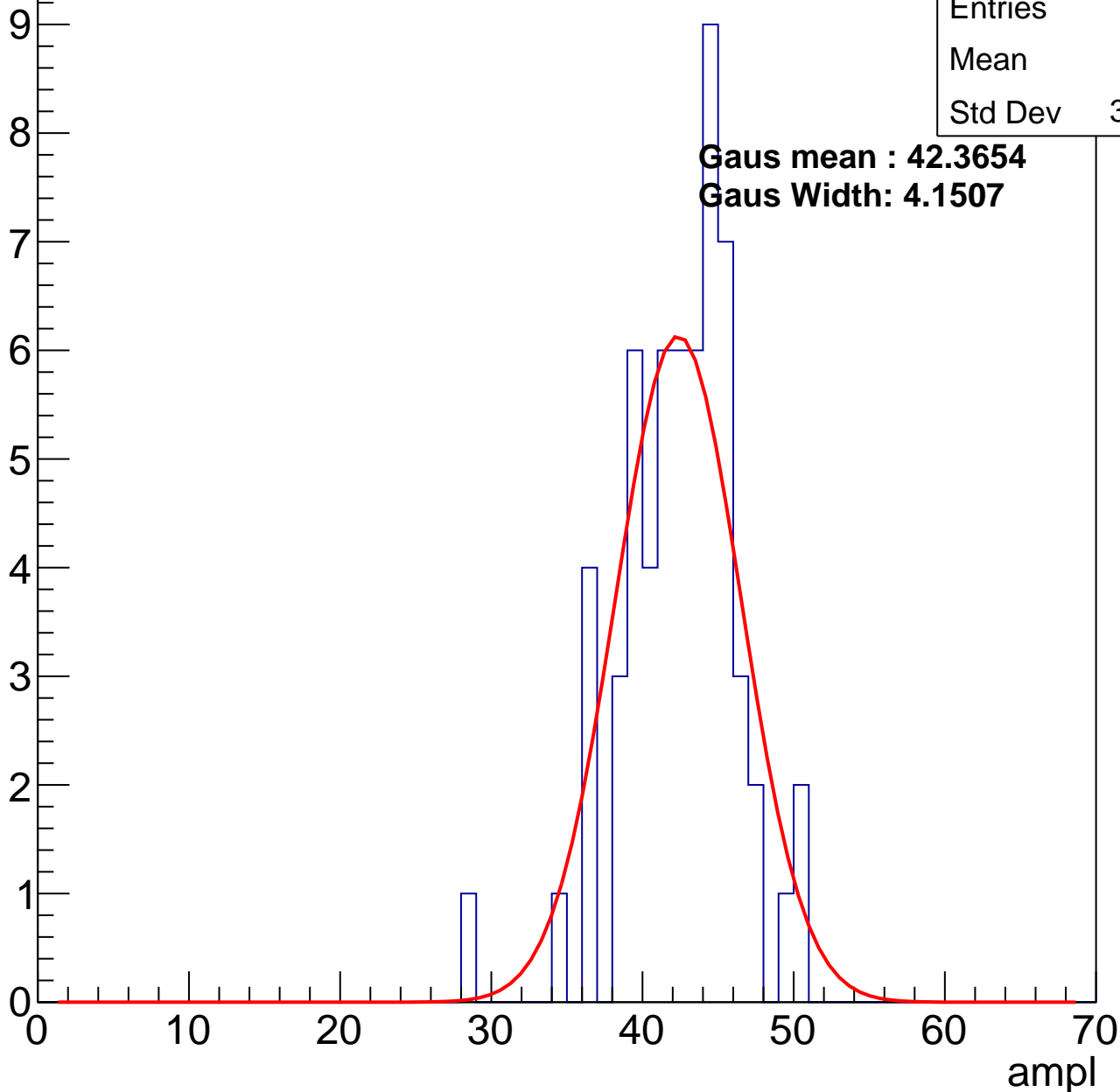
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42
Std Dev	3.858

Gaus mean : 42.3654

Gaus Width: 4.1507

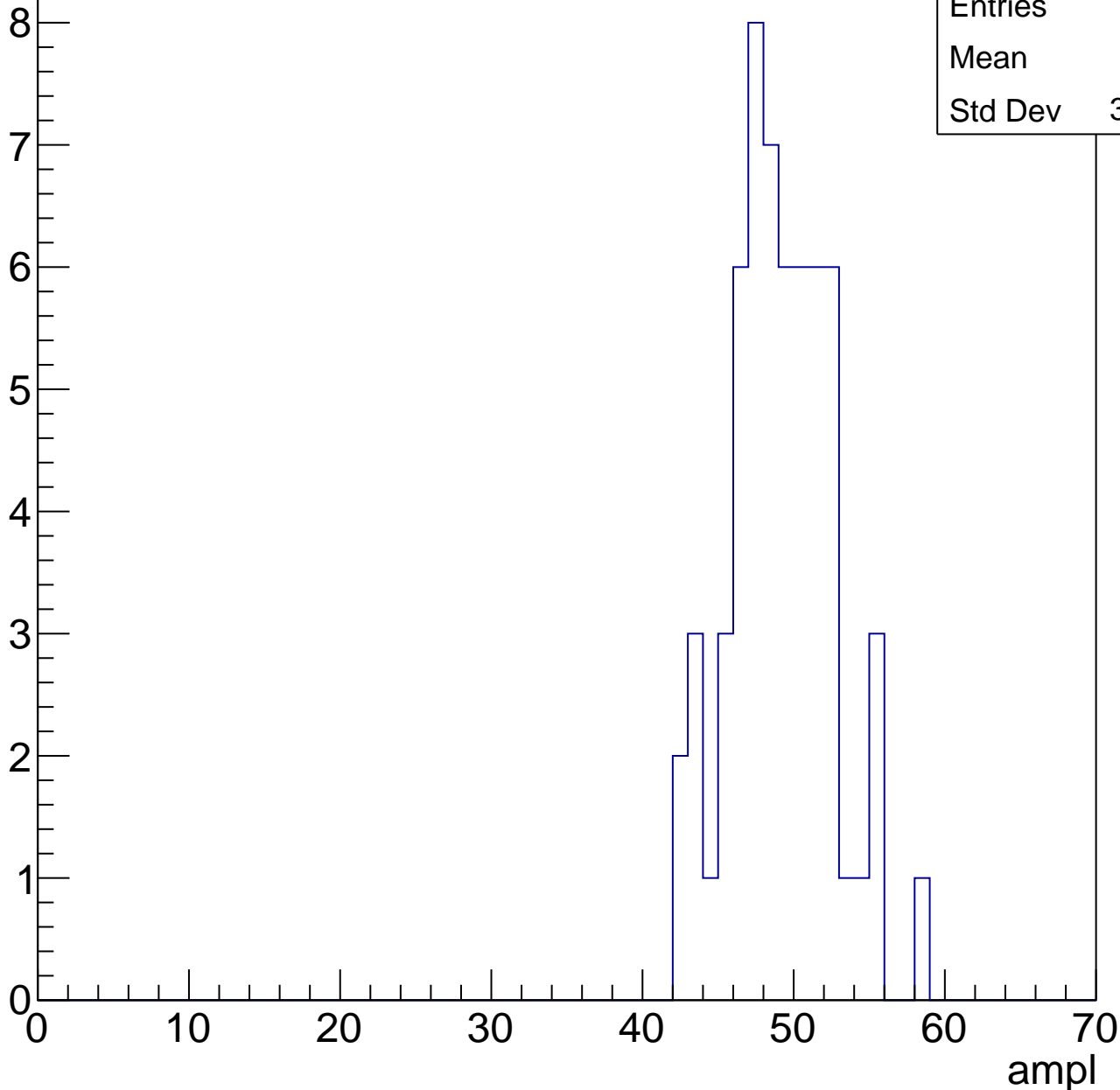


B1L103S, U17-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.7
Std Dev	3.368

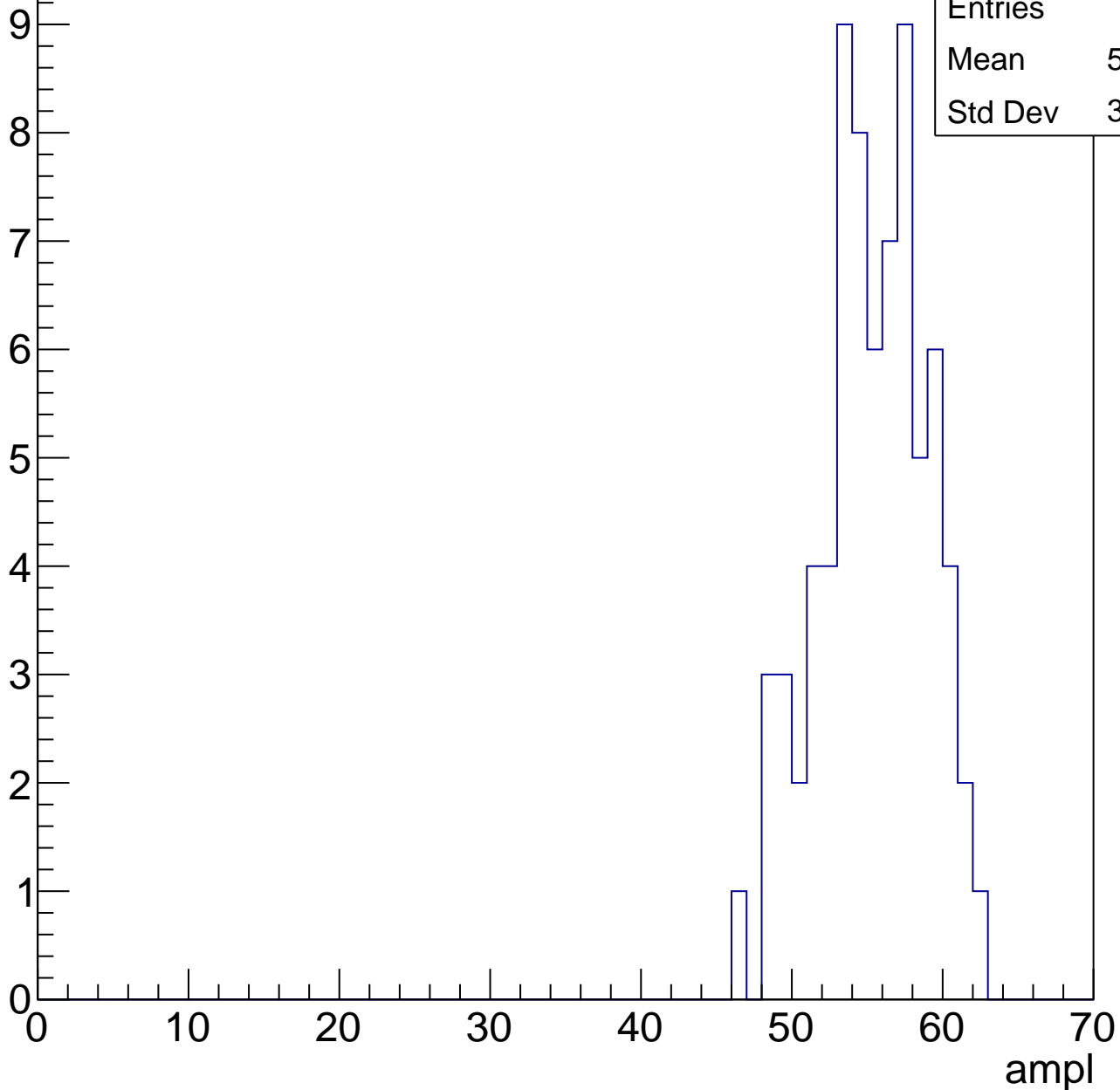


B1L103S, U17-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

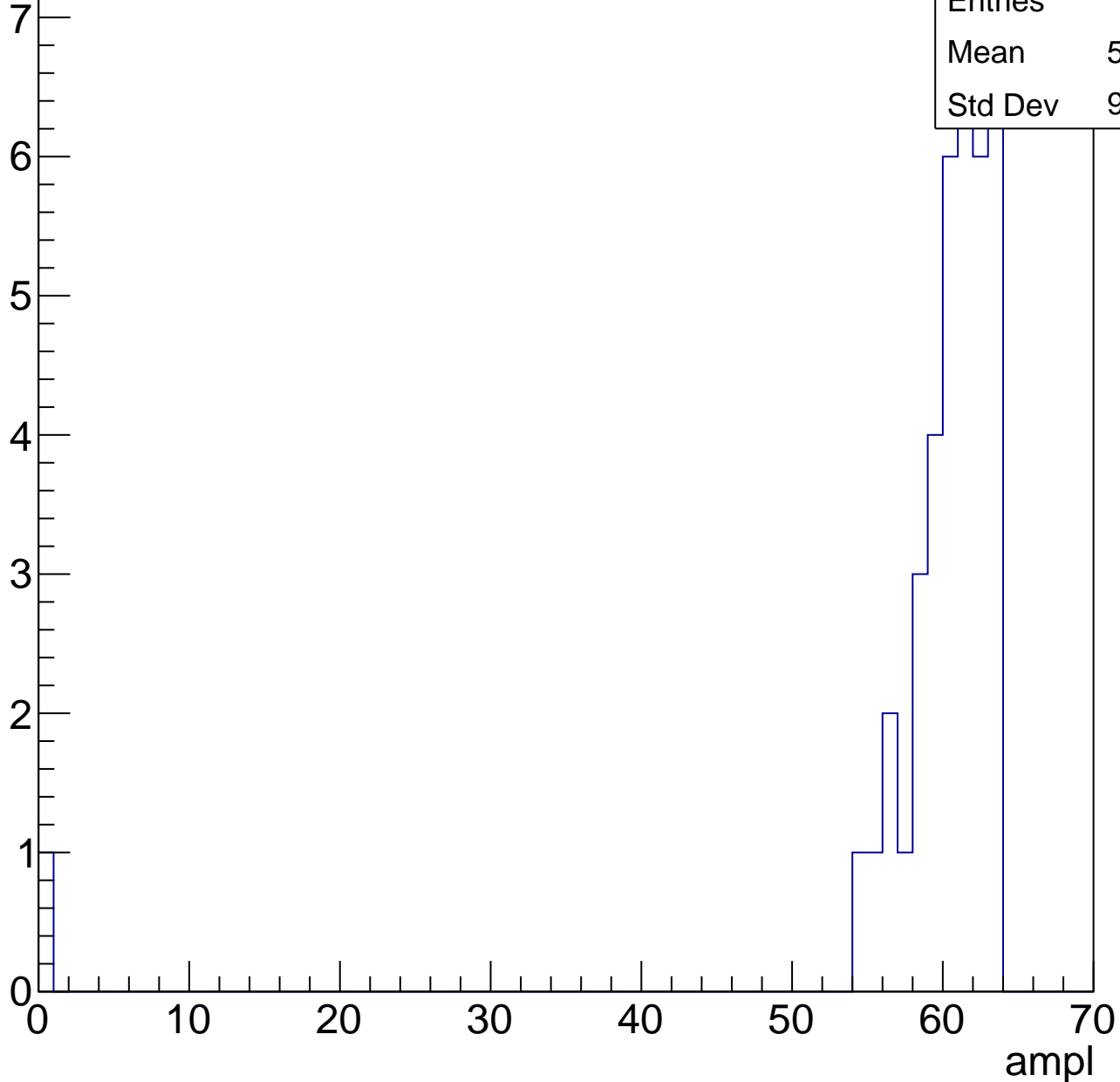
Entries	74
Mean	54.88
Std Dev	3.553



B1L103S, U17-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

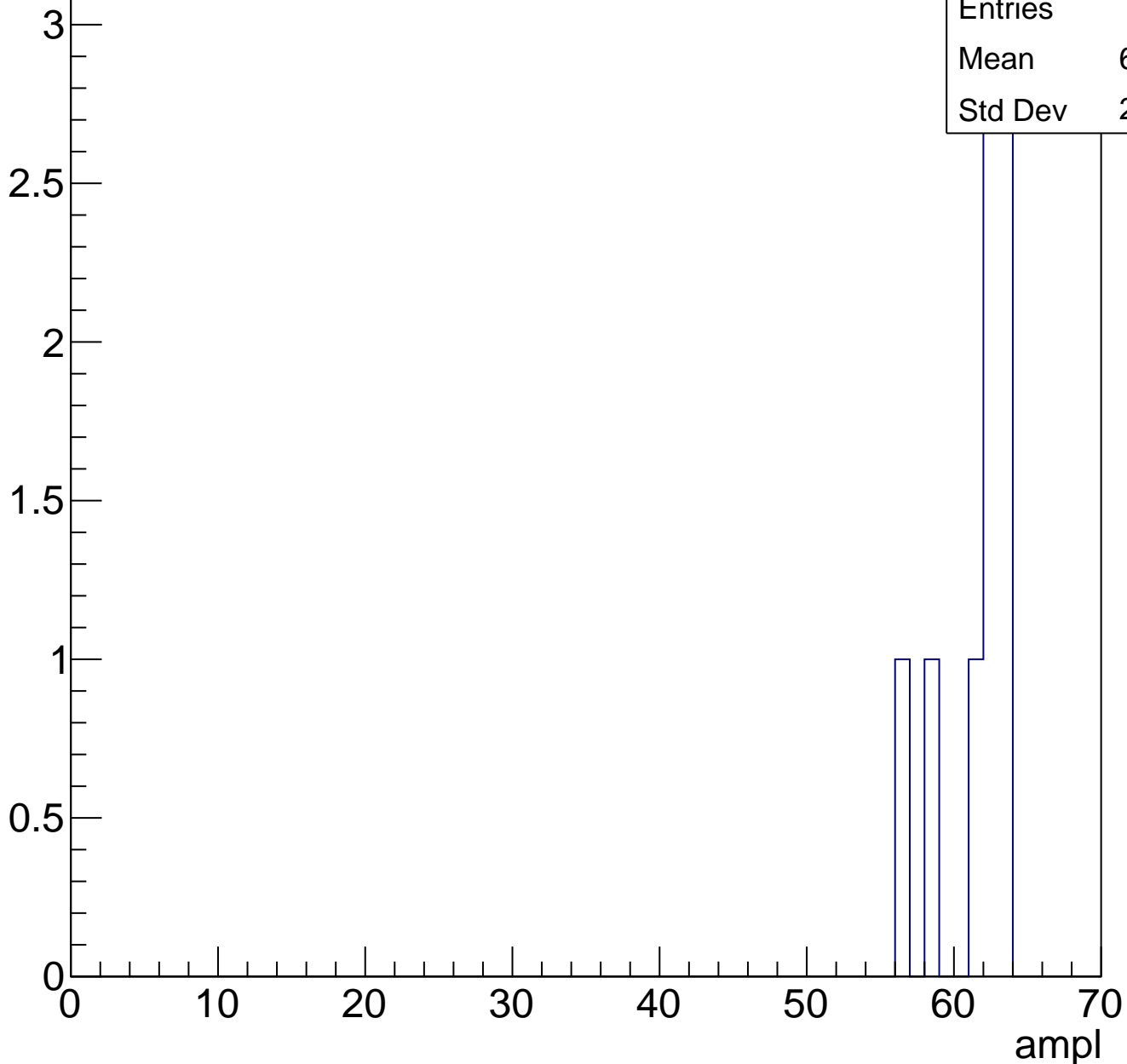


Entries	39
Mean	58.67
Std Dev	9.796

B1L103S, U17-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L103S, U17-ch70, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	29.32
Std Dev	4.675

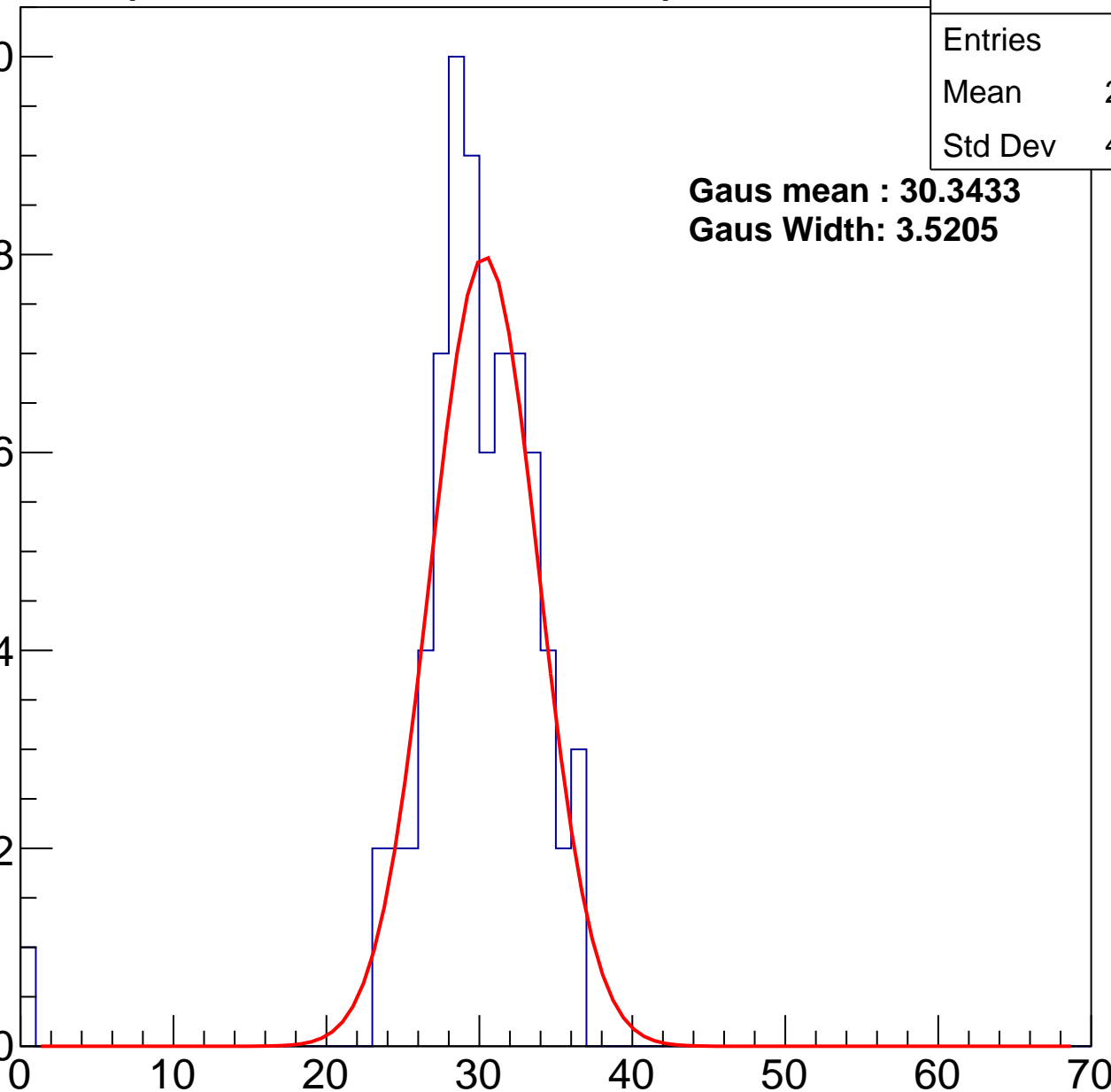
Gaus mean : 30.3433

Gaus Width: 3.5205

Entry

10
8
6
4
2
0

ampl



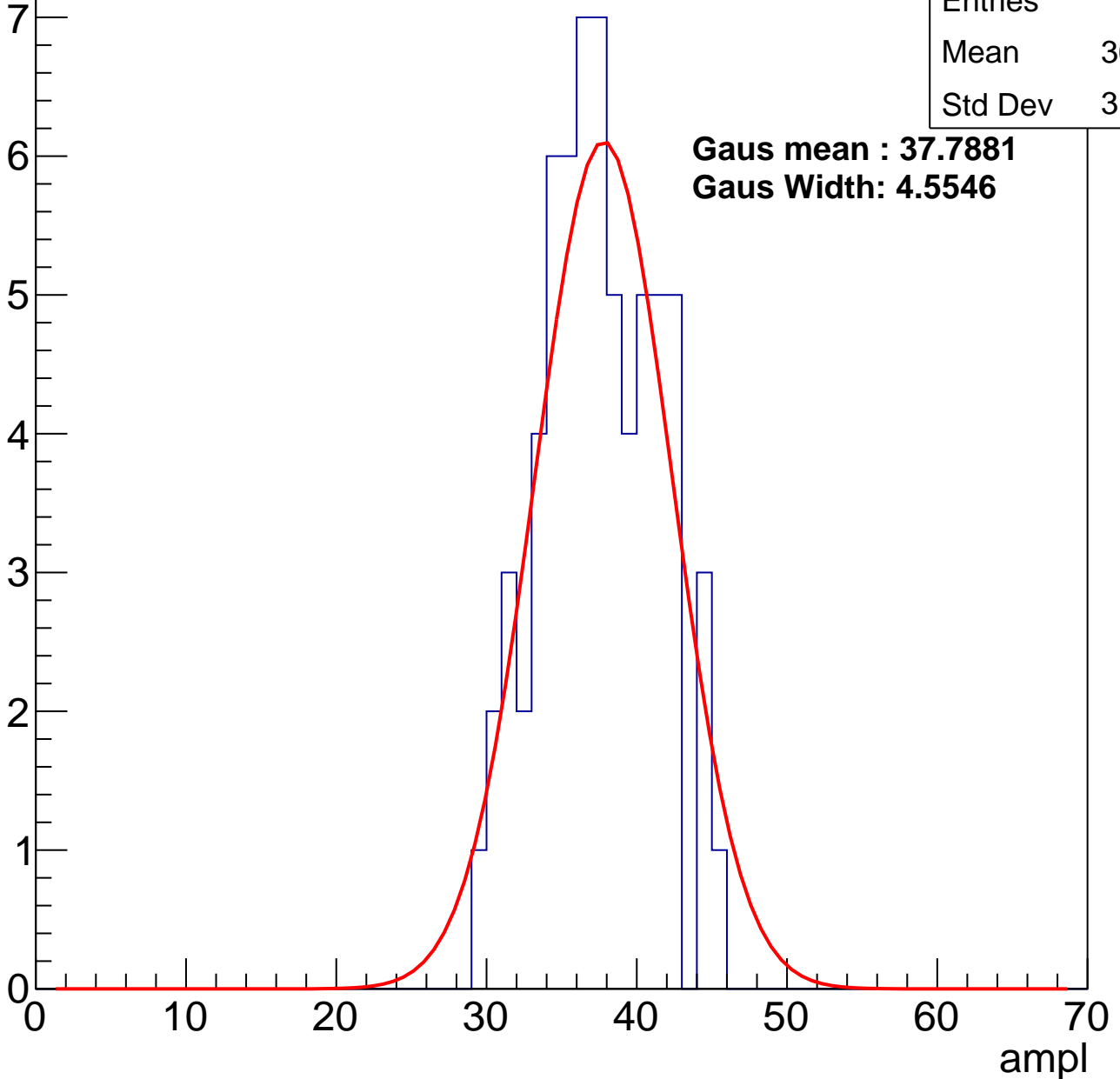
B1L103S, U17-ch70, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.98
Std Dev	3.796

Gaus mean : 37.7881
Gaus Width: 4.5546



B1L103S, U17-ch70, adc2

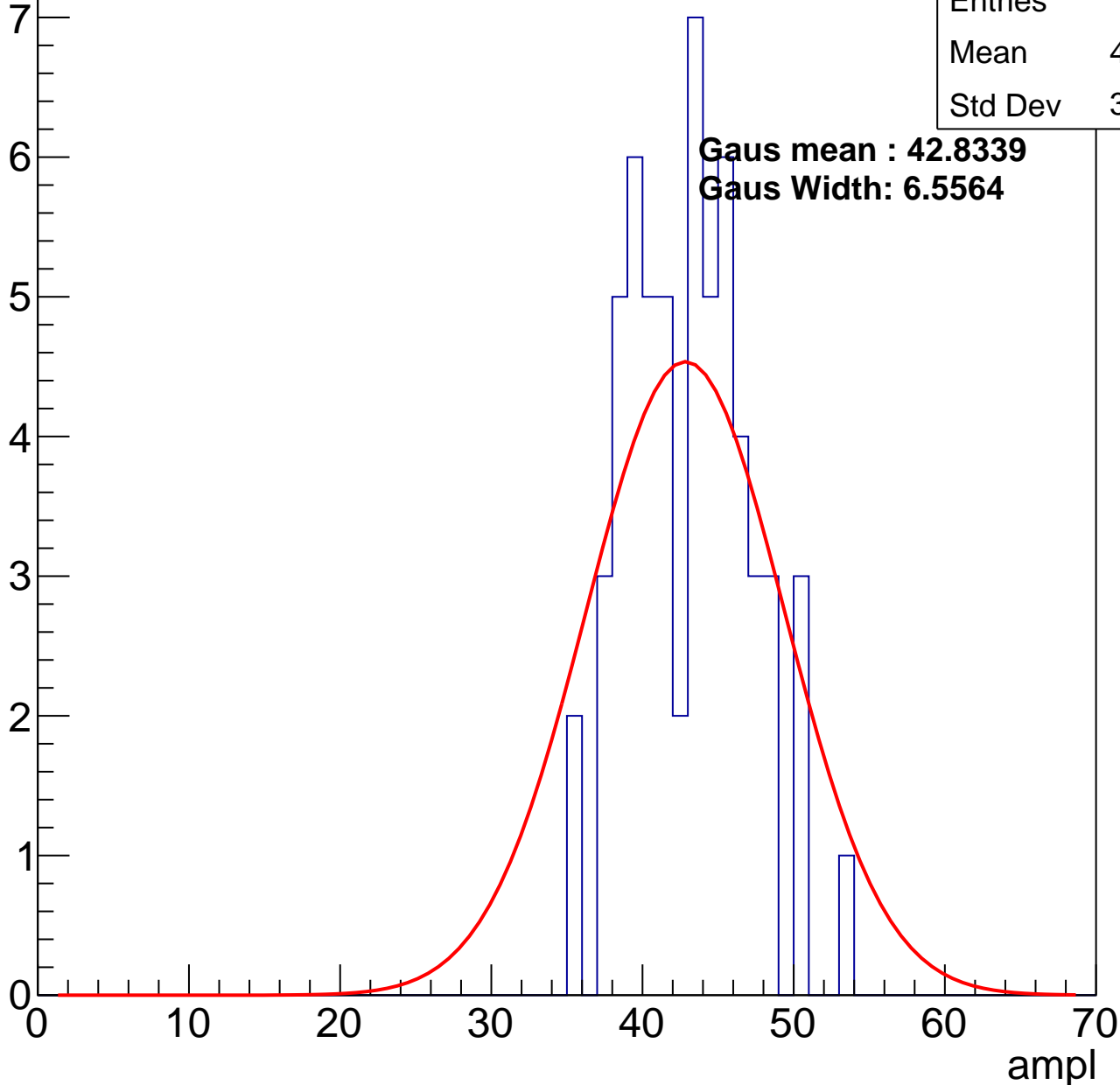
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.62
Std Dev	3.979

Gaus mean : 42.8339

Gaus Width: 6.5564

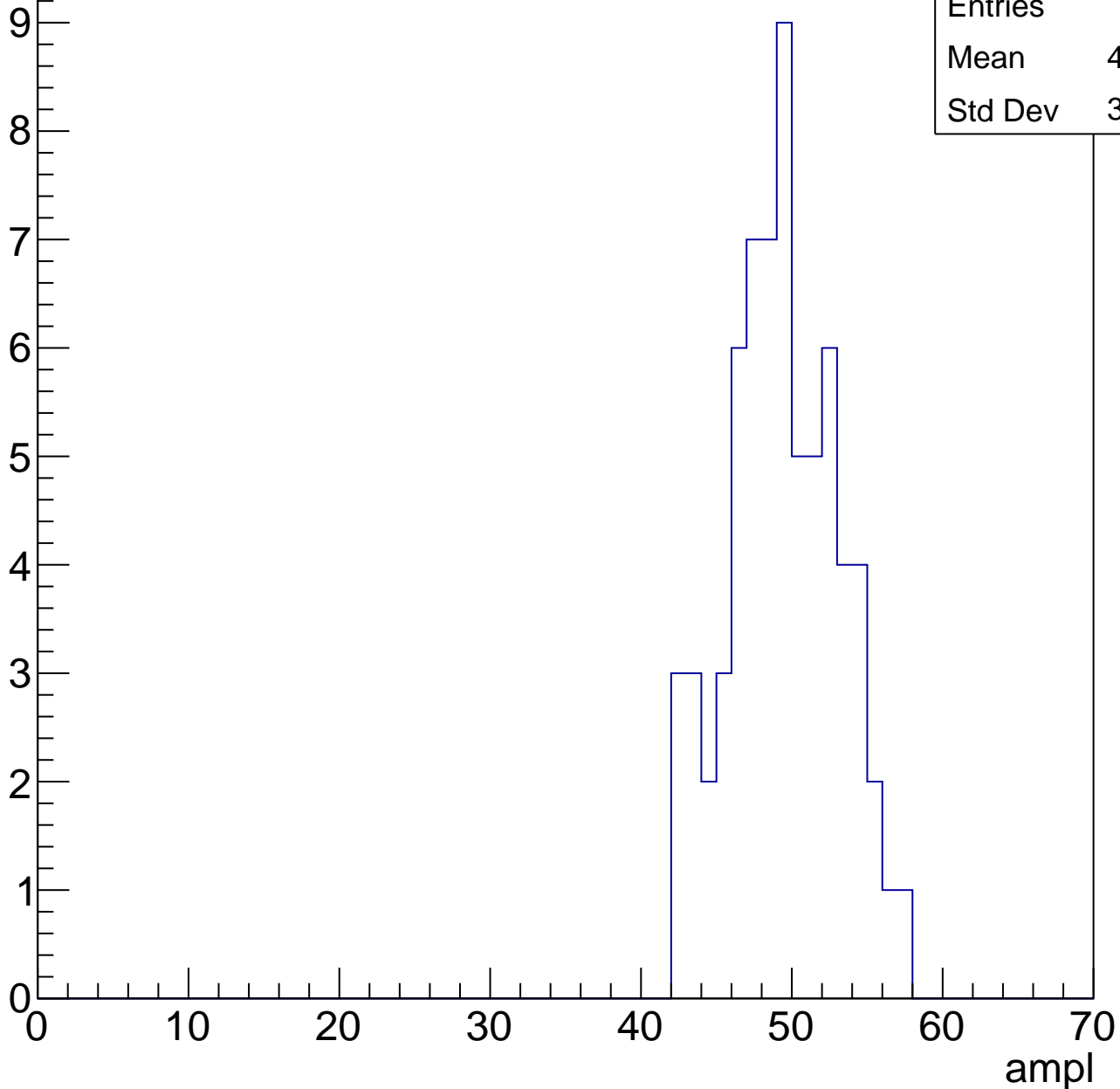


B1L103S, U17-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.94
Std Dev	3.576

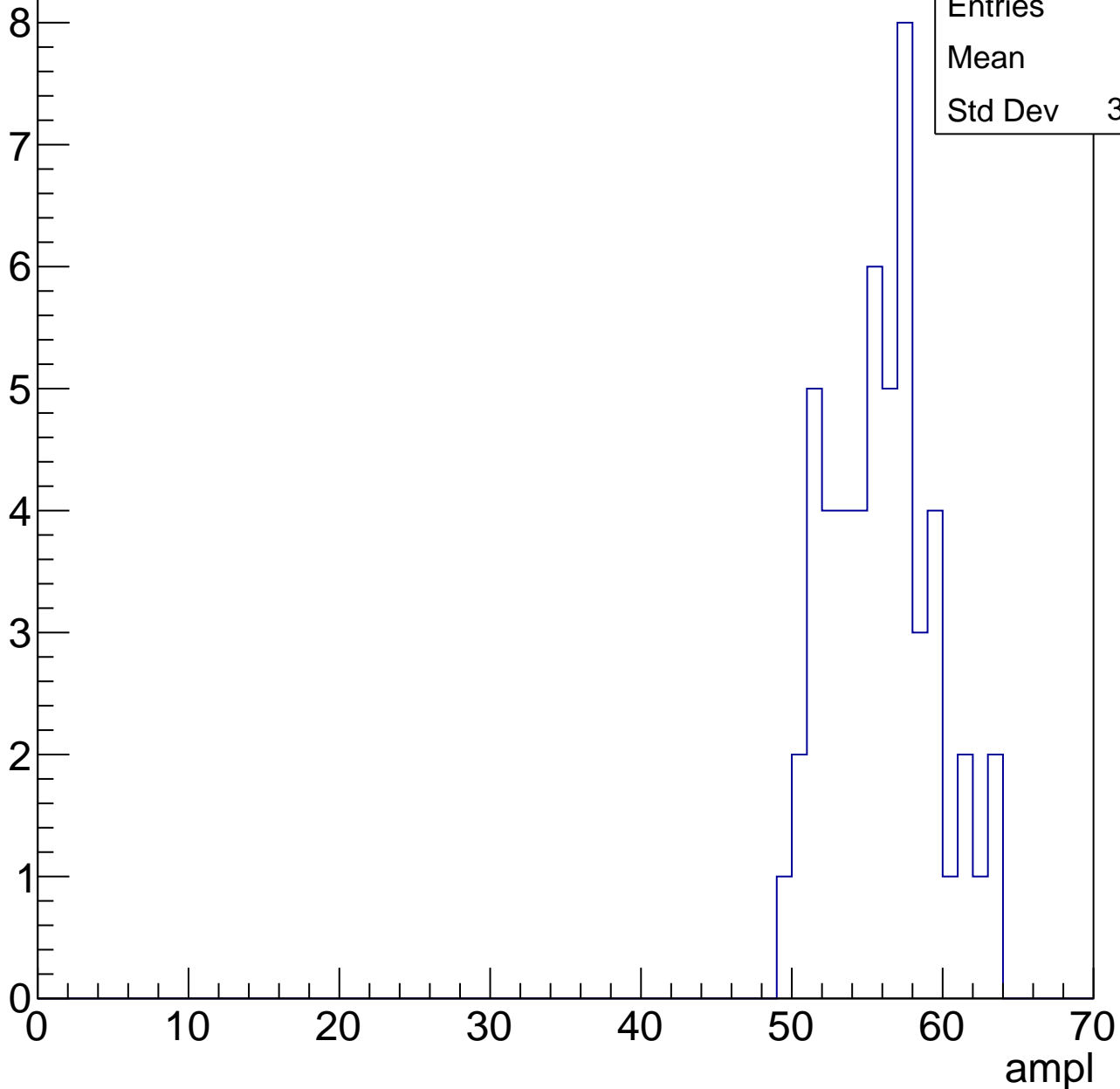


B1L103S, U17-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

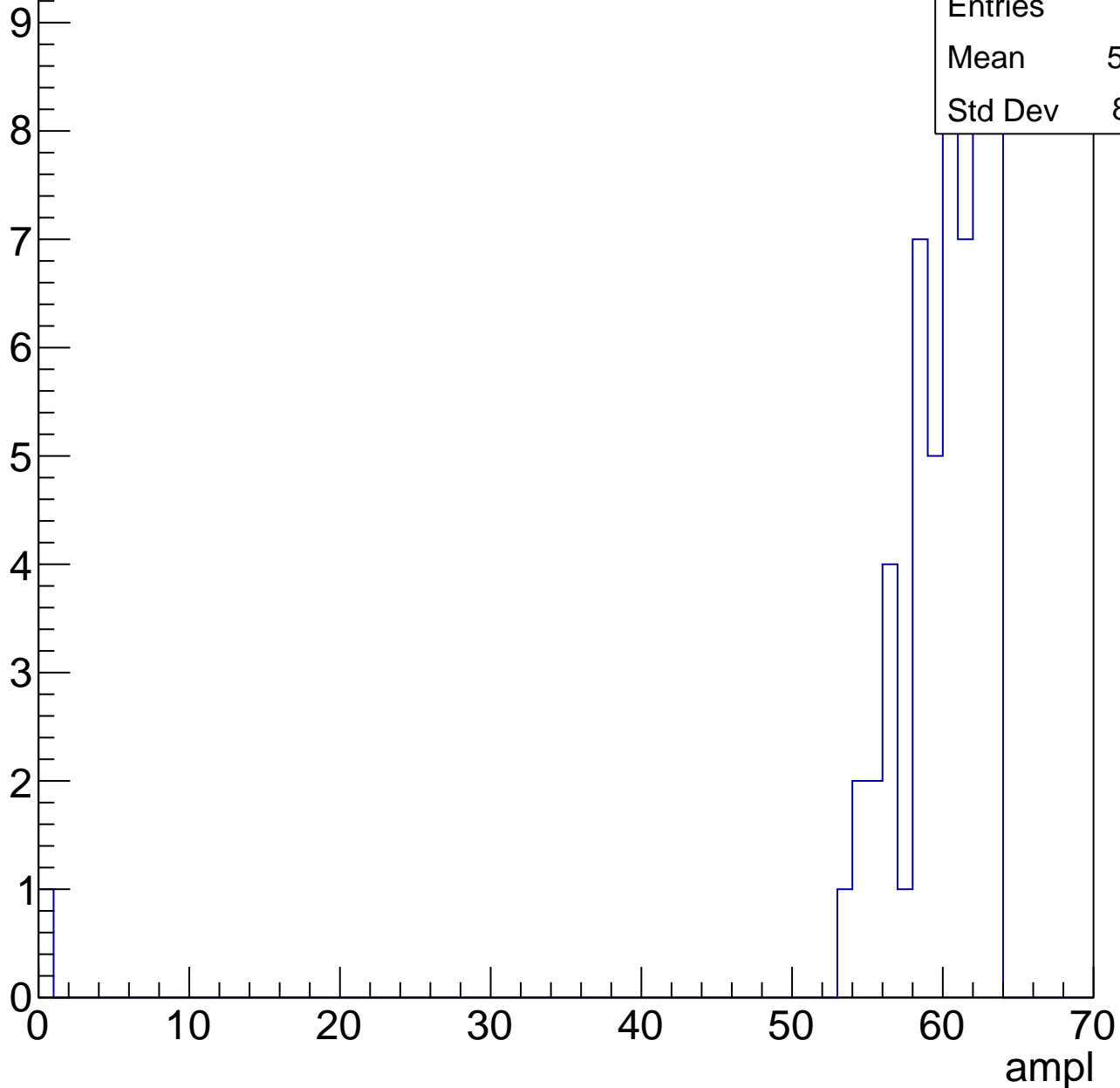
Entries	52
Mean	55.5
Std Dev	3.428



B1L103S, U17-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

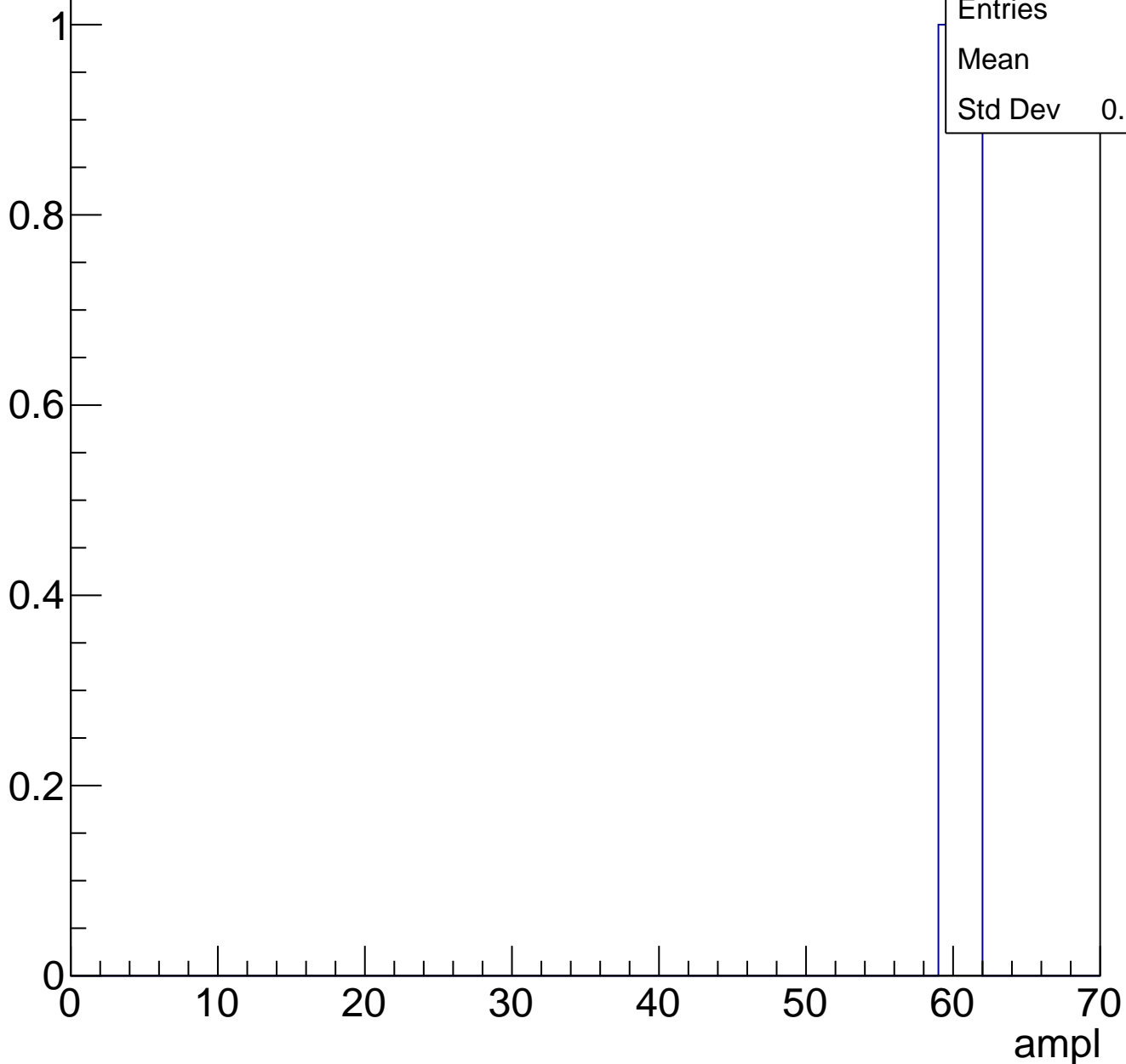
Entry



B1L103S, U17-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch71, adc0

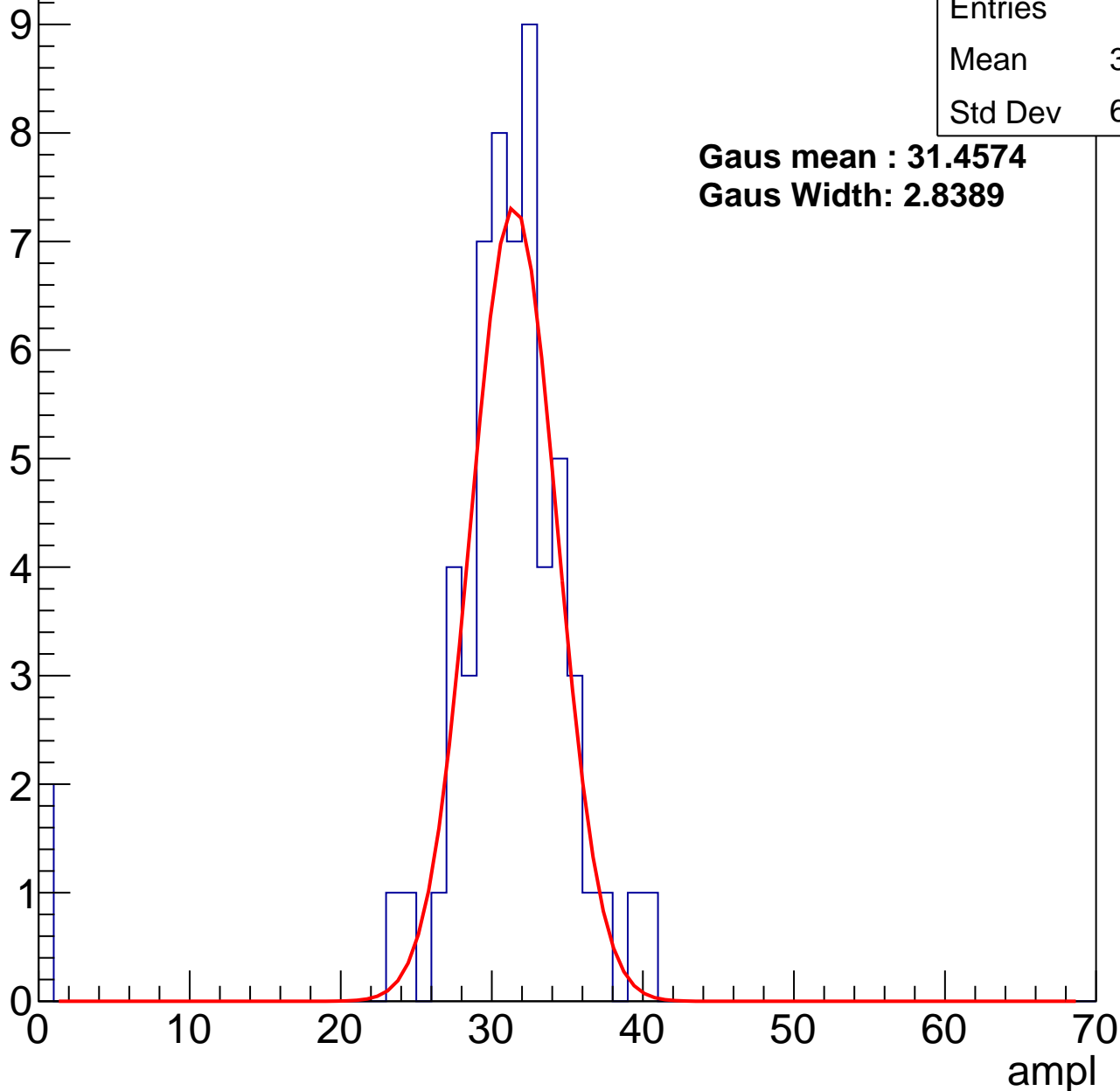
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	30.03
Std Dev	6.452

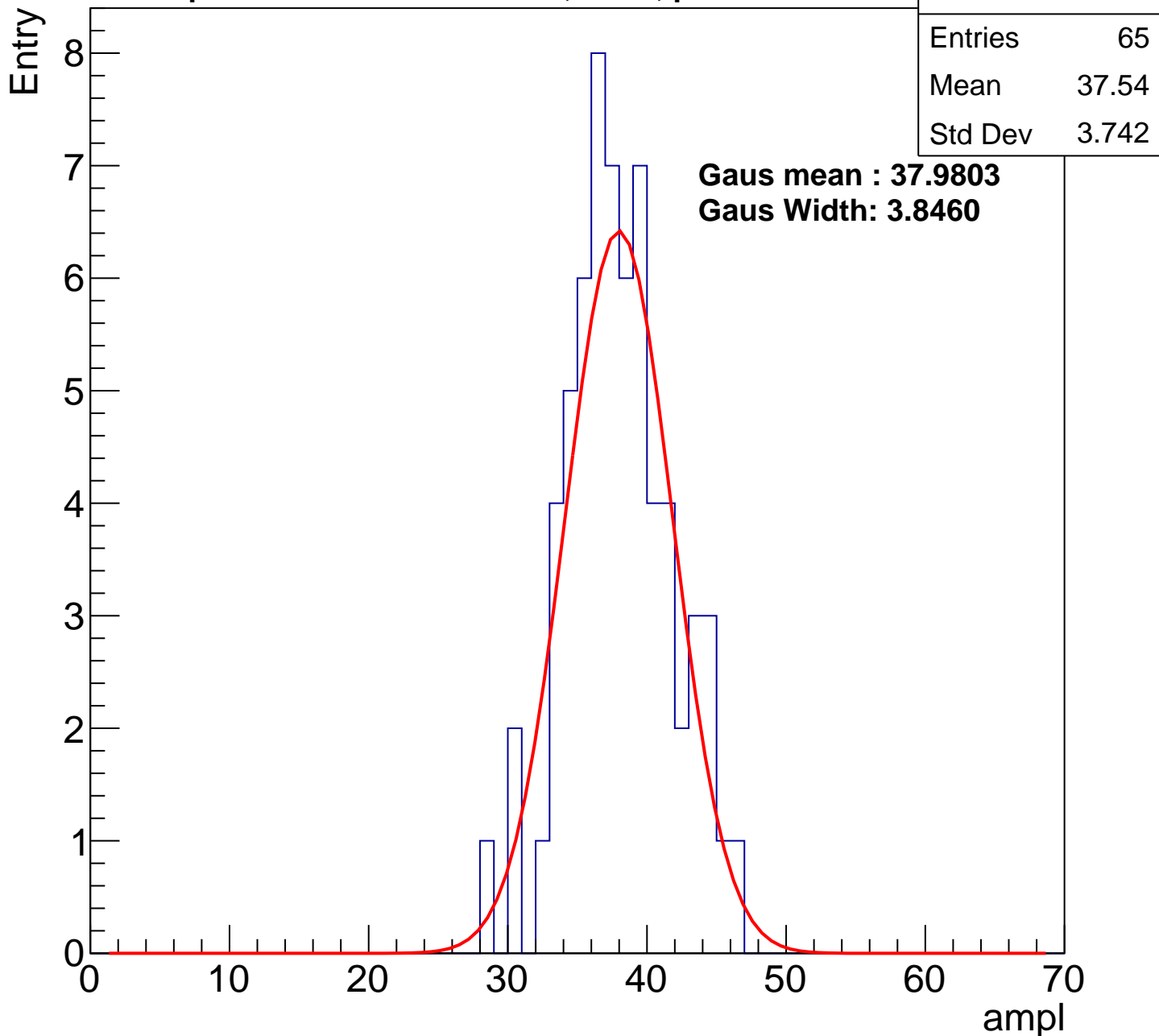
Gaus mean : 31.4574

Gaus Width: 2.8389



B1L103S, U17-ch71, adc1

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch71, adc2

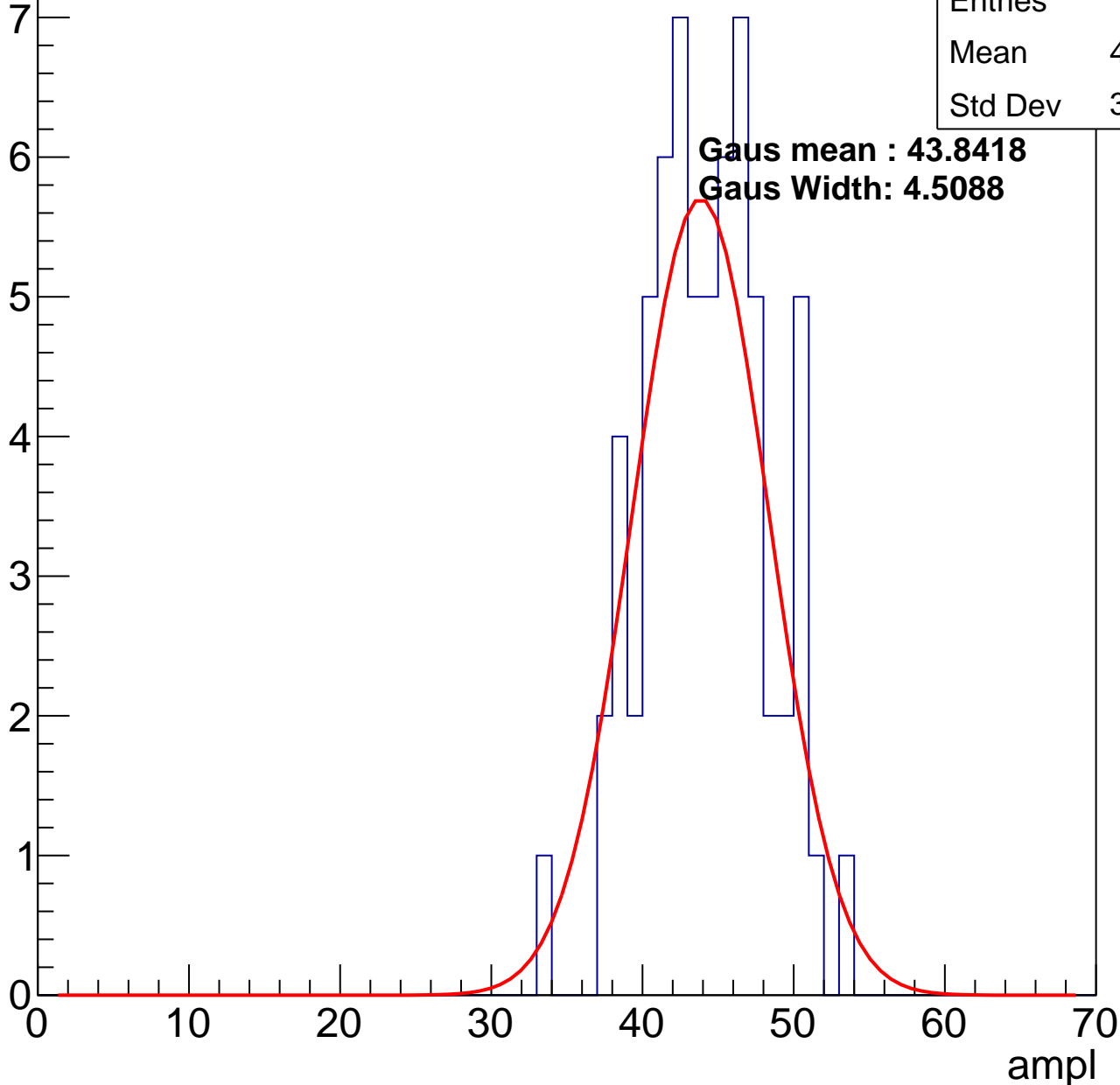
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	43.74
Std Dev	3.982

Gaus mean : 43.8418

Gaus Width: 4.5088

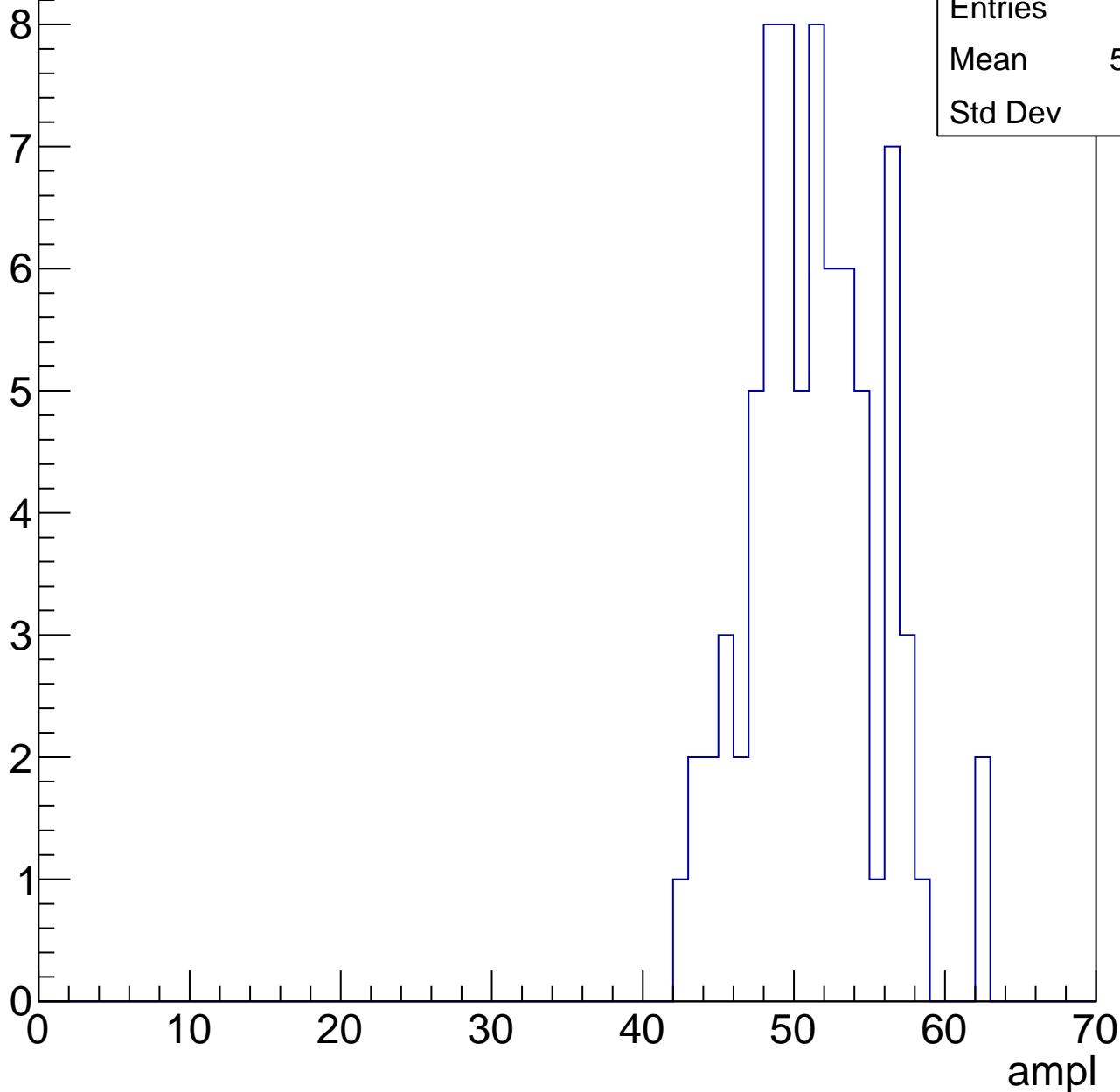


B1L103S, U17-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

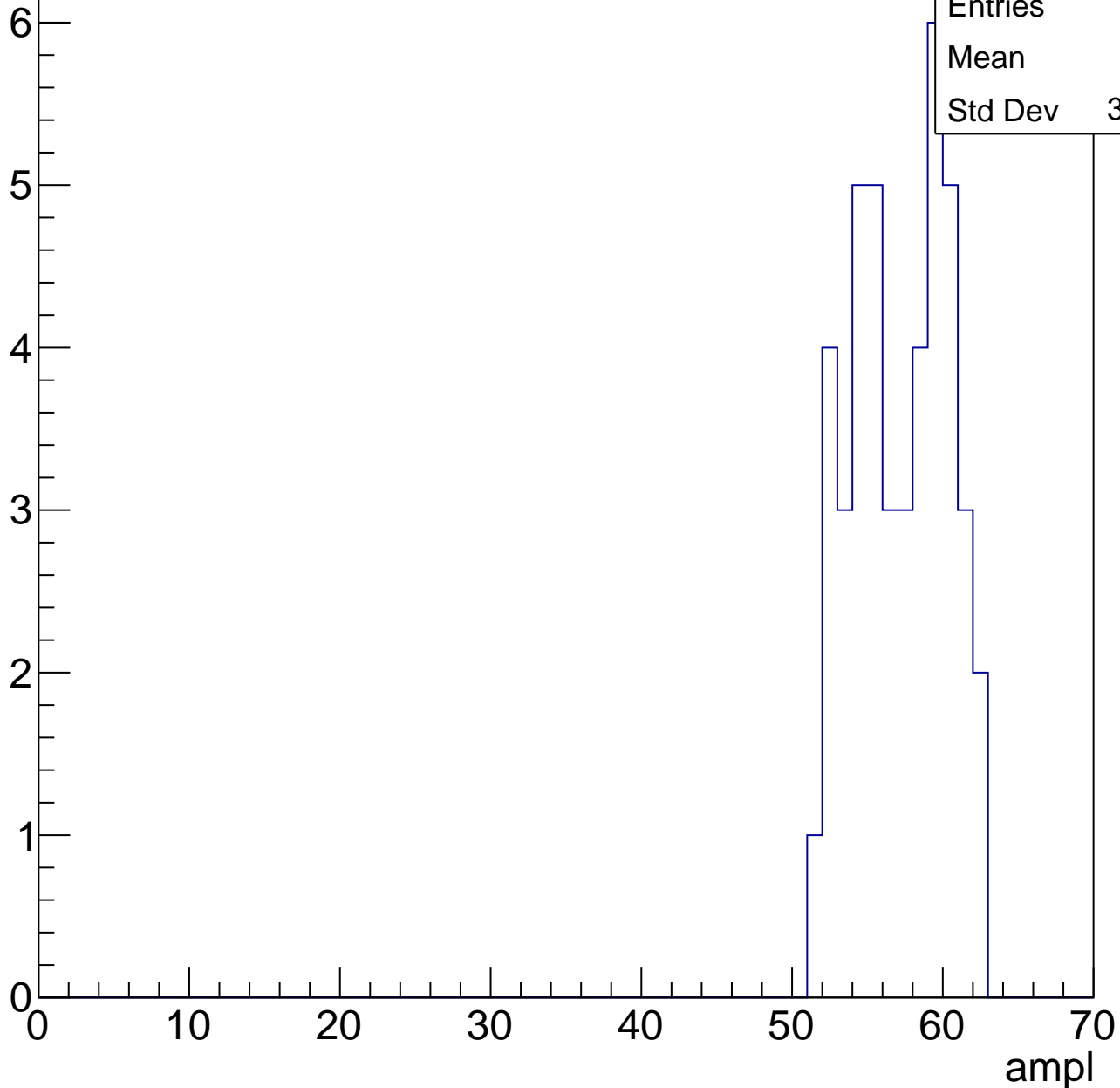
Entries	75
Mean	50.83
Std Dev	4.19



B1L103S, U17-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

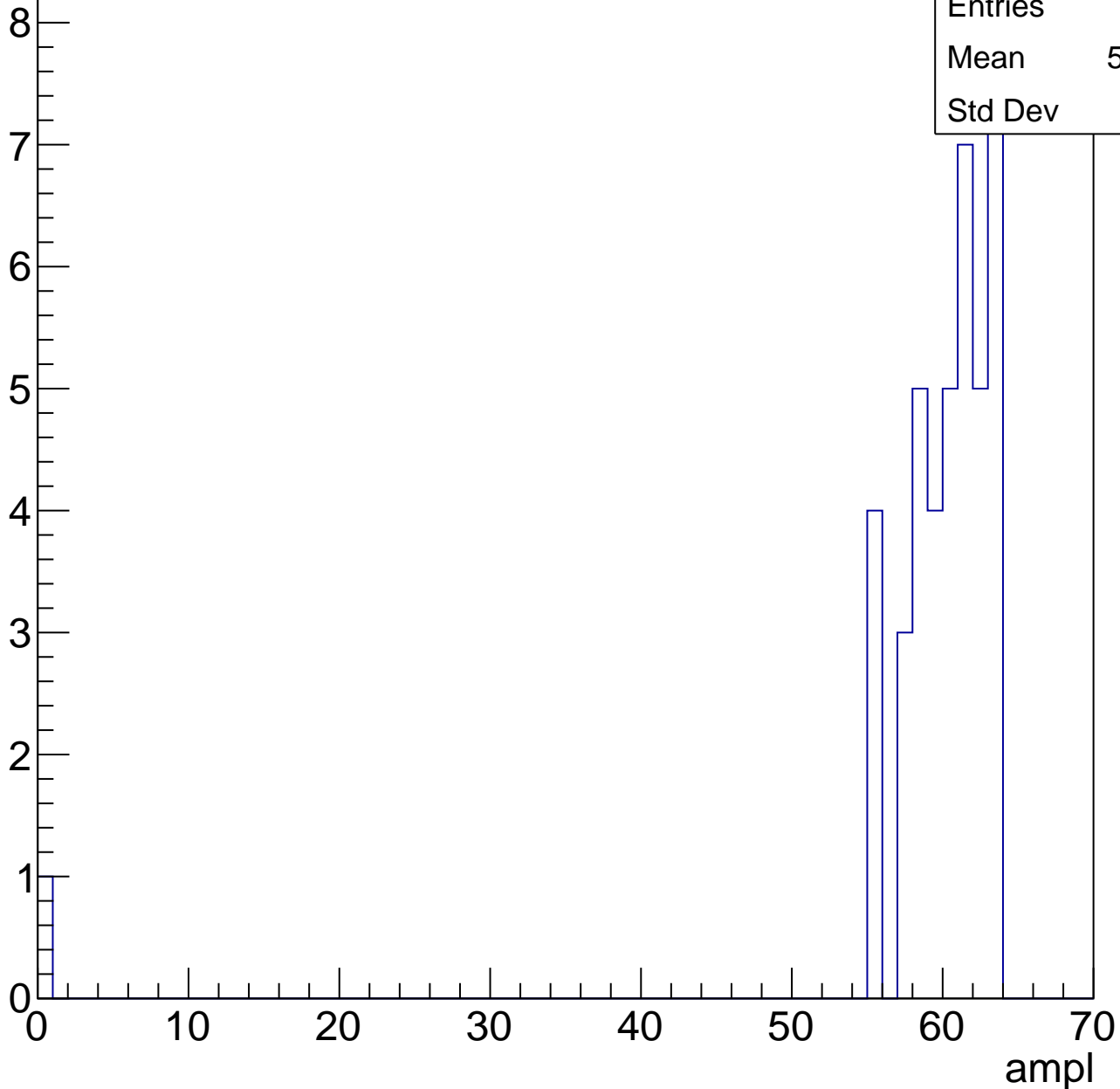


B1L103S, U17-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

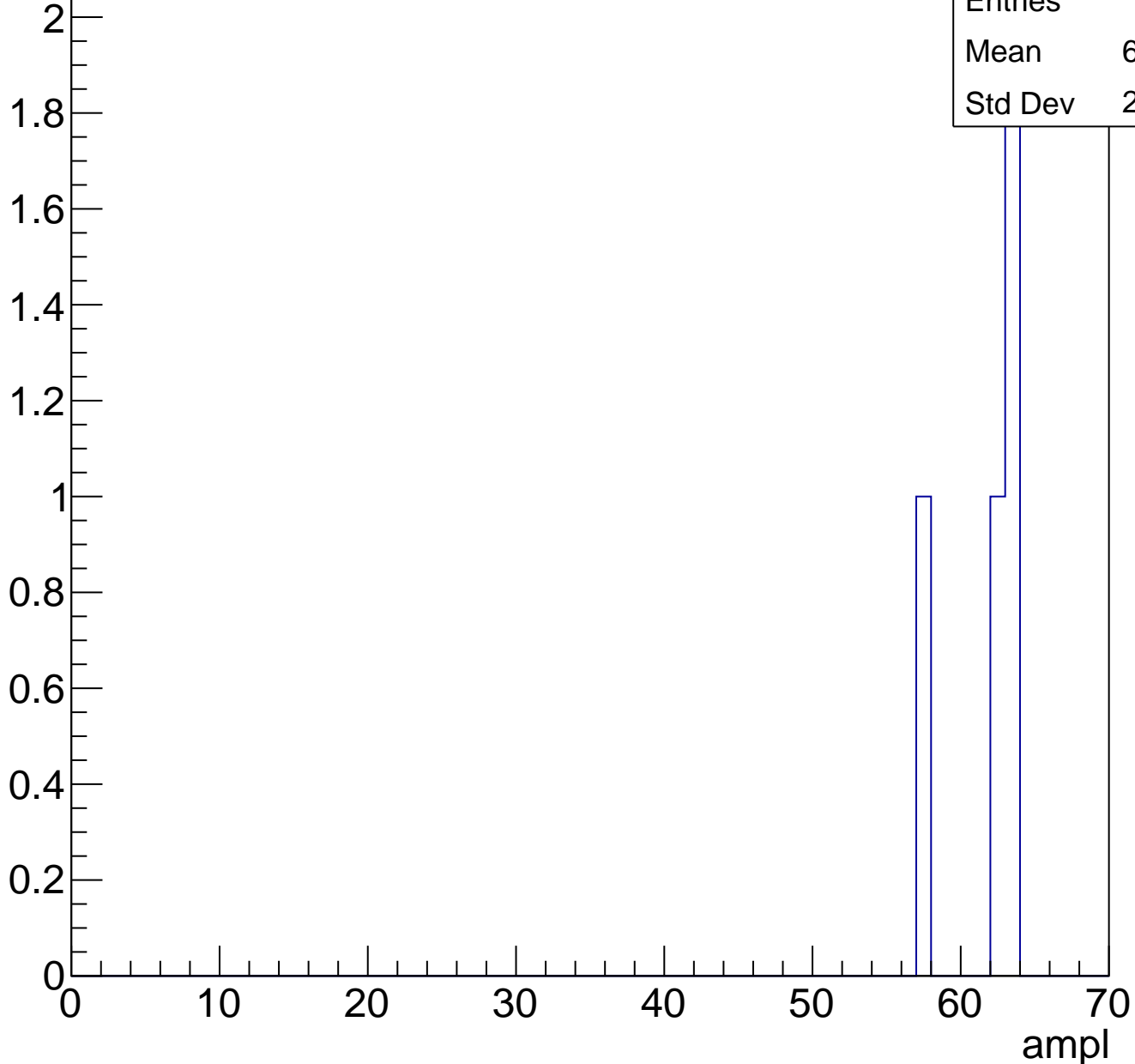
Entries	42
Mean	58.52
Std Dev	9.46



B1L103S, U17-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch72, adc0

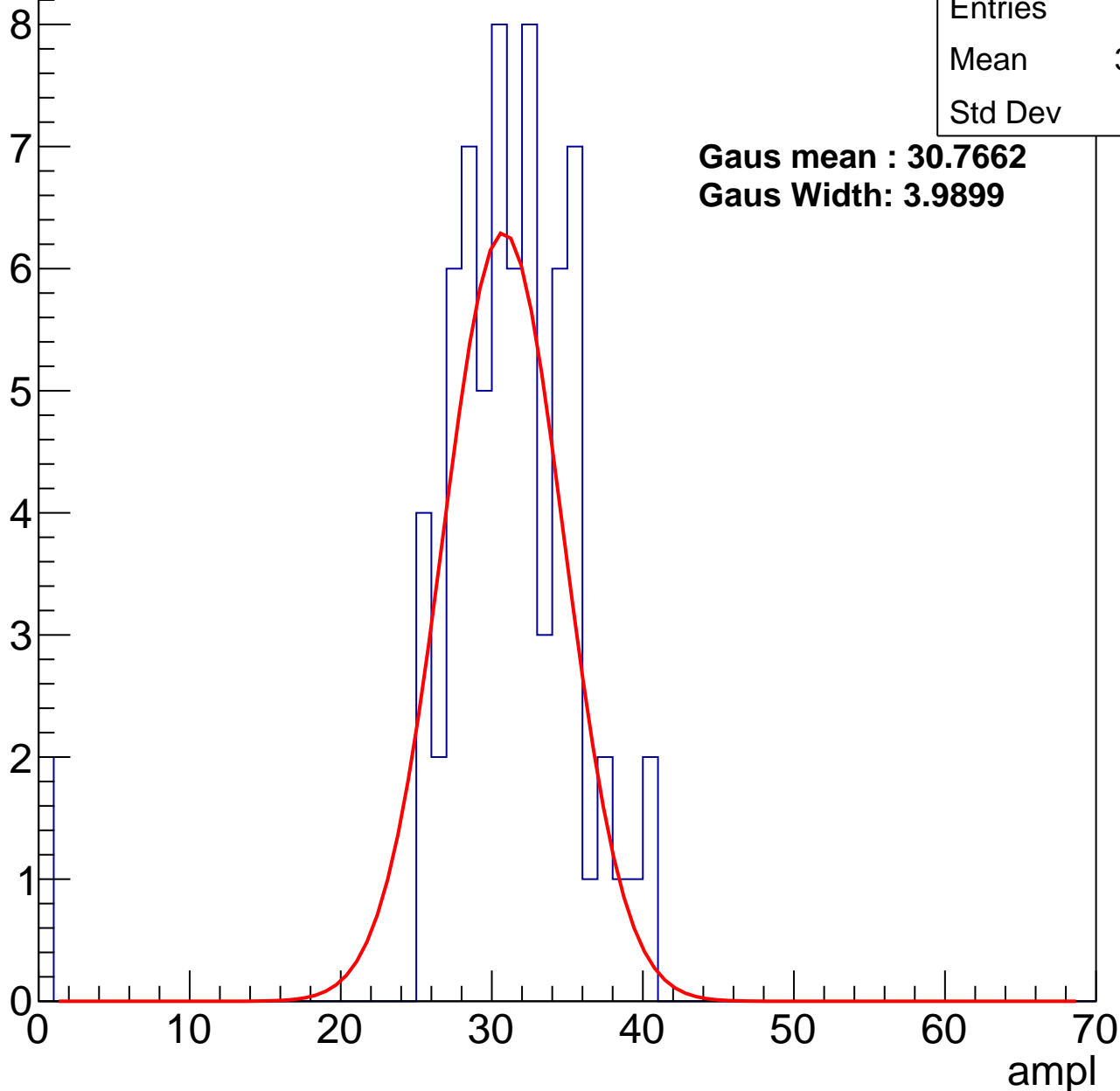
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.31
Std Dev	6.31

Gaus mean : 30.7662

Gaus Width: 3.9899



B1L103S, U17-ch72, adc1

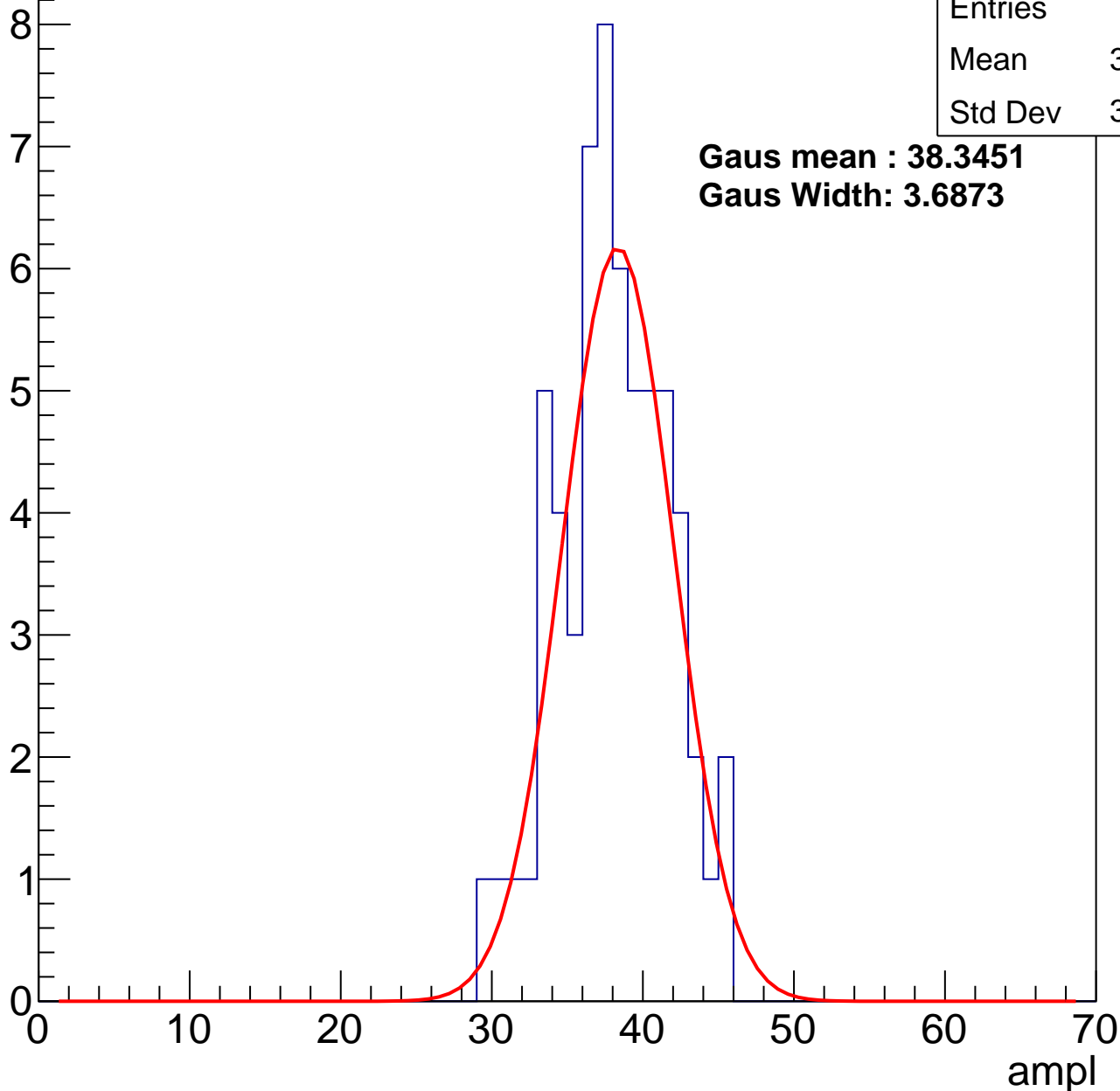
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.57
Std Dev	3.596

Gaus mean : 38.3451

Gaus Width: 3.6873



B1L103S, U17-ch72, adc2

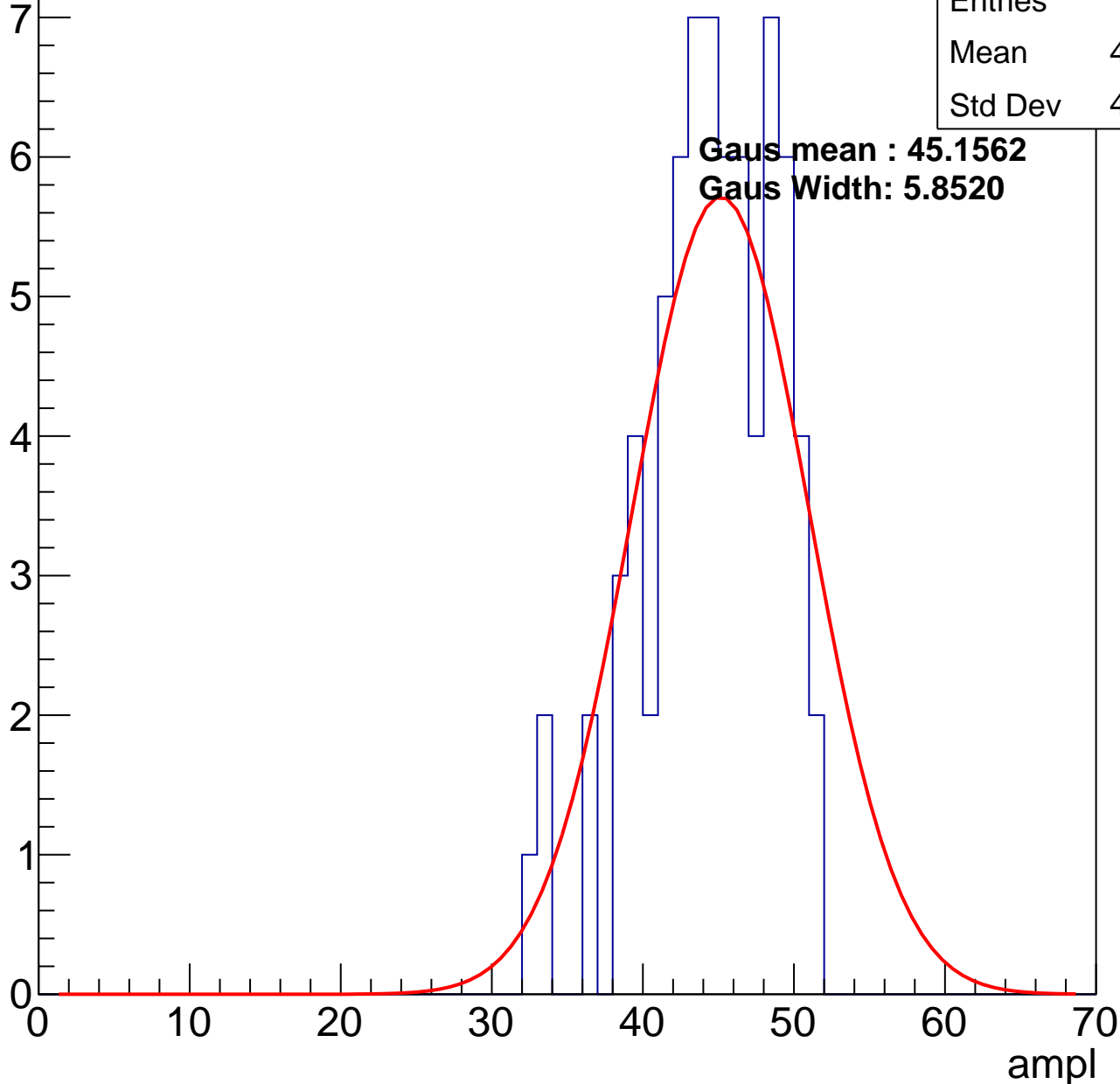
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	43.95
Std Dev	4.349

Gaus mean : 45.1562

Gaus Width: 5.8520

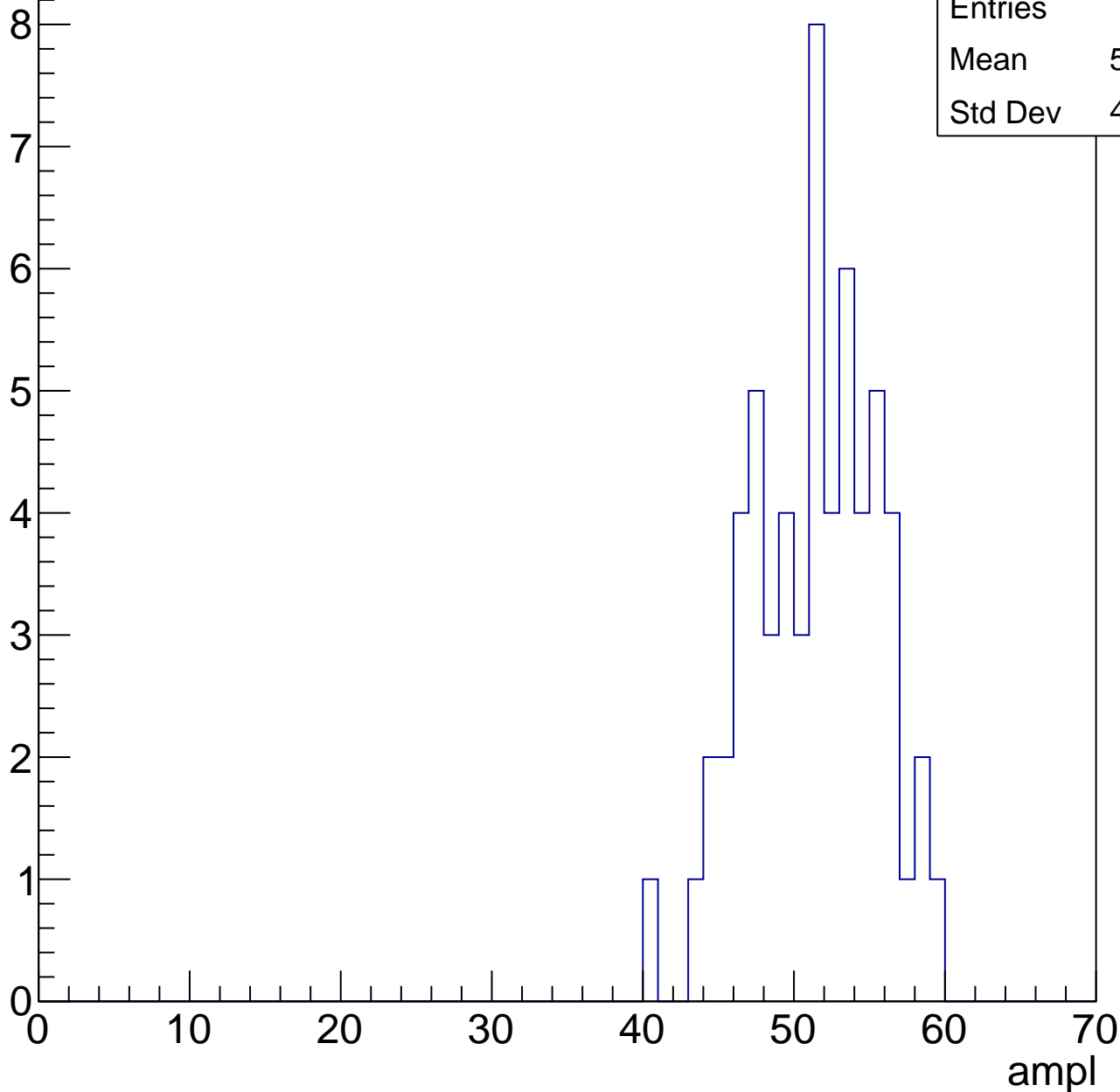


B1L103S, U17-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	50.85
Std Dev	4.126

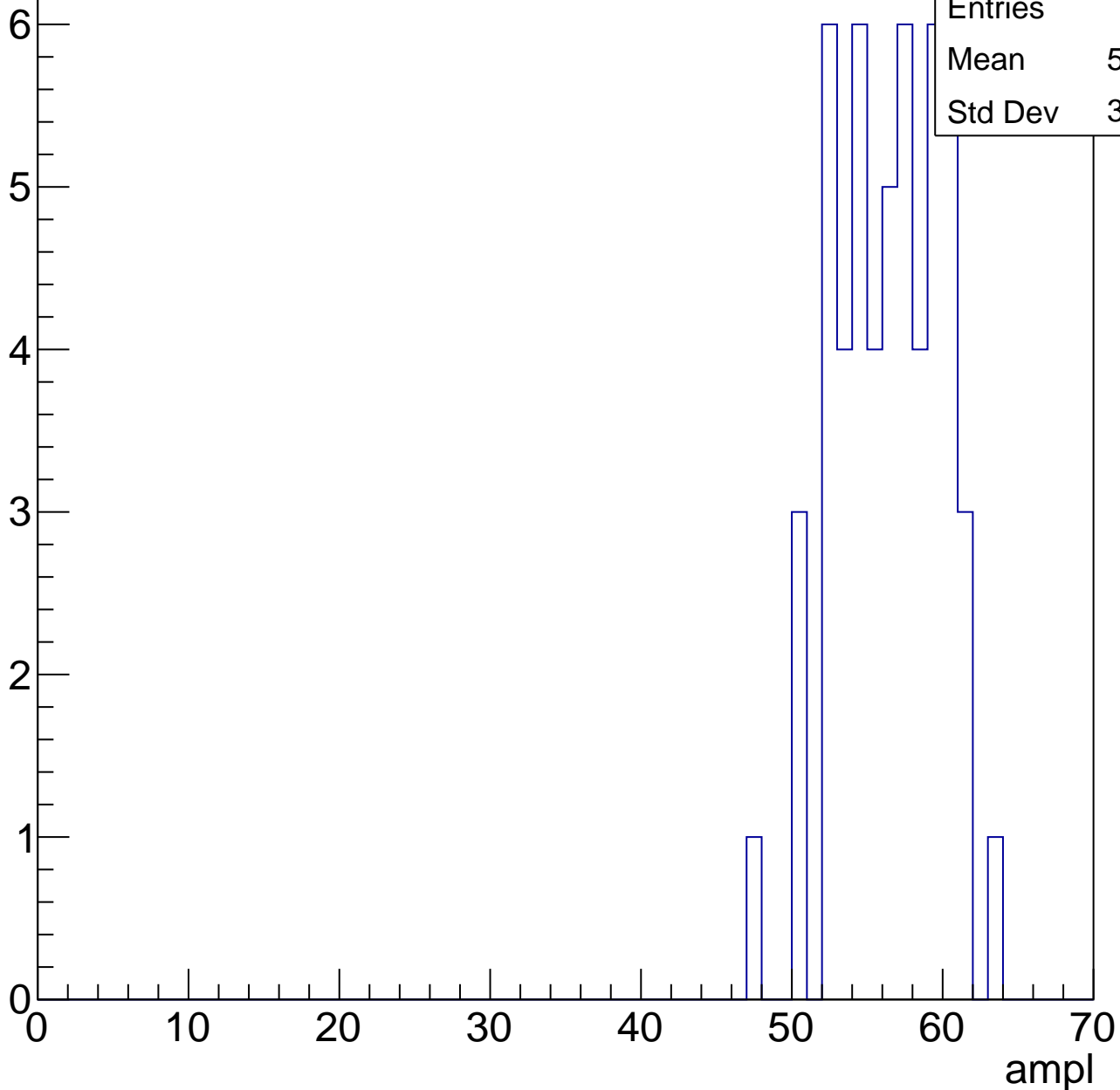


B1L103S, U17-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.98
Std Dev	3.424

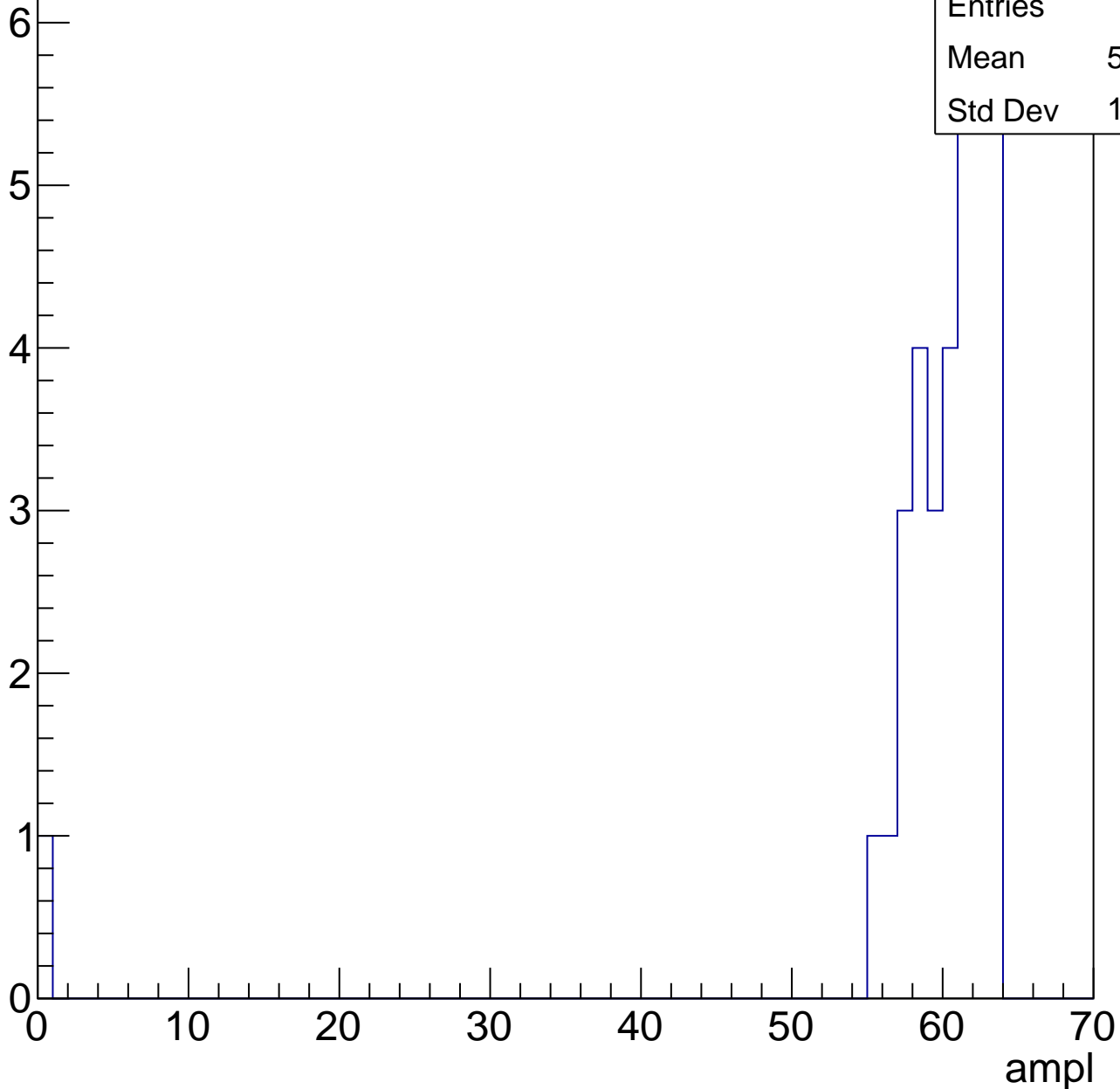


B1L103S, U17-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

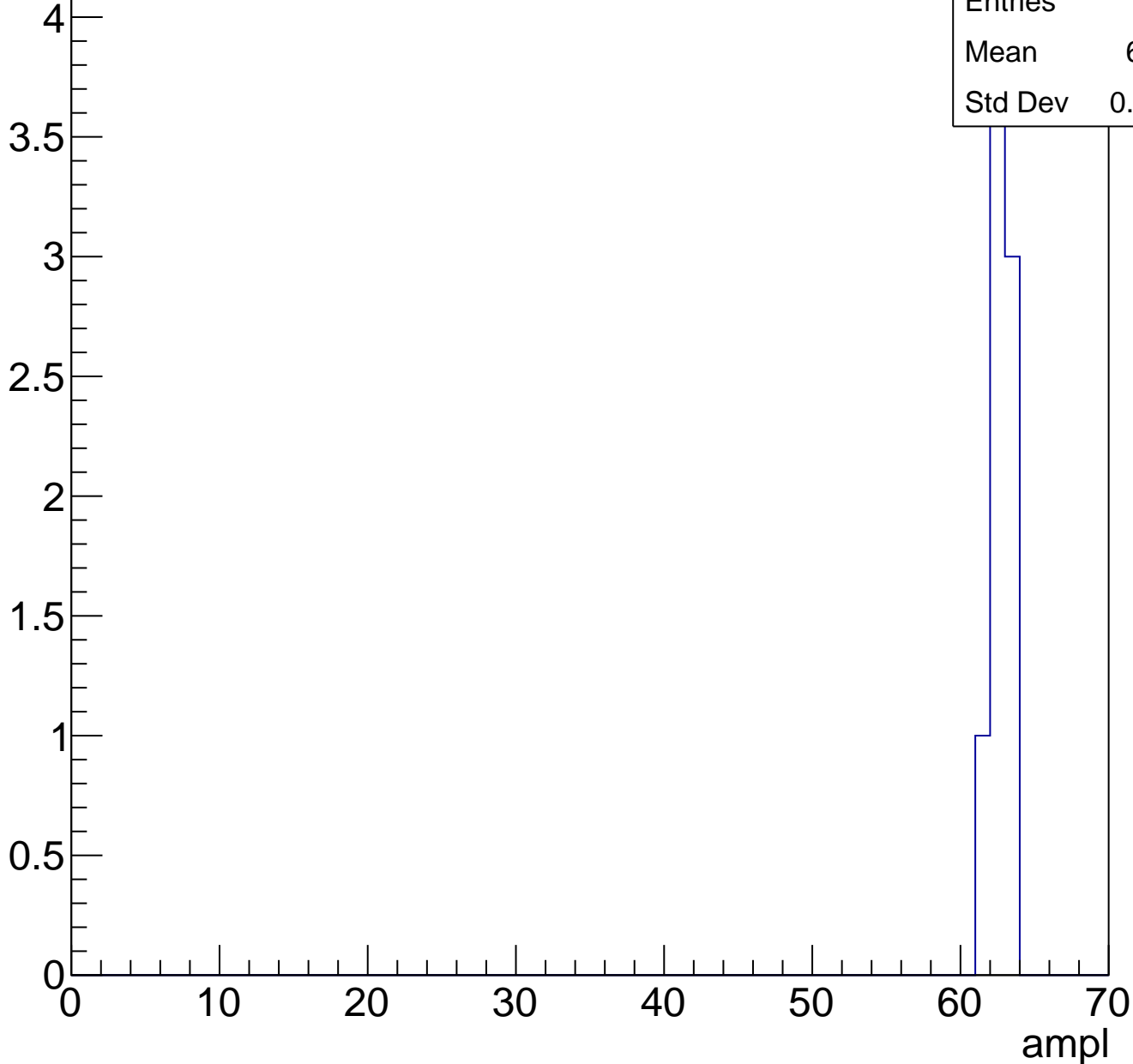
Entries	35
Mean	58.49
Std Dev	10.27



B1L103S, U17-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch73, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	28.11
Std Dev	7.483

Gaus mean : 30.1432

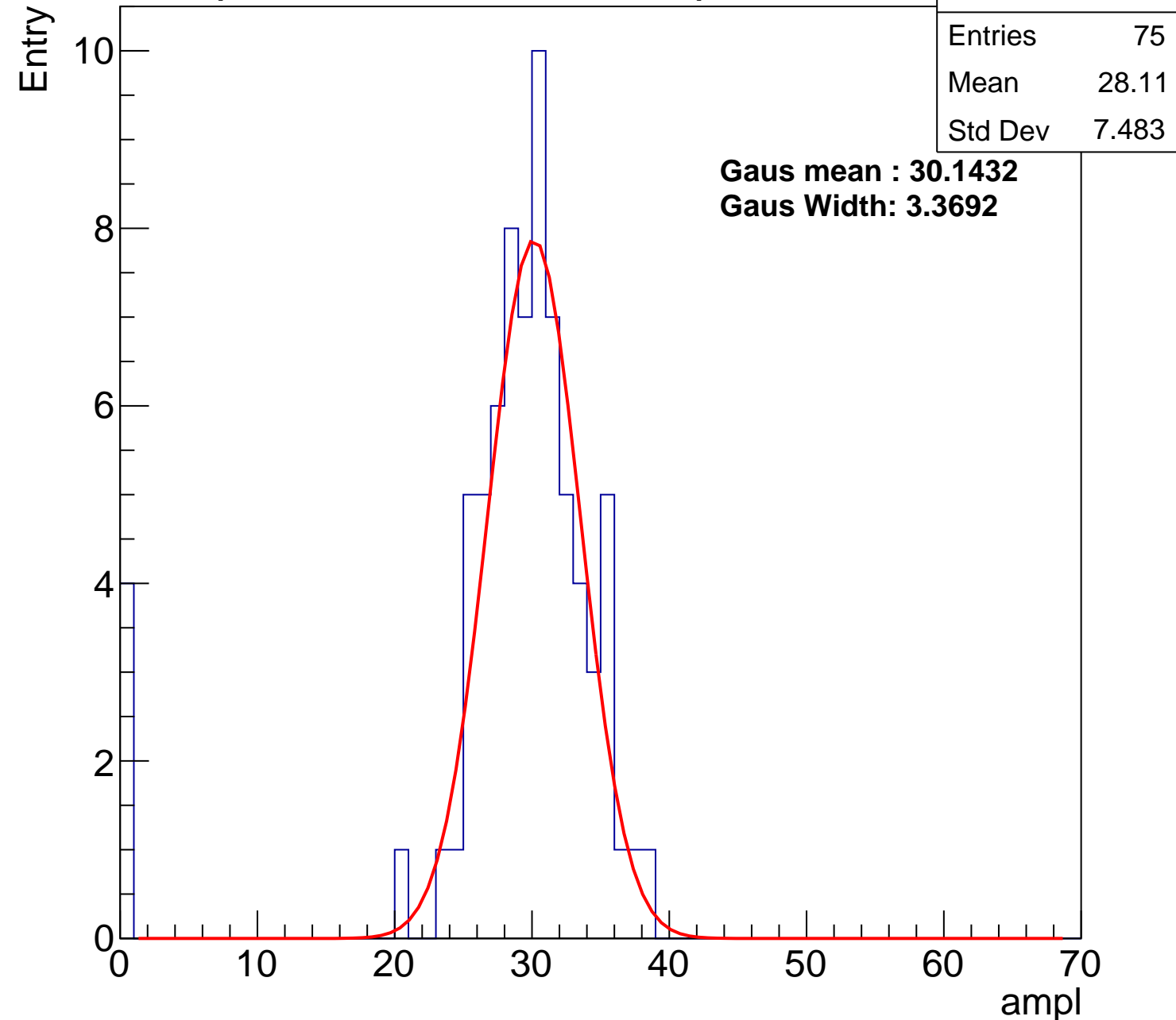
Gaus Width: 3.3692

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch73, adc1

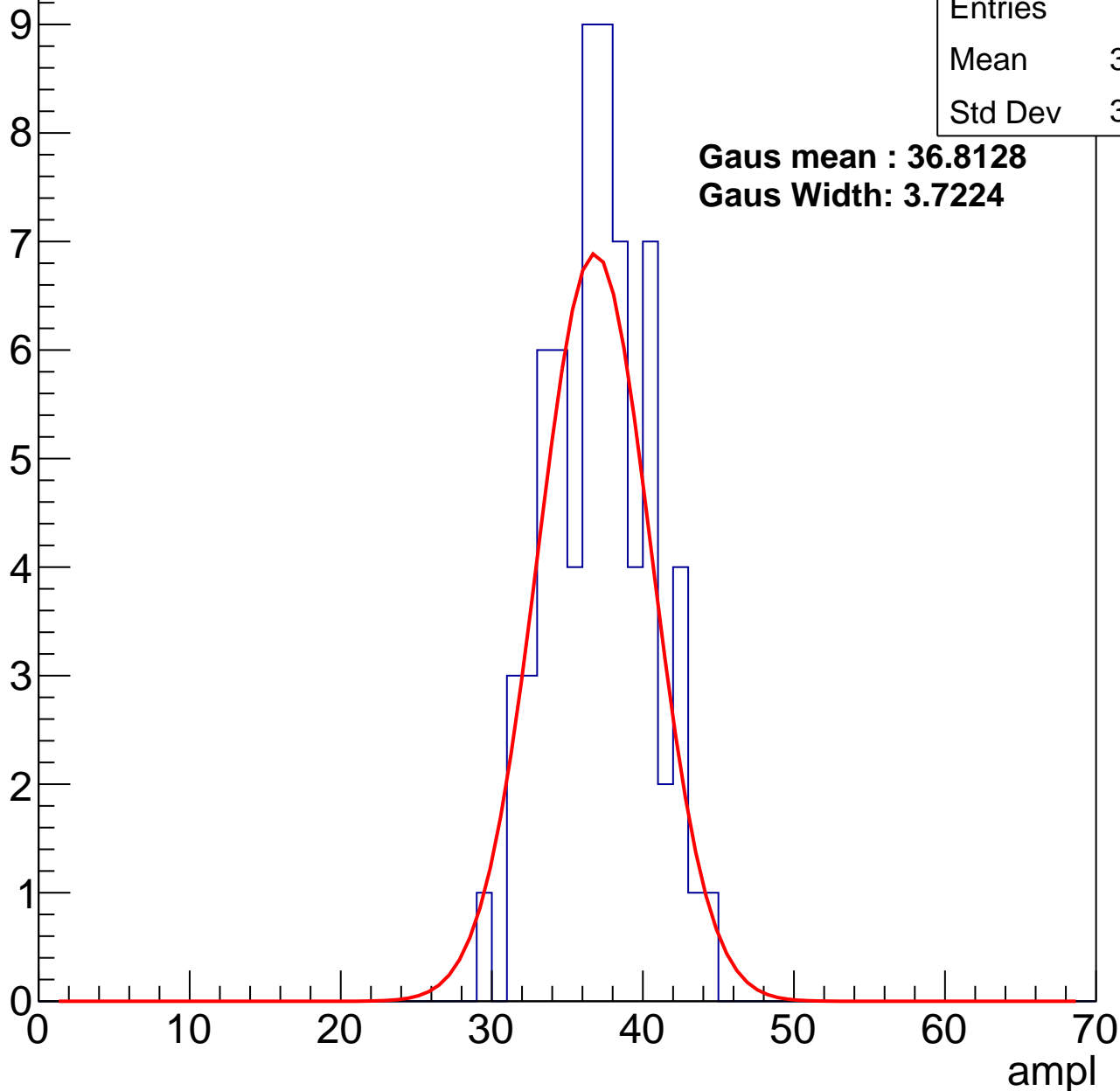
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.66
Std Dev	3.272

Gaus mean : 36.8128

Gaus Width: 3.7224



B1L103S, U17-ch73, adc2

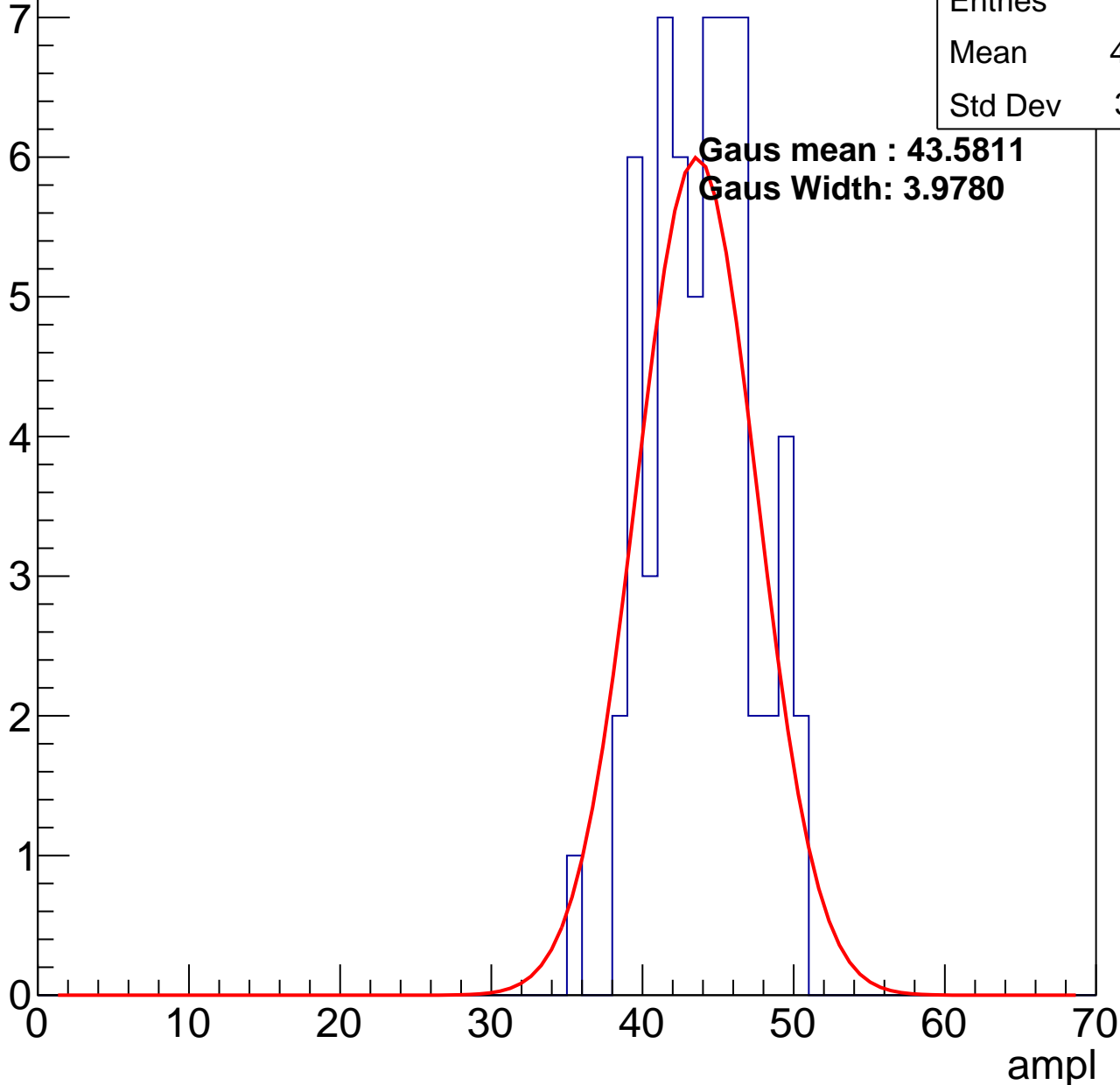
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	43.44
Std Dev	3.341

Gaus mean : 43.5811

Gaus Width: 3.9780

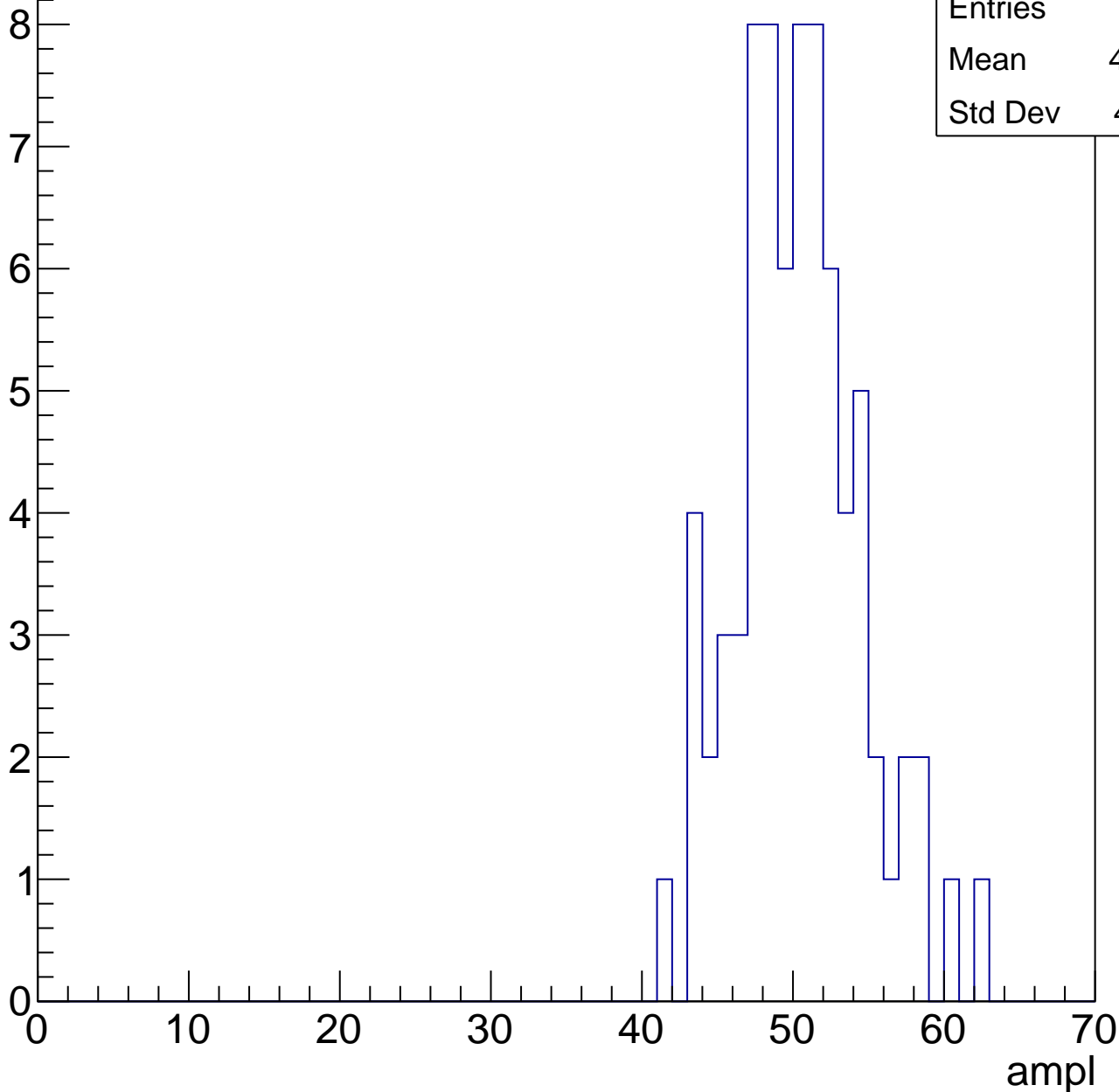


B1L103S, U17-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	49.97
Std Dev	4.141

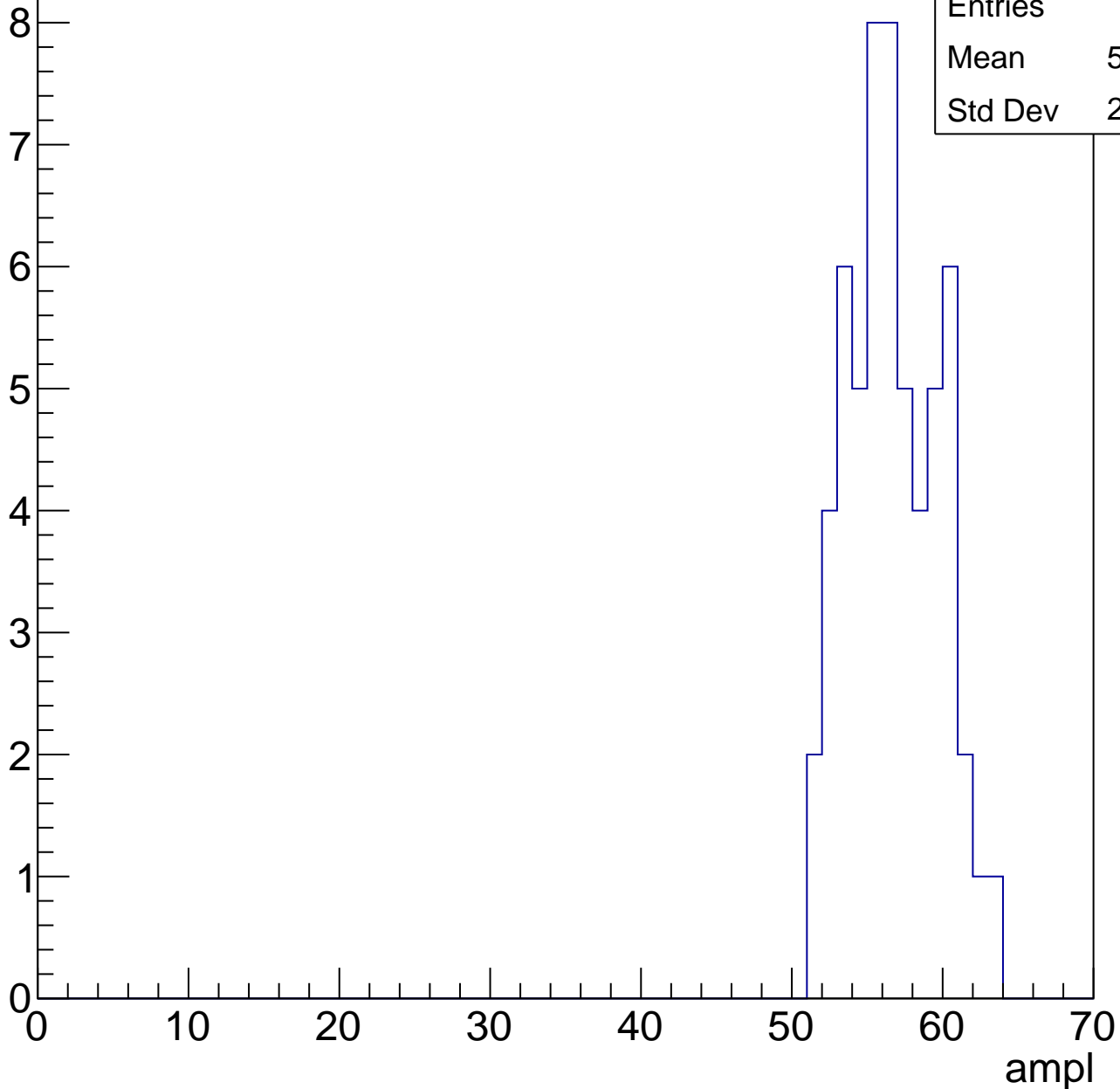


B1L103S, U17-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.23
Std Dev	2.932

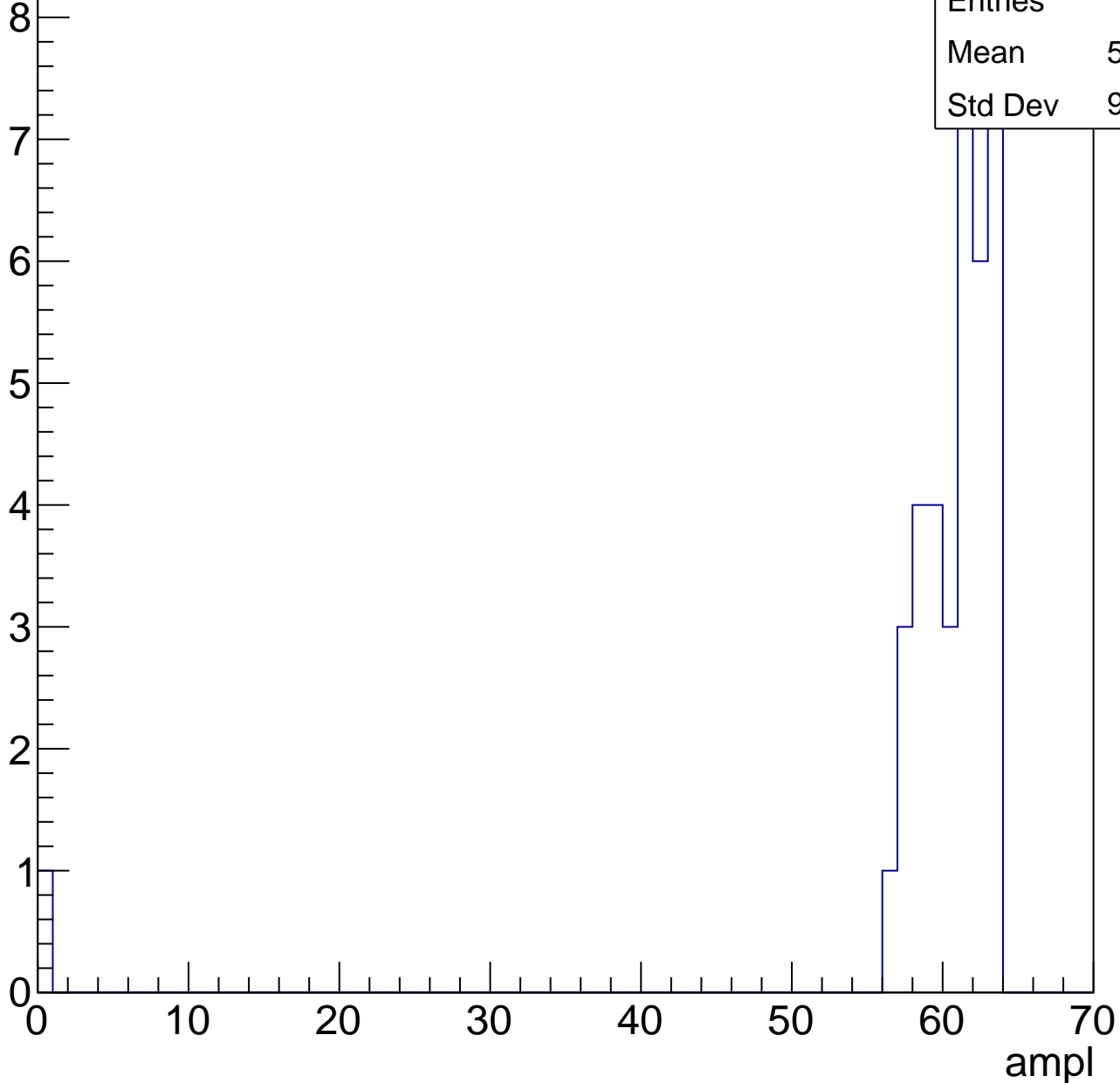


B1L103S, U17-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.92
Std Dev	9.898



B1L103S, U17-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	1.5

ampl

0 10 20 30 40 50 60 70

0

B1L103S, U17-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch74, adc0

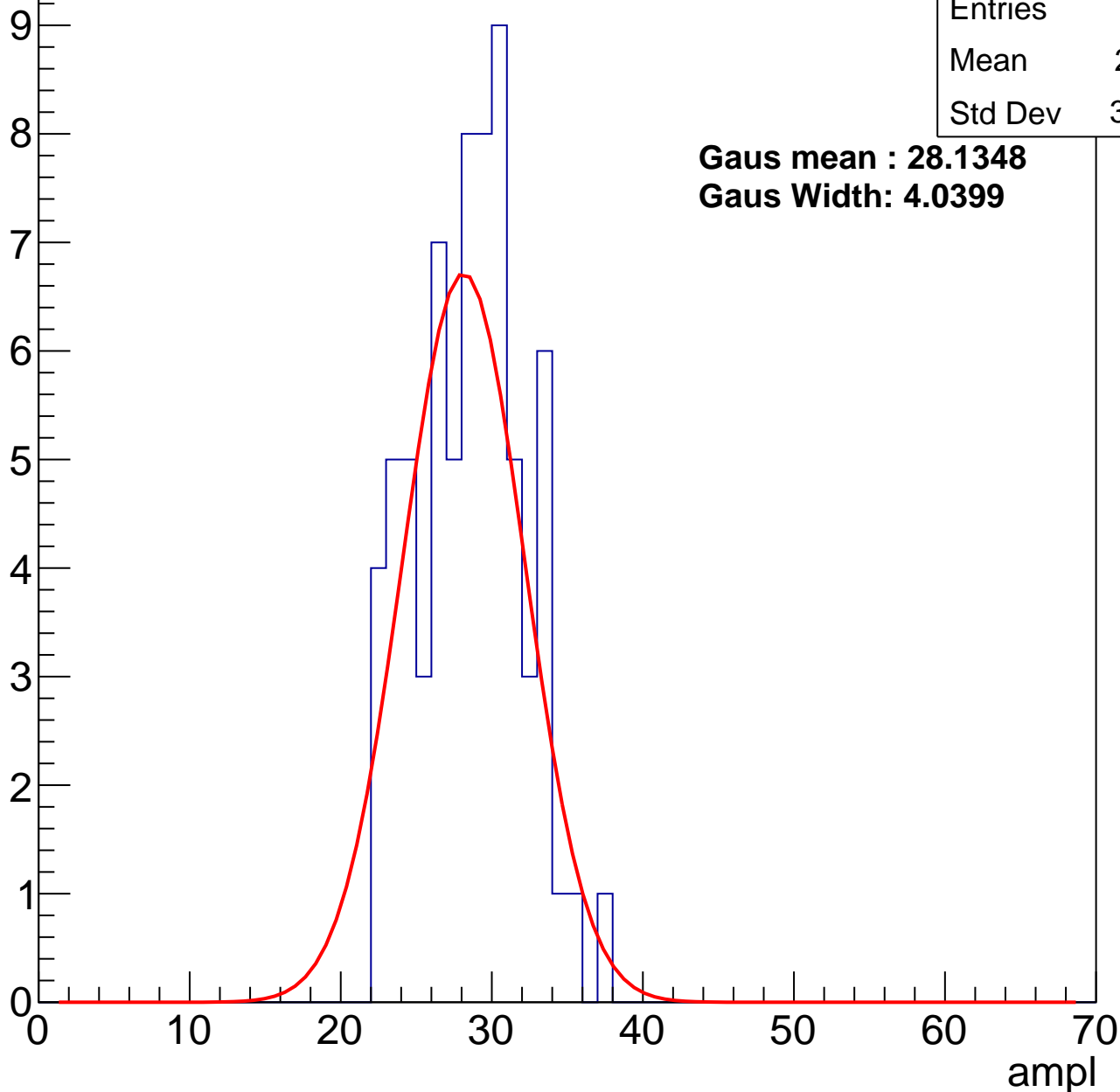
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.11
Std Dev	3.483

Gaus mean : 28.1348

Gaus Width: 4.0399



B1L103S, U17-ch74, adc1

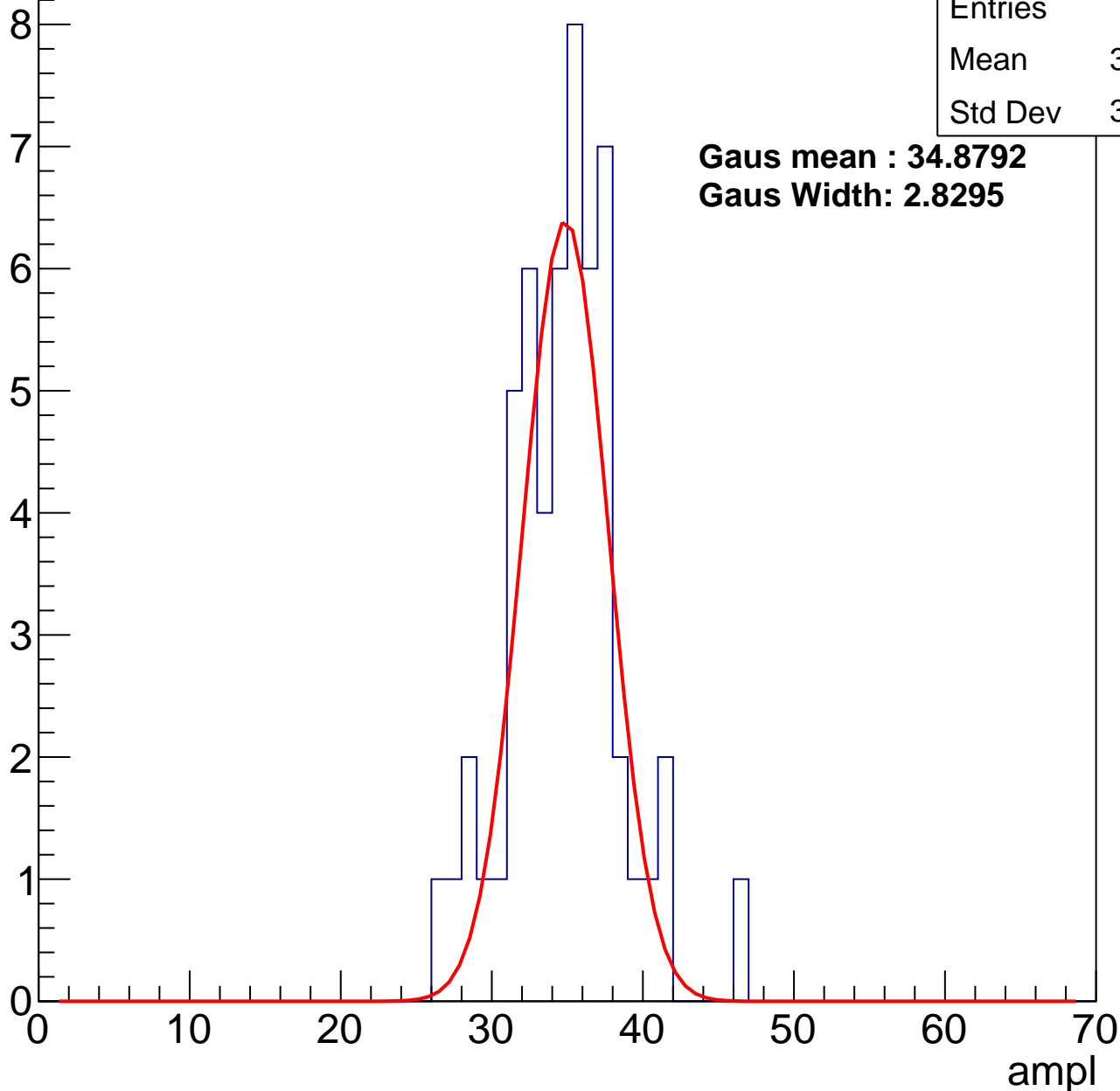
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	34.35
Std Dev	3.624

Gaus mean : 34.8792

Gaus Width: 2.8295



B1L103S, U17-ch74, adc2

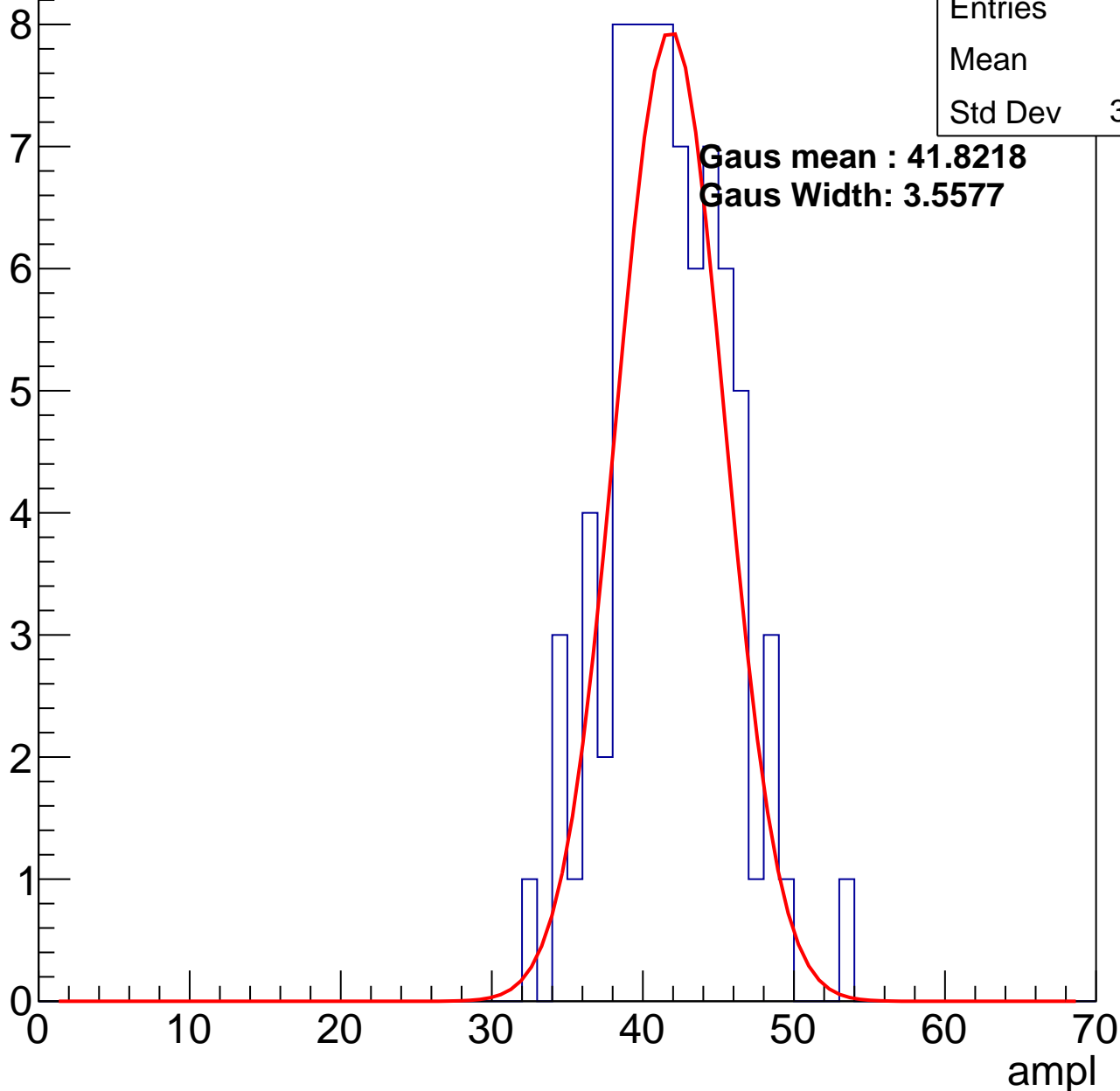
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	41.3
Std Dev	3.884

Gaus mean : 41.8218

Gaus Width: 3.5577

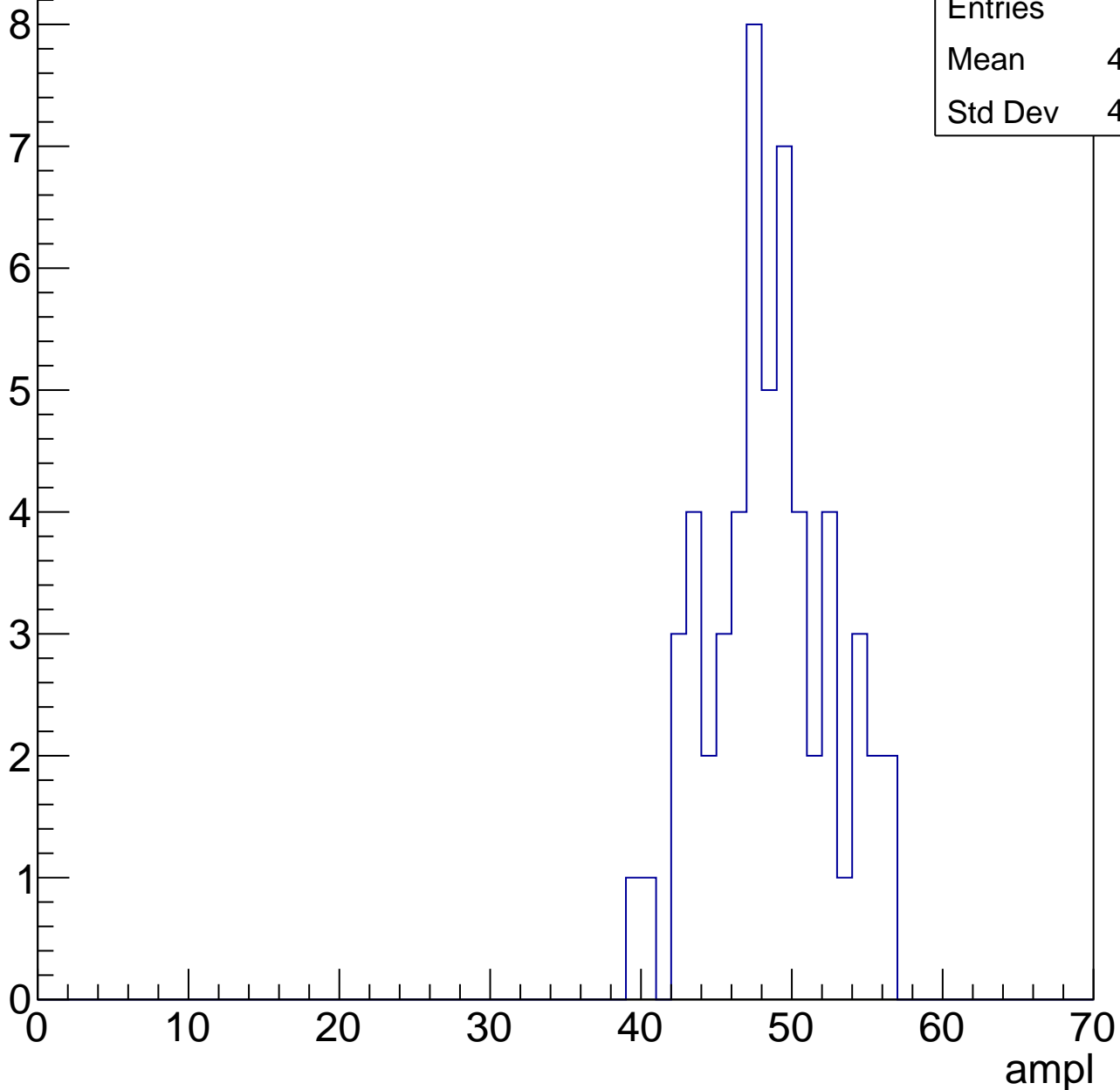


B1L103S, U17-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	48.04
Std Dev	4.009

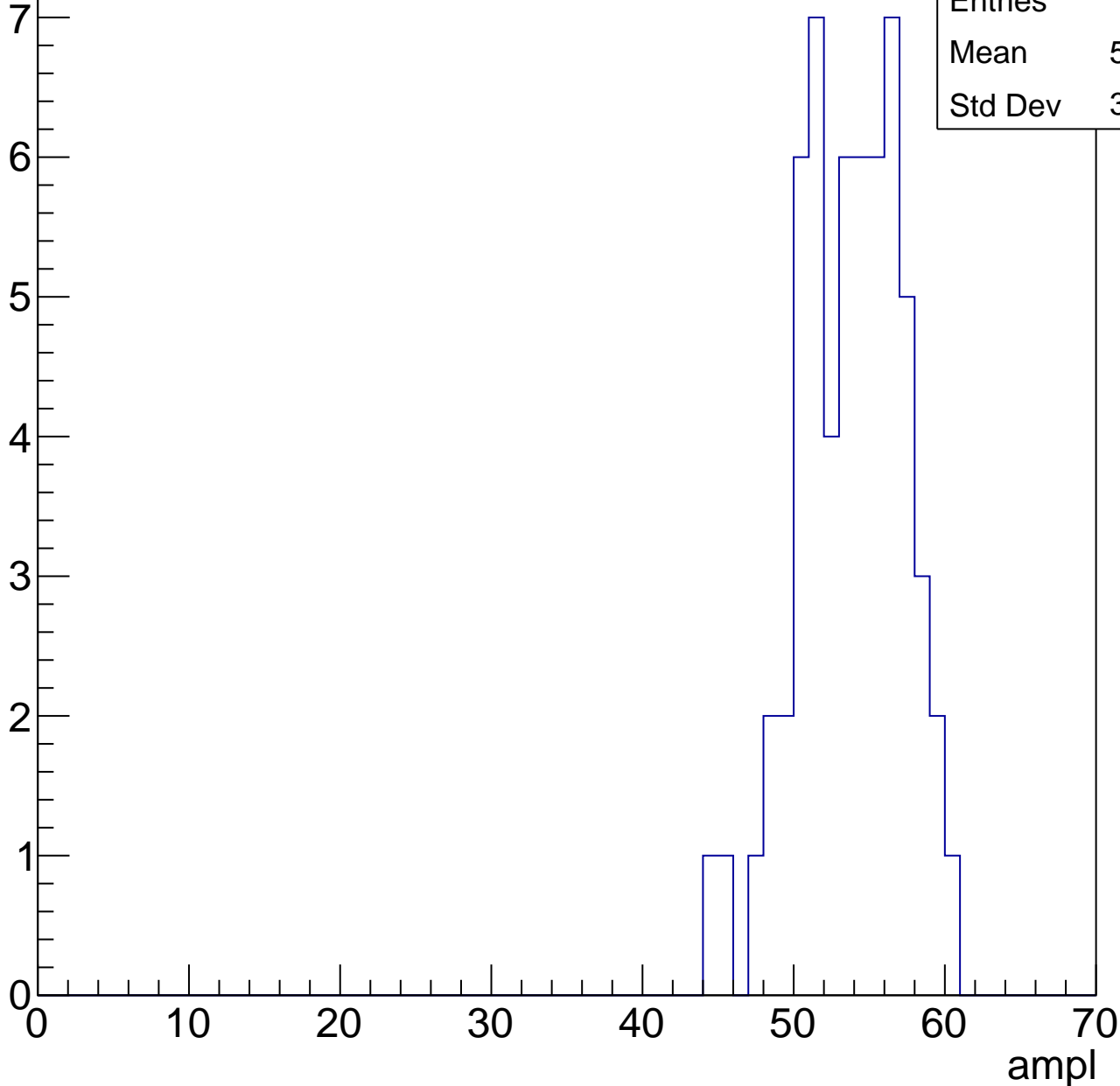


B1L103S, U17-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	53.27
Std Dev	3.449

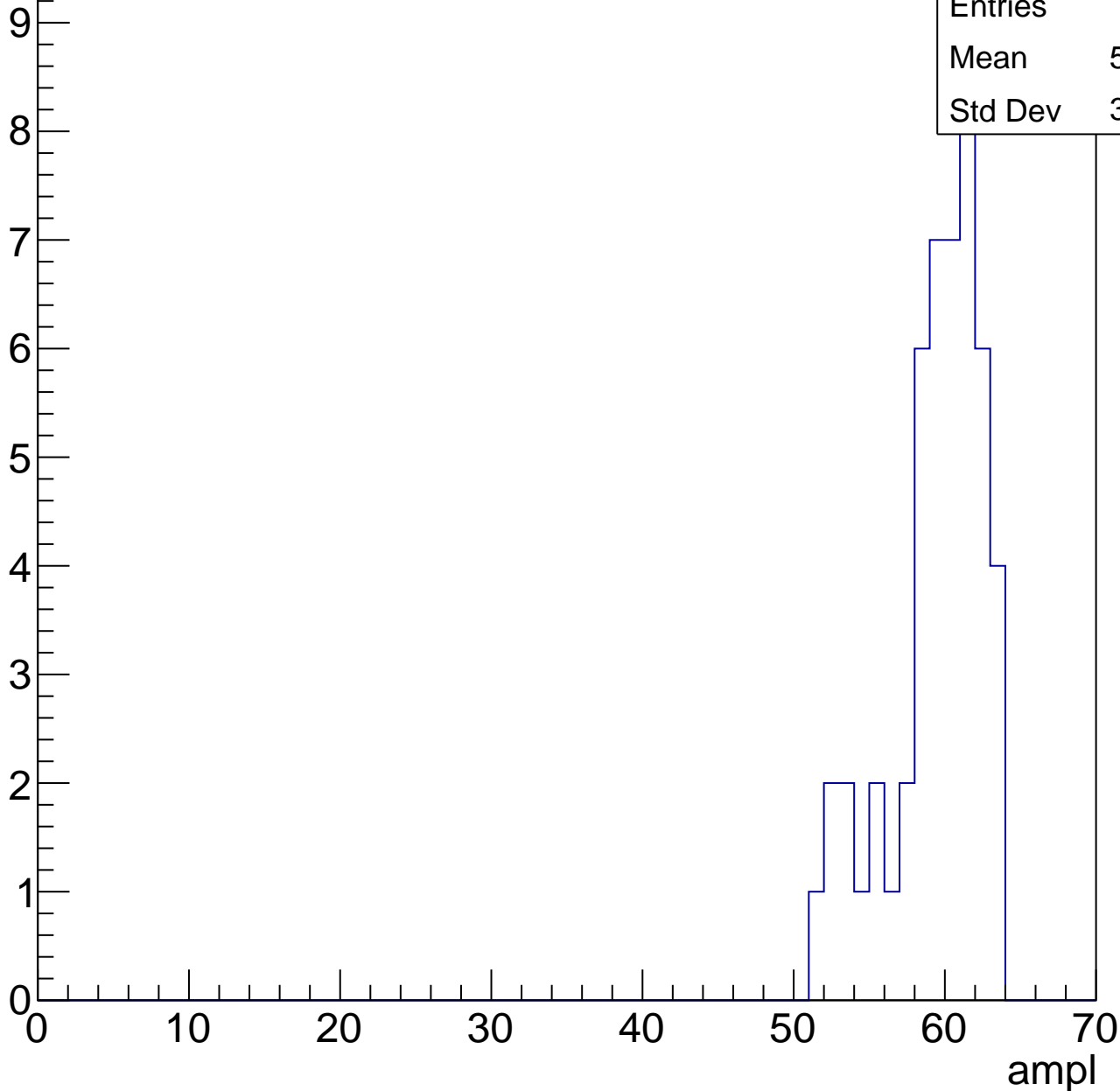


B1L103S, U17-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

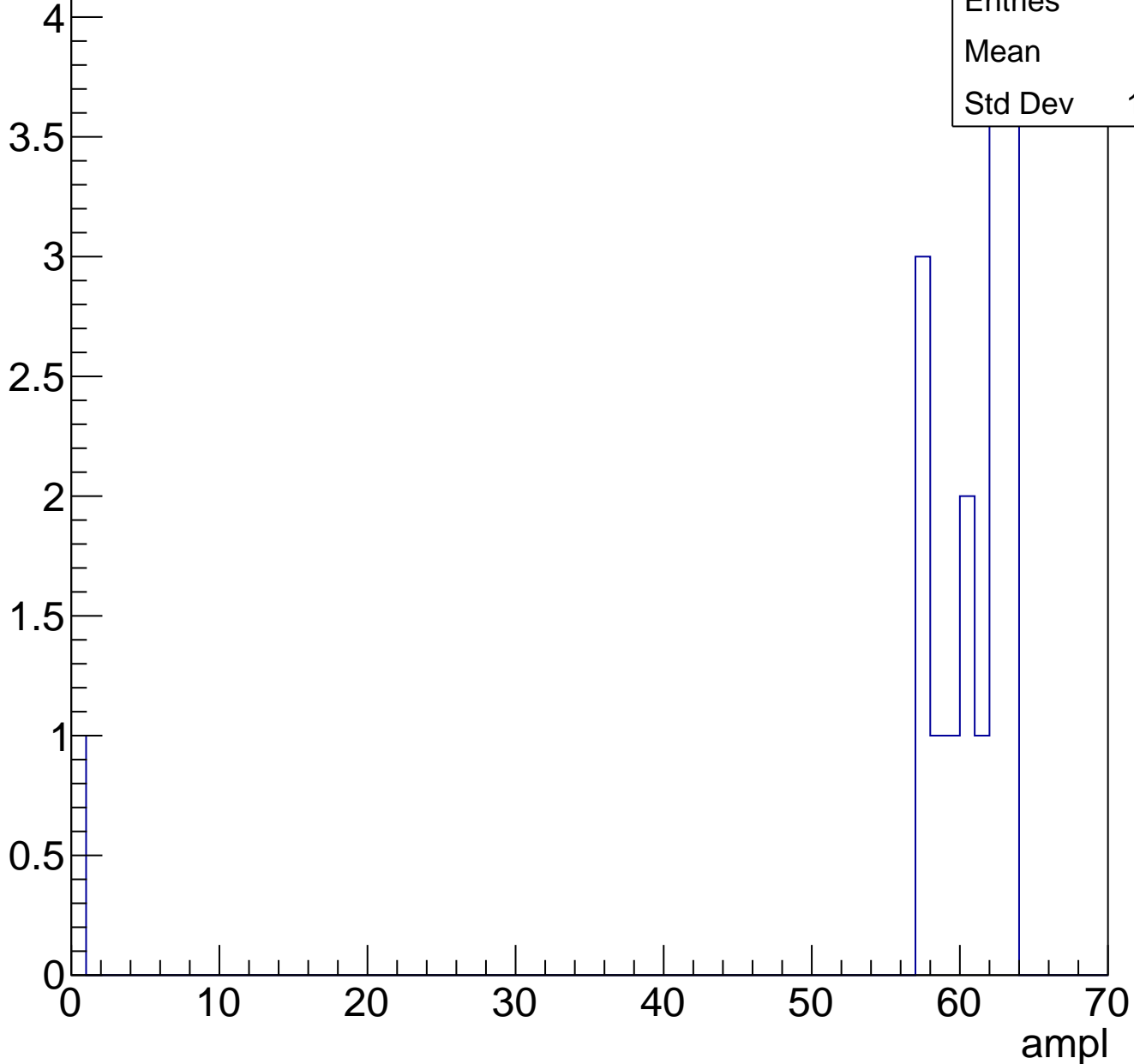
Entries	50
Mean	58.98
Std Dev	3.082



B1L103S, U17-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch75, adc0

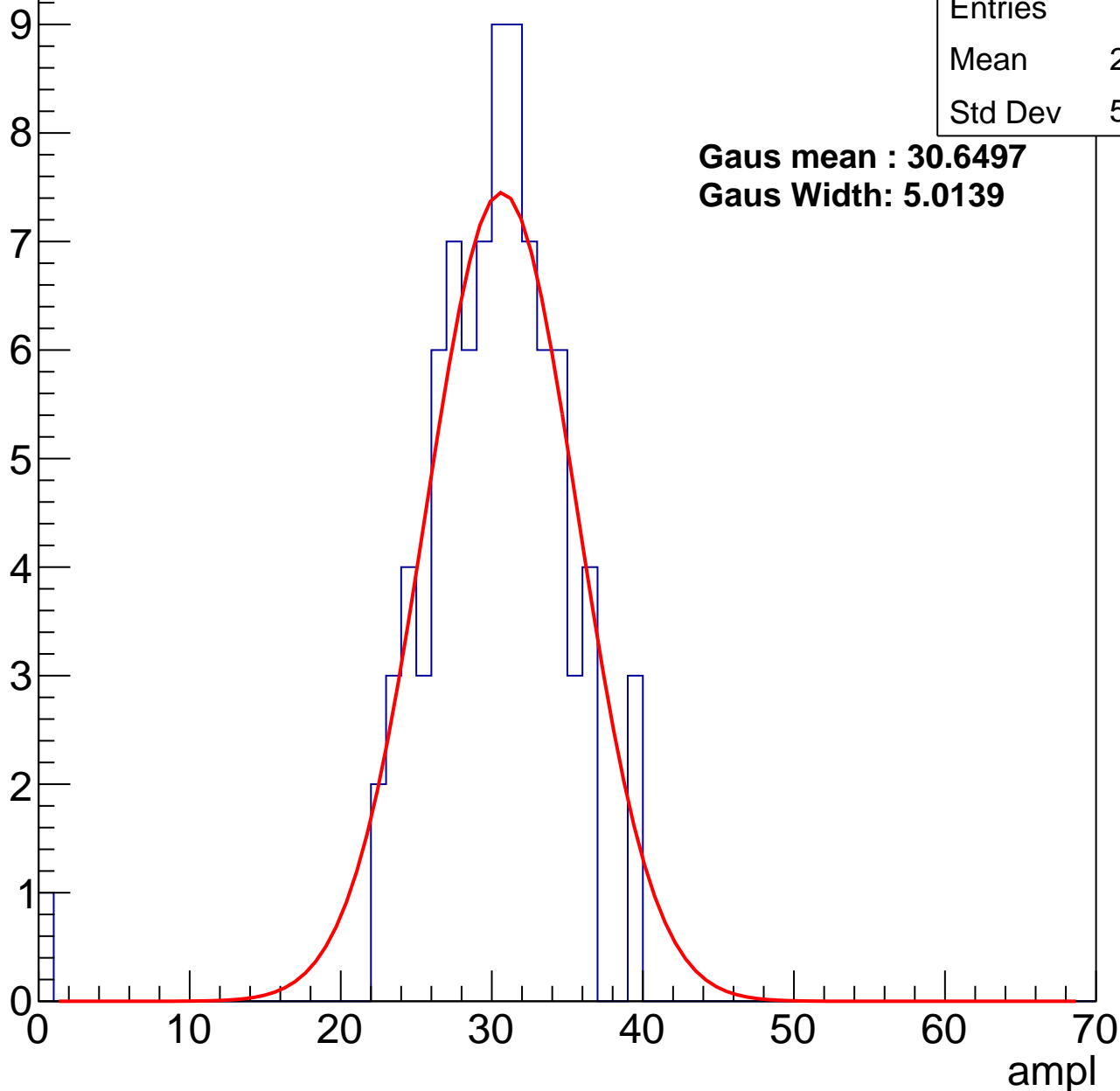
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	29.55
Std Dev	5.075

Gaus mean : 30.6497

Gaus Width: 5.0139



B1L103S, U17-ch75, adc1

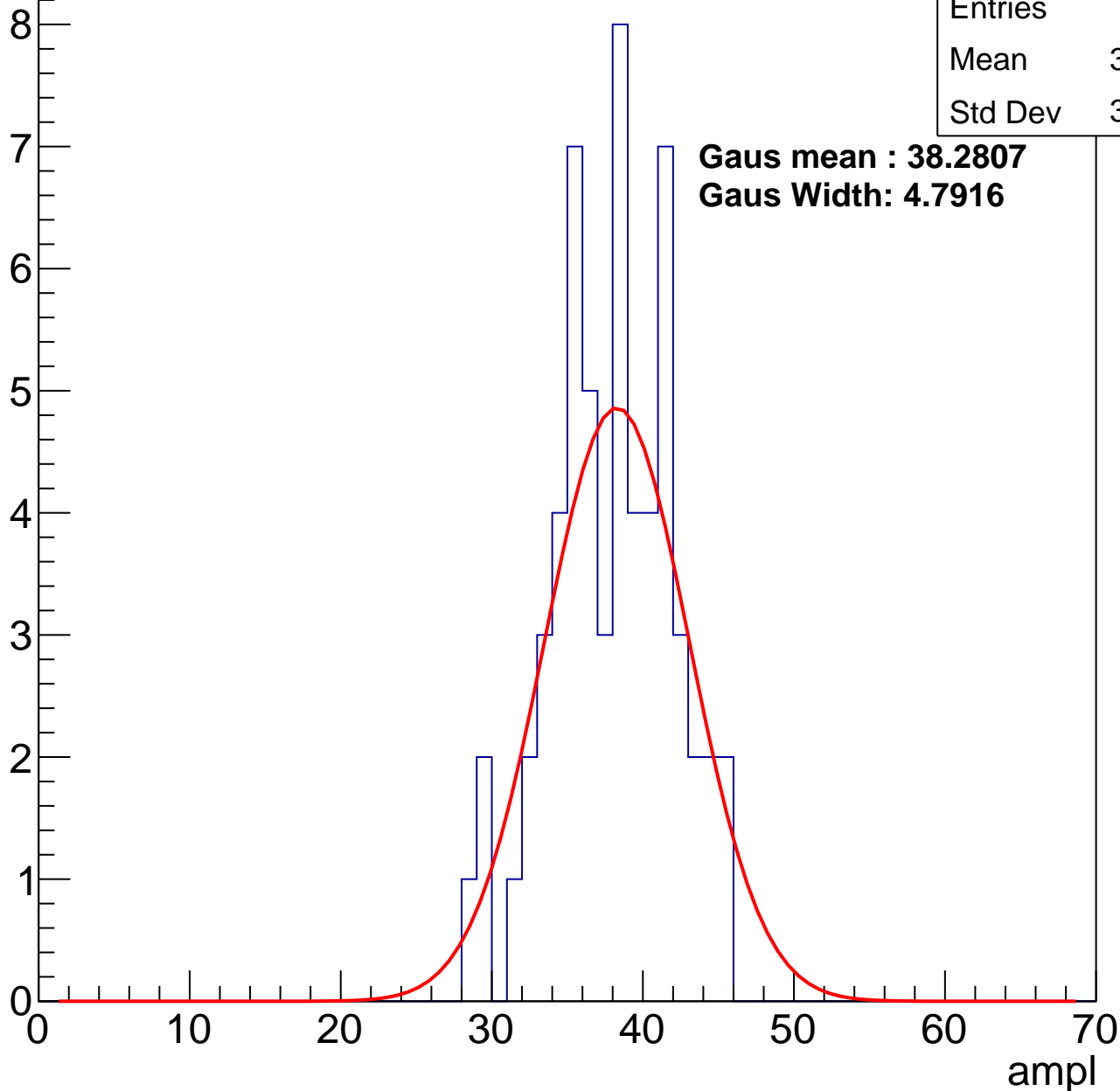
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37.48
Std Dev	3.964

Gaus mean : 38.2807

Gaus Width: 4.7916



B1L103S, U17-ch75, adc2

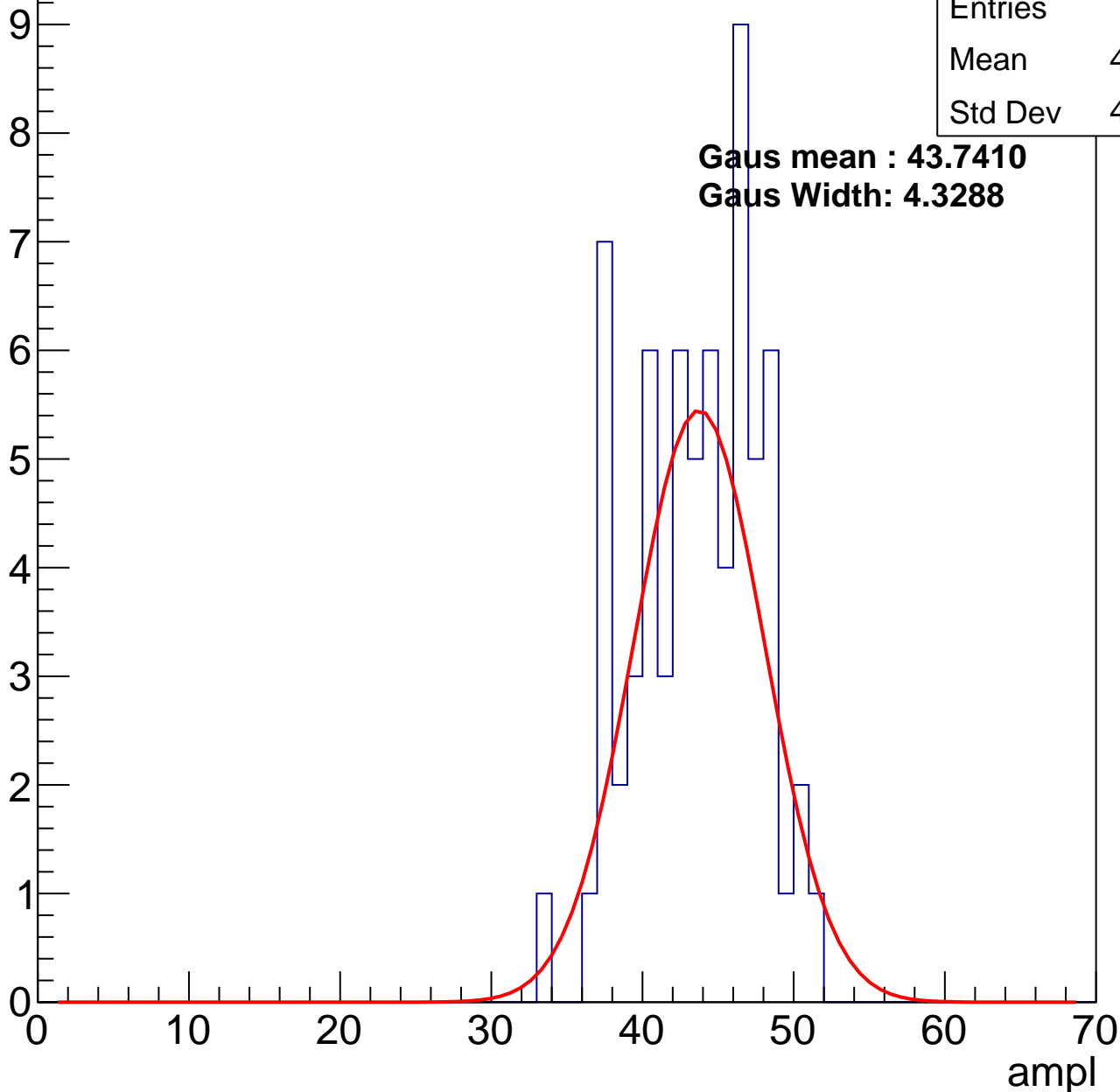
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.12
Std Dev	4.024

Gaus mean : 43.7410

Gaus Width: 4.3288

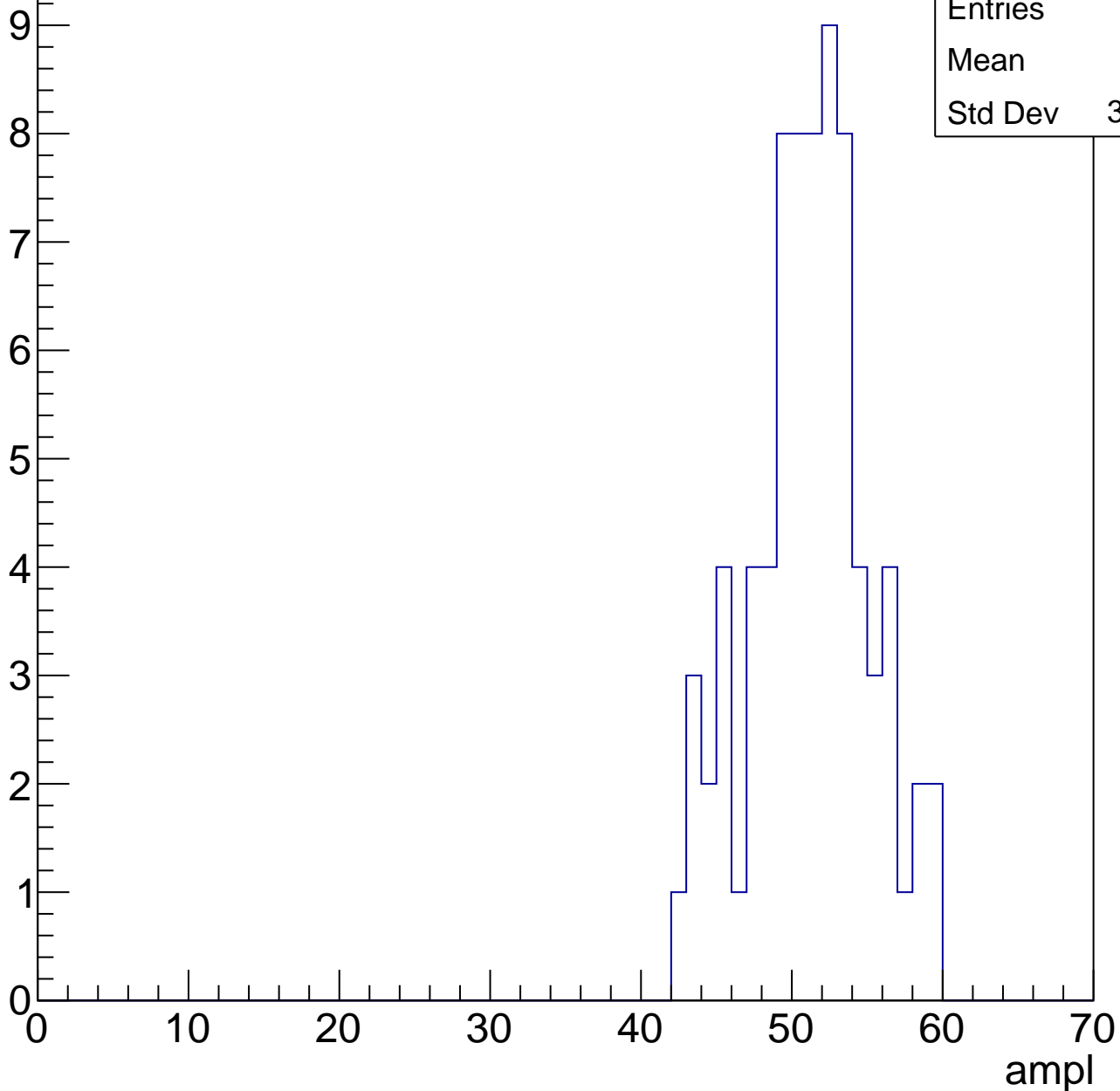


B1L103S, U17-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	50.7
Std Dev	3.907

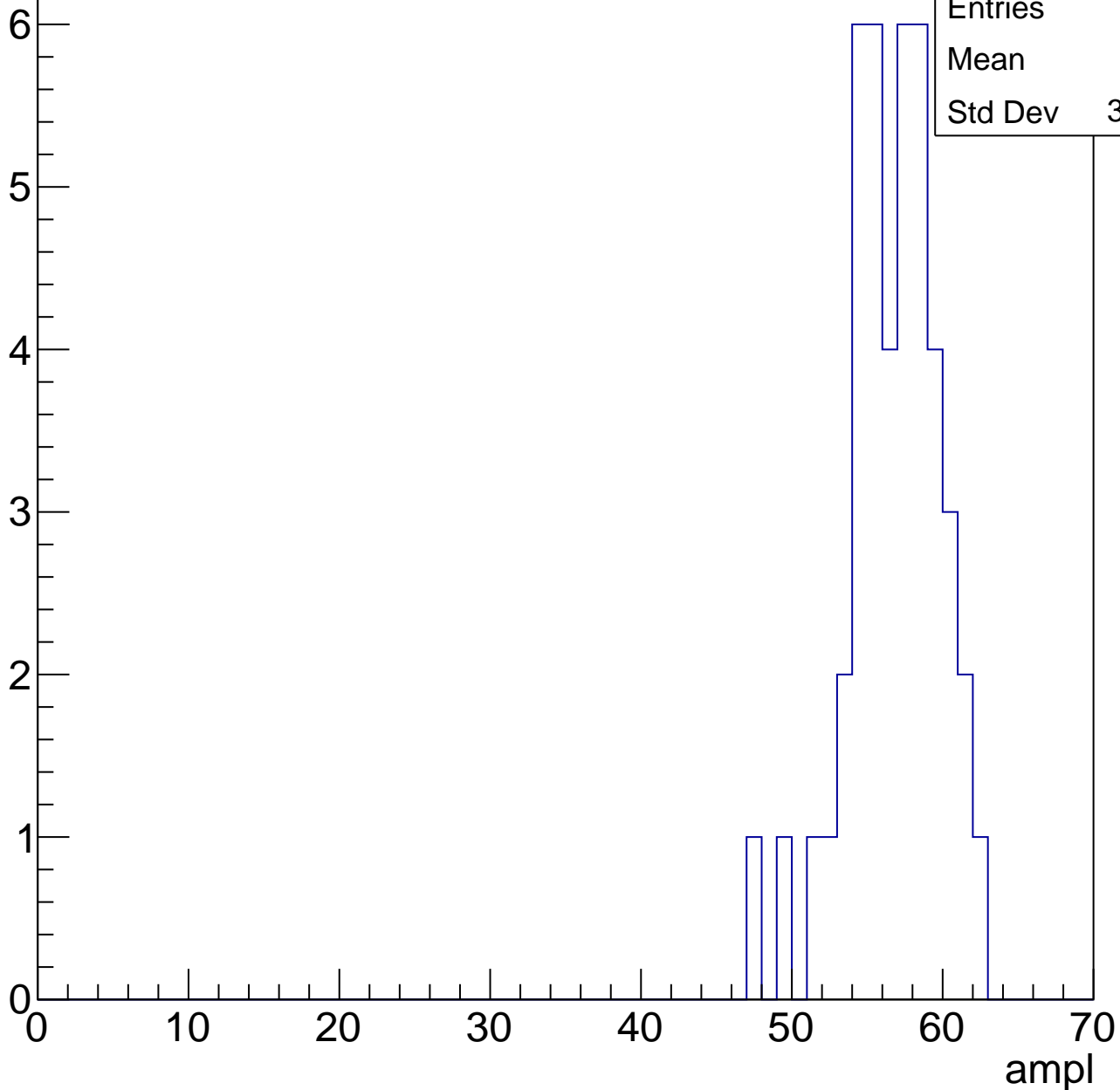


B1L103S, U17-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	56.2
Std Dev	3.079



B1L103S, U17-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

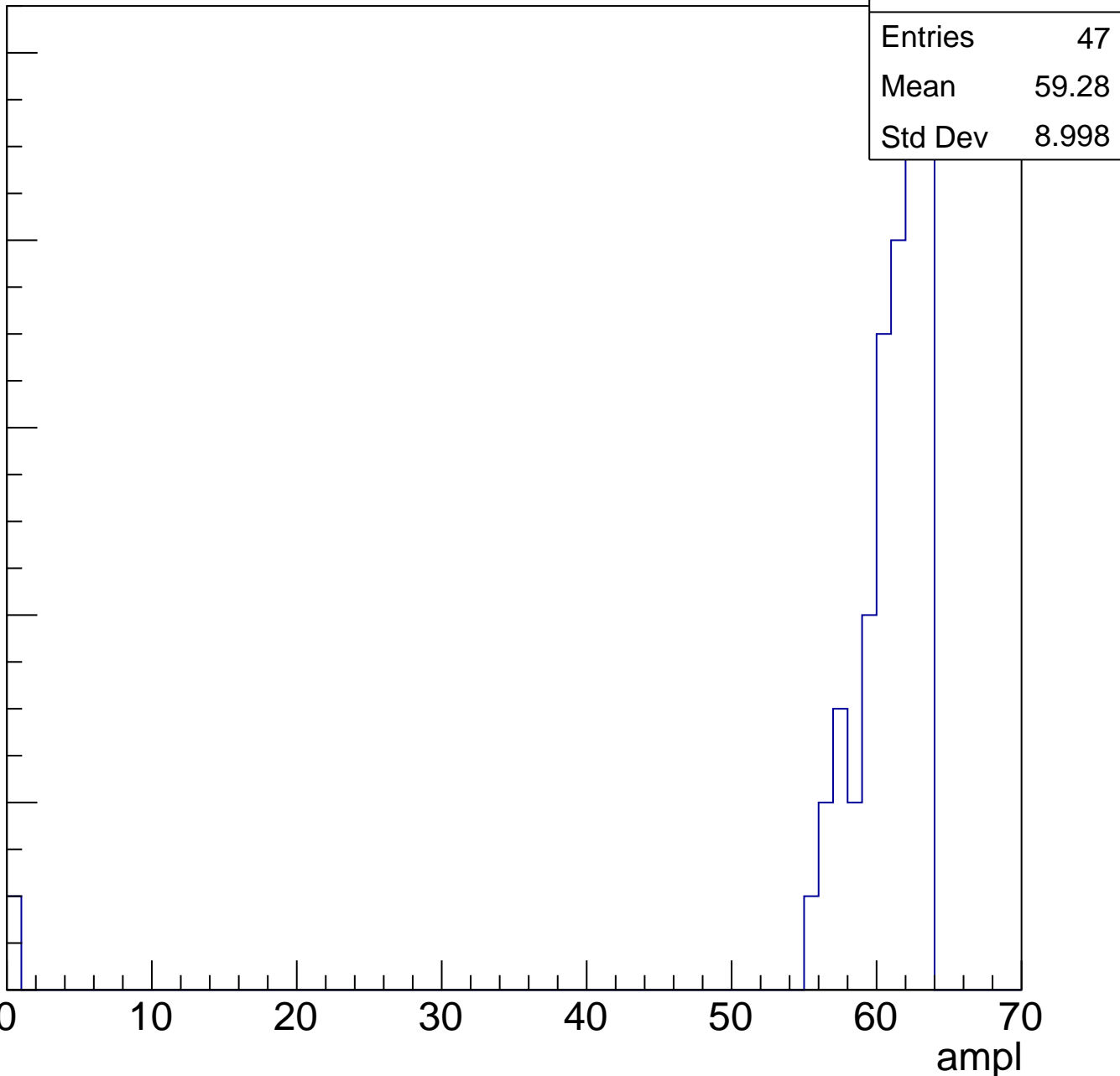
40

50

60

ampl

Entries	47
Mean	59.28
Std Dev	8.998



B1L103S, U17-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch76, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	28.62
Std Dev	5.122

Gaus mean : 30.4132

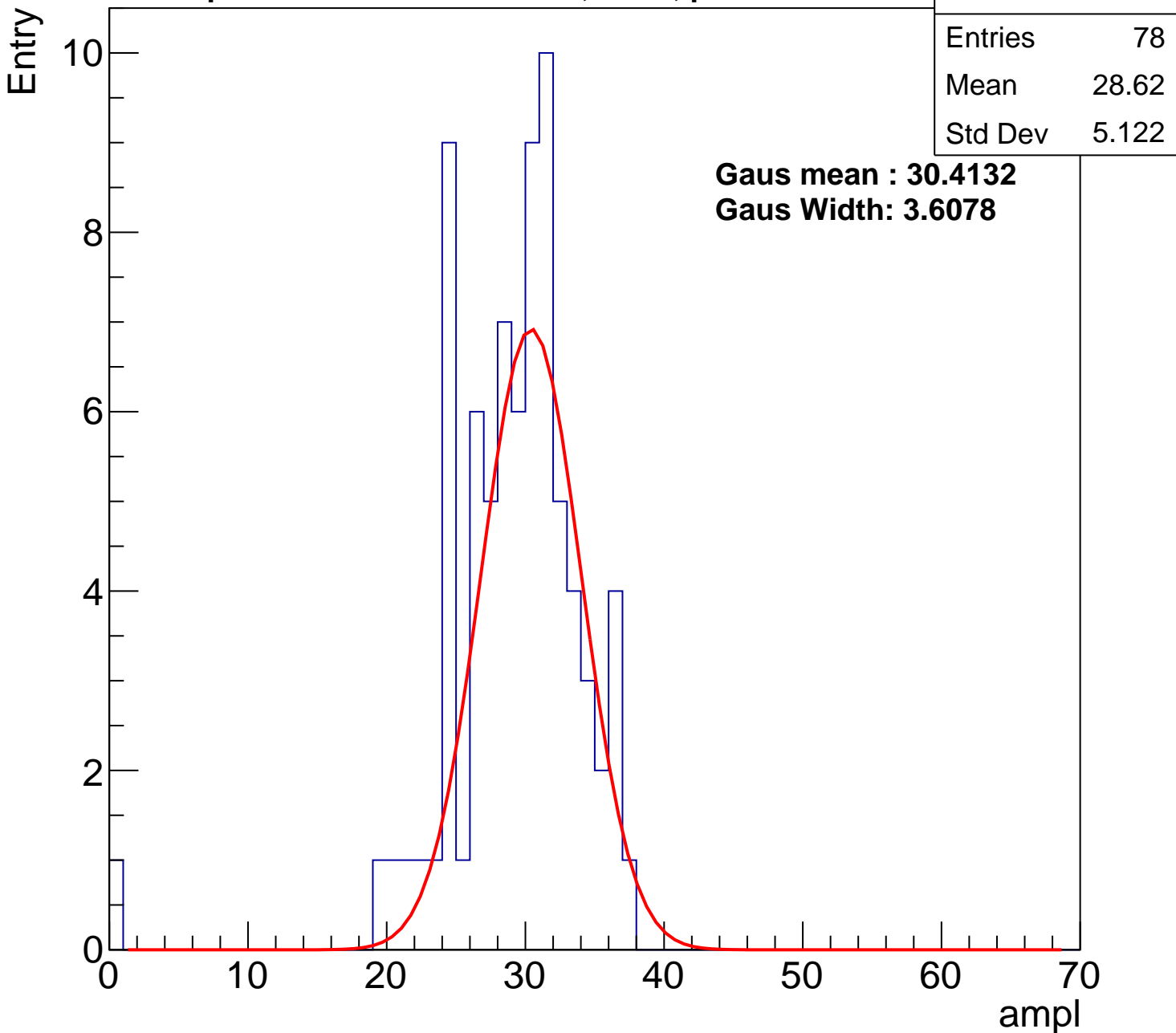
Gaus Width: 3.6078

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch76, adc1

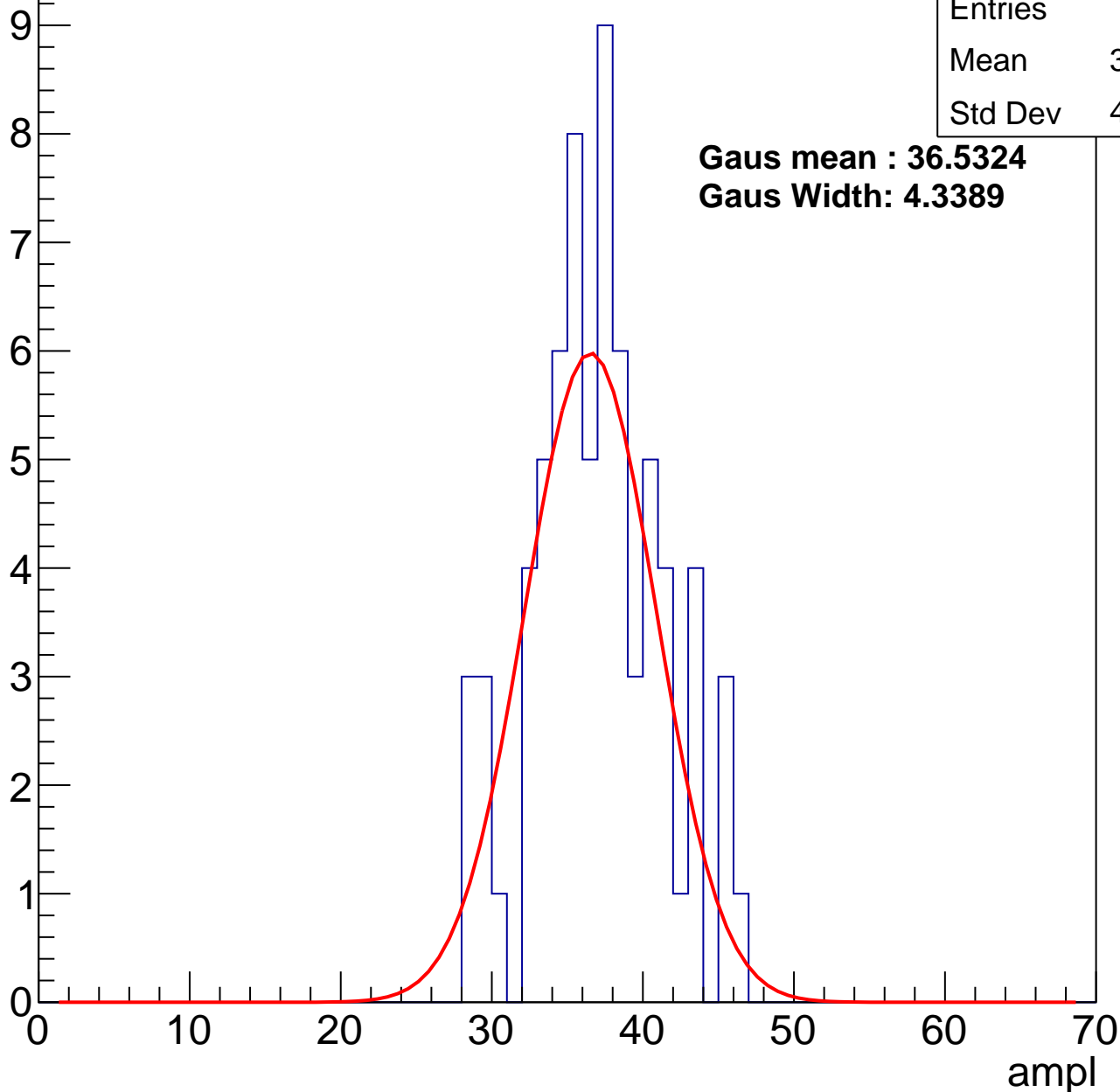
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.55
Std Dev	4.295

Gaus mean : 36.5324

Gaus Width: 4.3389



B1L103S, U17-ch76, adc2

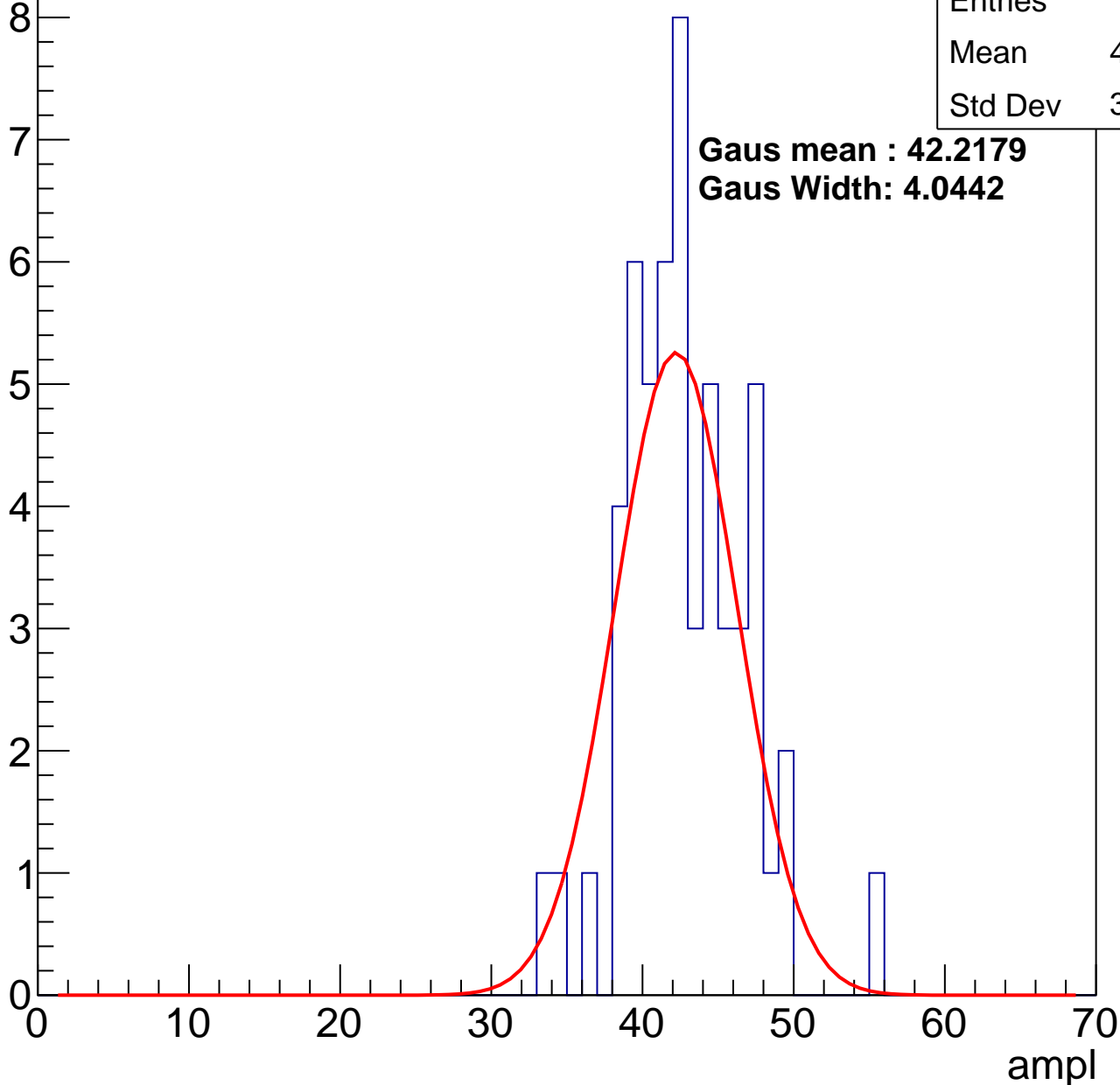
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.35
Std Dev	3.918

Gaus mean : 42.2179

Gaus Width: 4.0442



B1L103S, U17-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	49.04
Std Dev	4.166

Entry

10

8

6

4

2

0

0

10

20

30

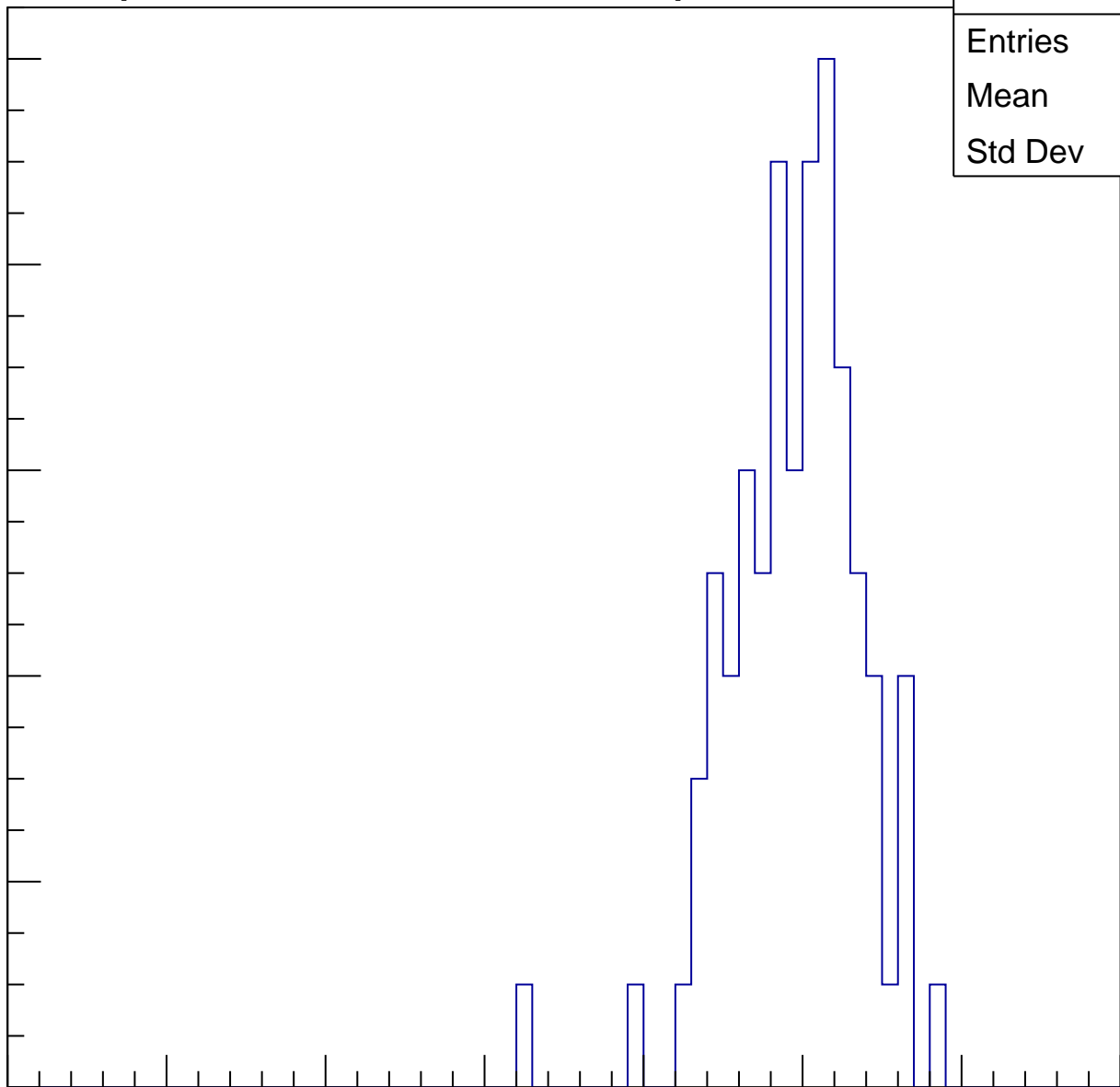
40

50

60

ampl

70

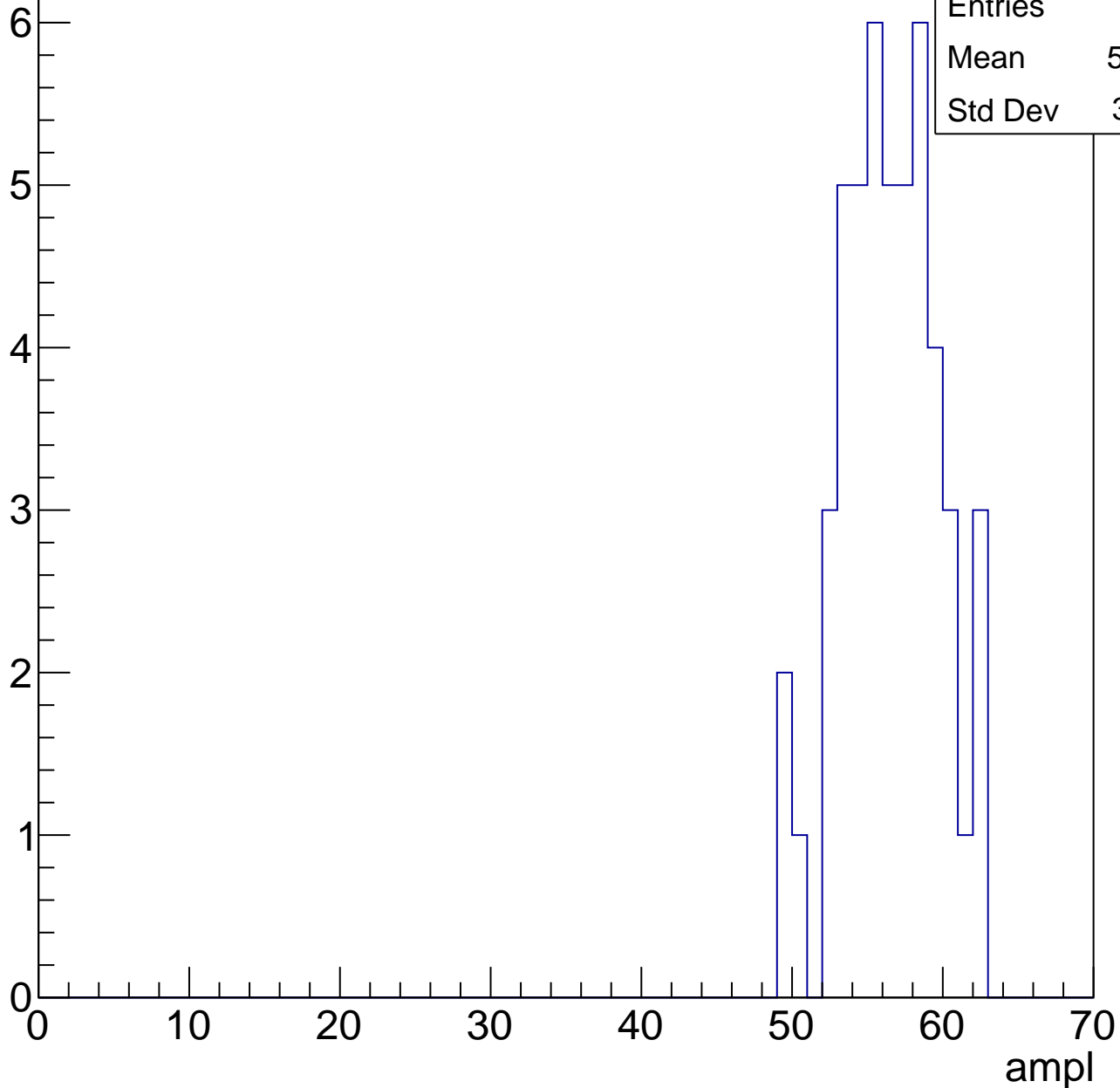


B1L103S, U17-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	56.02
Std Dev	3.191

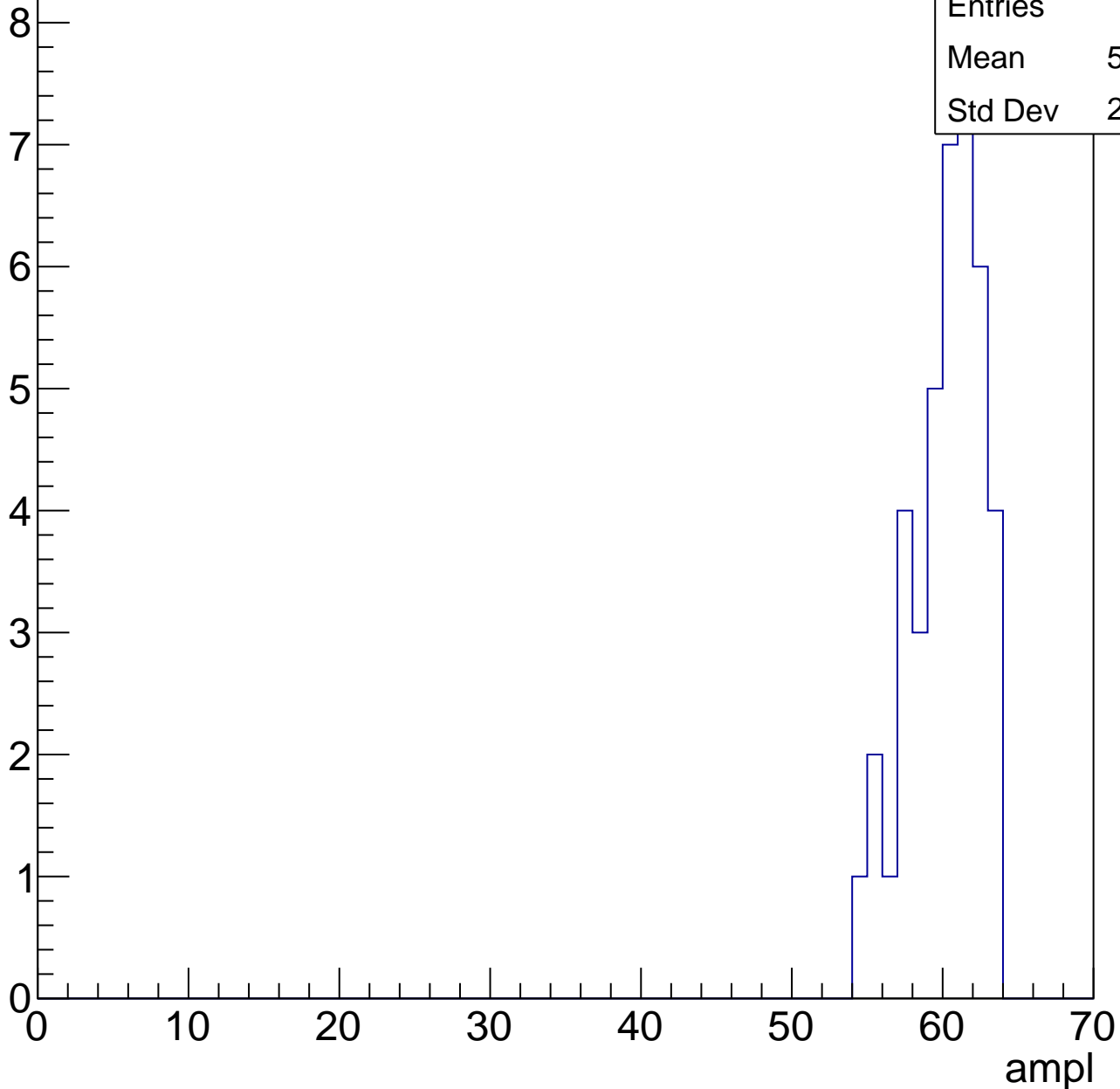


B1L103S, U17-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

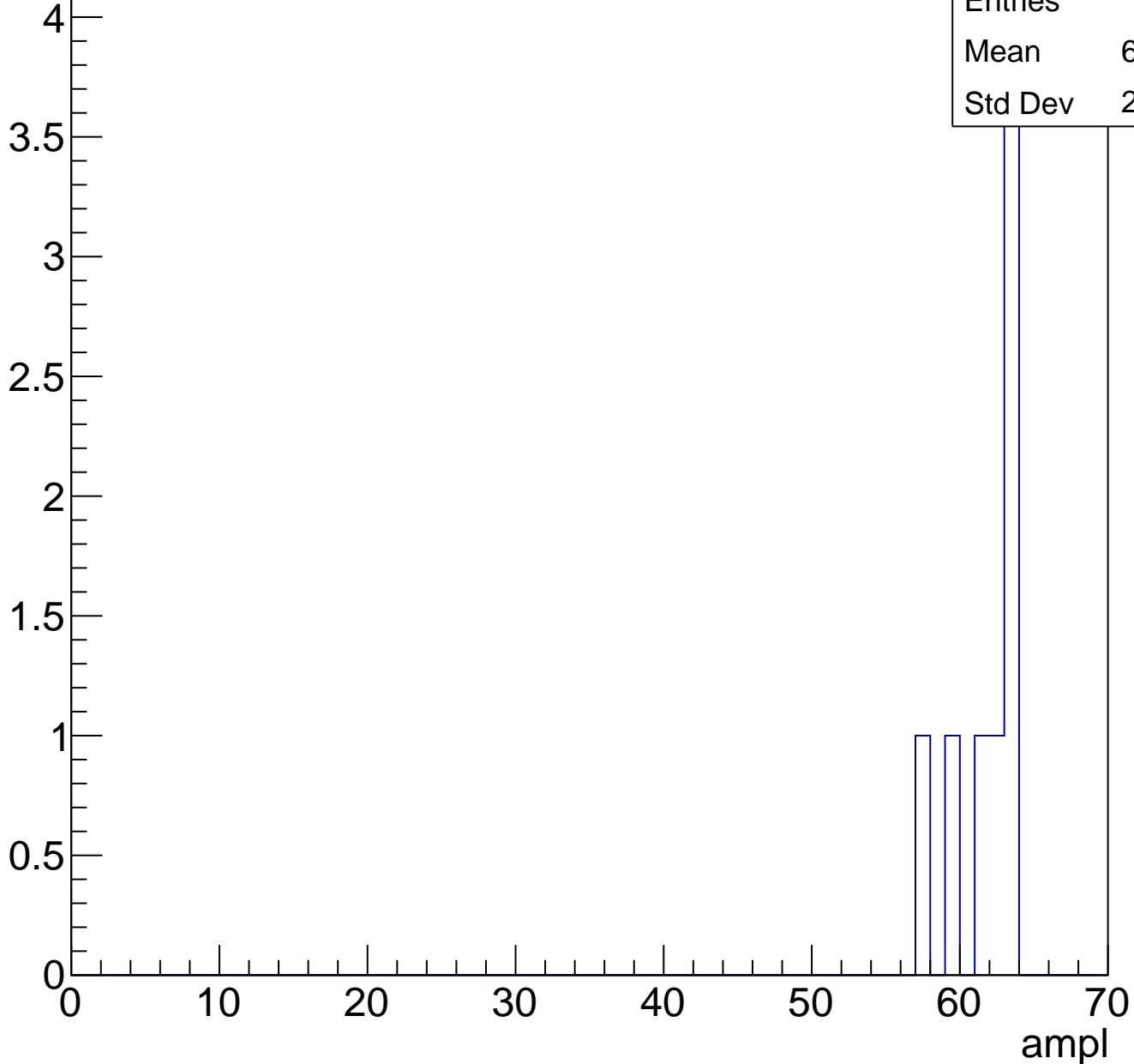
Entries	41
Mean	59.73
Std Dev	2.317



B1L103S, U17-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch77, adc0

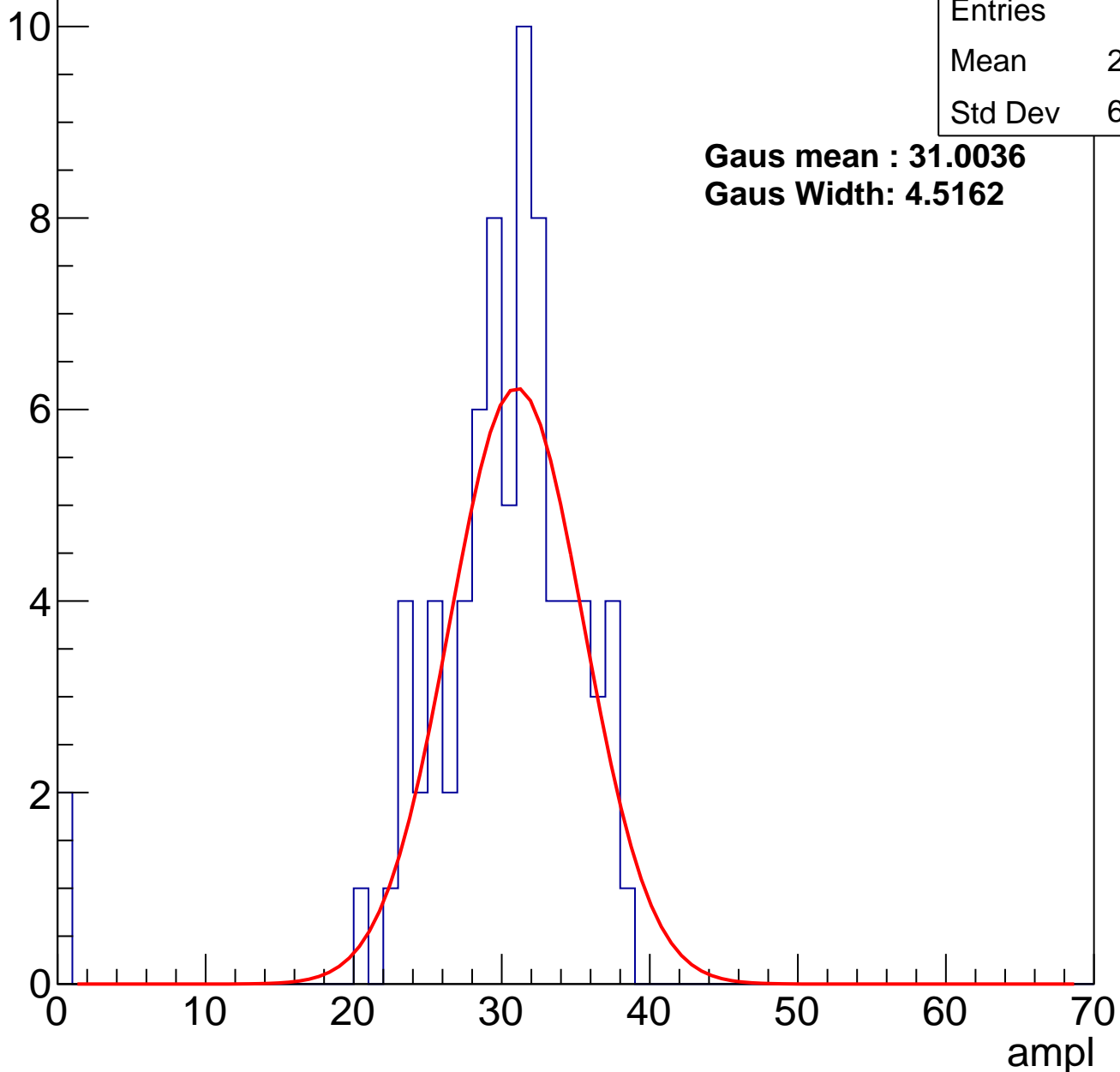
calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	29.35
Std Dev	6.252

Gaus mean : 31.0036

Gaus Width: 4.5162

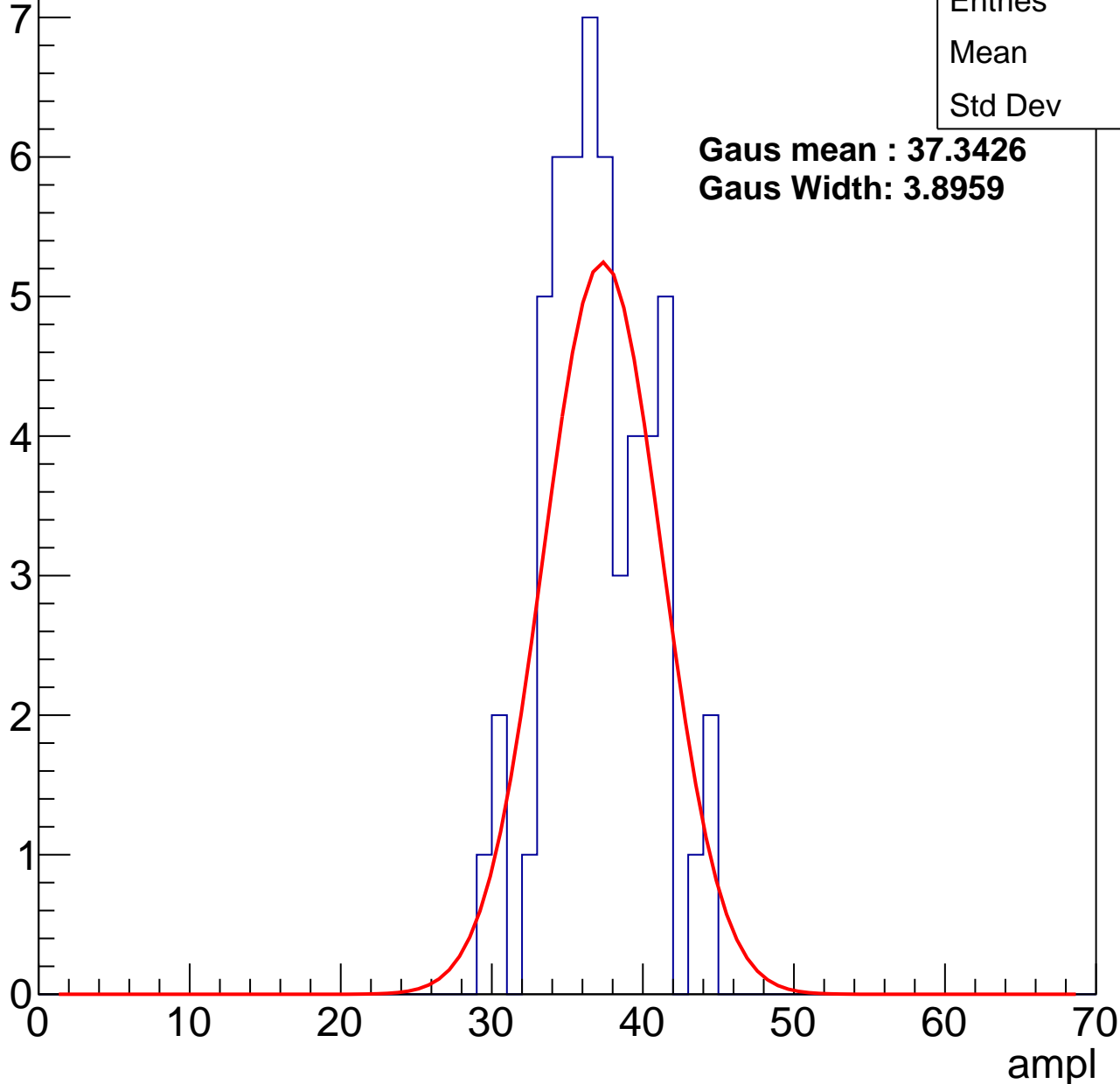
Entry



B1L103S, U17-ch77, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch77, adc2

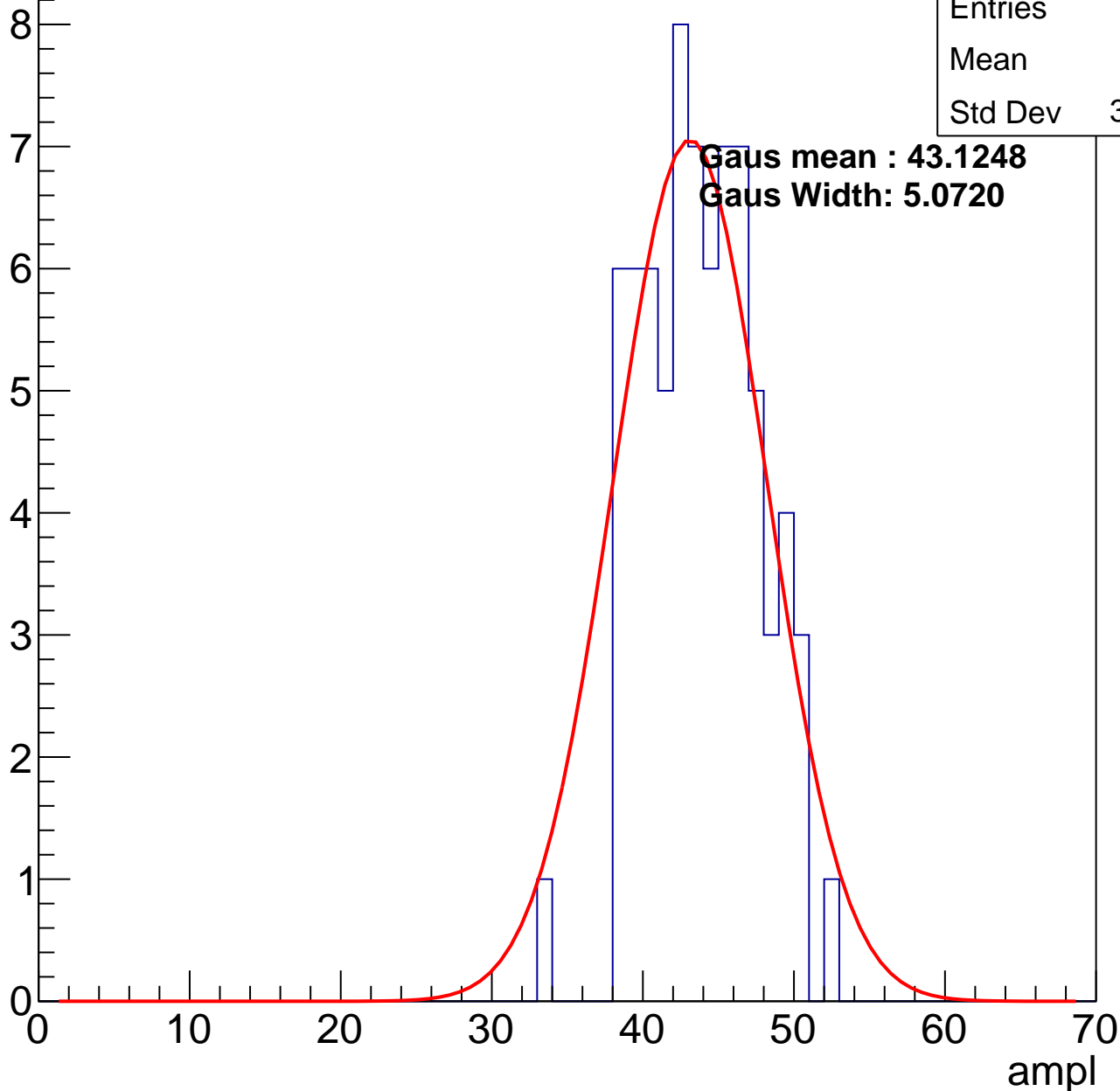
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	43.4
Std Dev	3.724

Gaus mean : 43.1248

Gaus Width: 5.0720

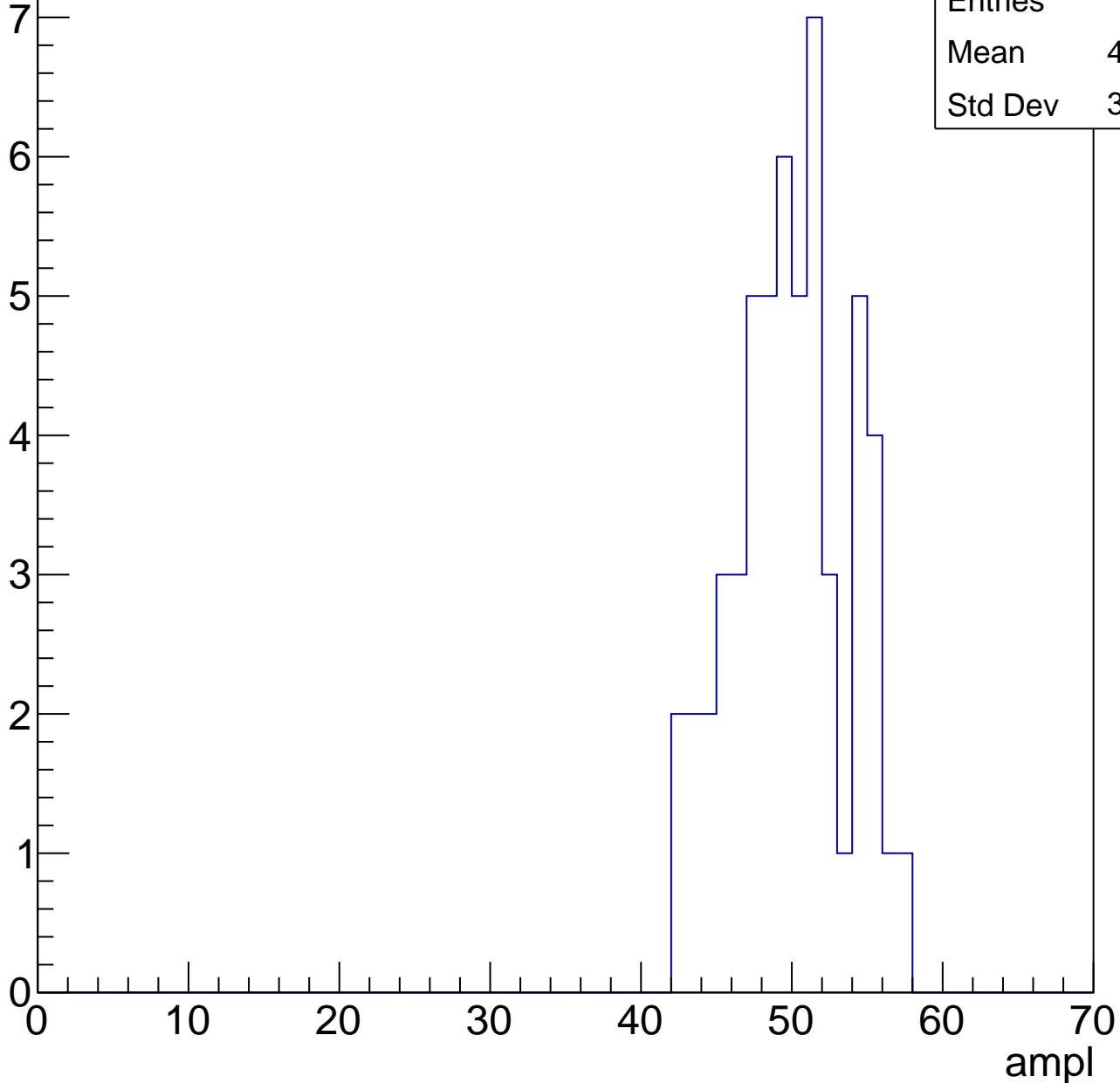


B1L103S, U17-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49.44
Std Dev	3.736

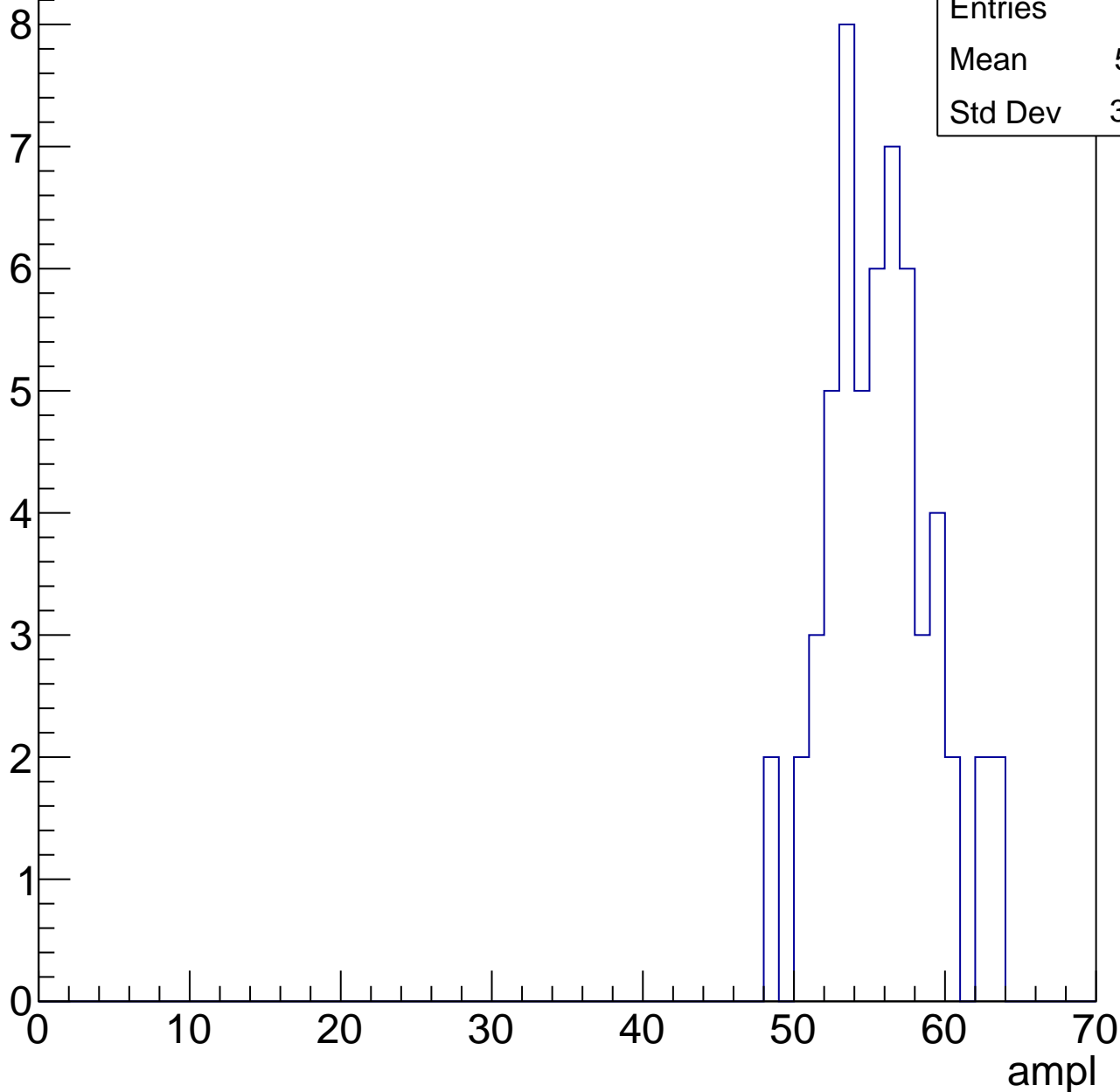


B1L103S, U17-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

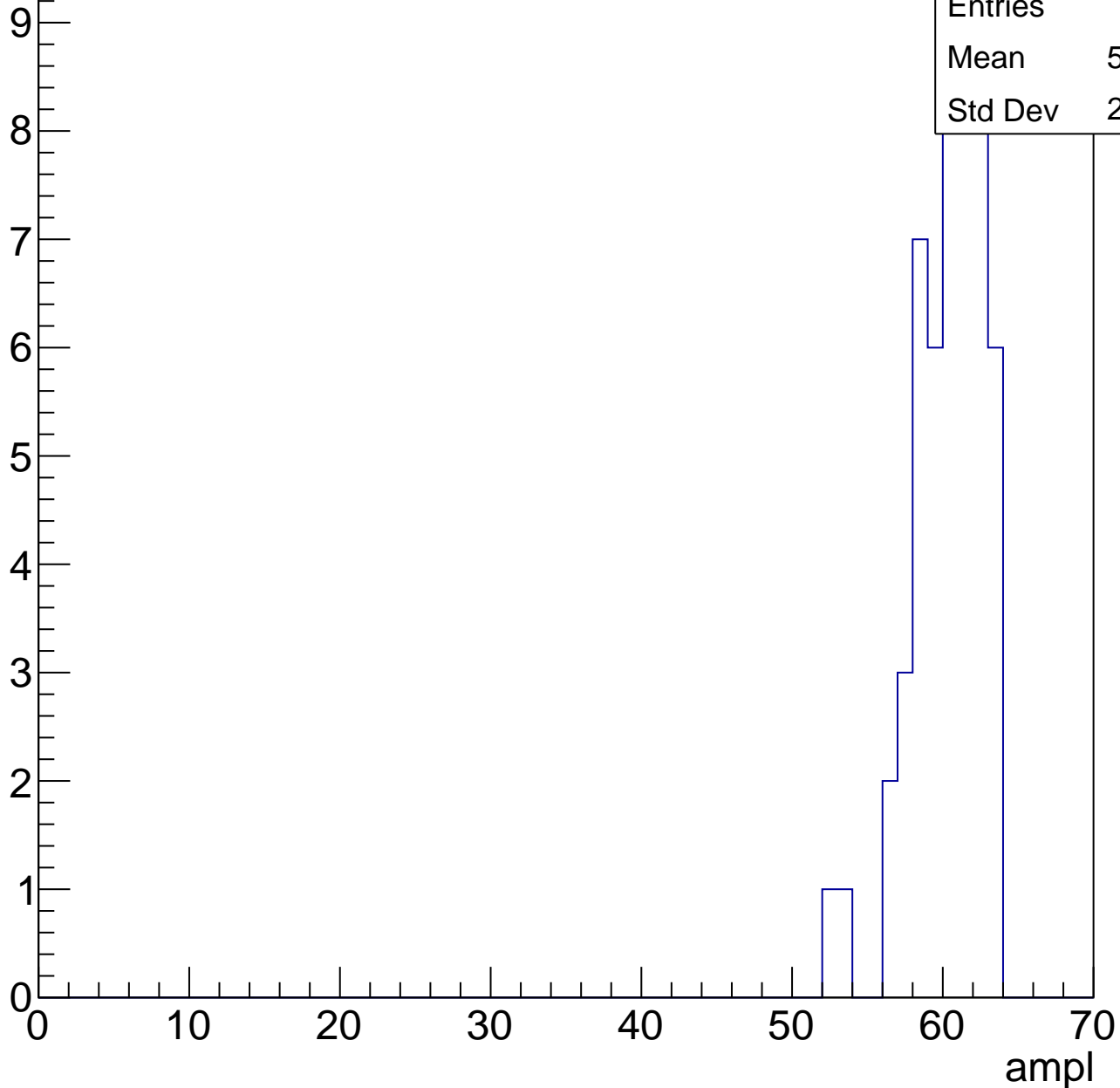
Entries	57
Mean	55.21
Std Dev	3.437



B1L103S, U17-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

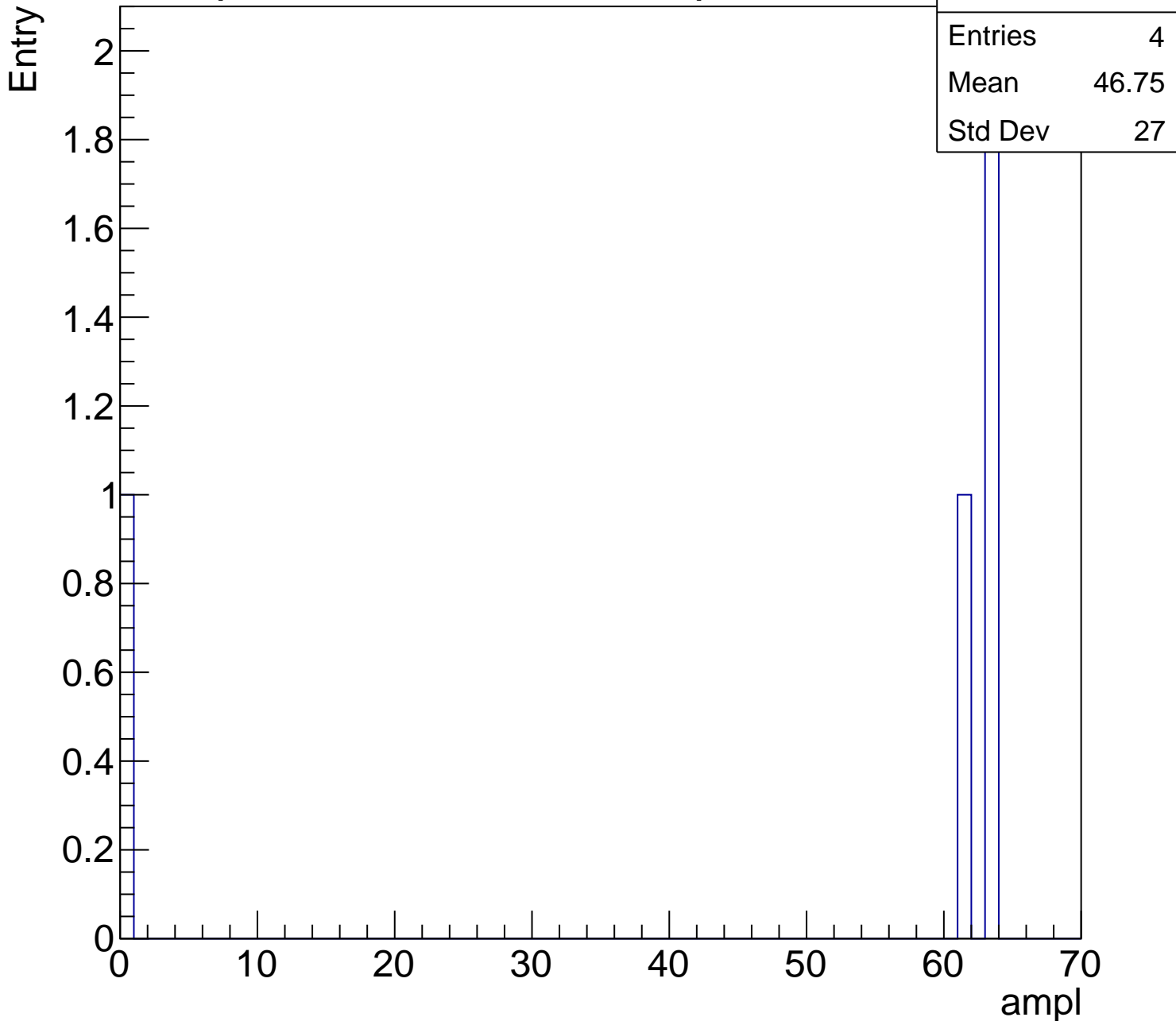
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch78, adc0

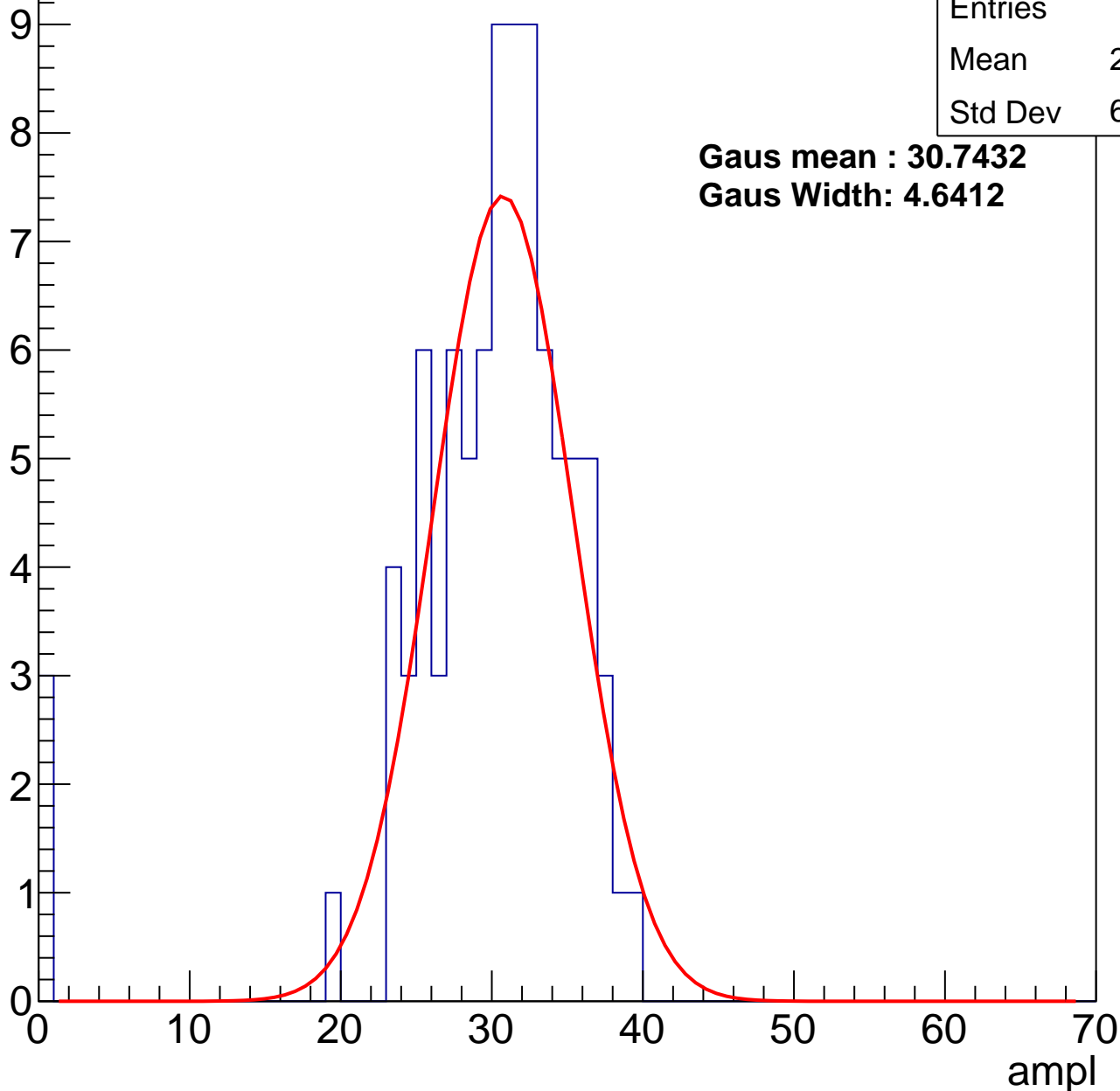
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	29.28
Std Dev	6.776

Gaus mean : 30.7432

Gaus Width: 4.6412



B1L103S, U17-ch78, adc1

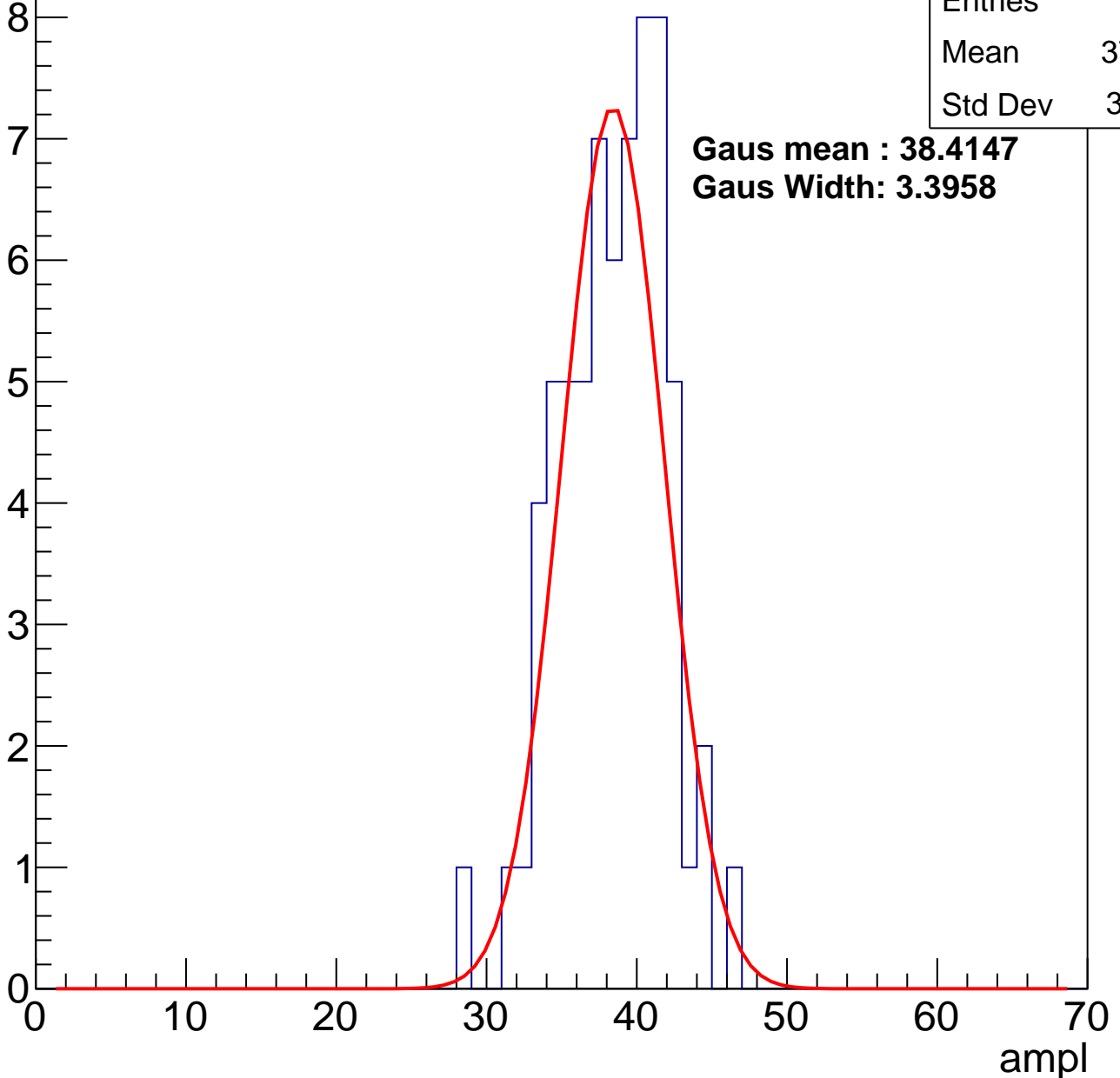
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37.96
Std Dev	3.431

Gaus mean : 38.4147

Gaus Width: 3.3958



B1L103S, U17-ch78, adc2

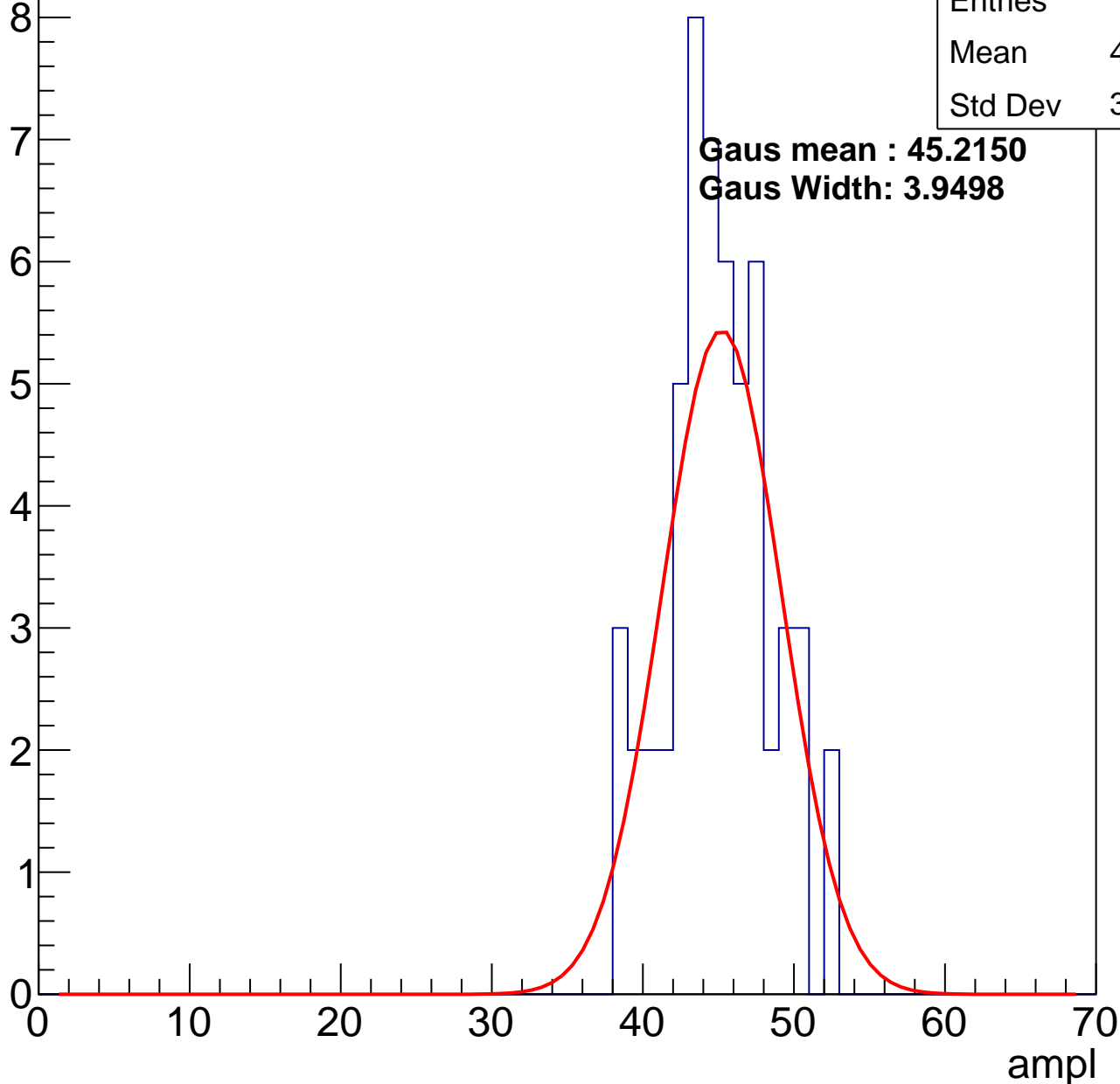
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	44.55
Std Dev	3.385

Gaus mean : 45.2150

Gaus Width: 3.9498

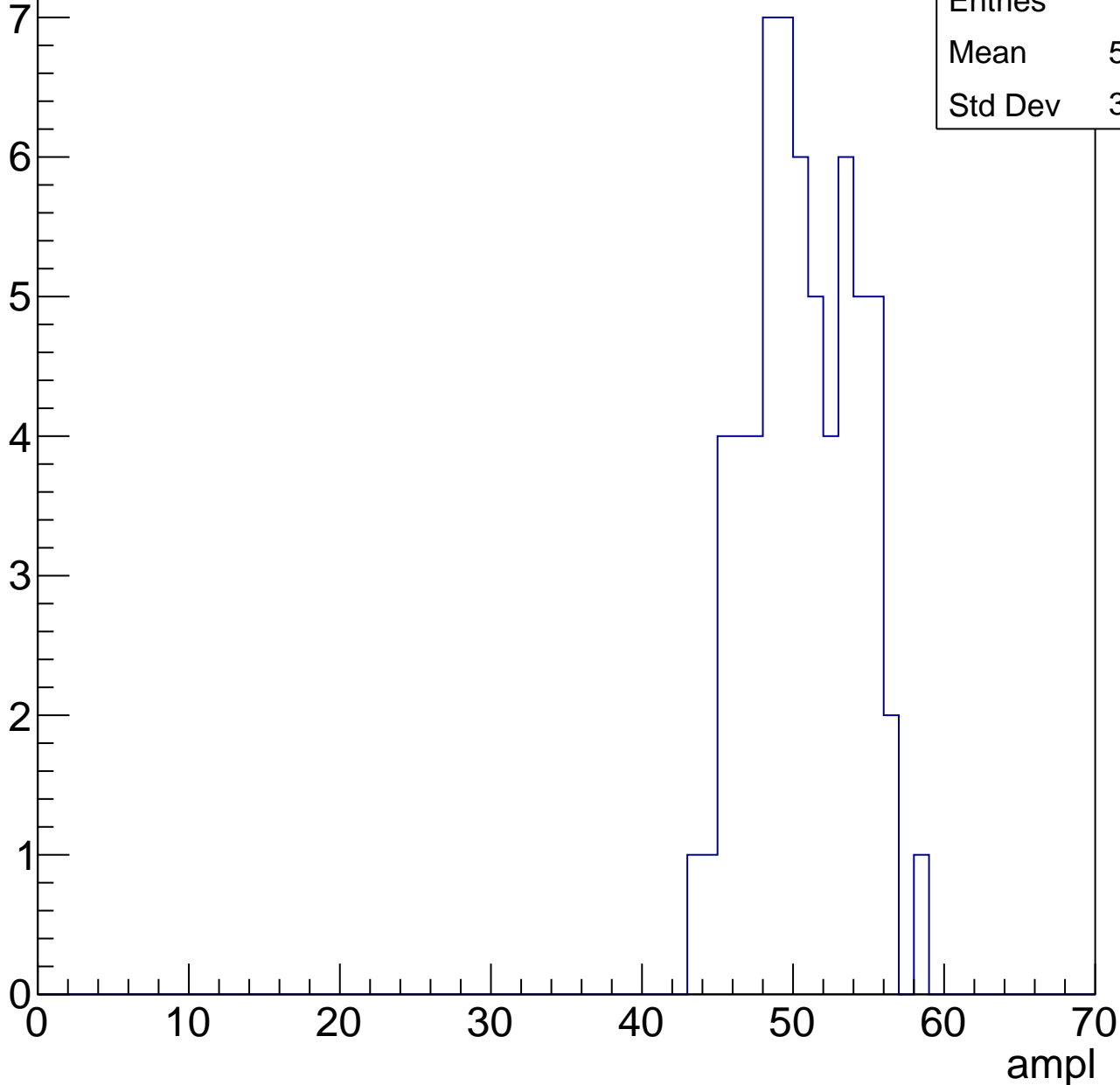


B1L103S, U17-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

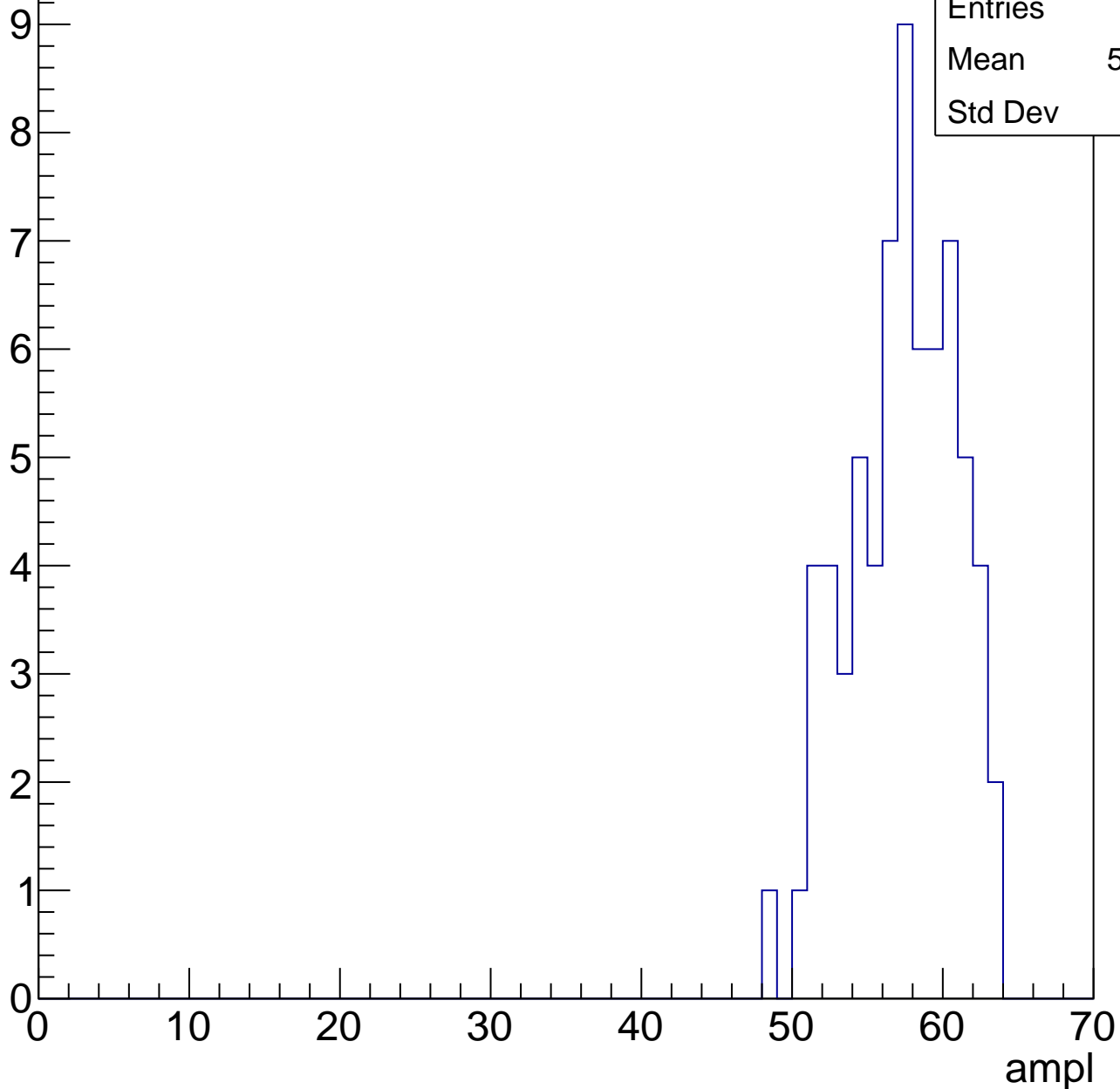
Entries	62
Mean	50.23
Std Dev	3.438



B1L103S, U17-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

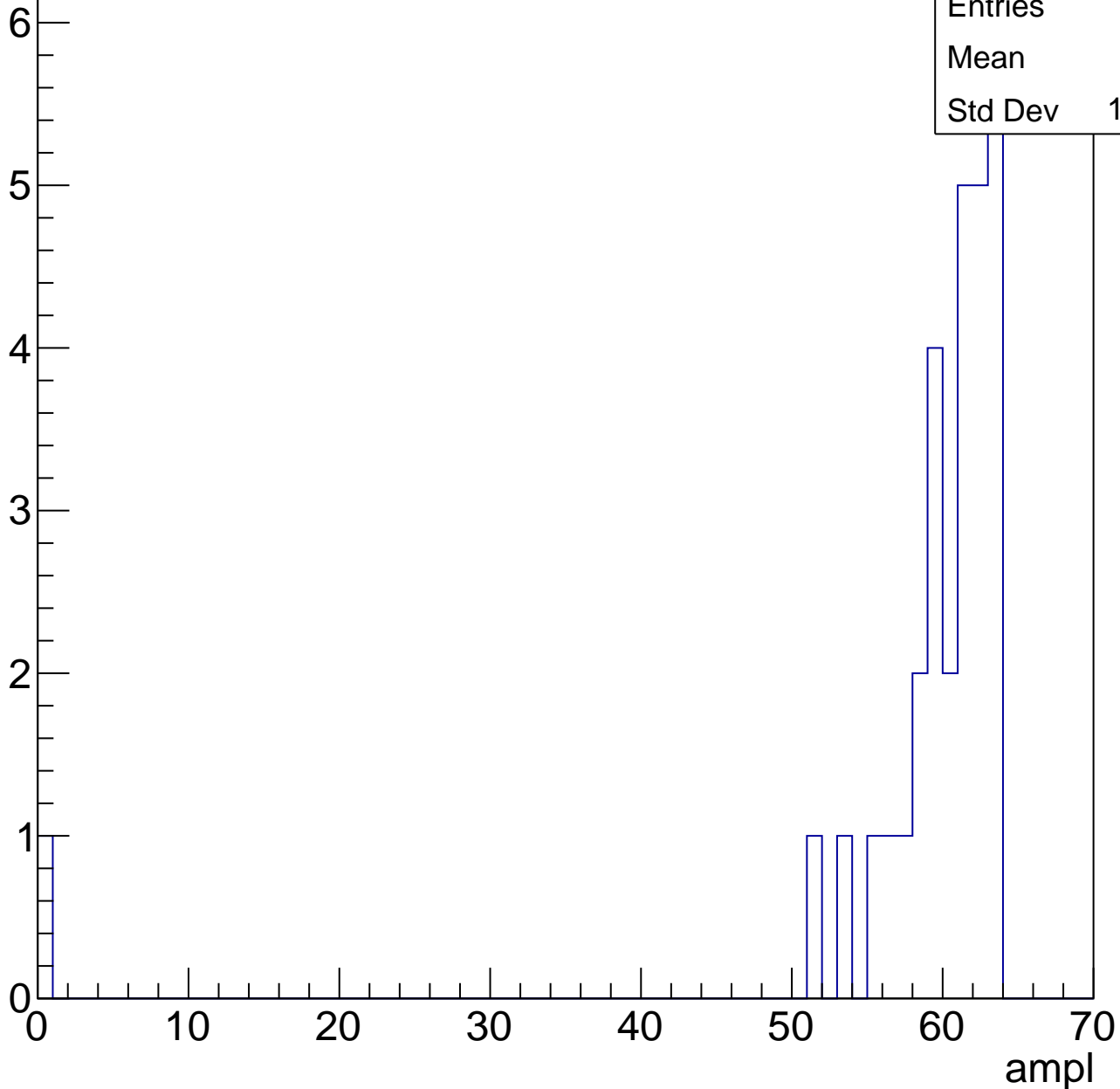


B1L103S, U17-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

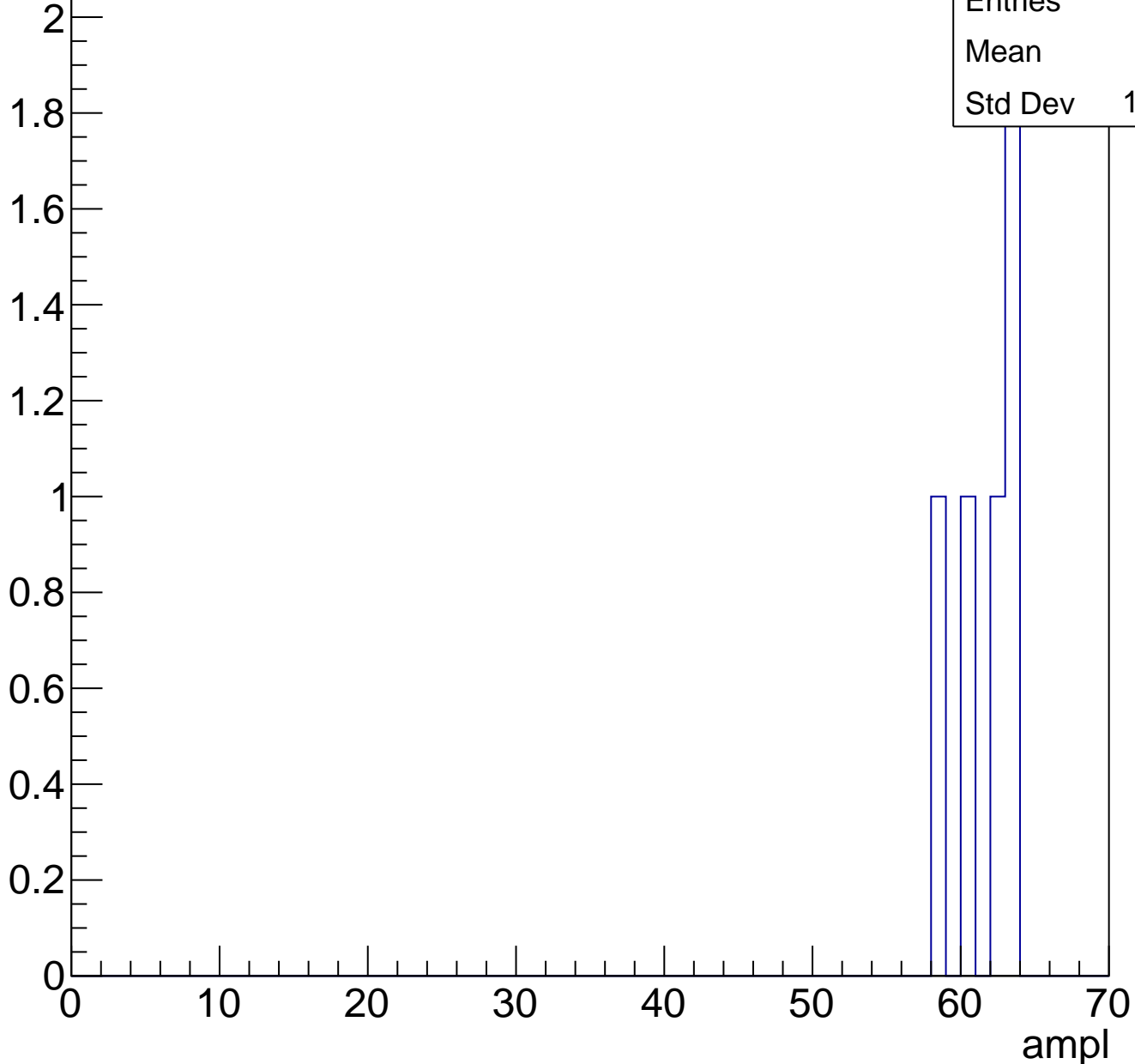
Entries	30
Mean	57.9
Std Dev	11.16



B1L103S, U17-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch79, adc0

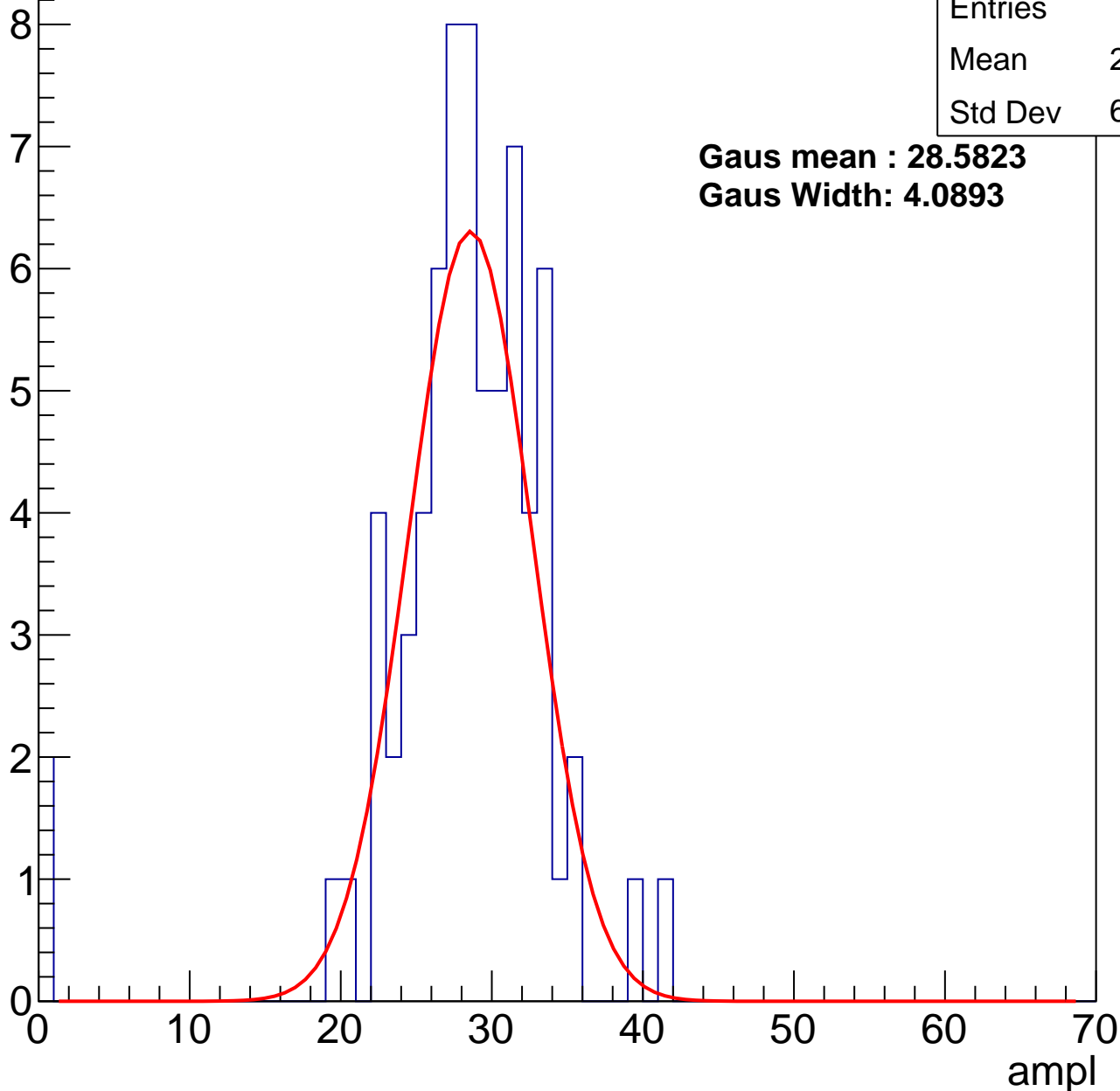
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	27.65
Std Dev	6.212

Gaus mean : 28.5823

Gaus Width: 4.0893



B1L103S, U17-ch79, adc1

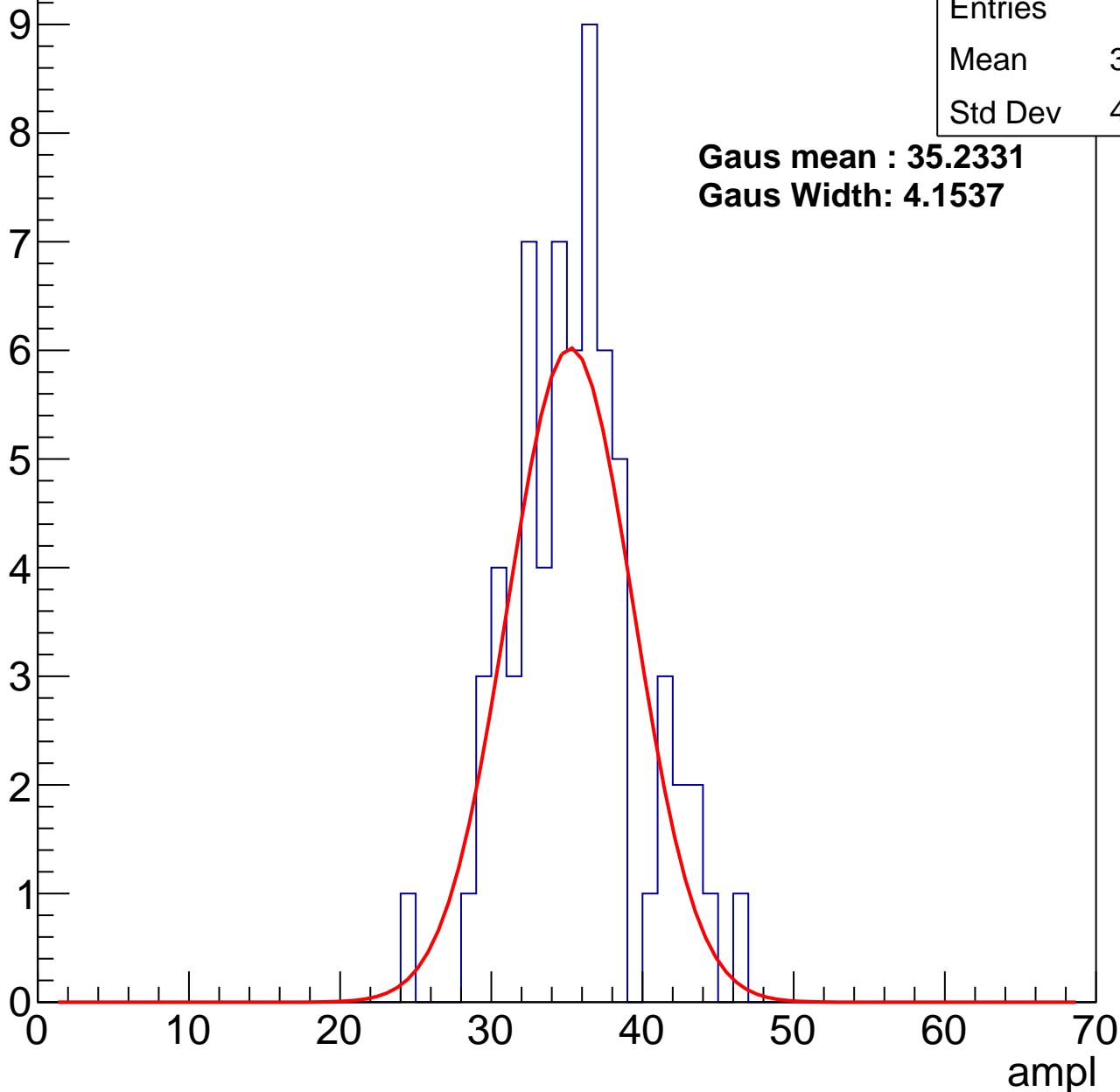
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.08
Std Dev	4.175

Gaus mean : 35.2331

Gaus Width: 4.1537



B1L103S, U17-ch79, adc2

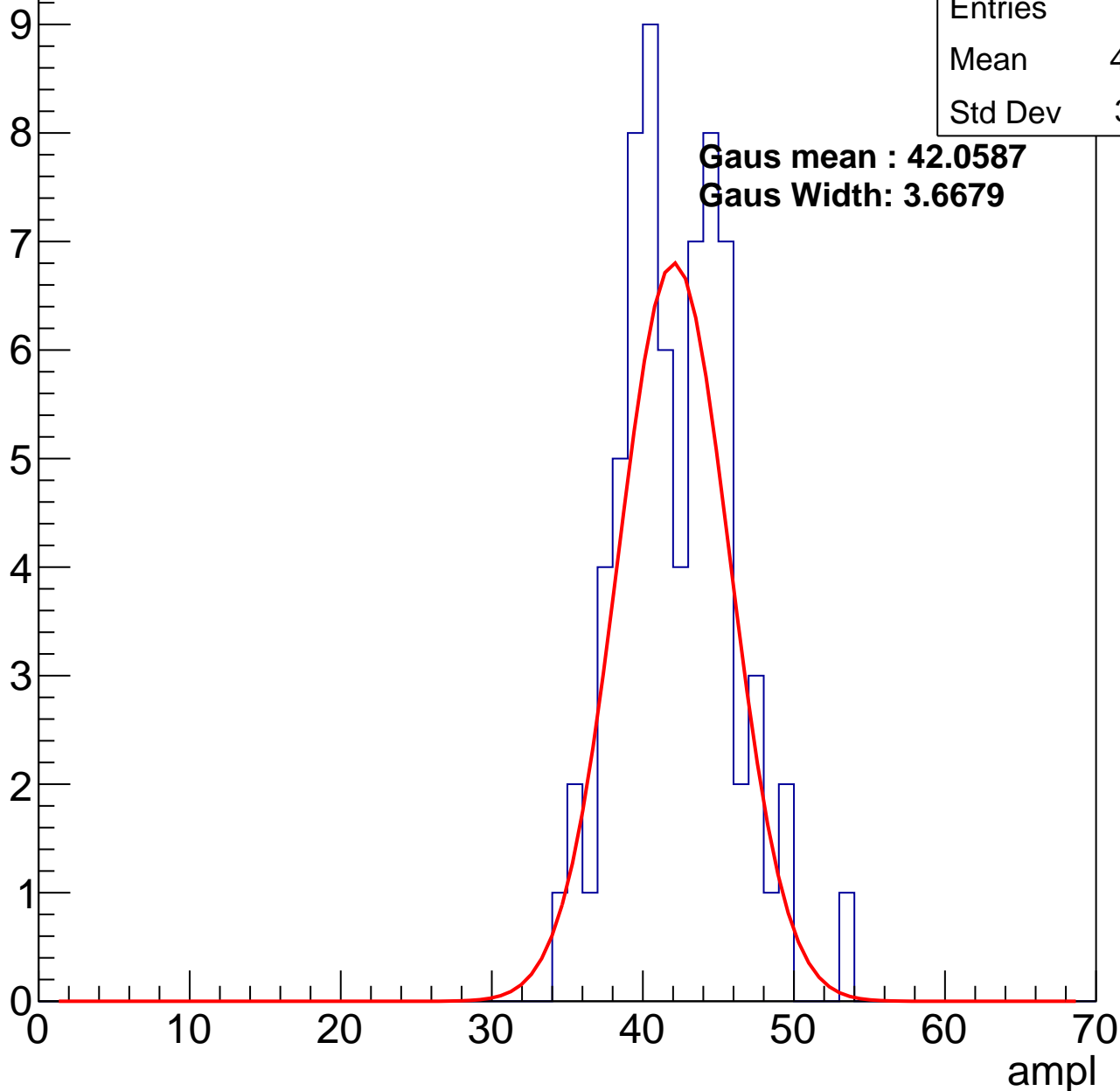
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	41.75
Std Dev	3.661

Gaus mean : 42.0587

Gaus Width: 3.6679

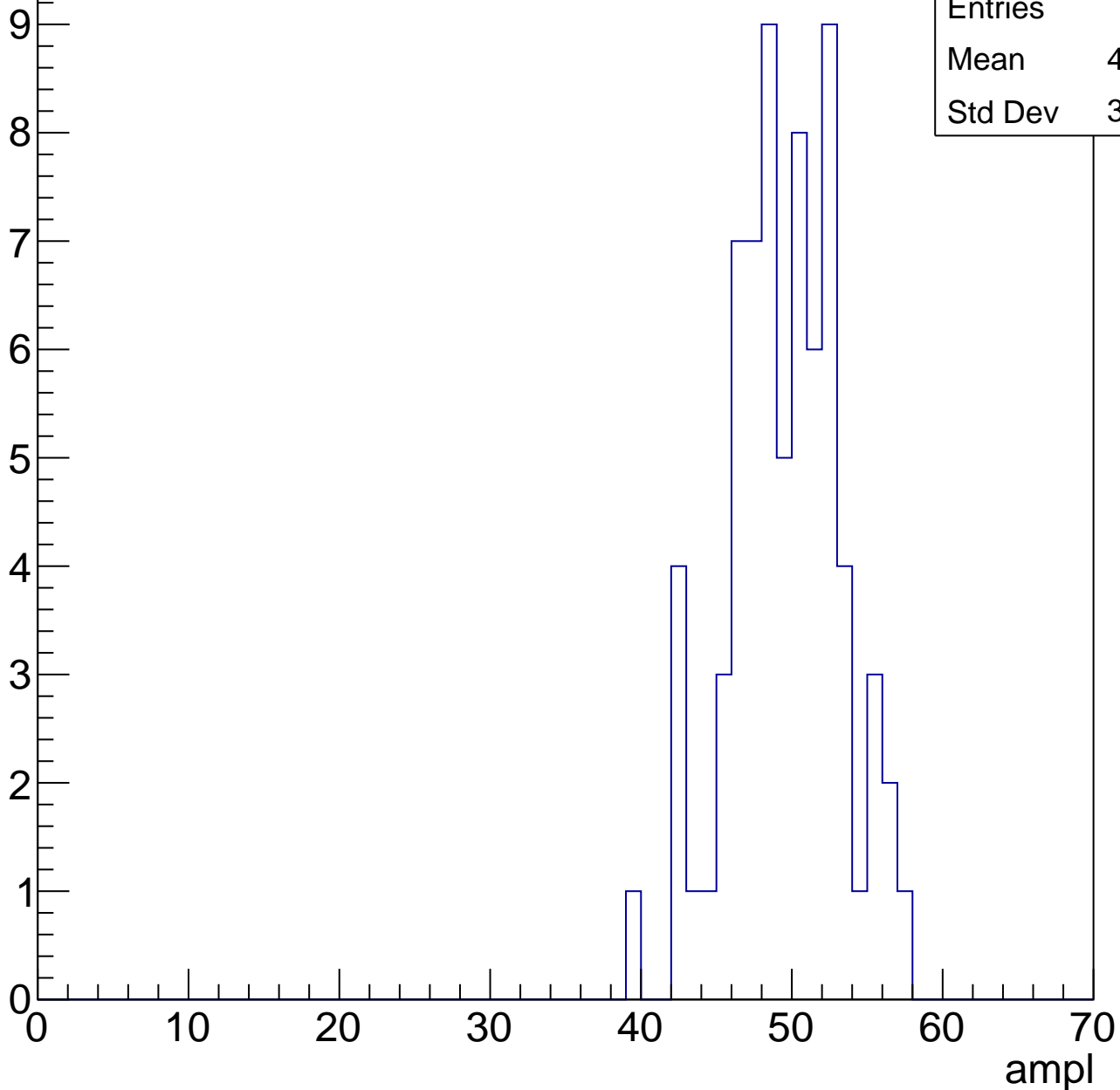


B1L103S, U17-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.04
Std Dev	3.683

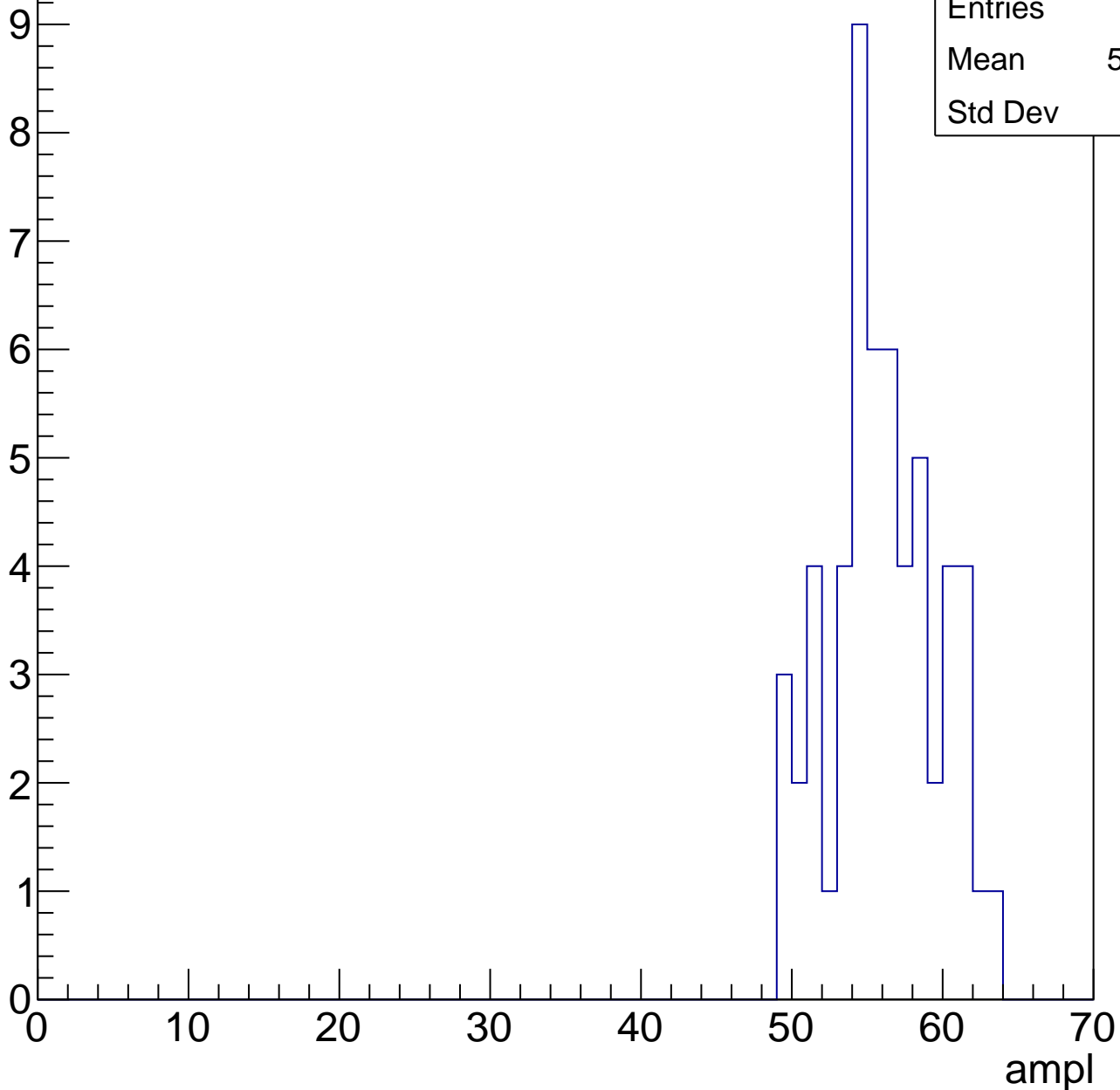


B1L103S, U17-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.57
Std Dev	3.52



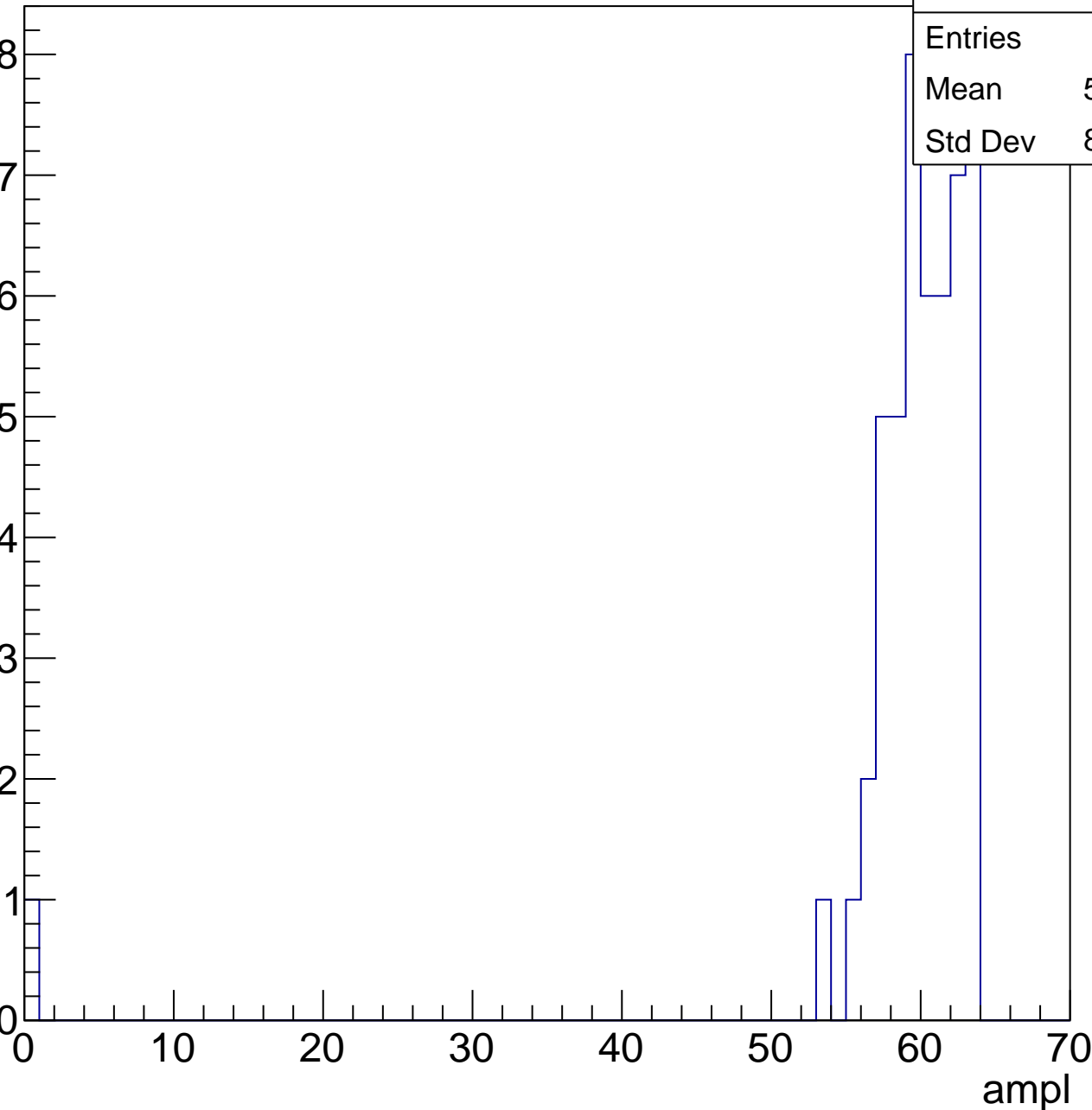
B1L103S, U17-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.62
Std Dev	8.706

8
7
6
5
4
3
2
1
0



B1L103S, U17-ch79, adc6

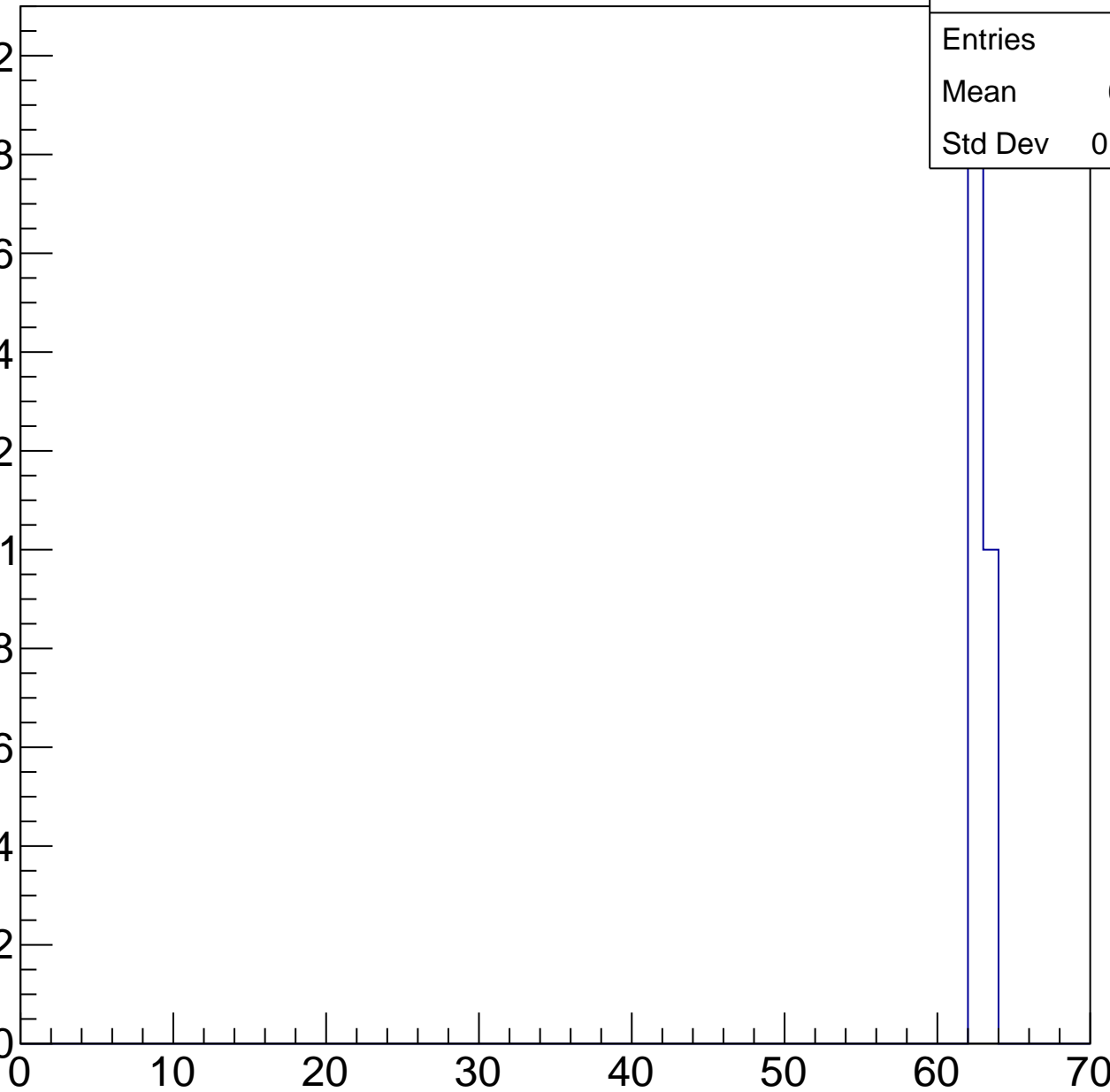
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L103S, U17-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch80, adc0

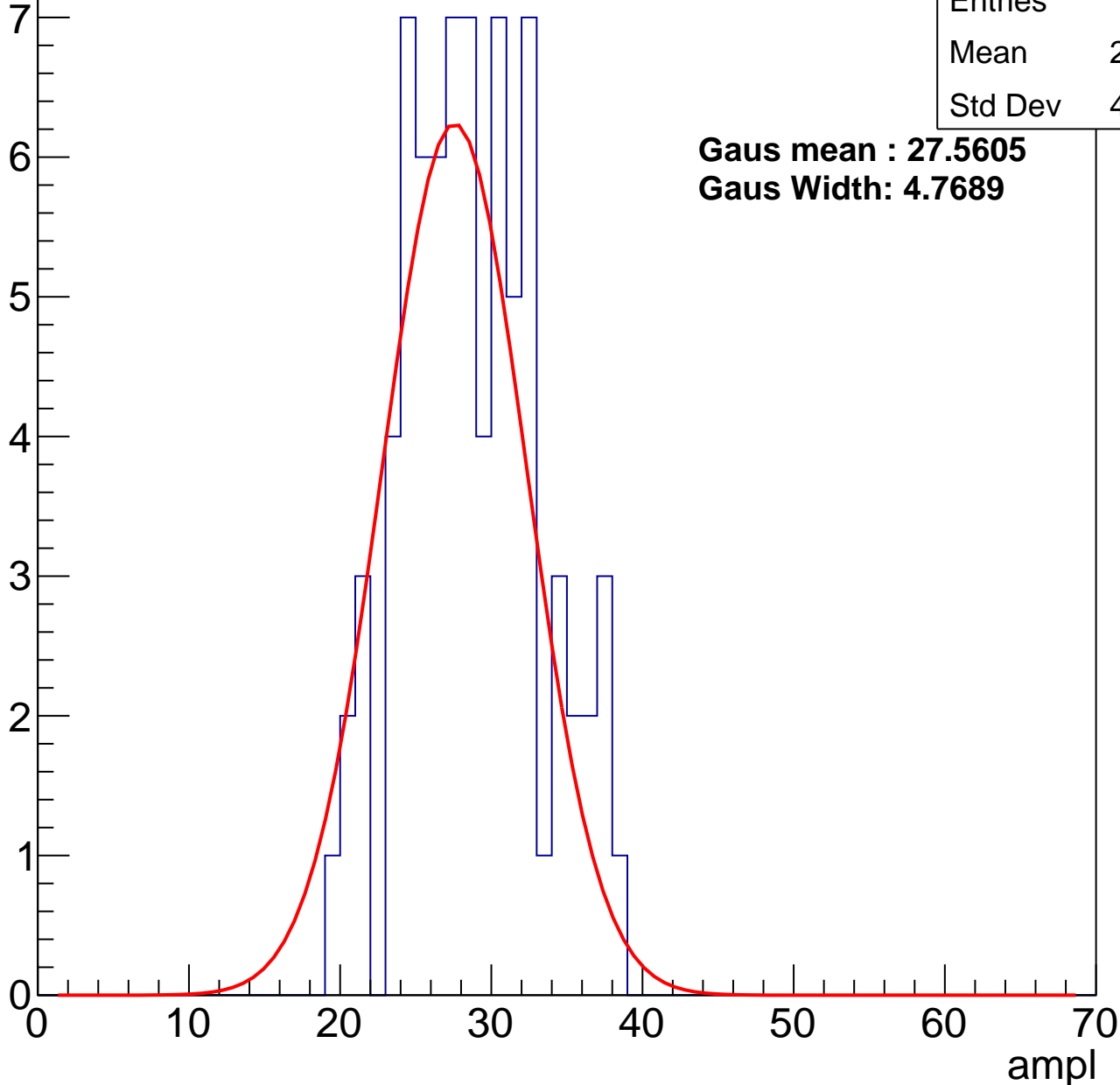
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	28.26
Std Dev	4.442

Gaus mean : 27.5605

Gaus Width: 4.7689



B1L103S, U17-ch80, adc1

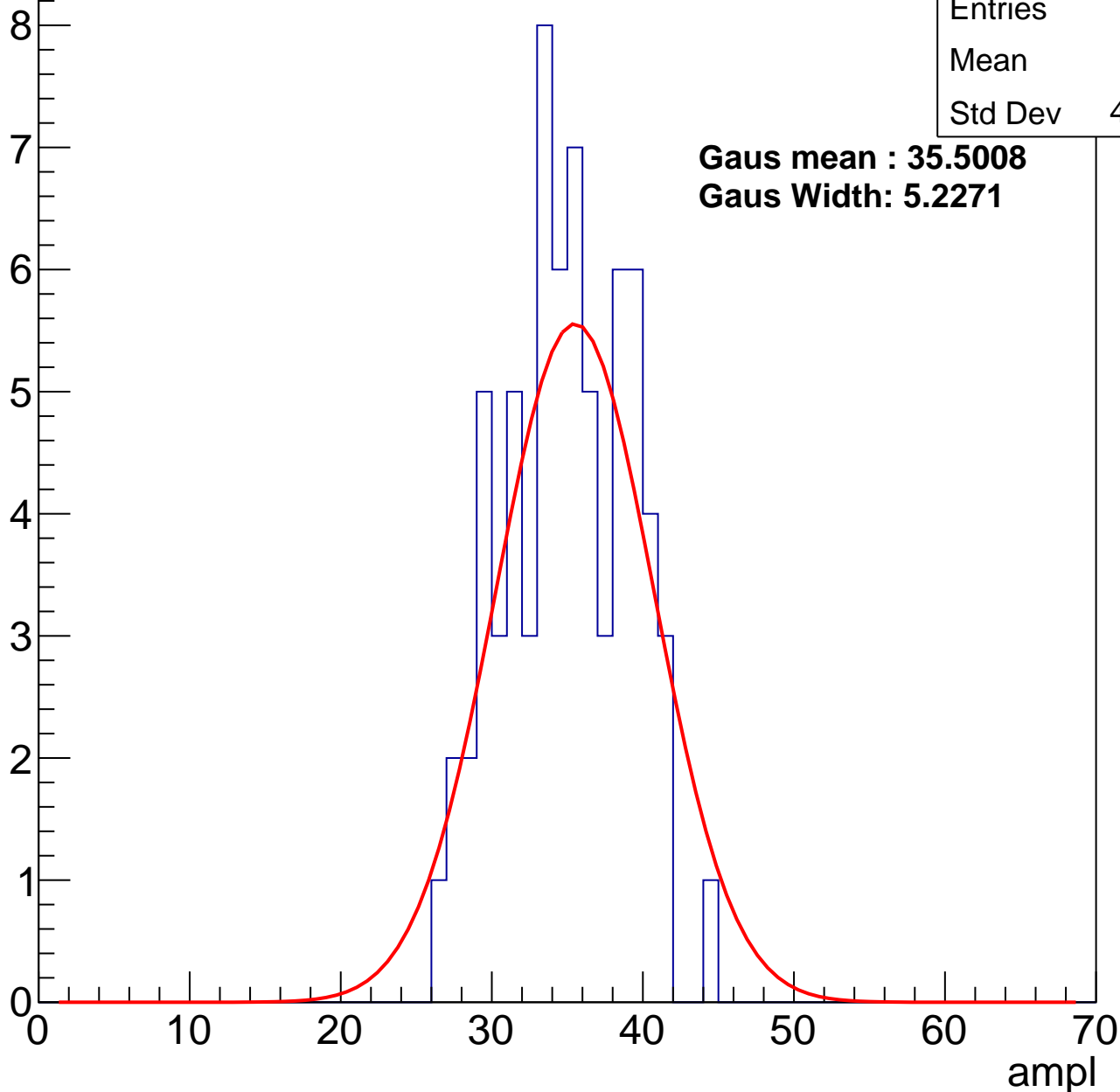
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.5
Std Dev	4.045

Gaus mean : 35.5008

Gaus Width: 5.2271



B1L103S, U17-ch80, adc2

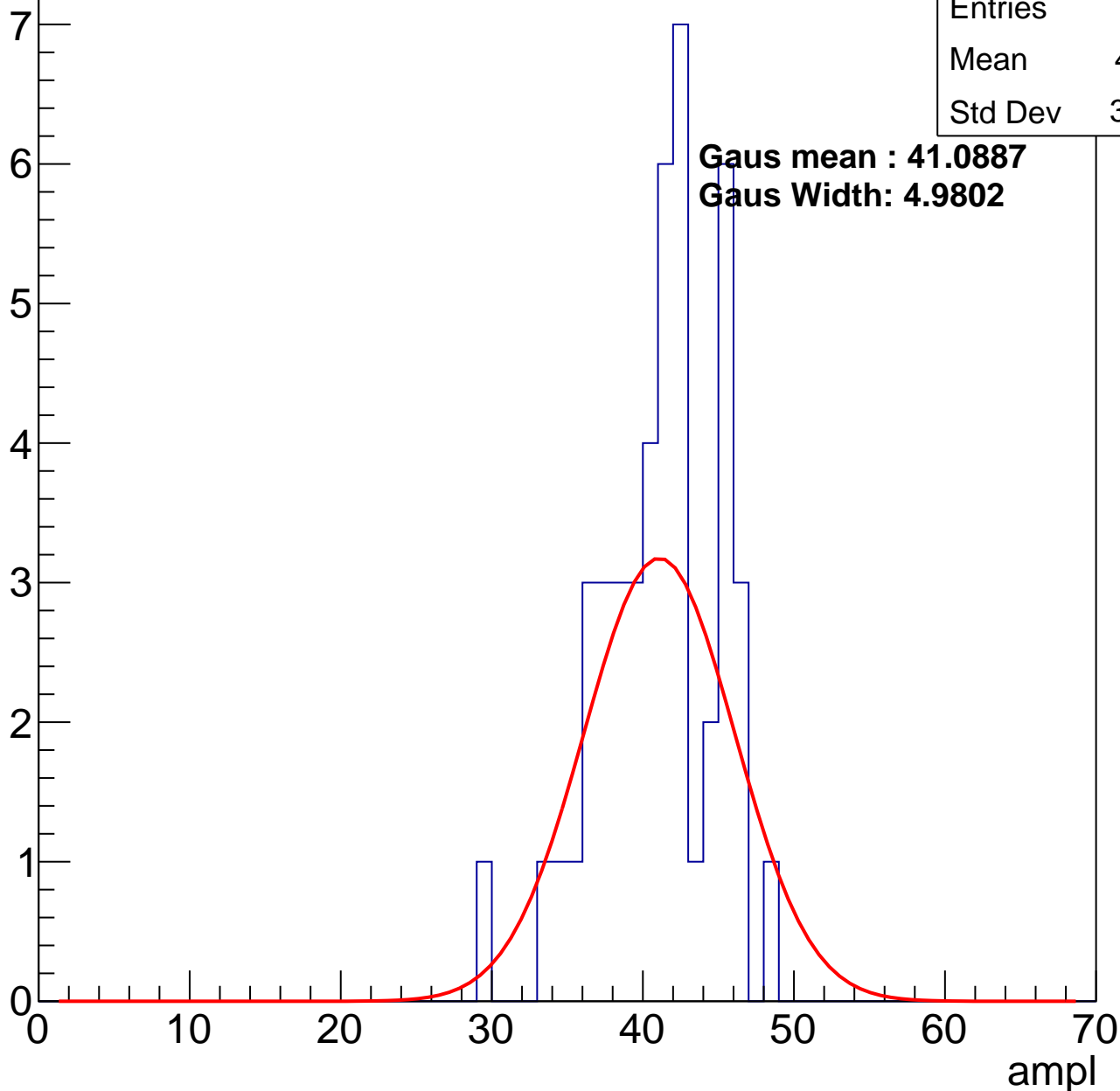
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	40.61
Std Dev	3.904

Gaus mean : 41.0887

Gaus Width: 4.9802

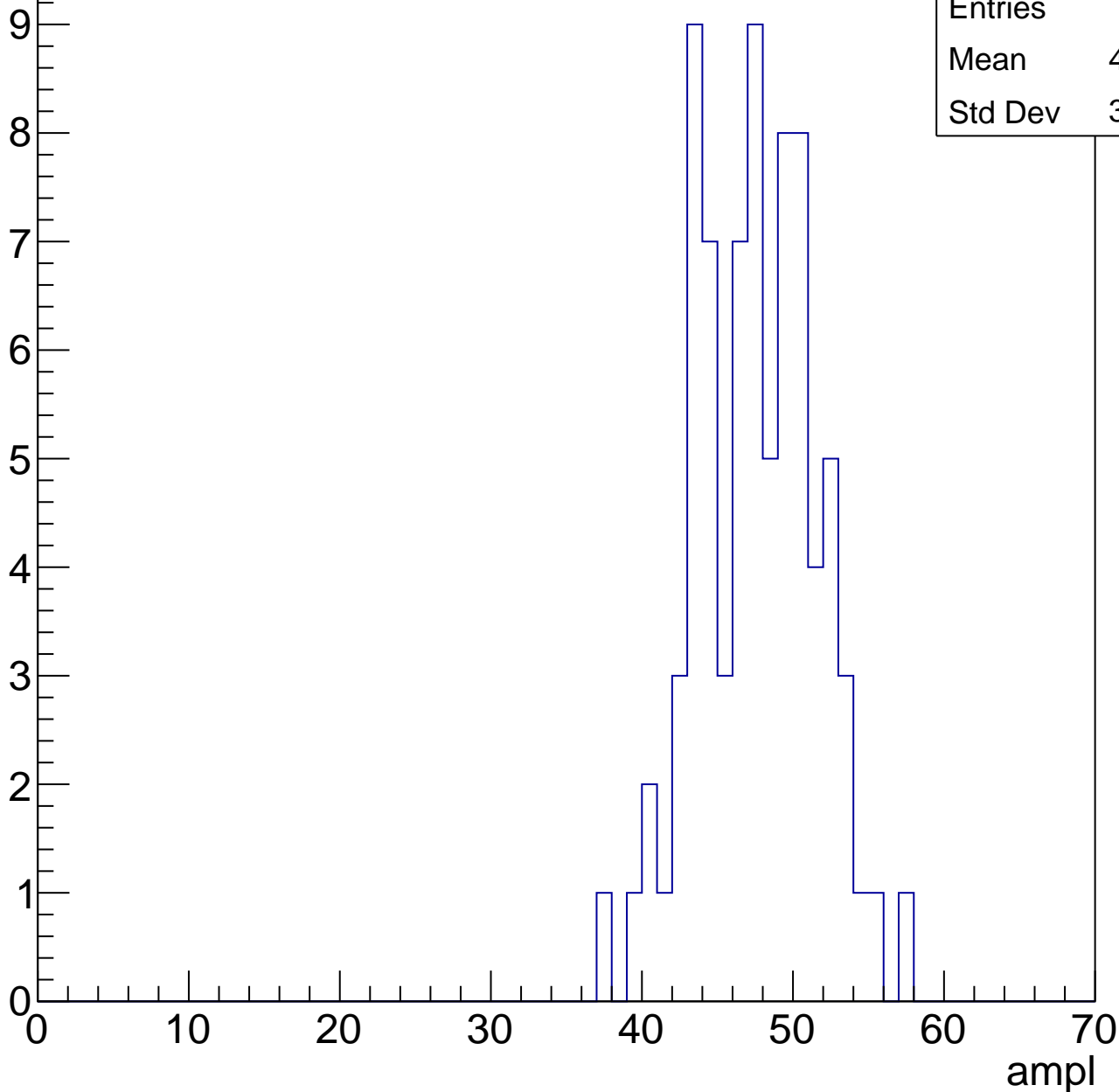


B1L103S, U17-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

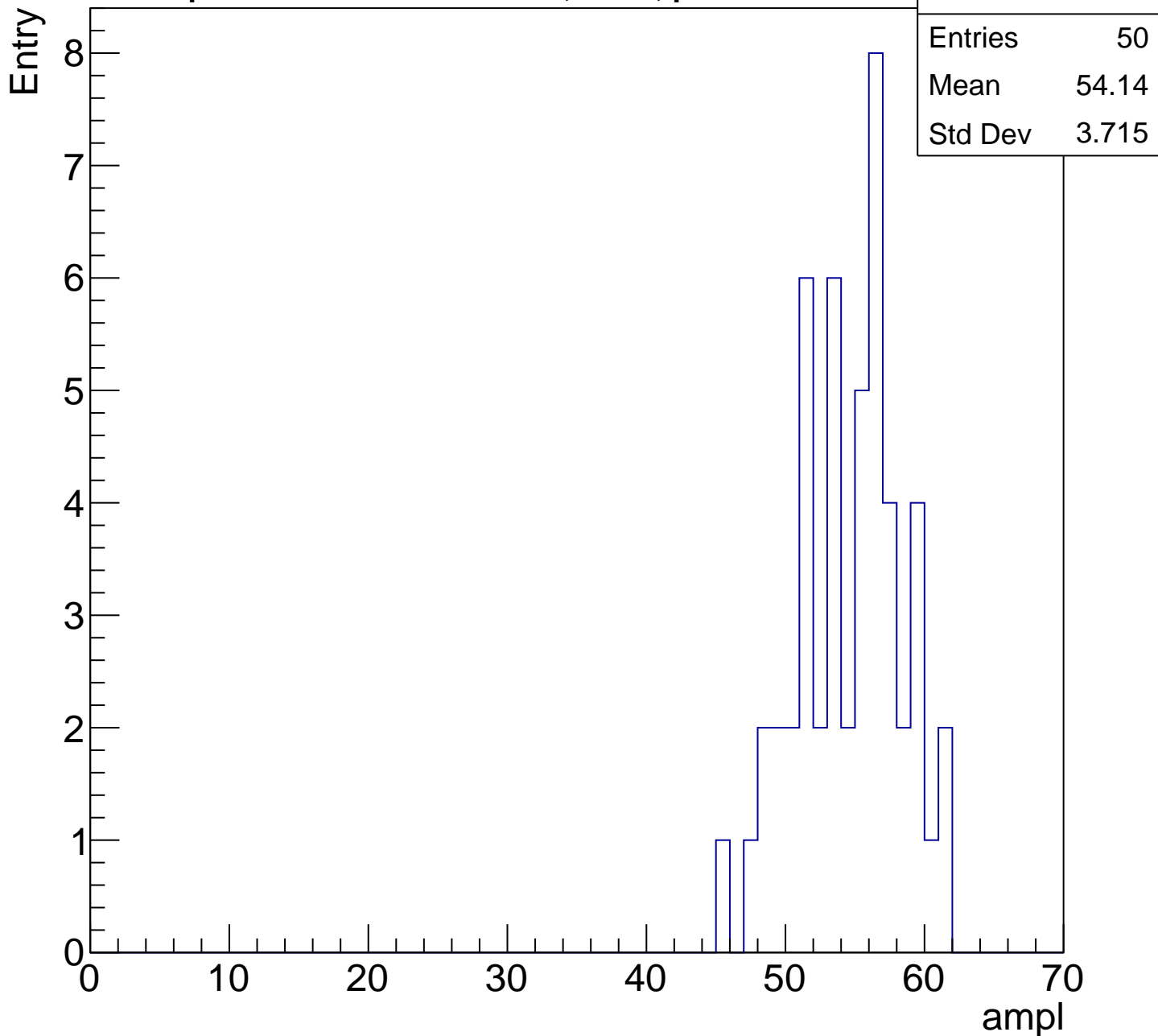
Entry

Entries	79
Mean	47.08
Std Dev	3.926



B1L103S, U17-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

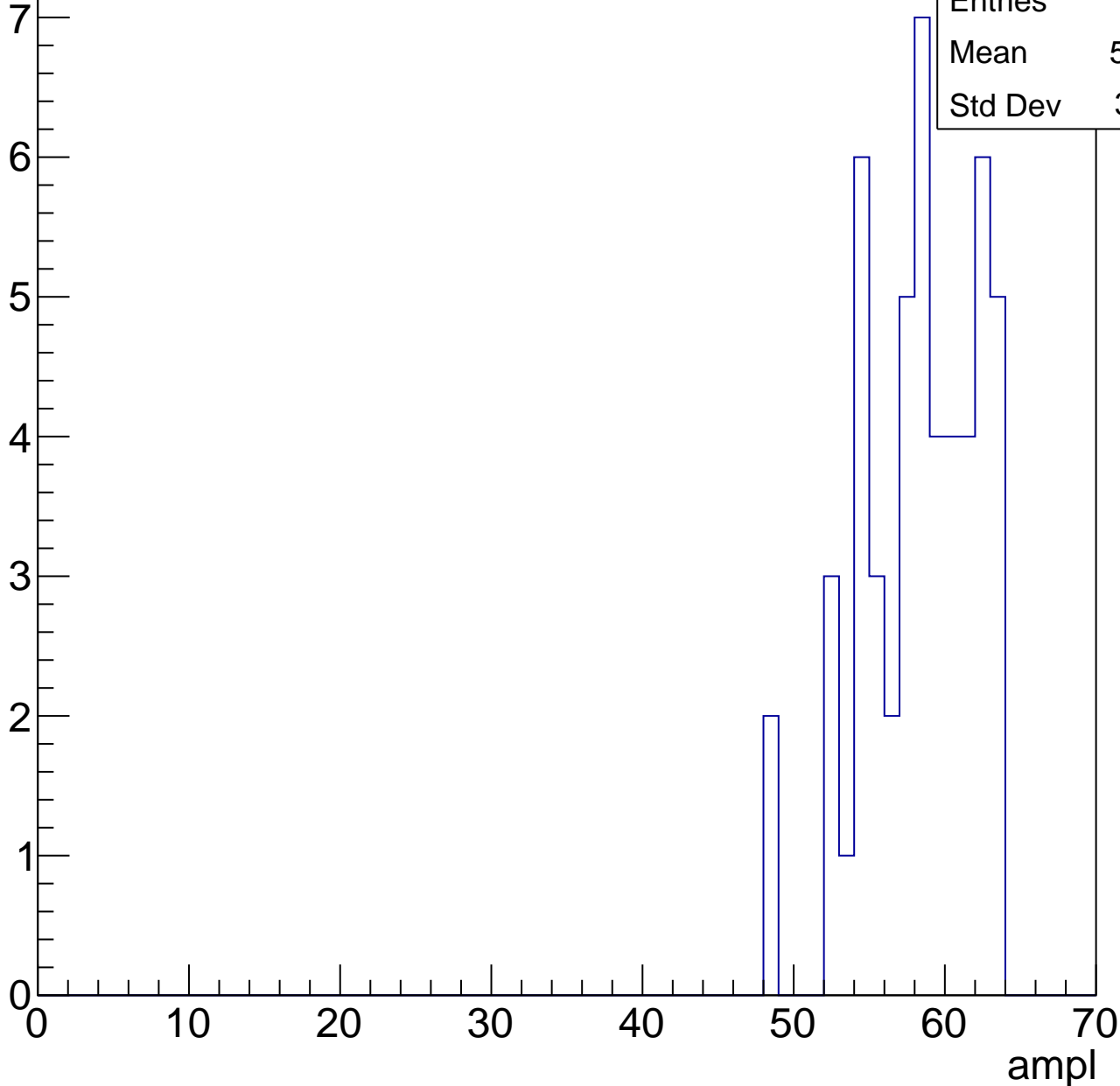


B1L103S, U17-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.77
Std Dev	3.781

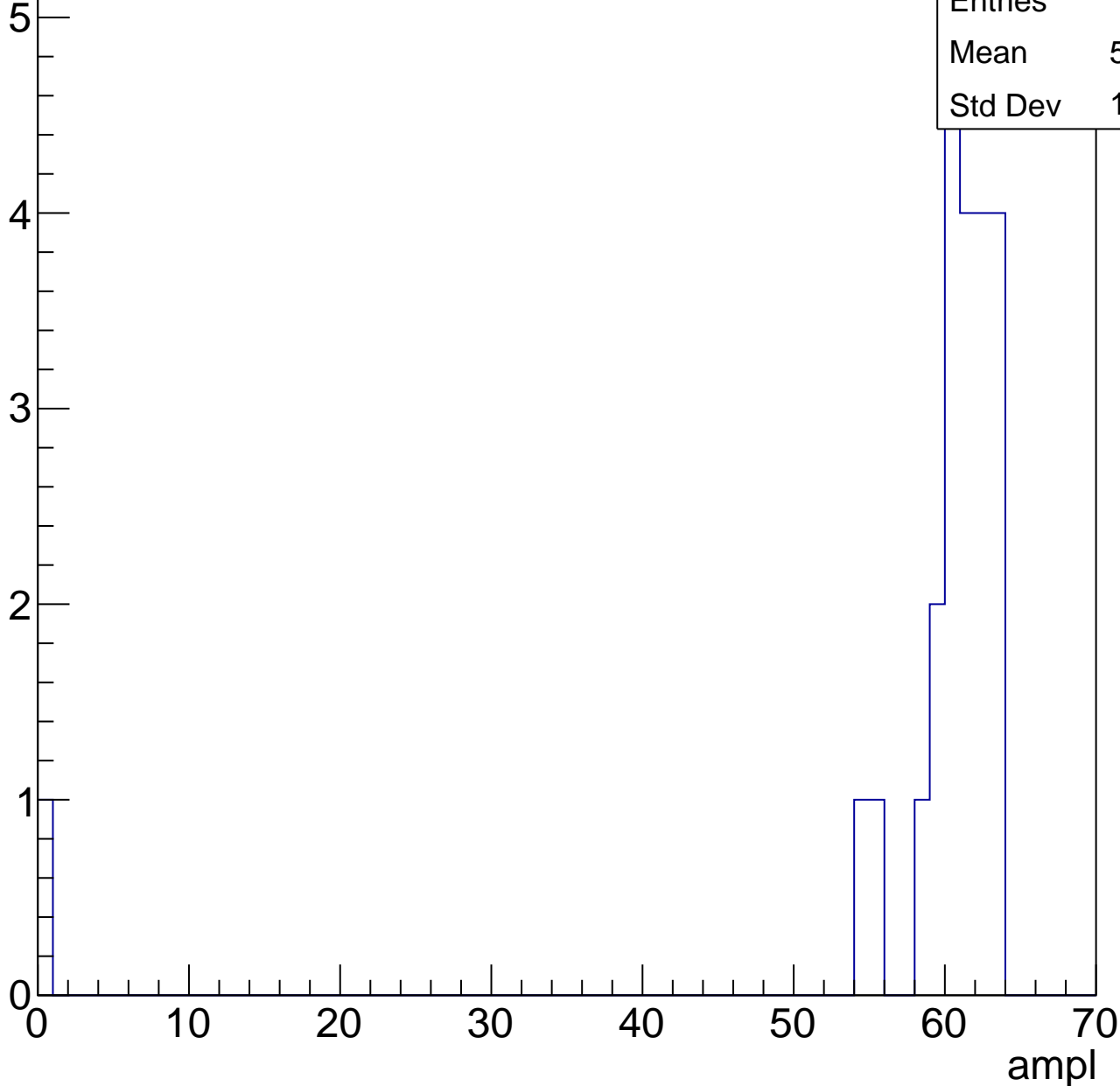


B1L103S, U17-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	57.78
Std Dev	12.53



B1L103S, U17-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch81, adc0

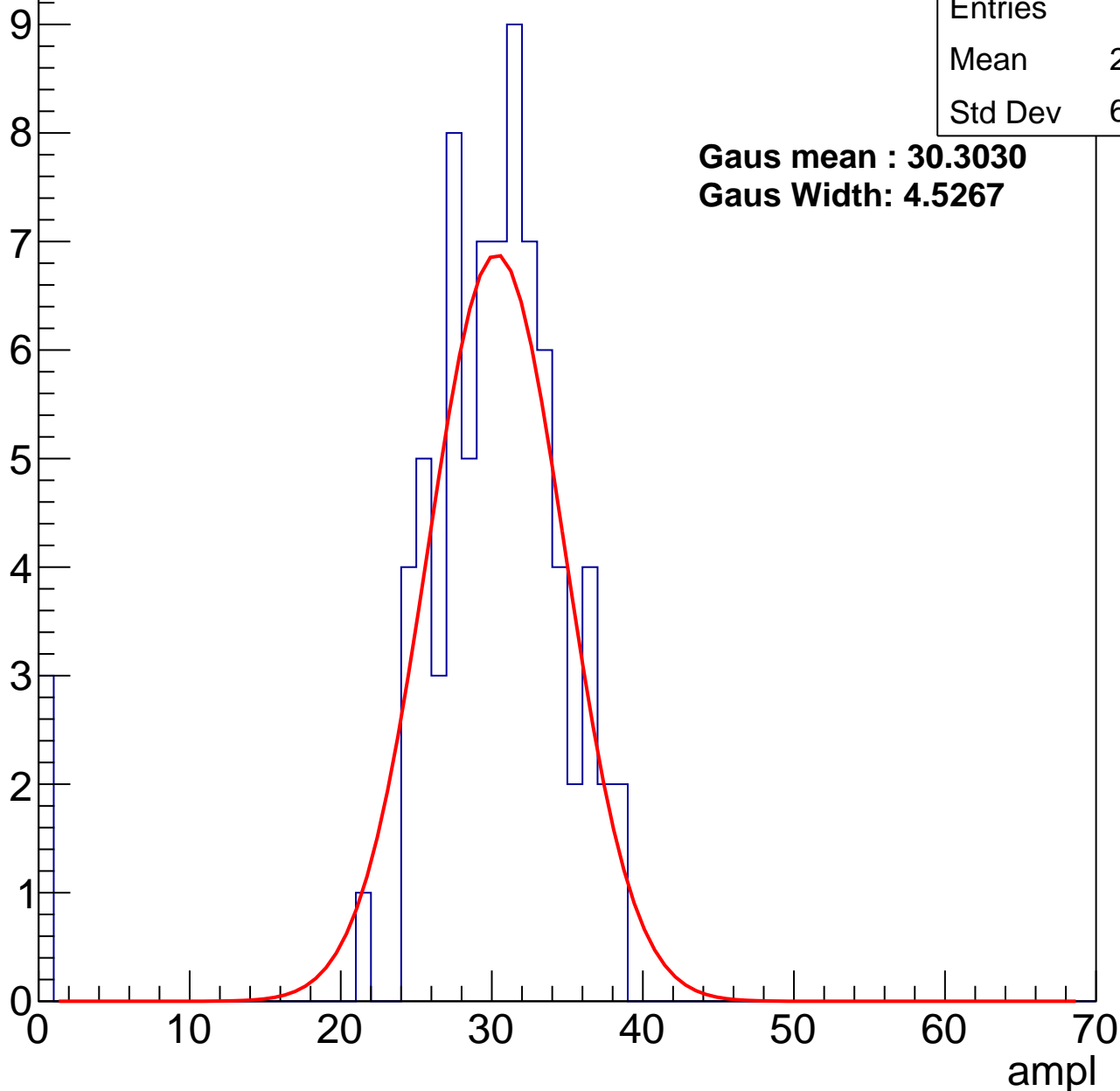
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.99
Std Dev	6.835

Gaus mean : 30.3030

Gaus Width: 4.5267



B1L103S, U17-ch81, adc1

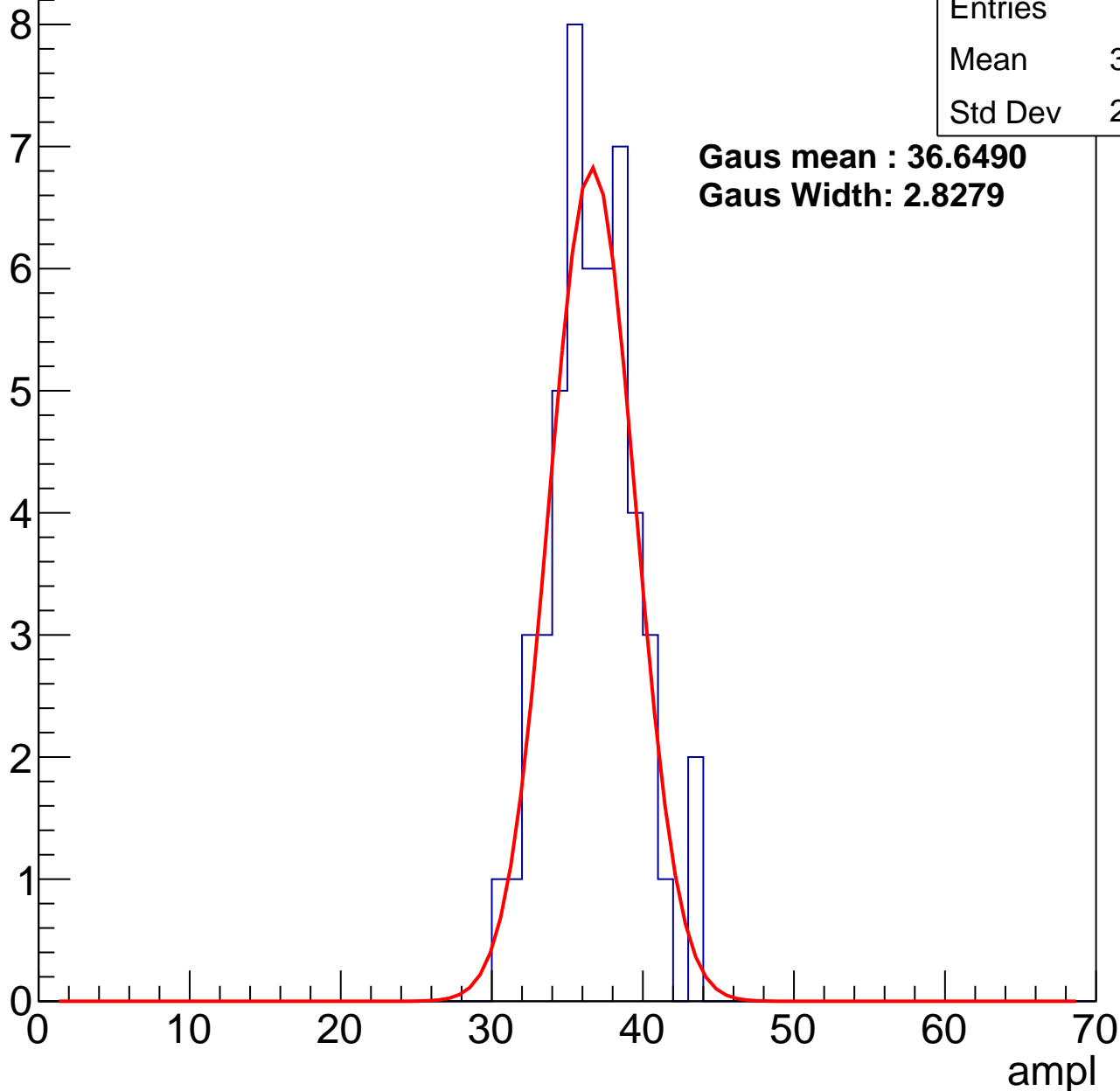
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	36.26
Std Dev	2.834

Gaus mean : 36.6490

Gaus Width: 2.8279



B1L103S, U17-ch81, adc2

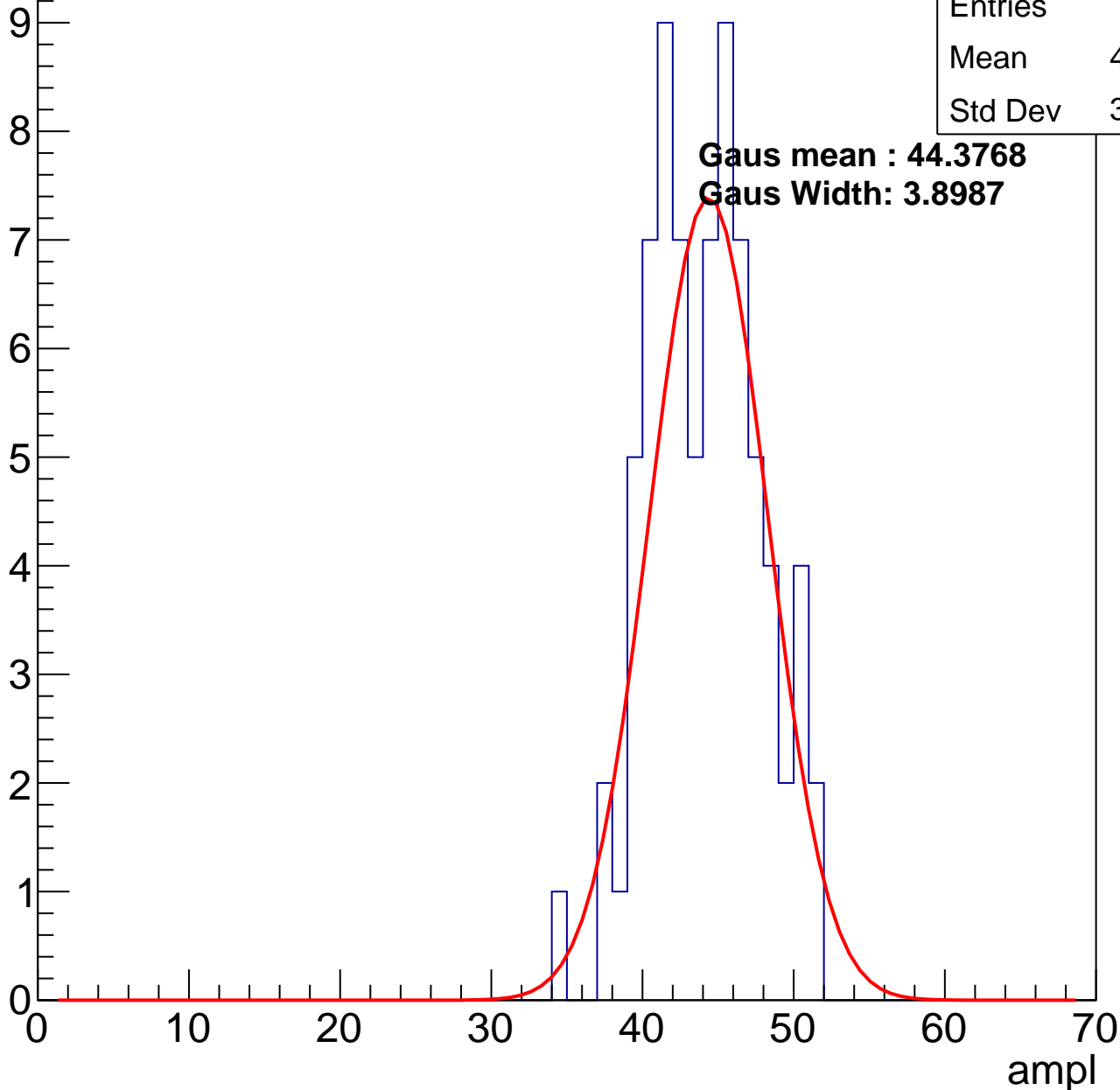
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	43.65
Std Dev	3.628

Gaus mean : 44.3768

Gaus Width: 3.8987

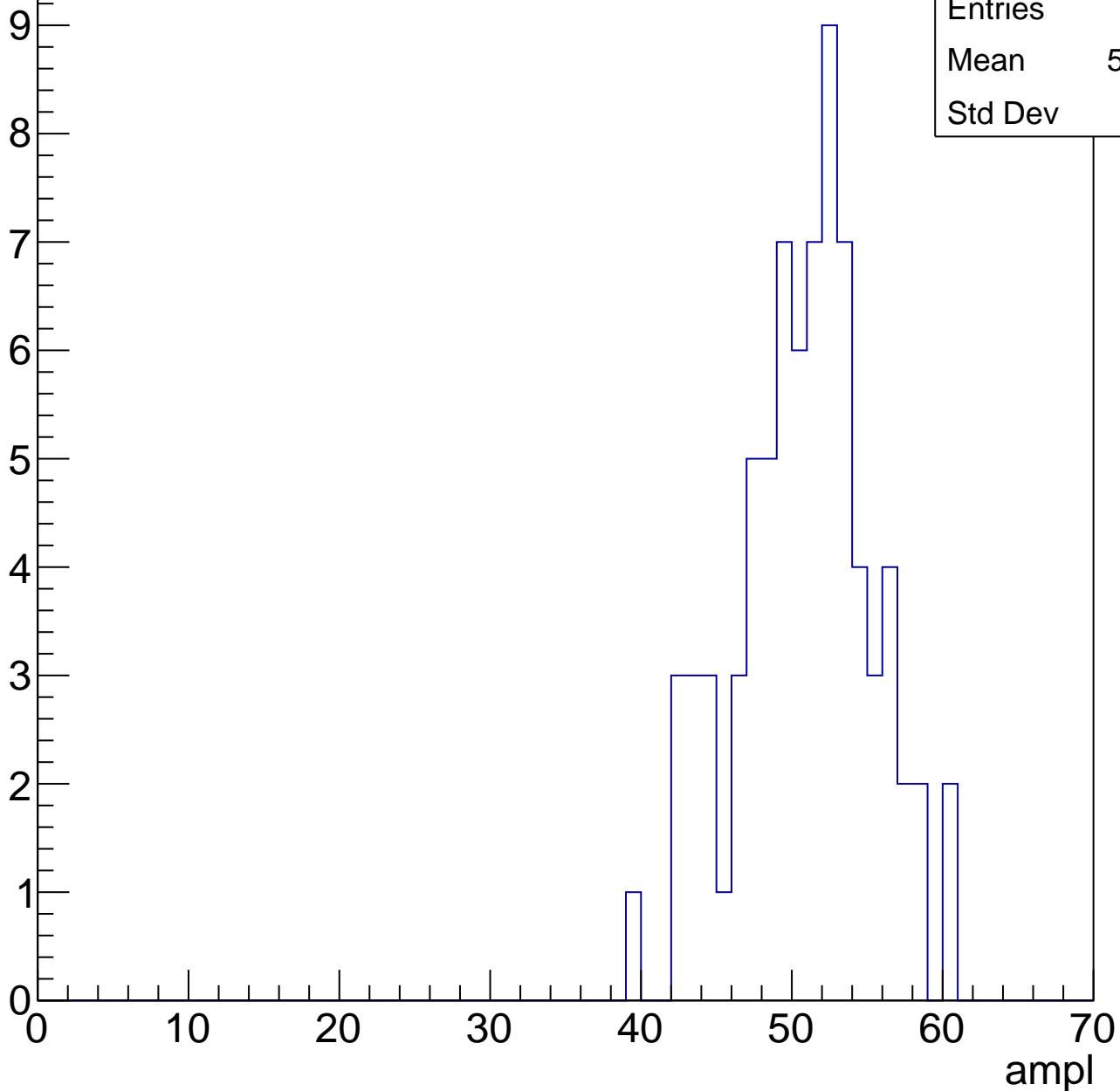


B1L103S, U17-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	50.36
Std Dev	4.44

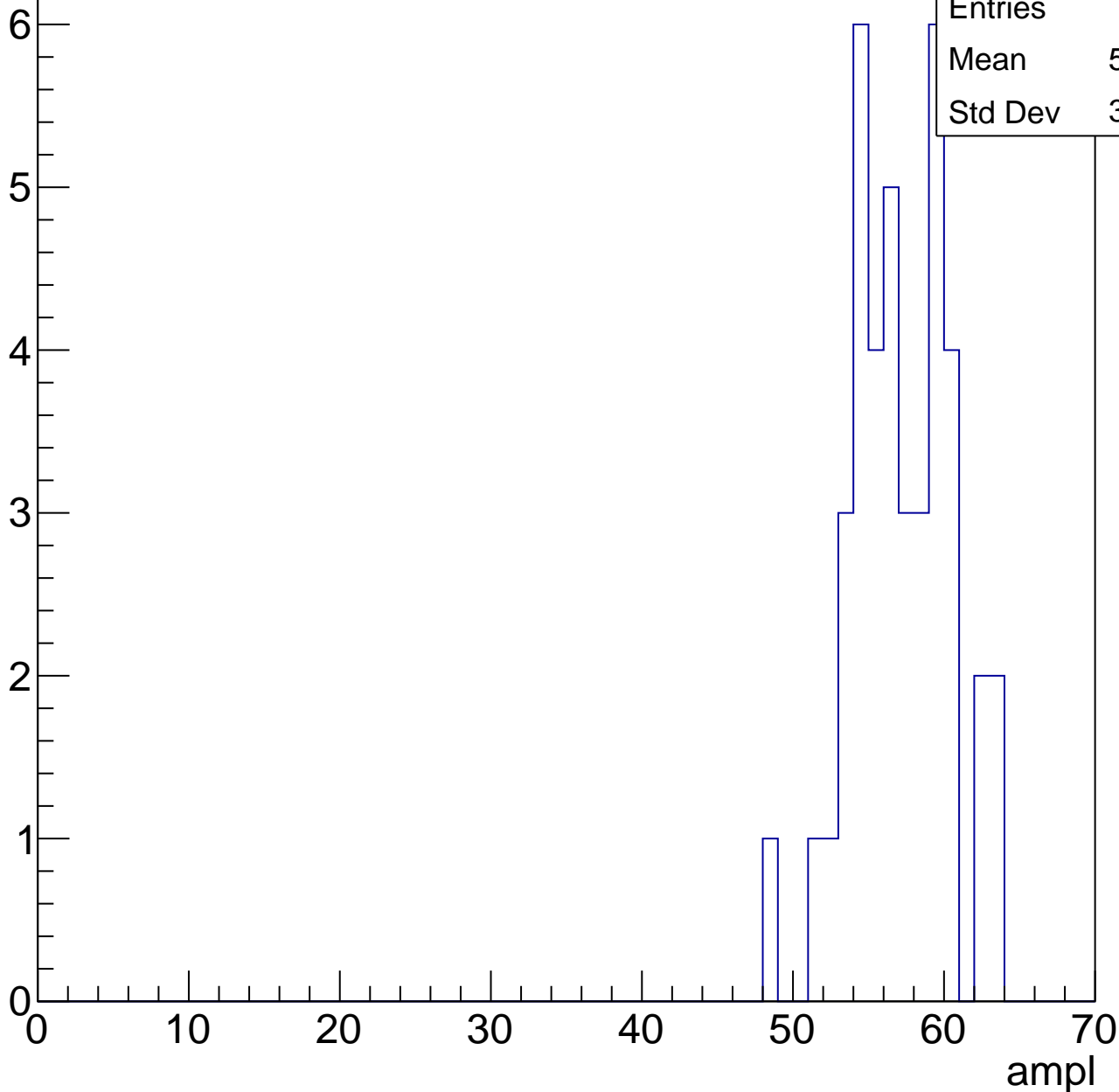


B1L103S, U17-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	56.66
Std Dev	3.295

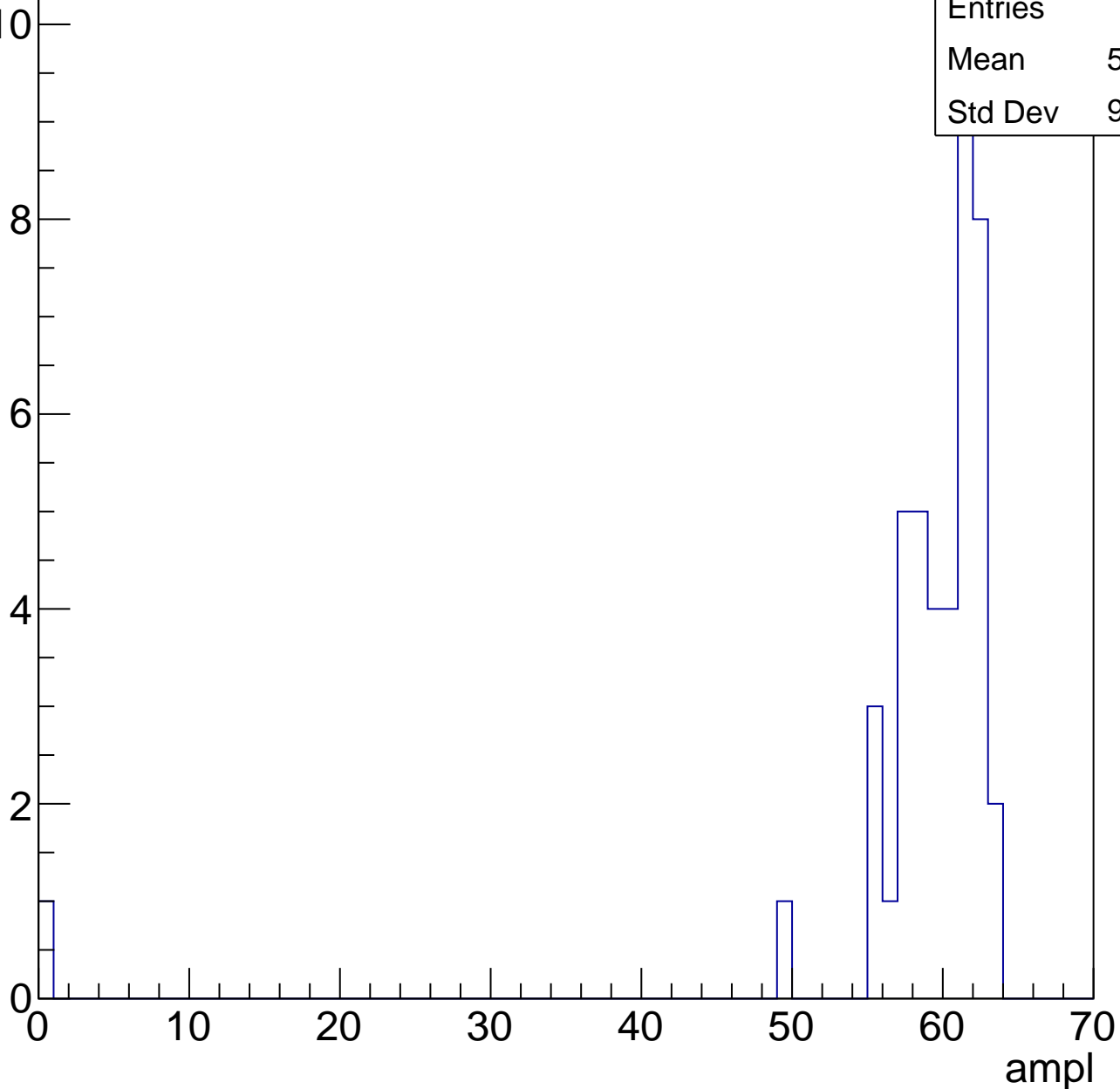


B1L103S, U17-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.02
Std Dev	9.255



B1L103S, U17-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U17-ch82, adc0

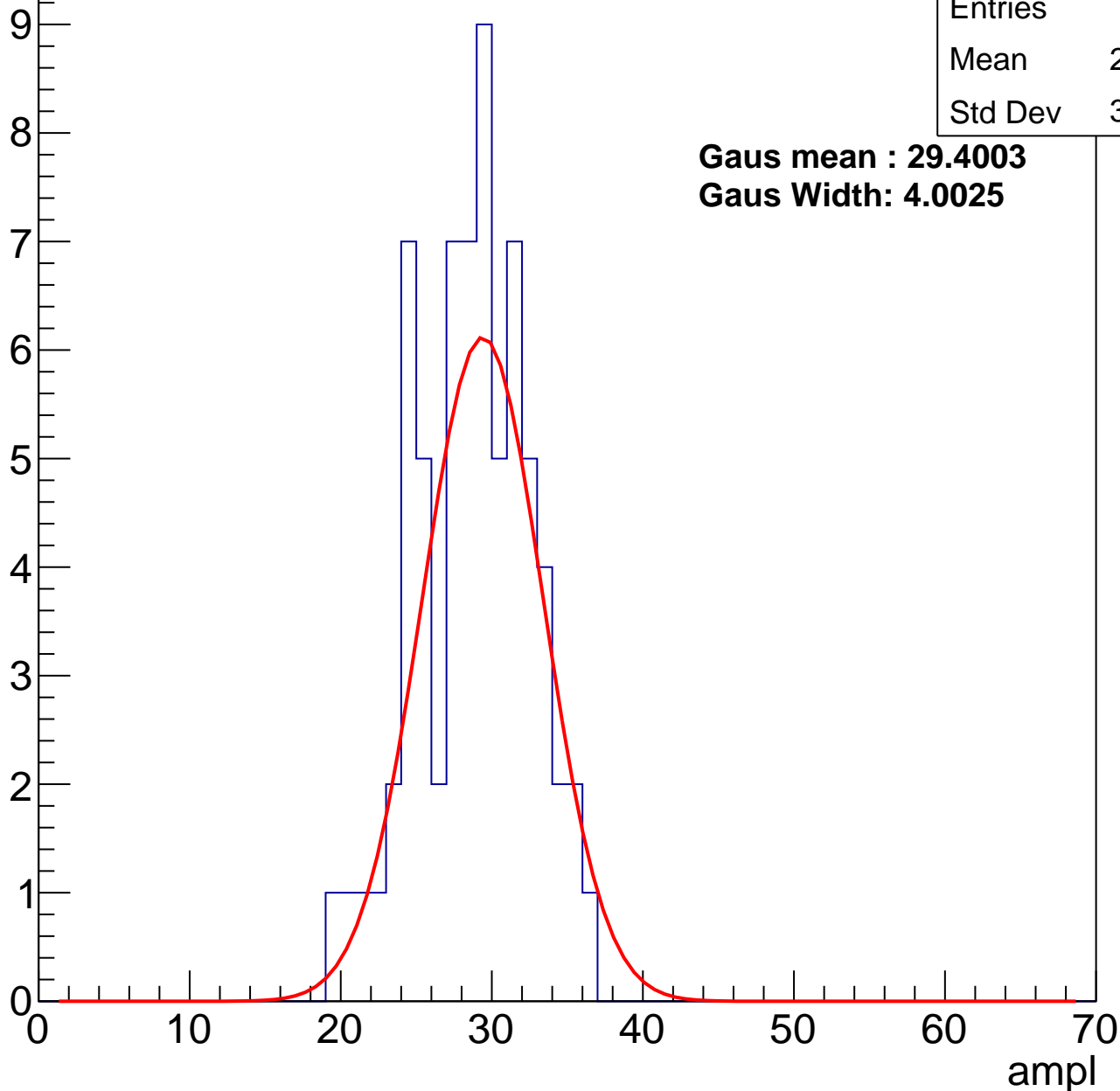
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.29
Std Dev	3.715

Gaus mean : 29.4003

Gaus Width: 4.0025



B1L103S, U17-ch82, adc1

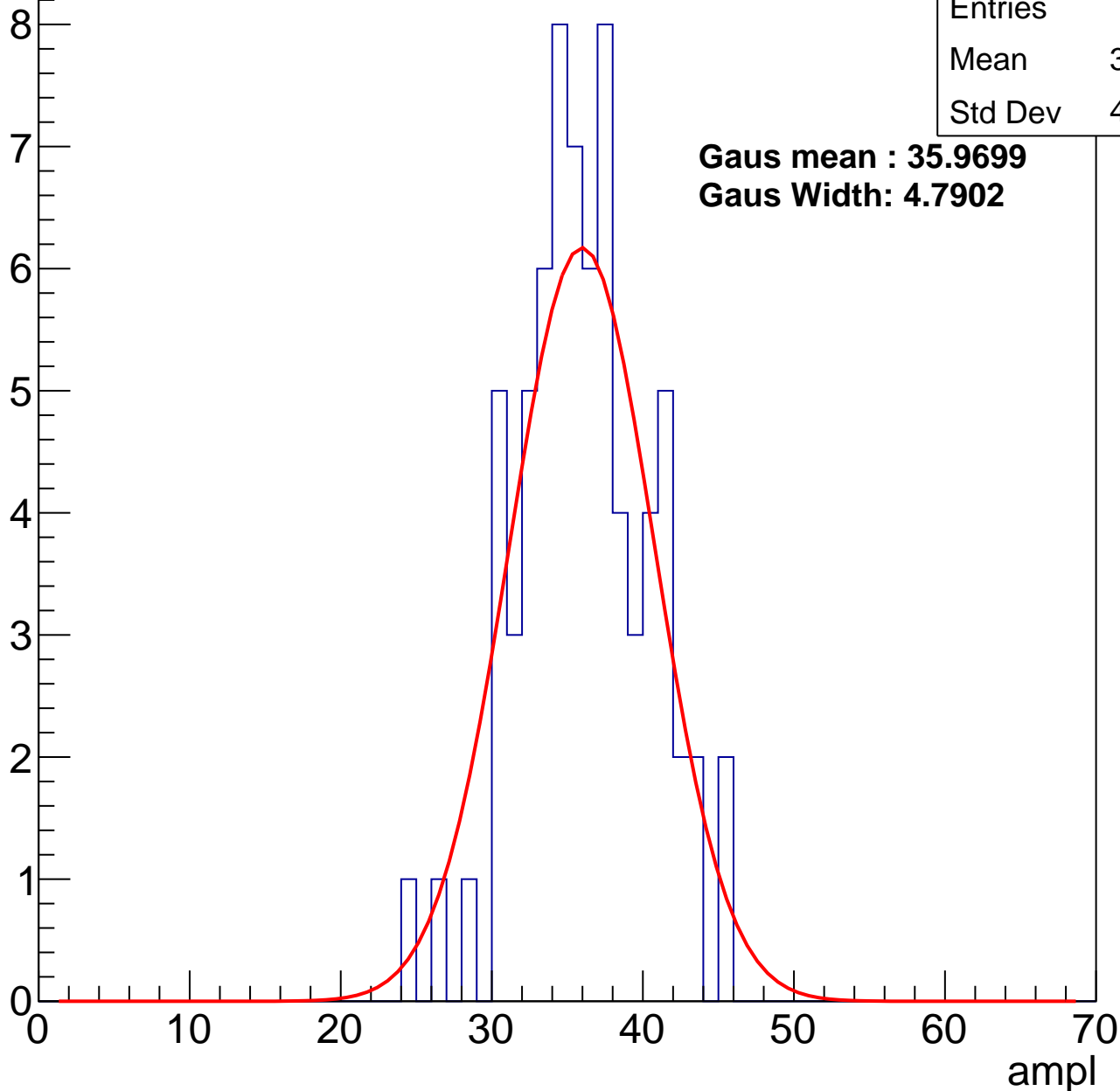
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.64
Std Dev	4.218

Gaus mean : 35.9699

Gaus Width: 4.7902



B1L103S, U17-ch82, adc2

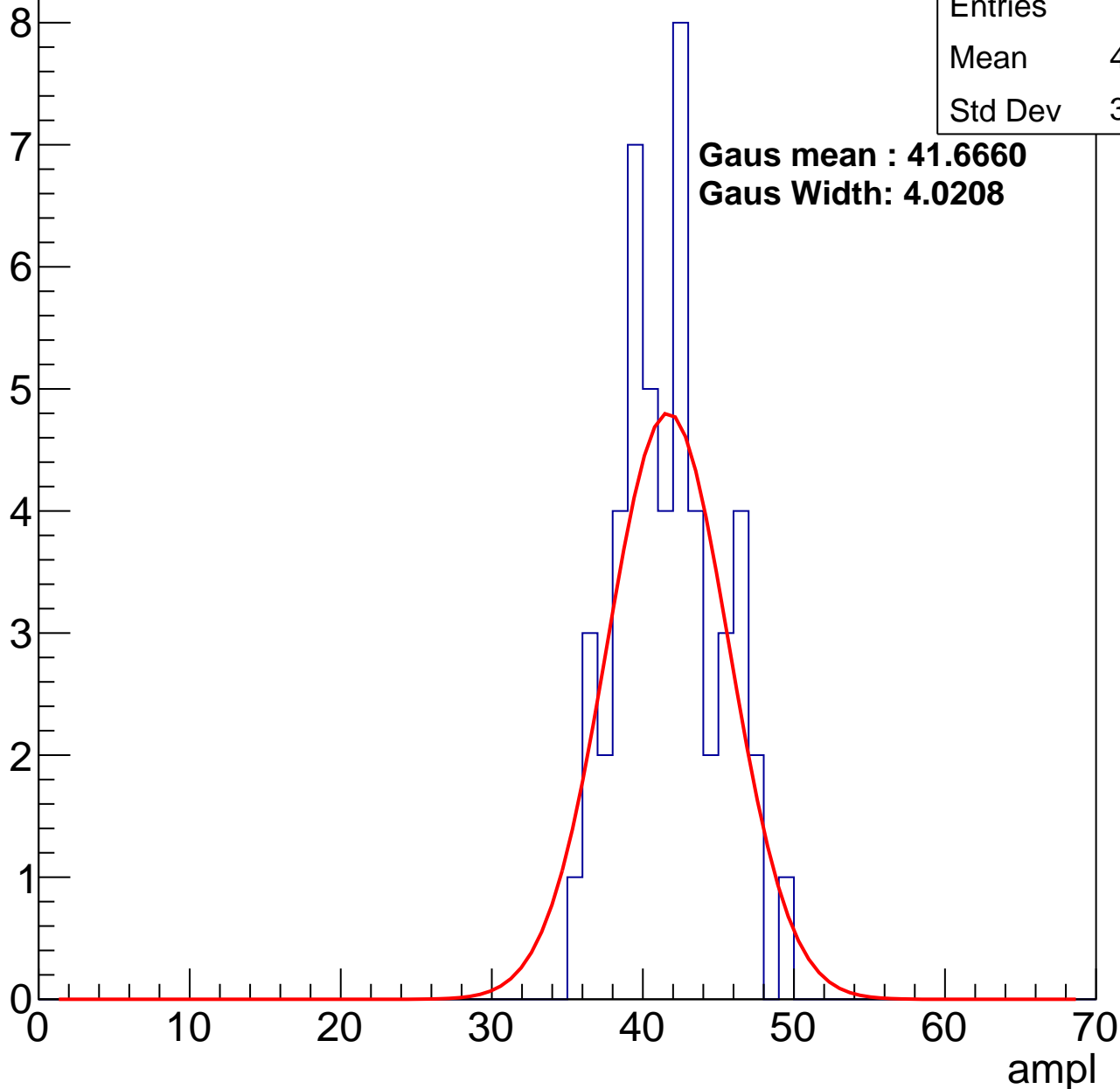
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	41.28
Std Dev	3.268

Gaus mean : 41.6660

Gaus Width: 4.0208

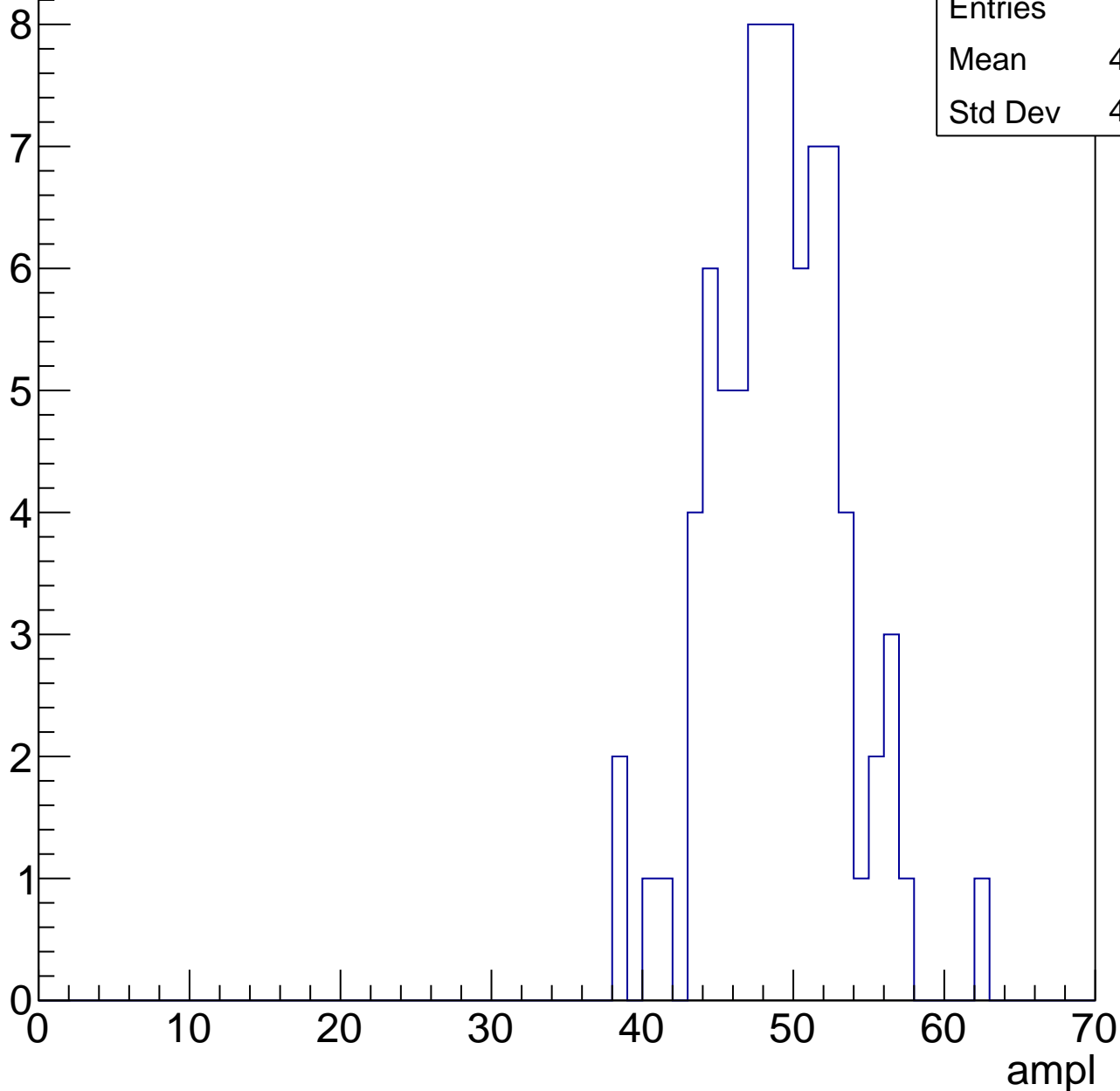


B1L103S, U17-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	48.55
Std Dev	4.283

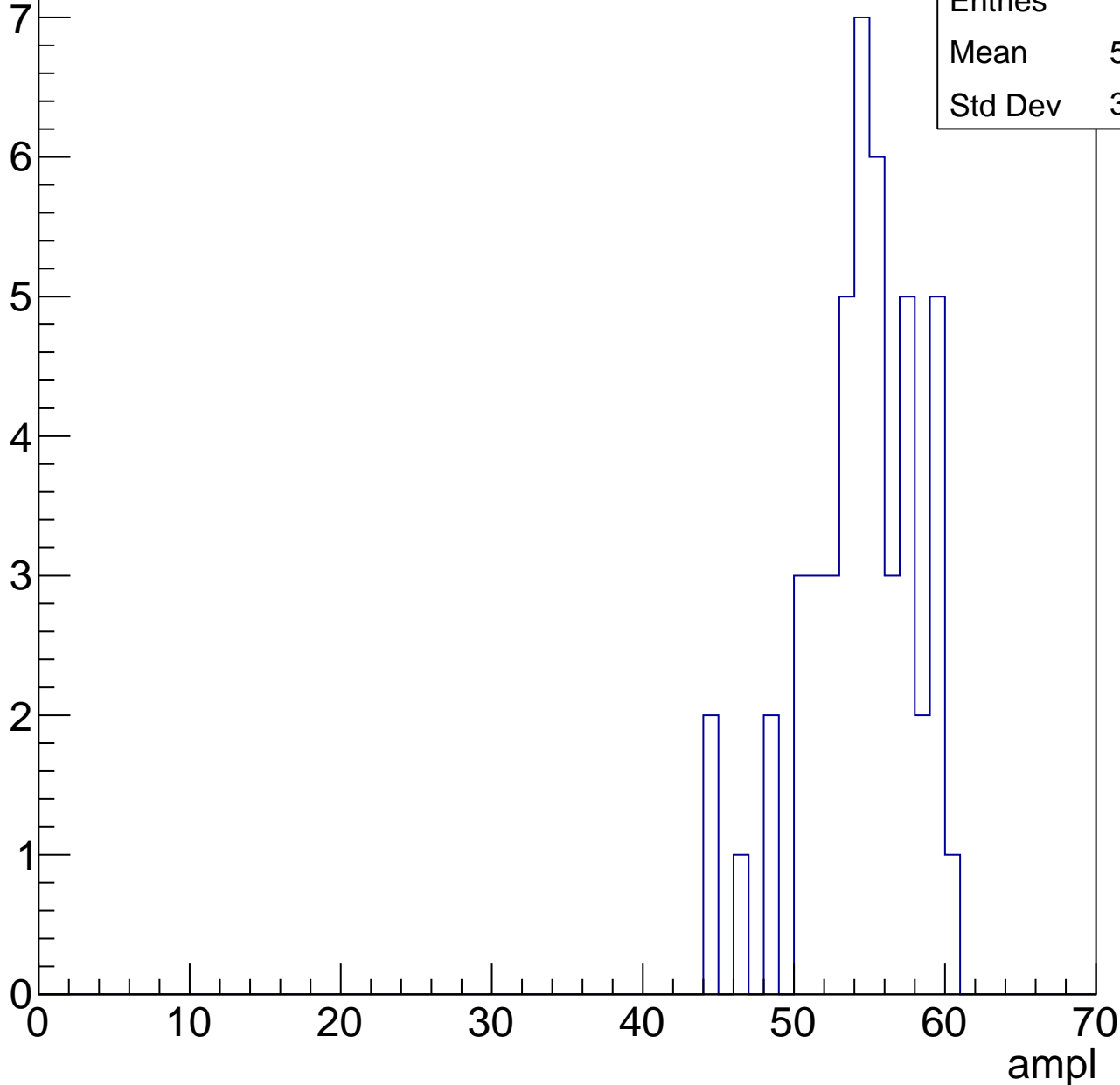


B1L103S, U17-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

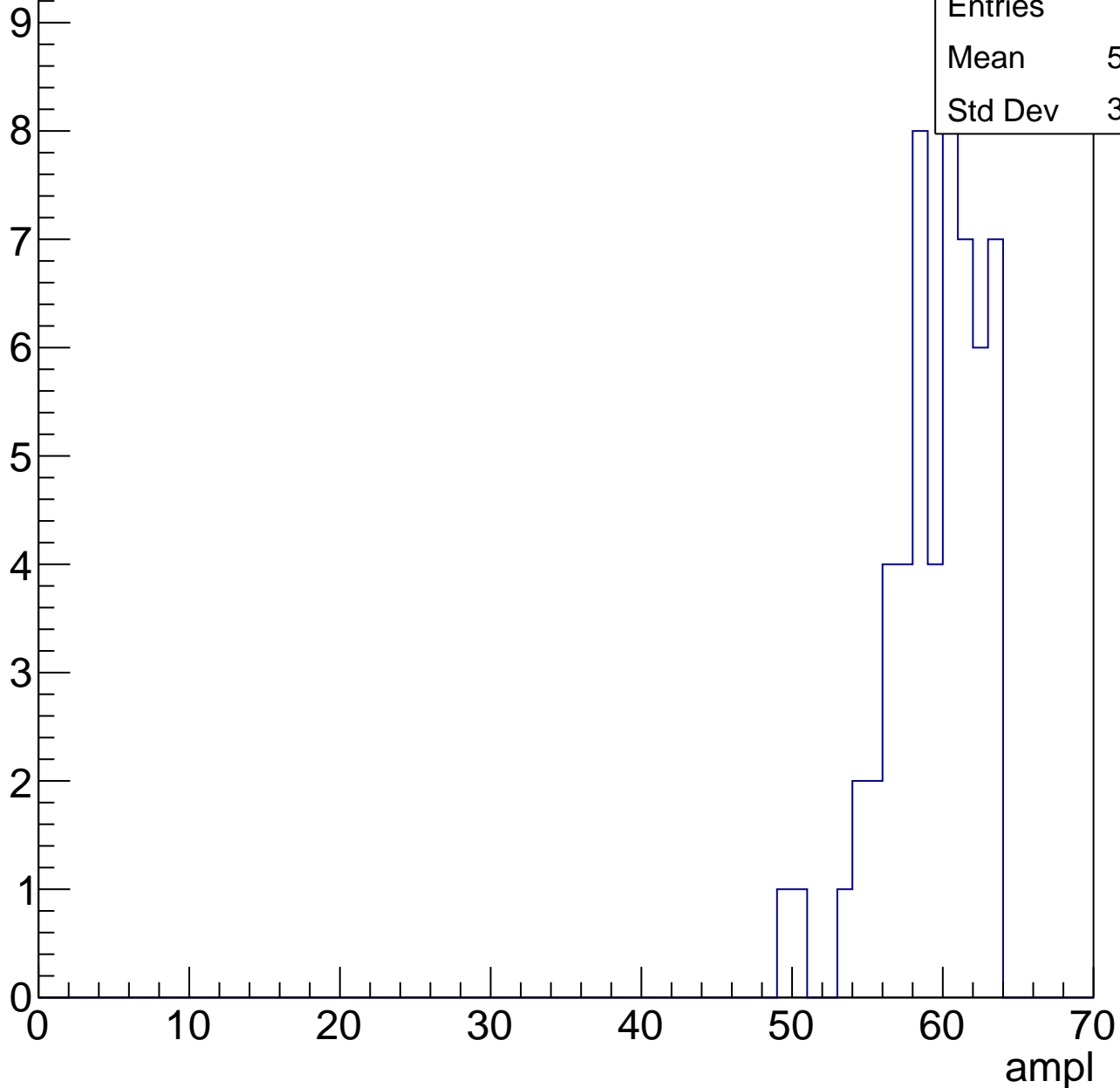
Entries	48
Mean	53.88
Std Dev	3.773



B1L103S, U17-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

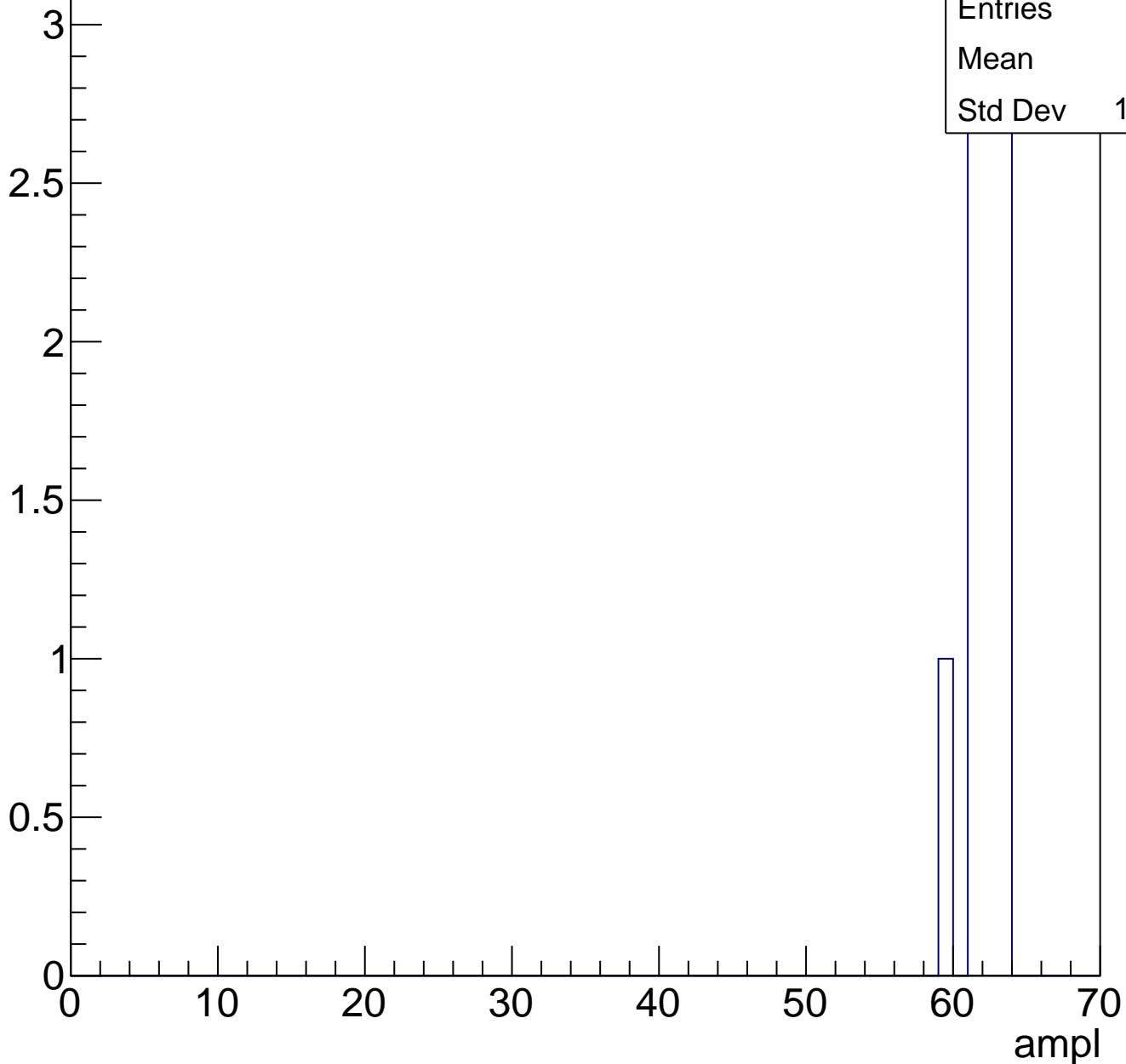


Entries	56
Mean	58.96
Std Dev	3.168

B1L103S, U17-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch83, adc0

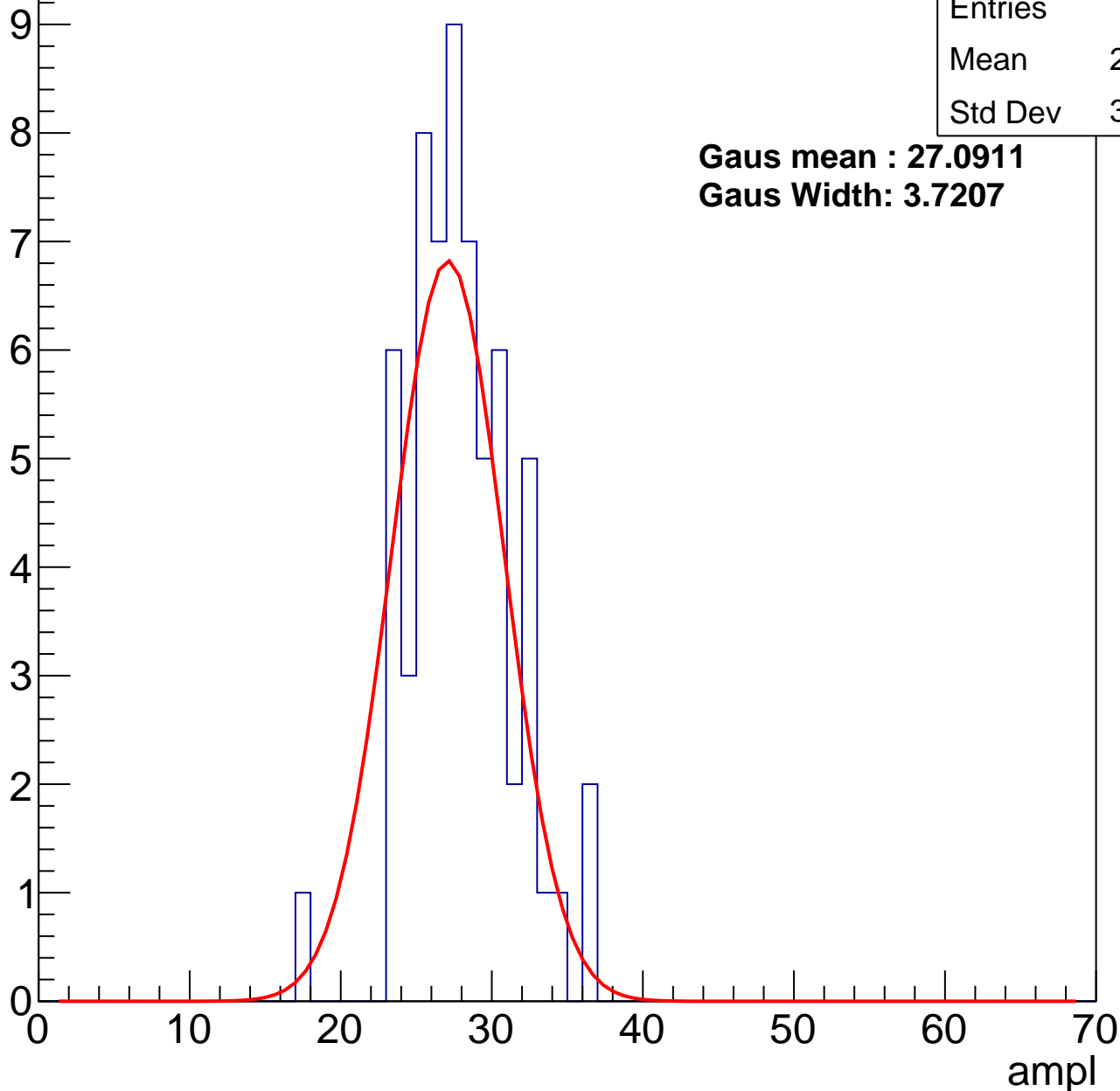
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.52
Std Dev	3.403

Gaus mean : 27.0911

Gaus Width: 3.7207



B1L103S, U17-ch83, adc1

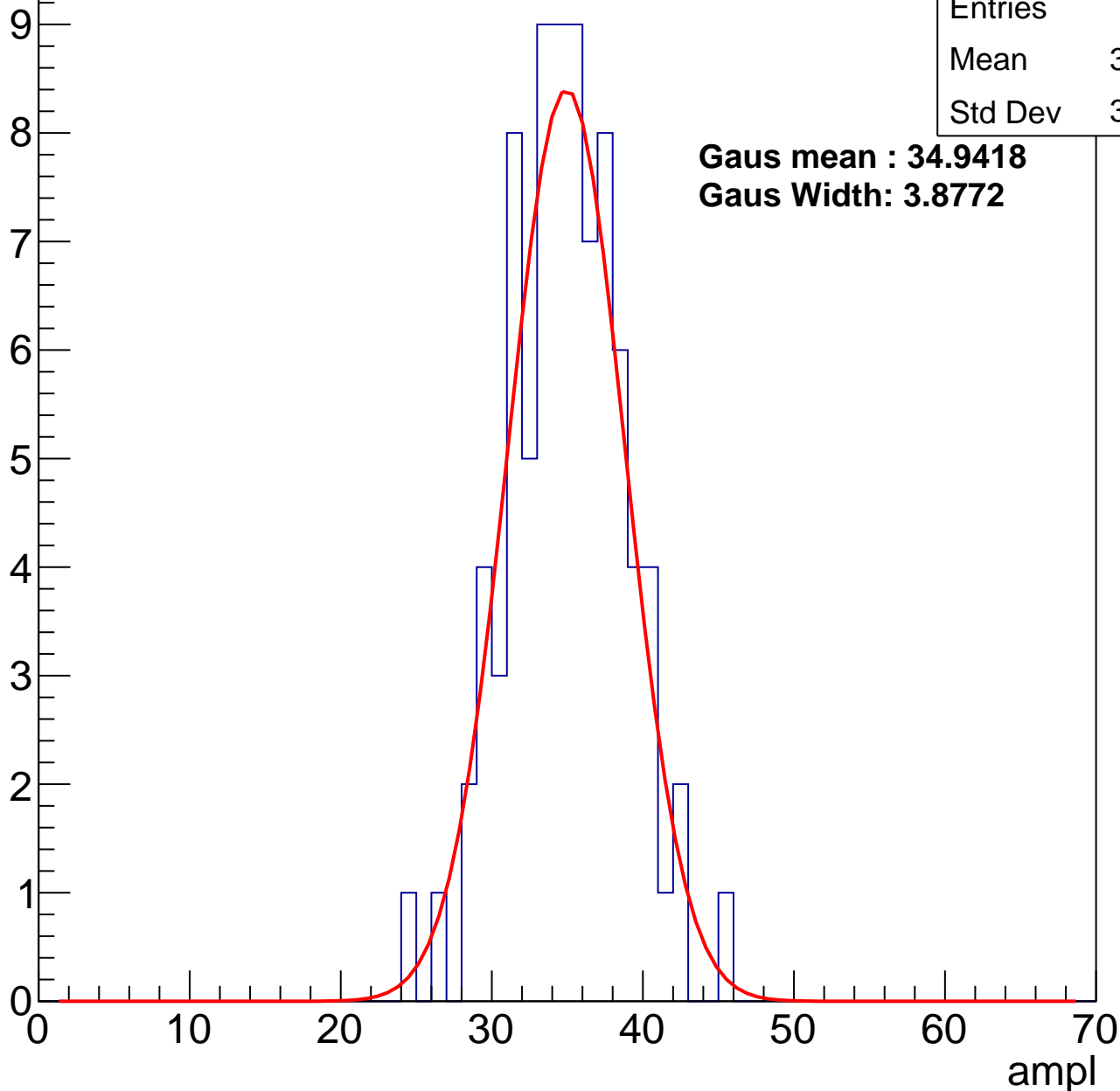
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	34.52
Std Dev	3.794

Gaus mean : 34.9418

Gaus Width: 3.8772



B1L103S, U17-ch83, adc2

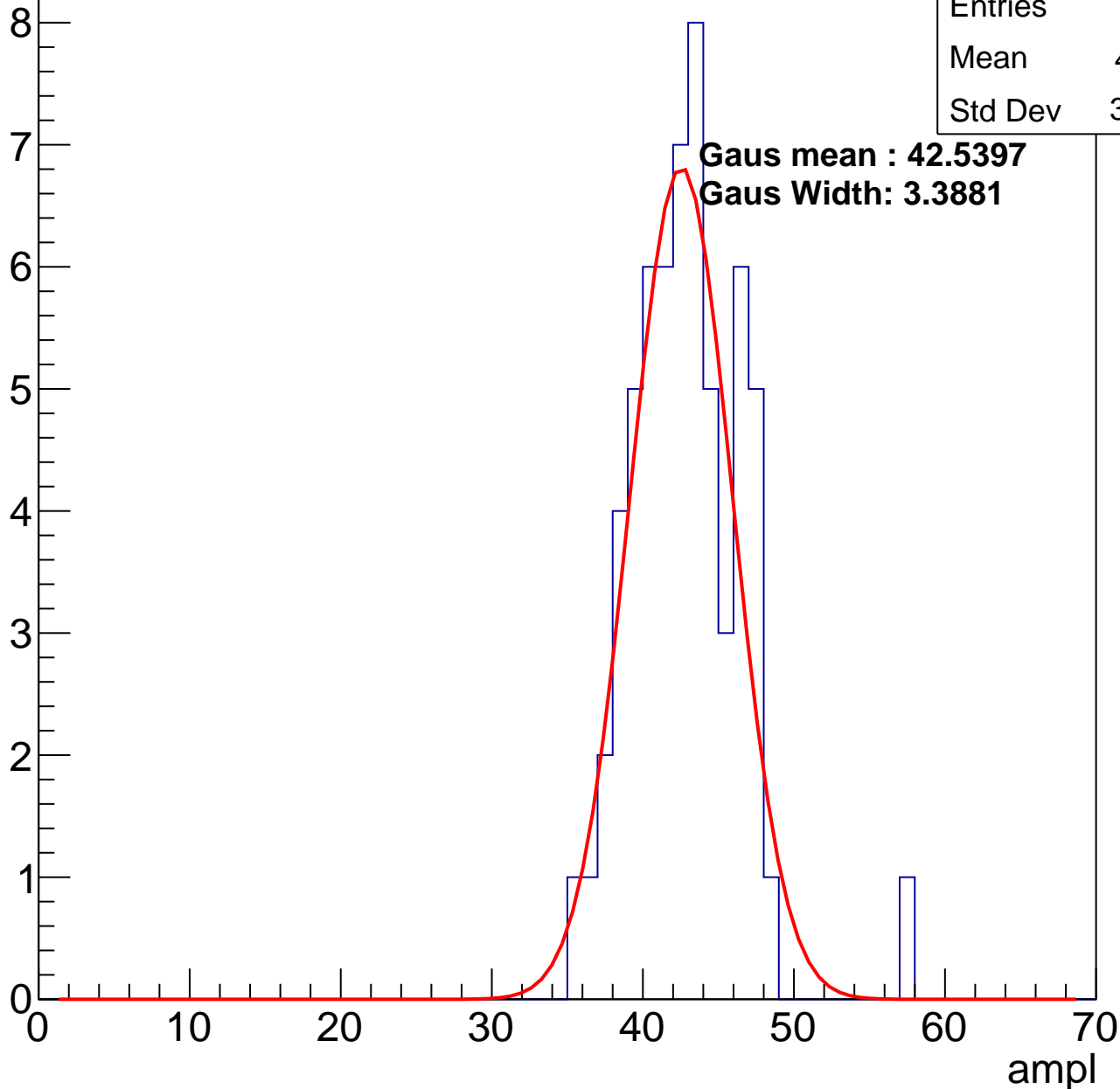
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.41
Std Dev	3.623

Gaus mean : 42.5397

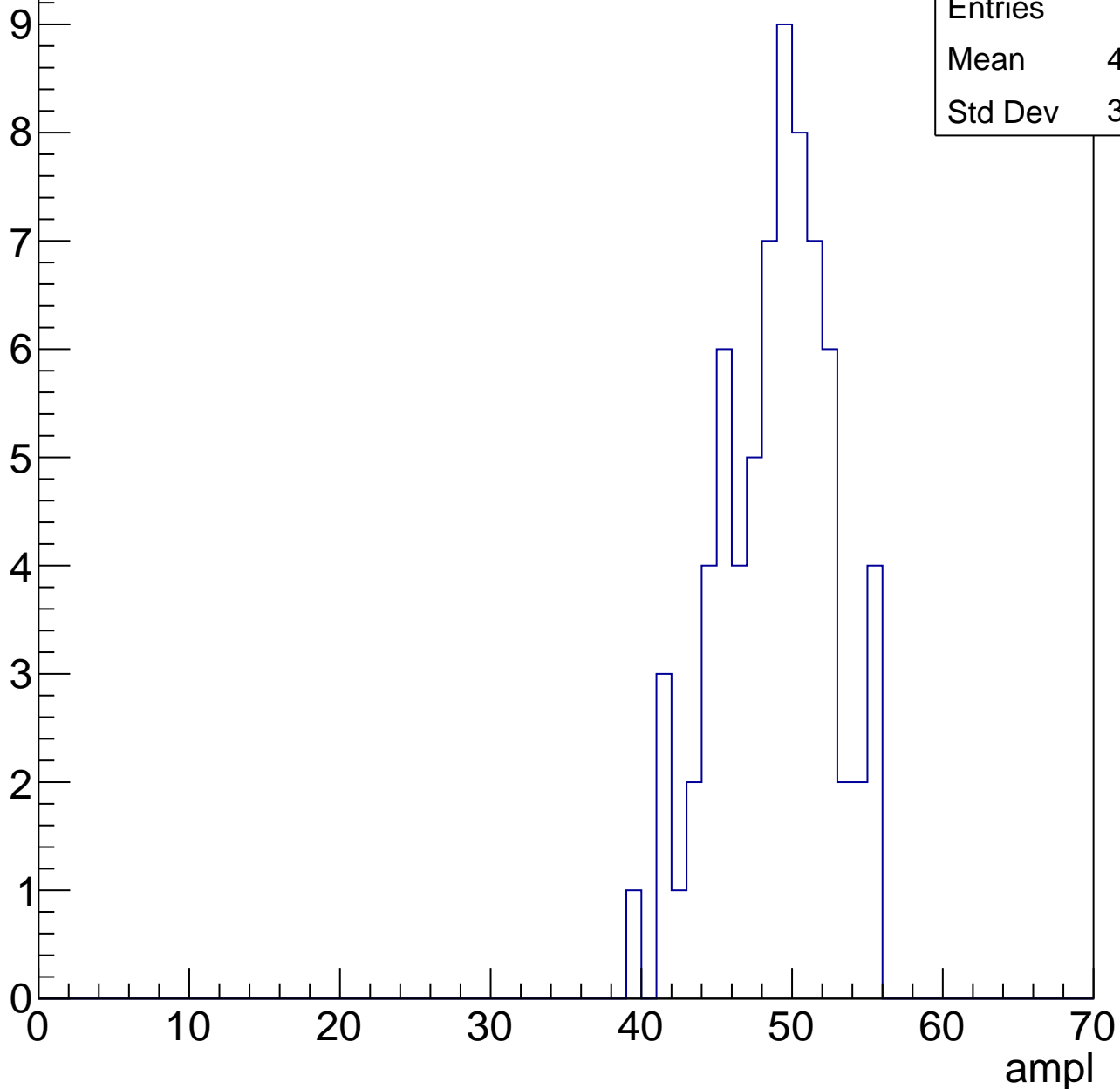
Gaus Width: 3.3881



B1L103S, U17-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

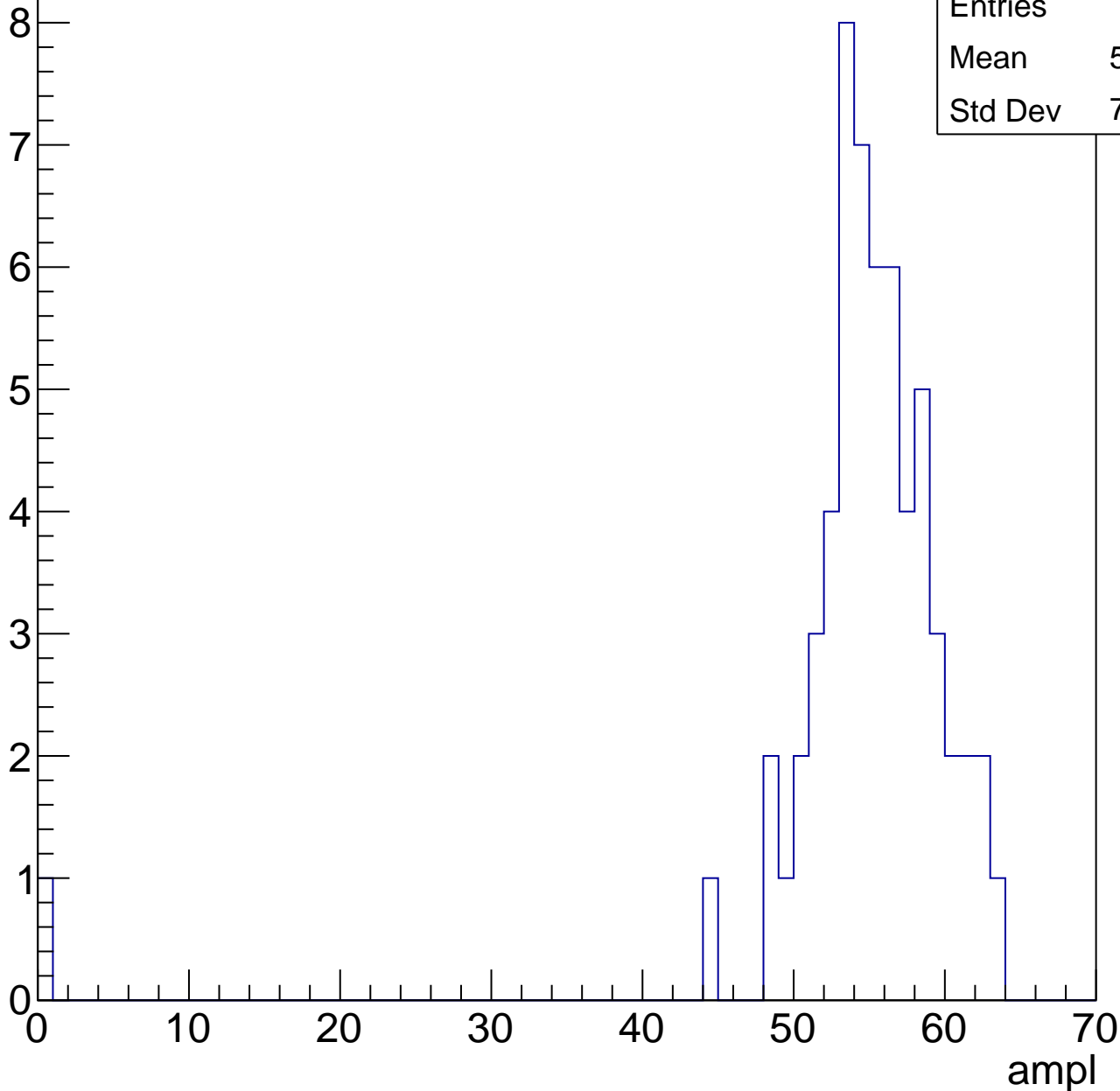


B1L103S, U17-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

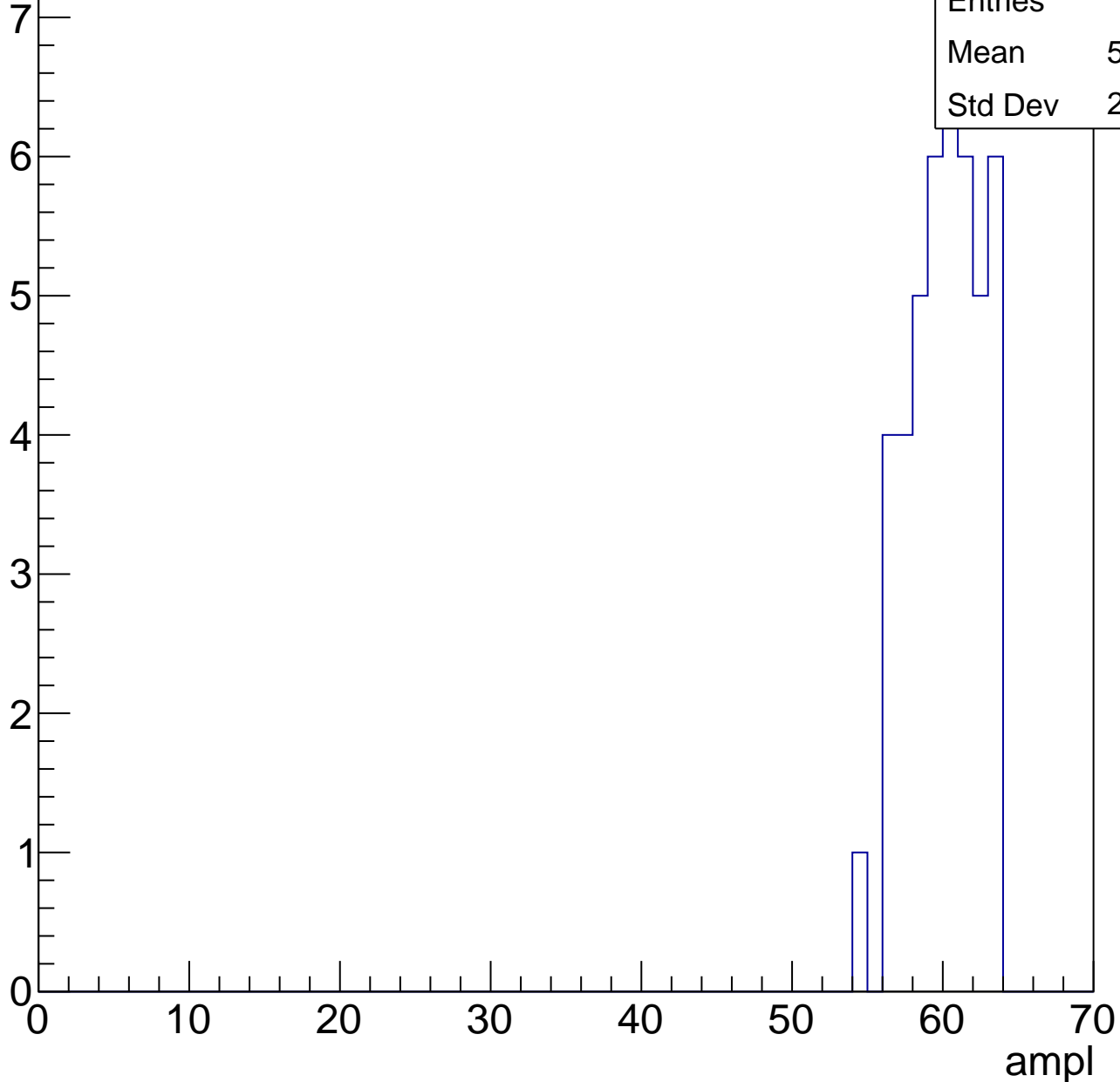
Entries	60
Mean	54.03
Std Dev	7.954



B1L103S, U17-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

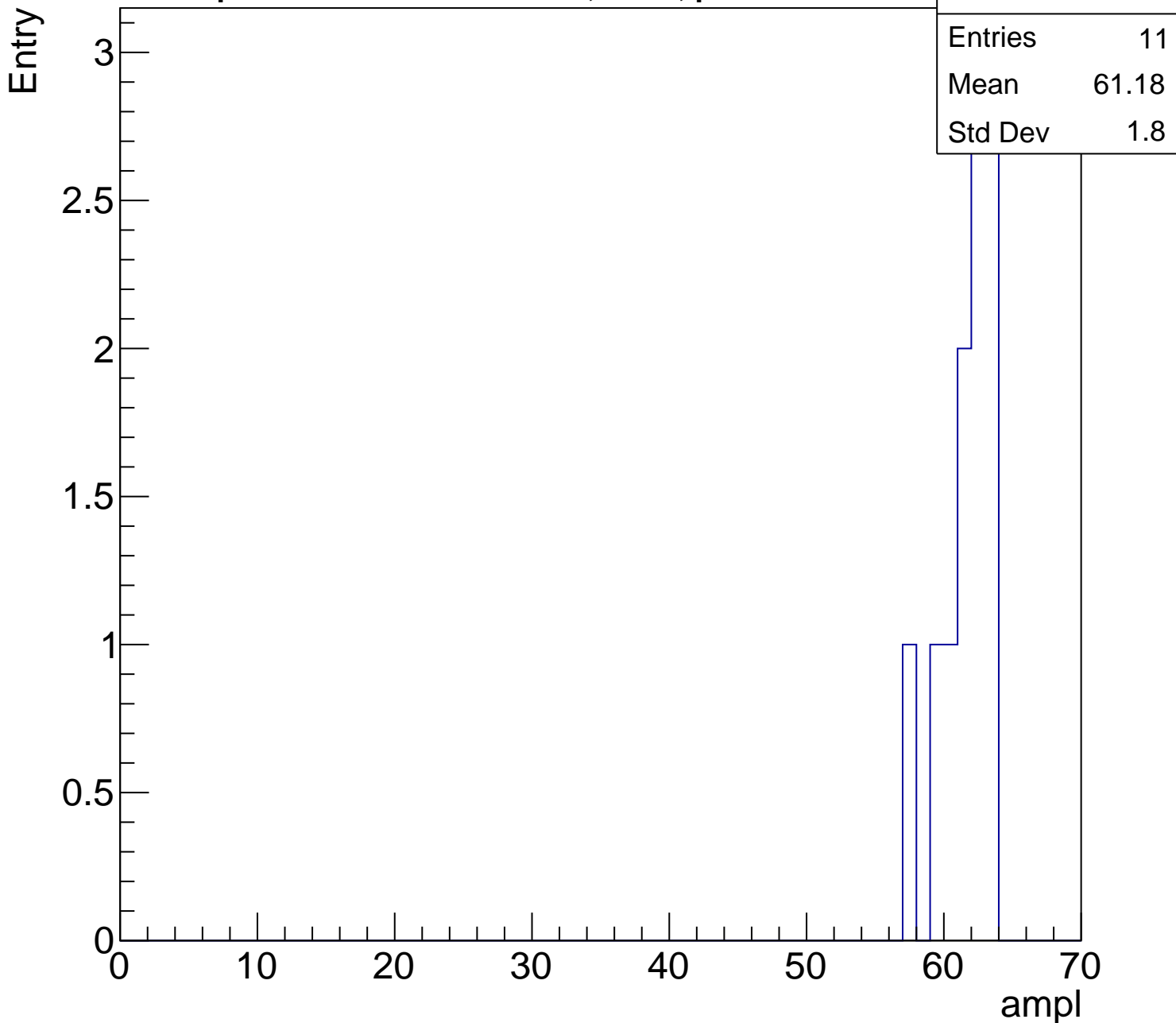
Mean

61.18

Std Dev

1.8

ampl



B1L103S, U17-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch84, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	29.22
Std Dev	4.975

Gaus mean : 30.0663

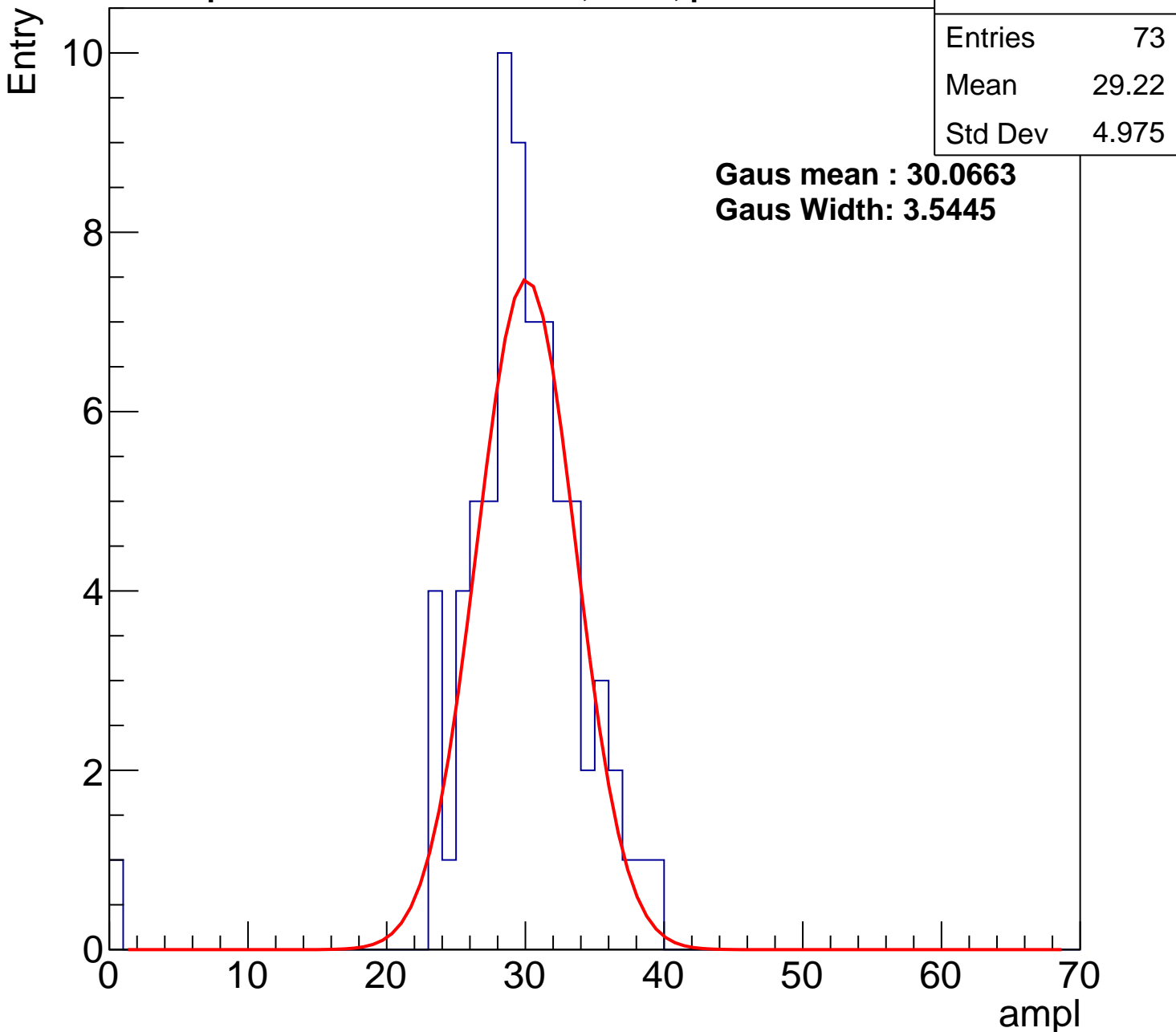
Gaus Width: 3.5445

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch84, adc1

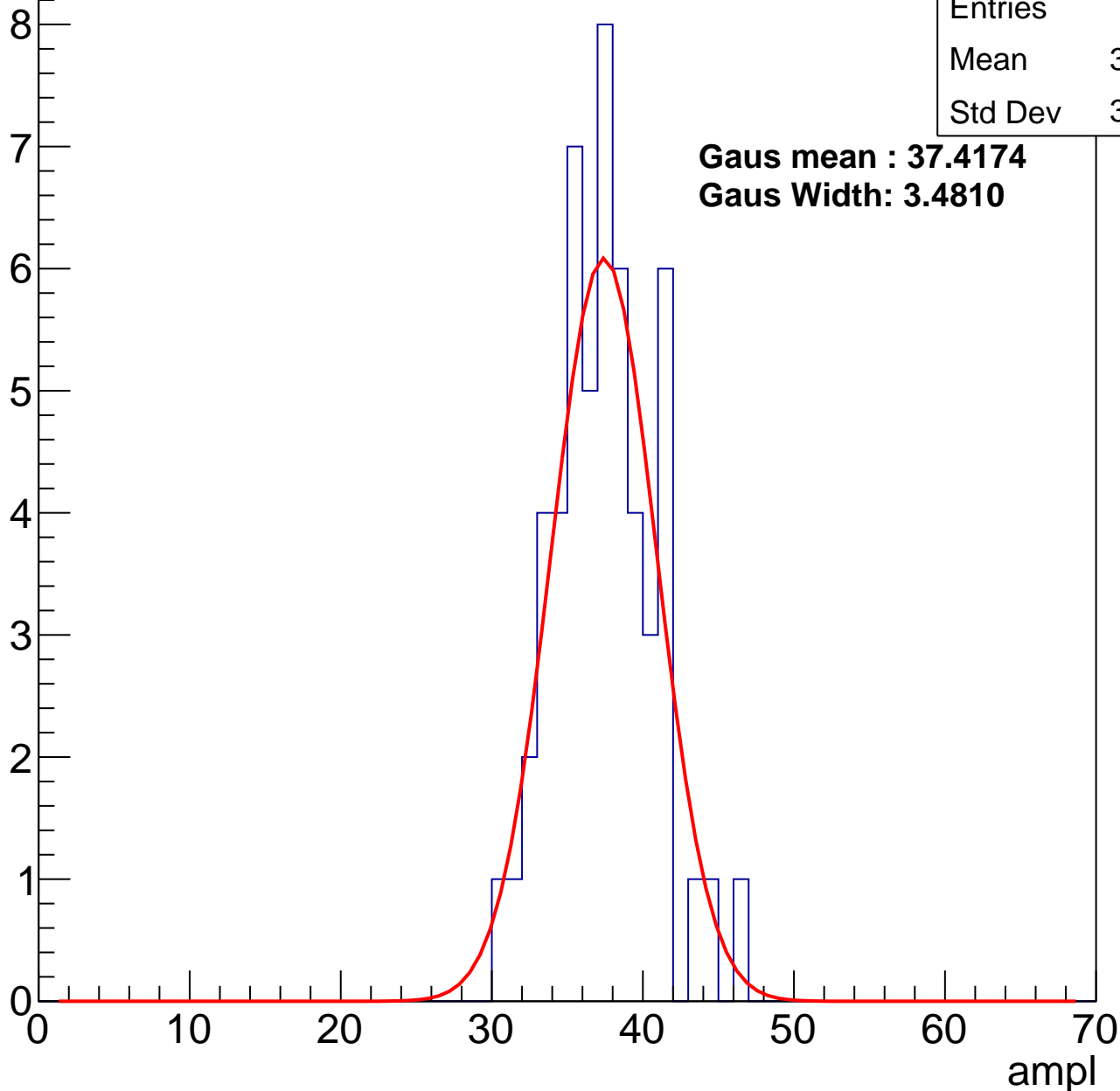
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.98
Std Dev	3.274

Gaus mean : 37.4174

Gaus Width: 3.4810



B1L103S, U17-ch84, adc2

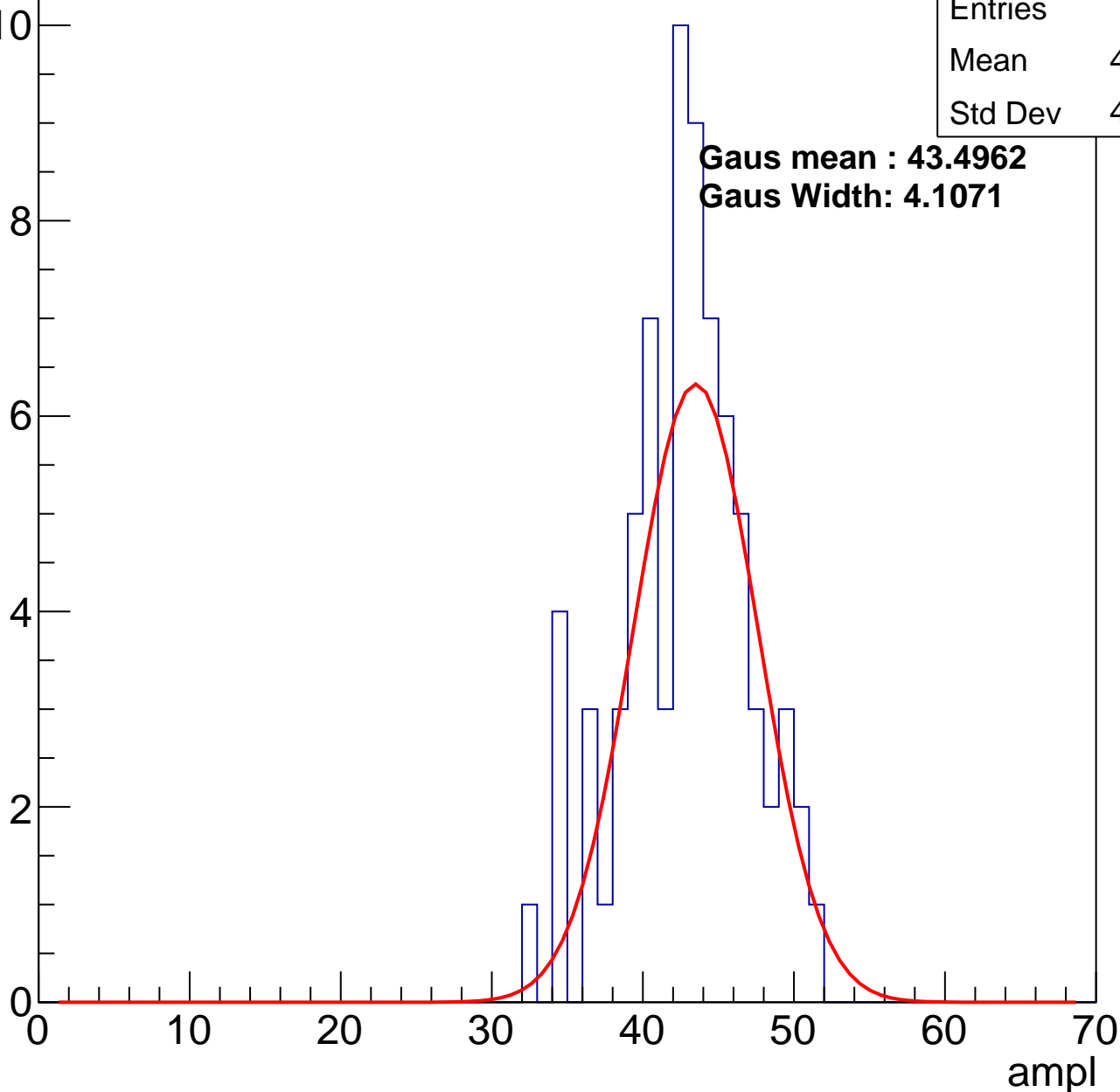
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.33
Std Dev	4.129

Gaus mean : 43.4962

Gaus Width: 4.1071

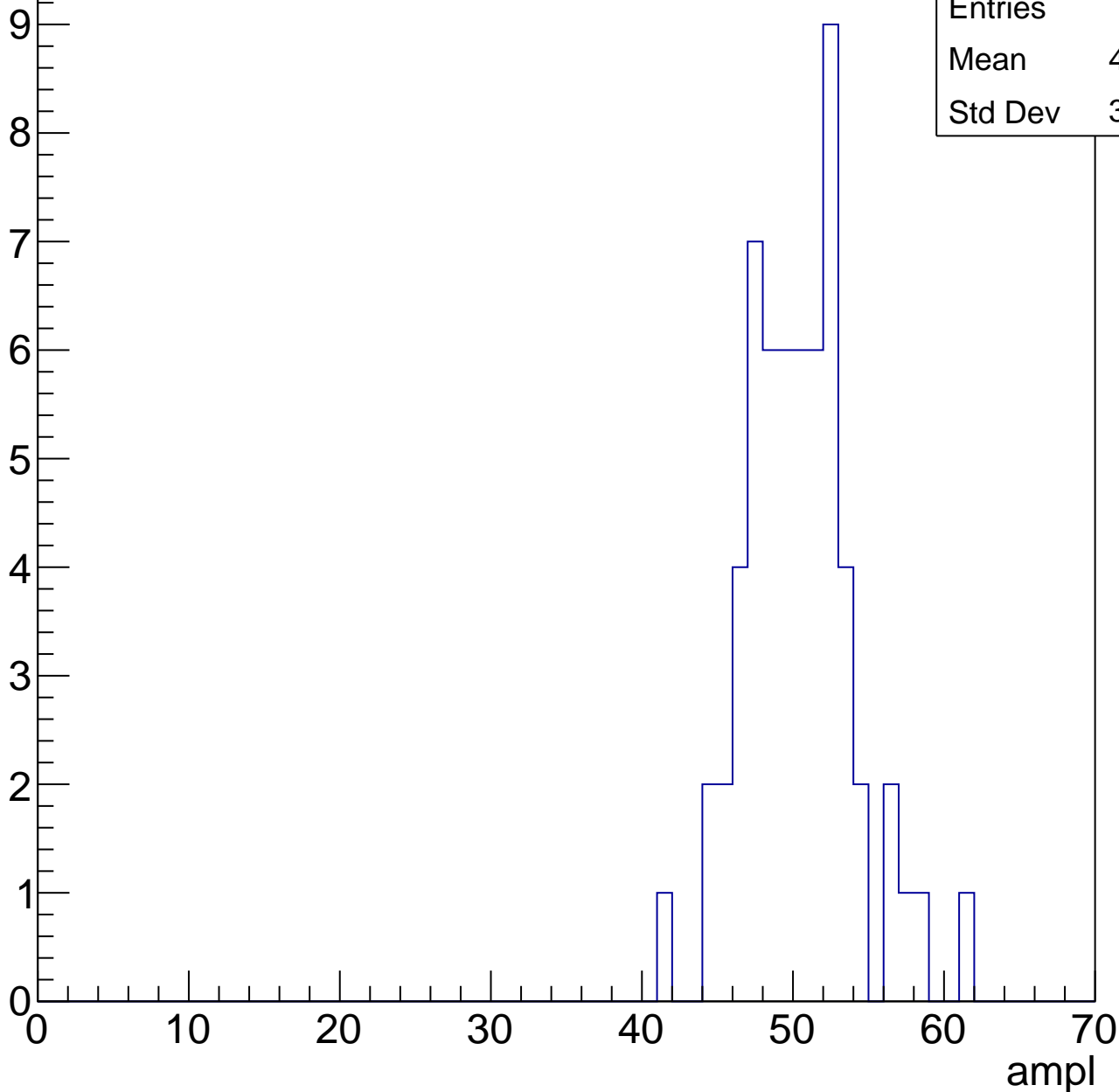


B1L103S, U17-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.93
Std Dev	3.596

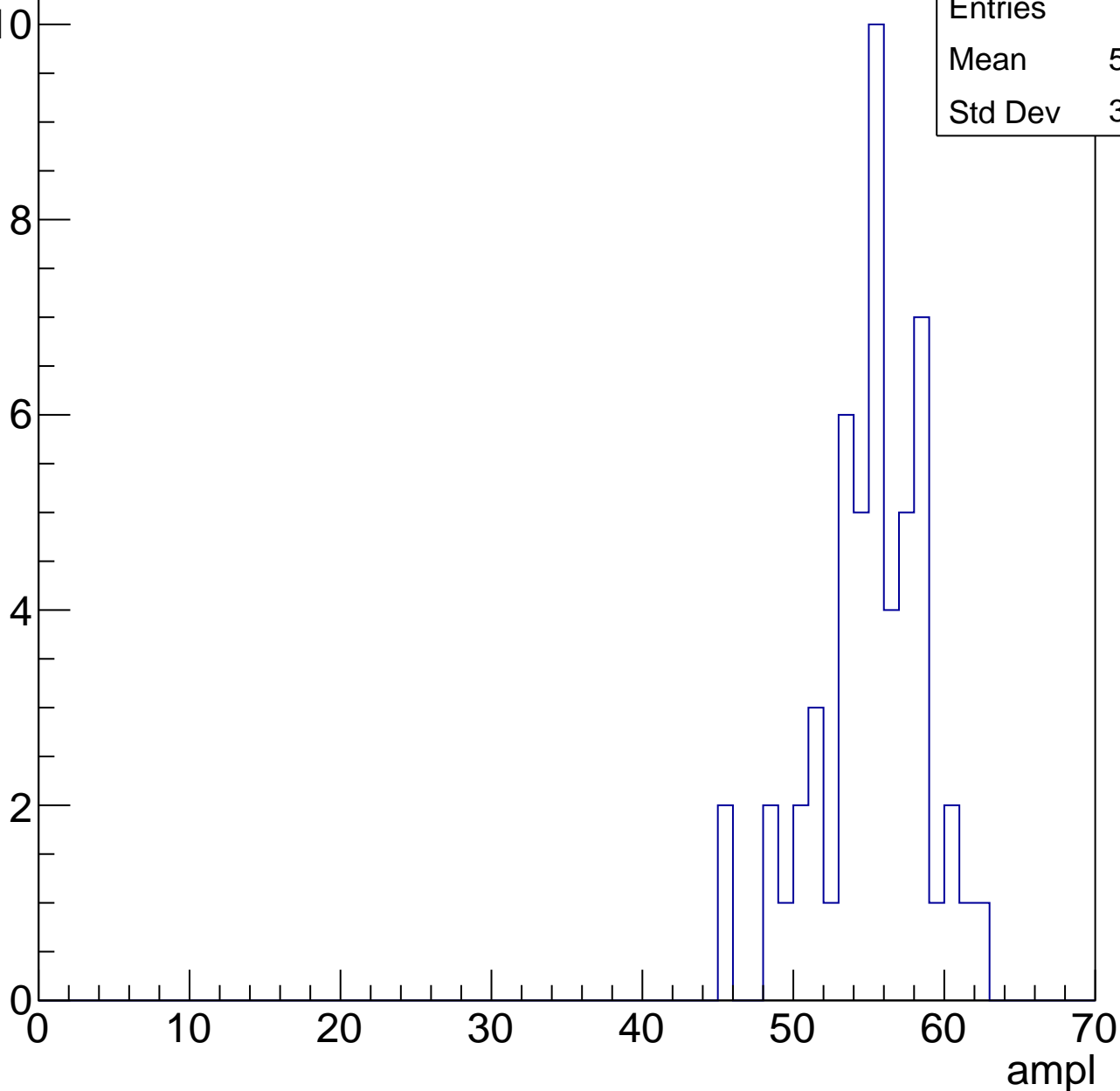


B1L103S, U17-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

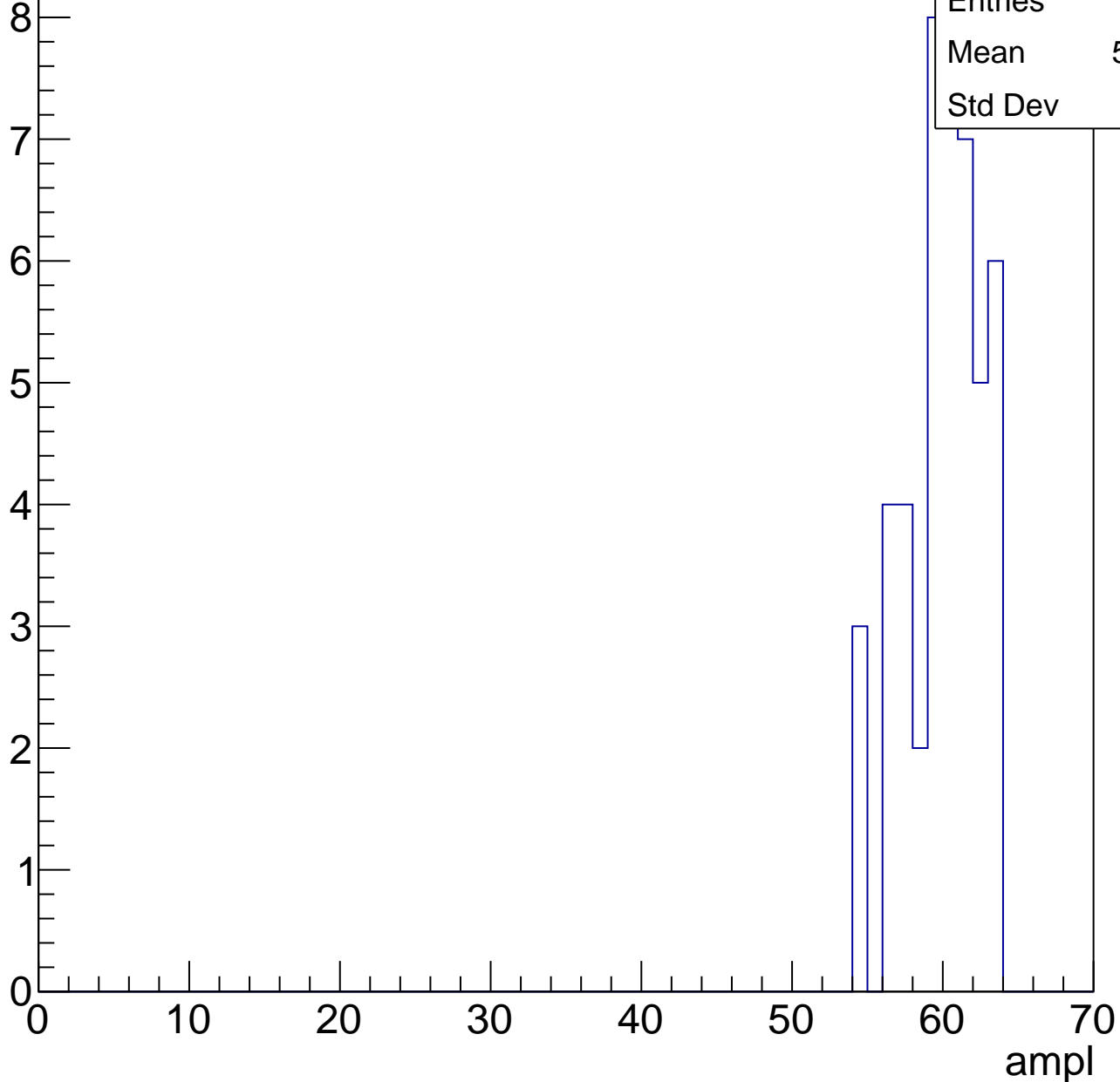
Entries	53
Mean	54.62
Std Dev	3.635



B1L103S, U17-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

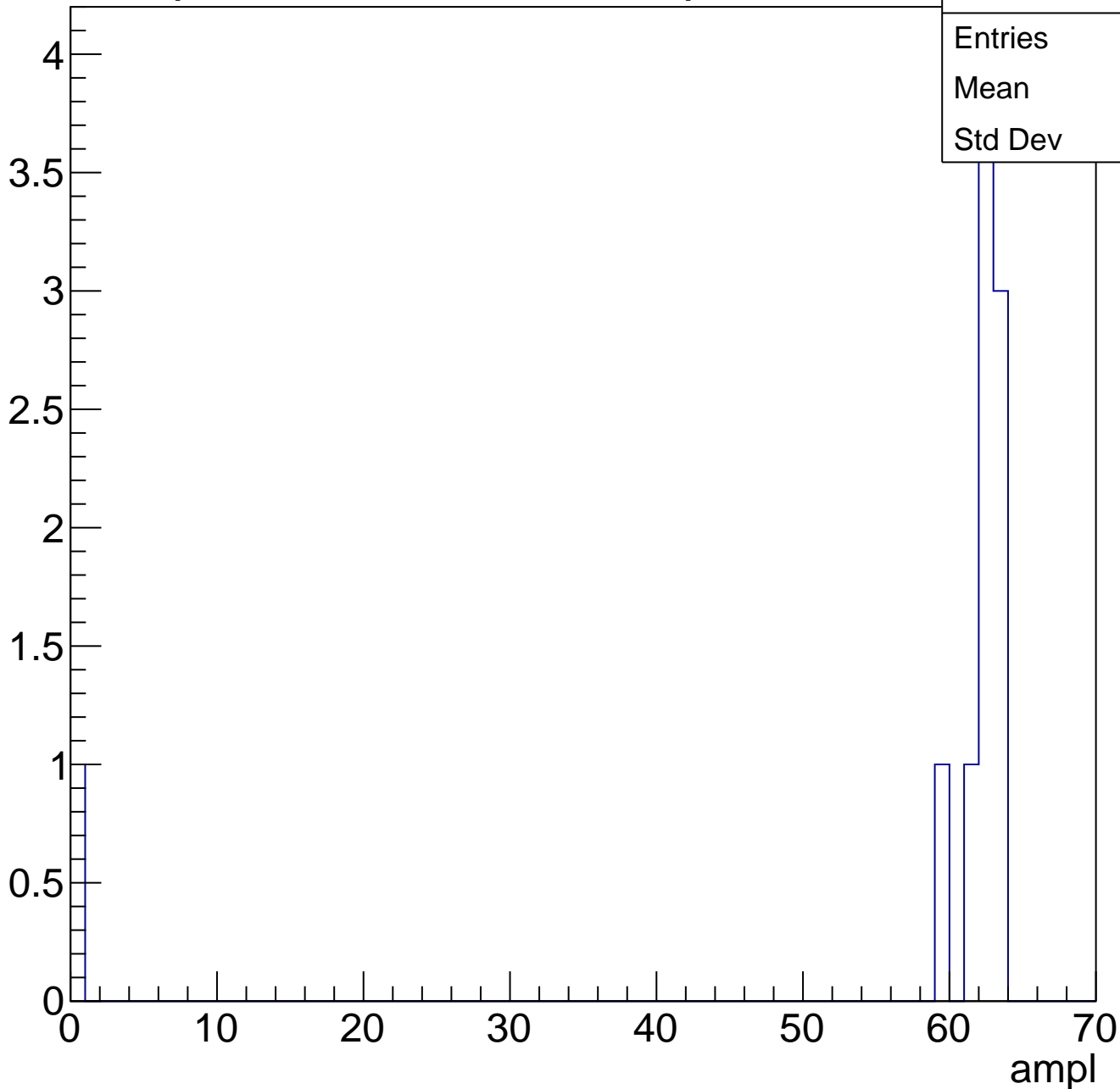
Entry



B1L103S, U17-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch85, adc0

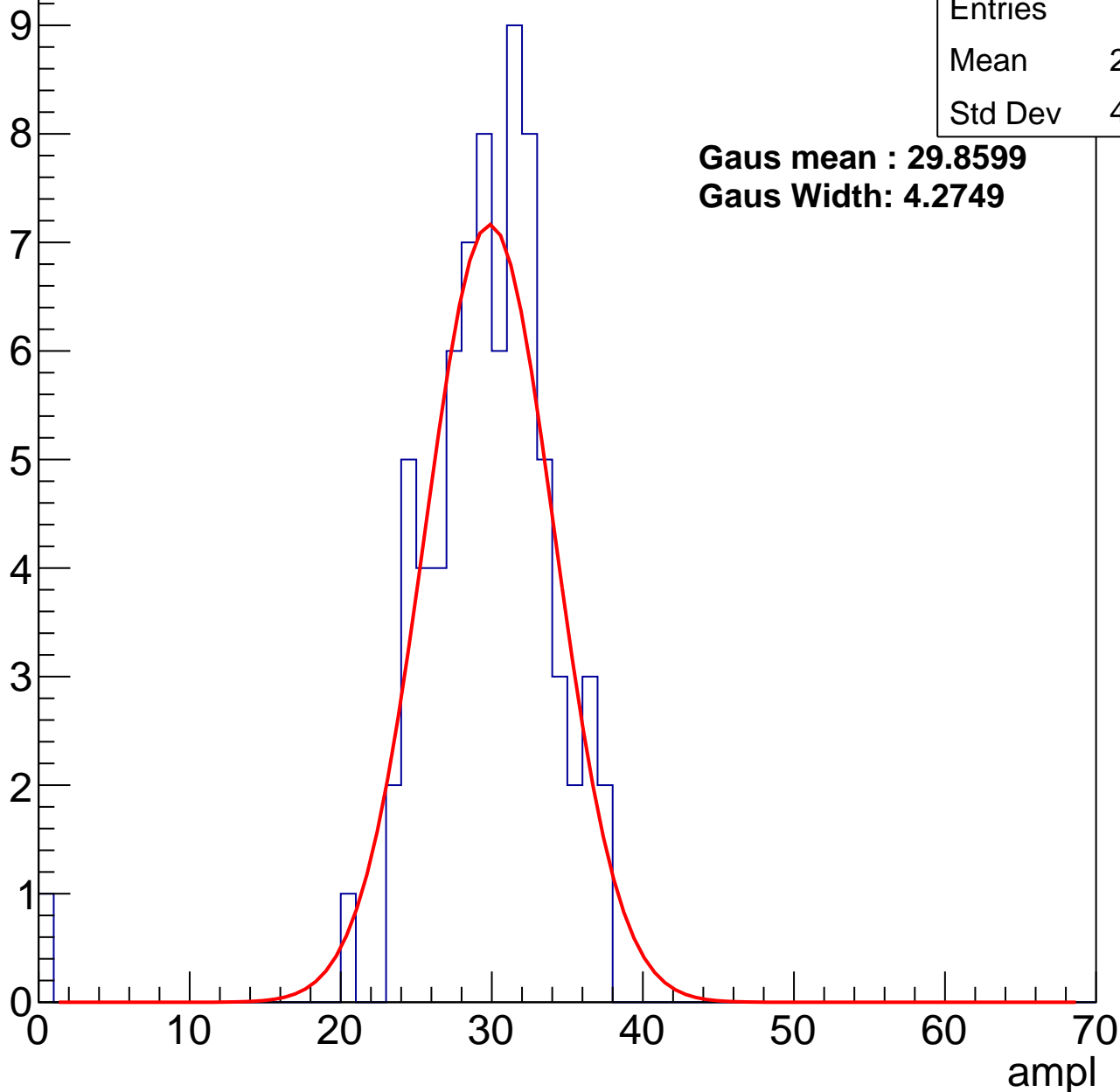
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.13
Std Dev	4.953

Gaus mean : 29.8599

Gaus Width: 4.2749



B1L103S, U17-ch85, adc1

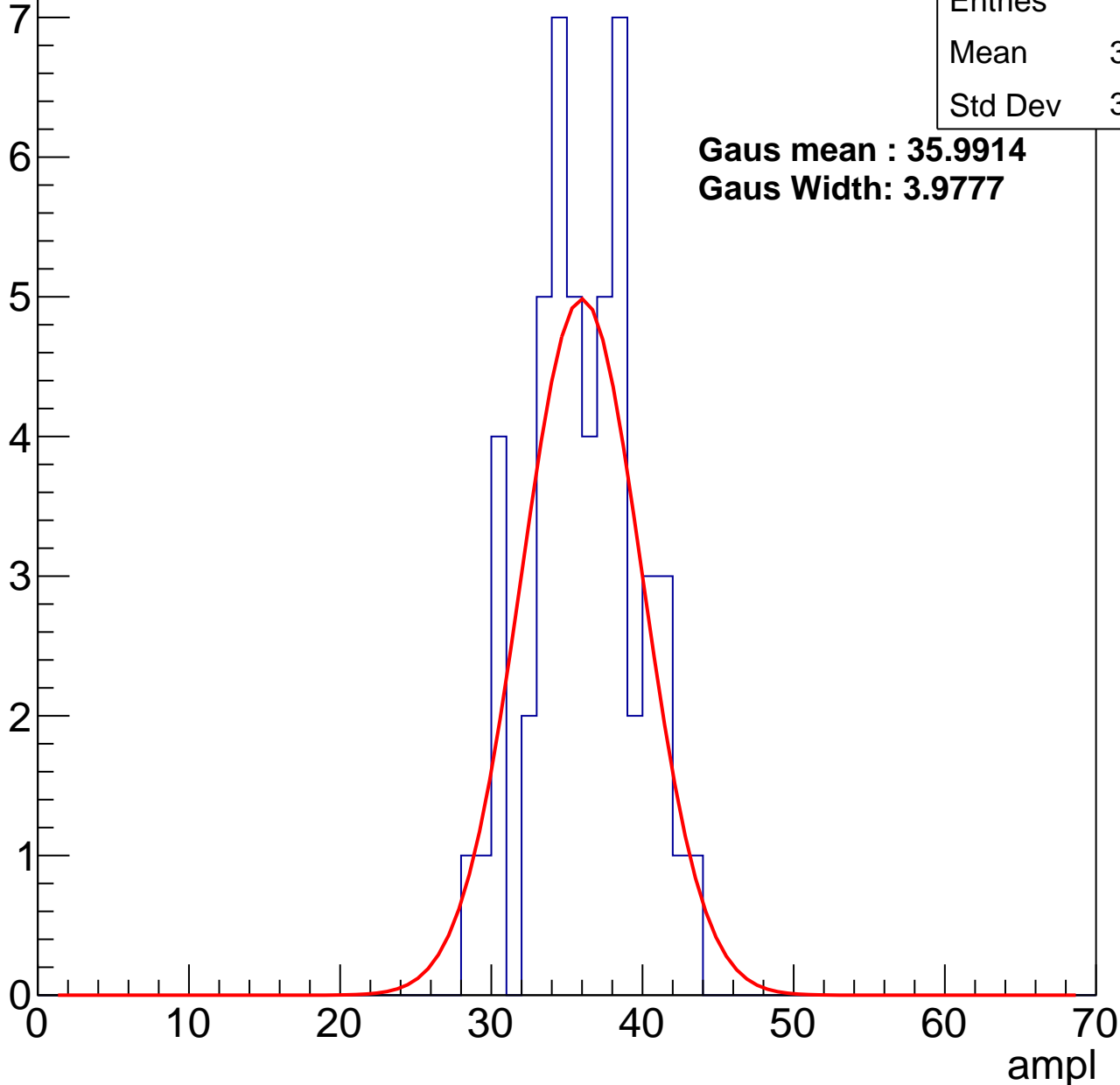
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	35.69
Std Dev	3.489

Gaus mean : 35.9914

Gaus Width: 3.9777



B1L103S, U17-ch85, adc2

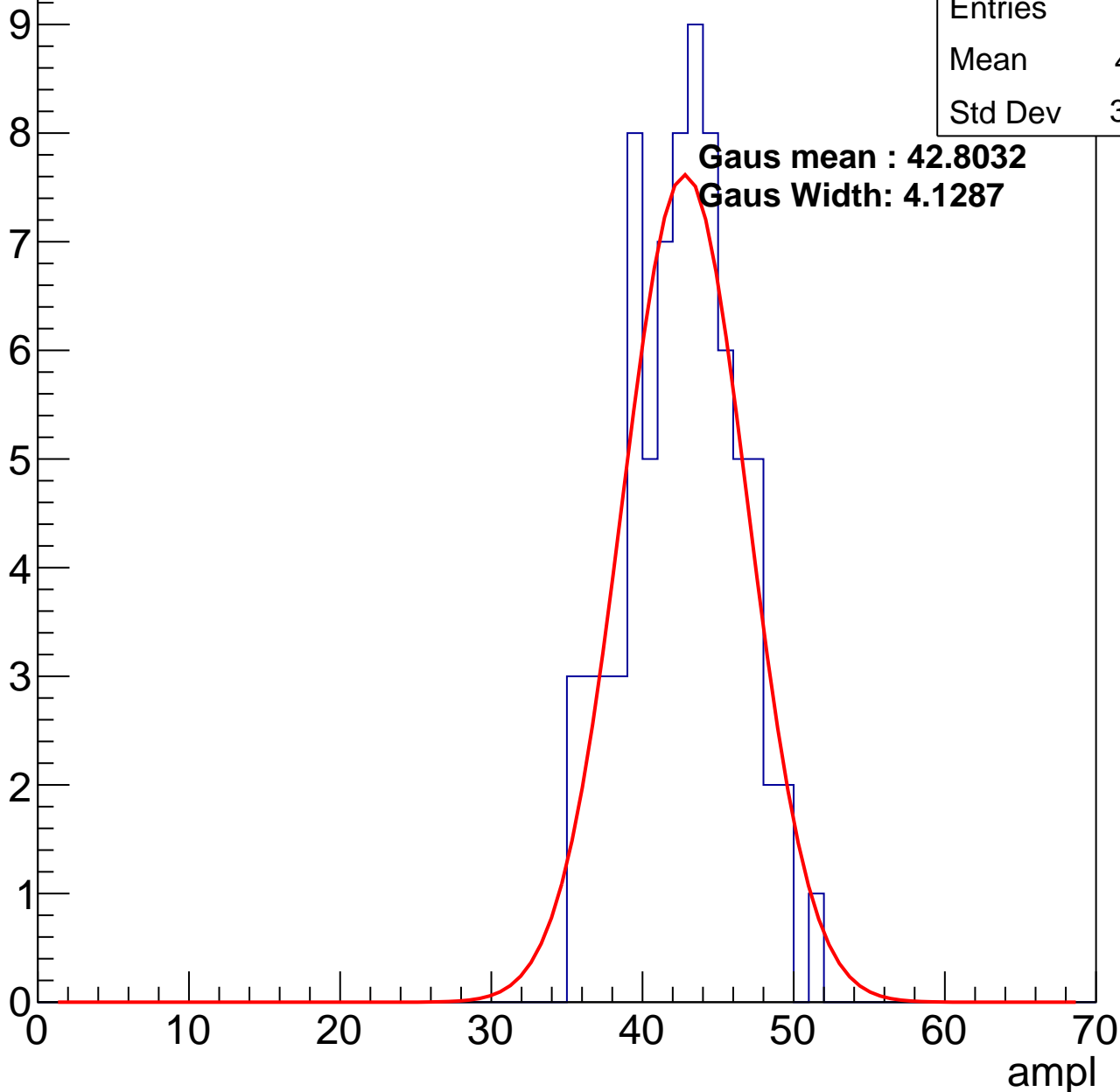
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	42.21
Std Dev	3.625

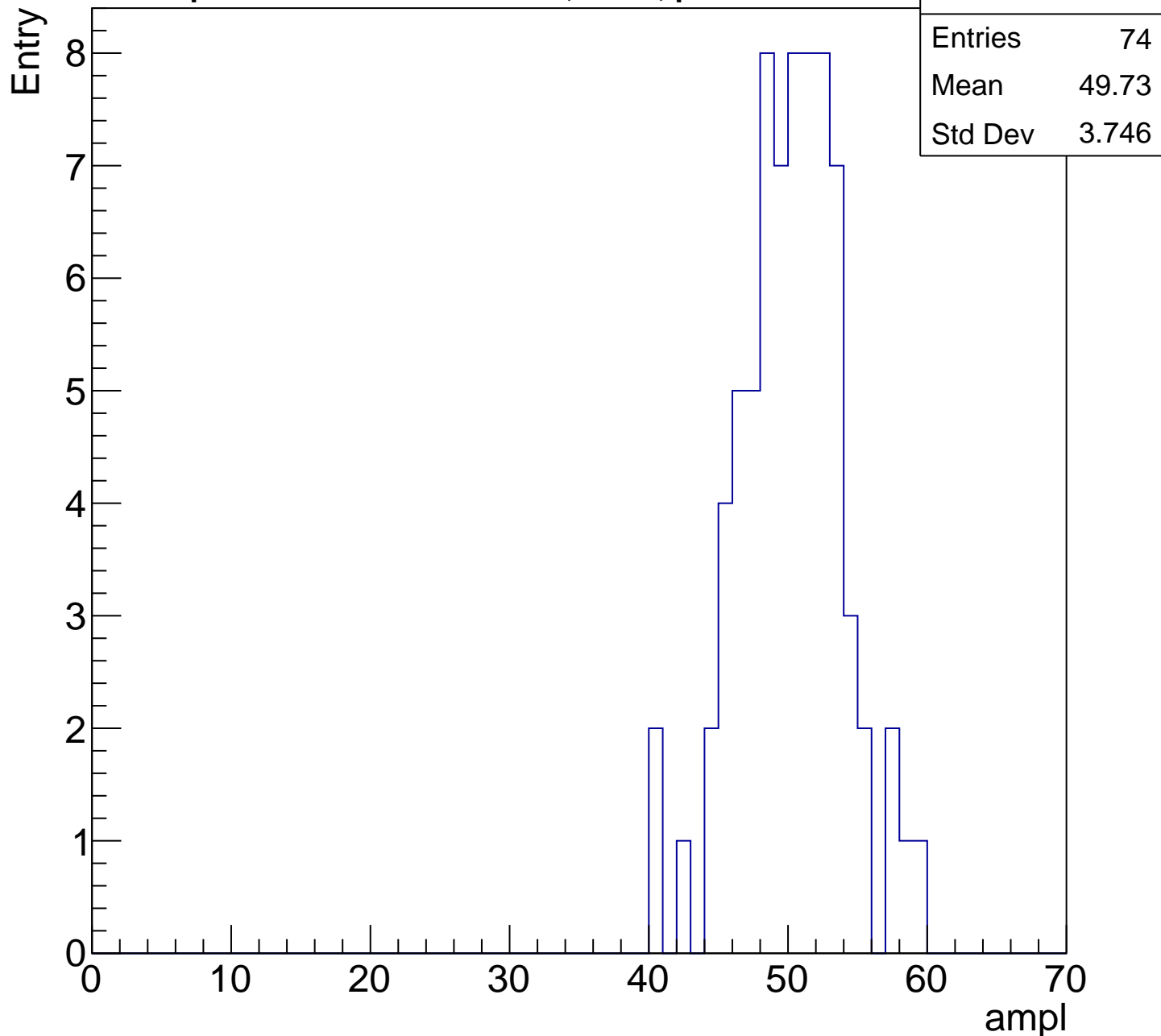
Gaus mean : 42.8032

Gaus Width: 4.1287



B1L103S, U17-ch85, adc3

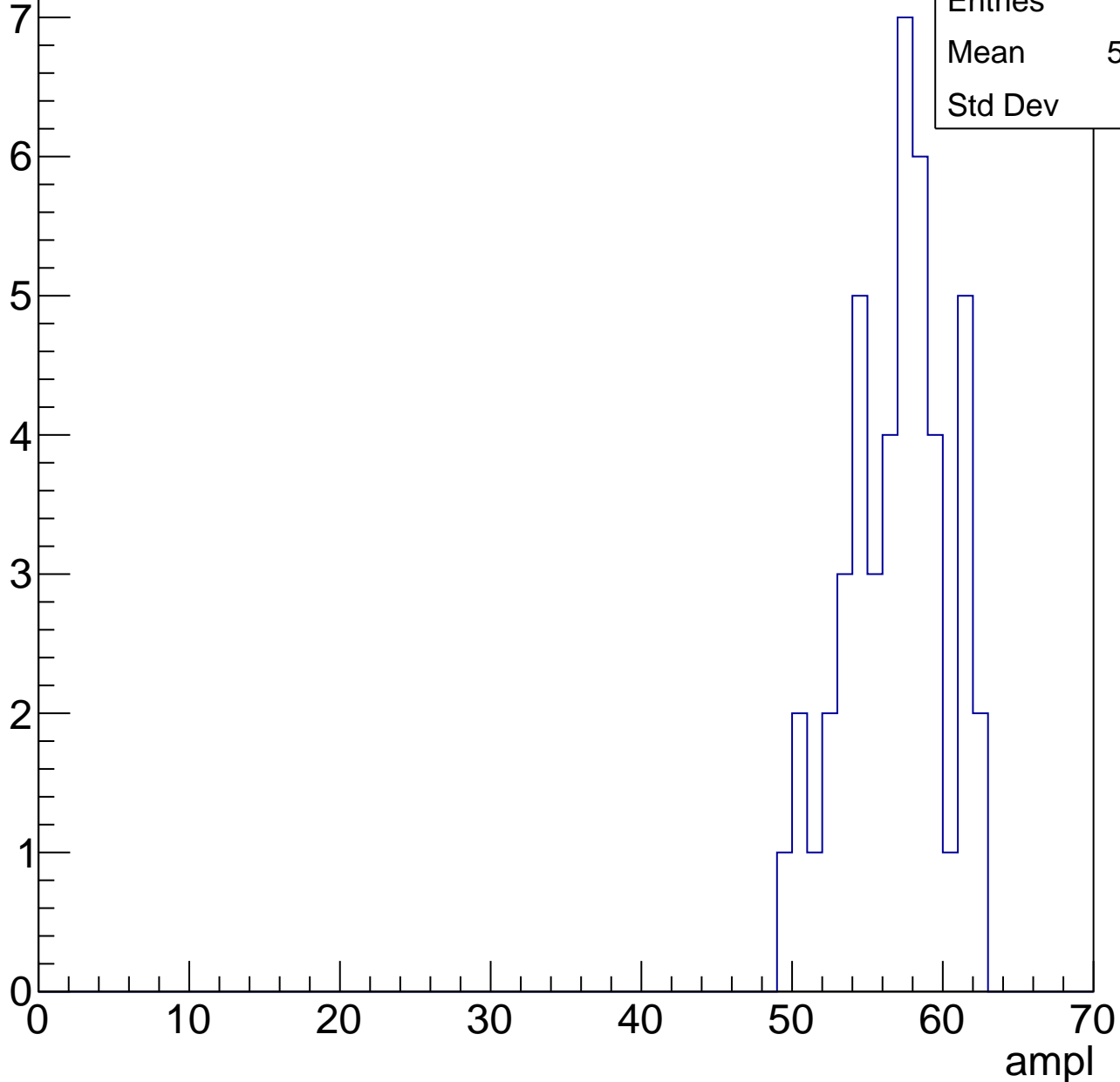
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

6

5

4

3

2

1

0

Entries

44

Mean

57.64

Std Dev

9.23

ampl

0

10

20

30

40

50

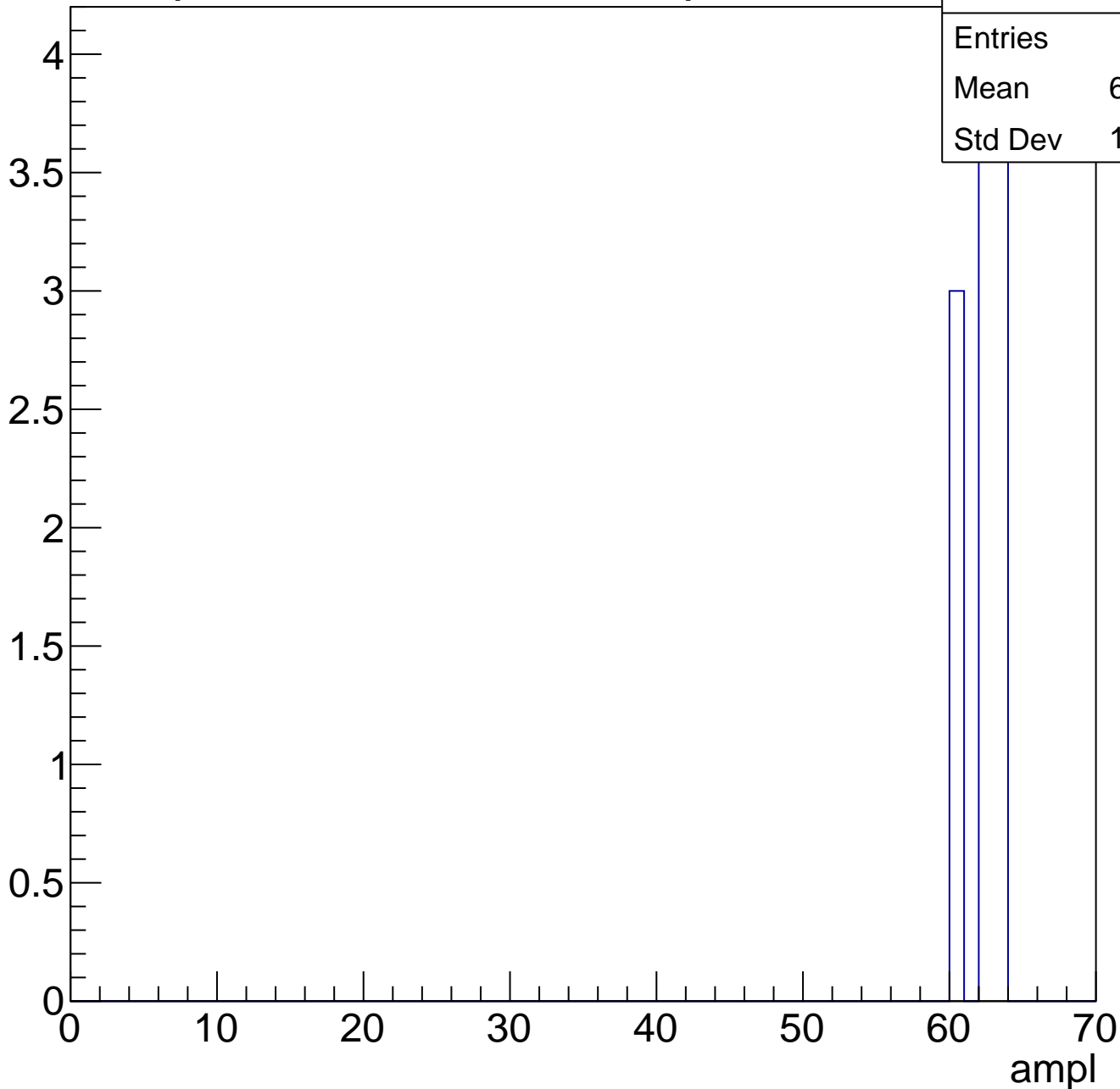
60

70

B1L103S, U17-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch86, adc0

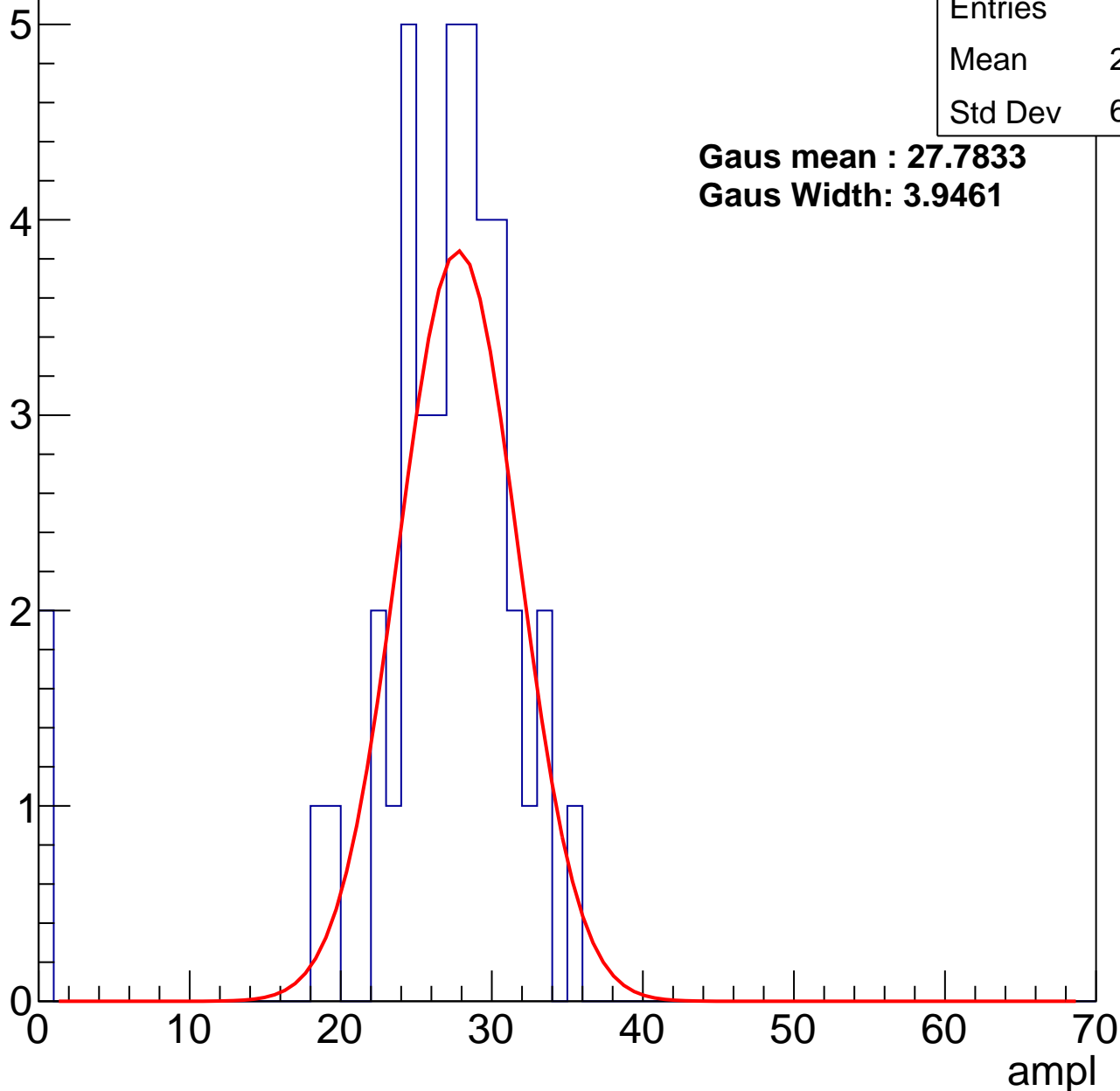
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	25.79
Std Dev	6.756

Gaus mean : 27.7833

Gaus Width: 3.9461



B1L103S, U17-ch86, adc1

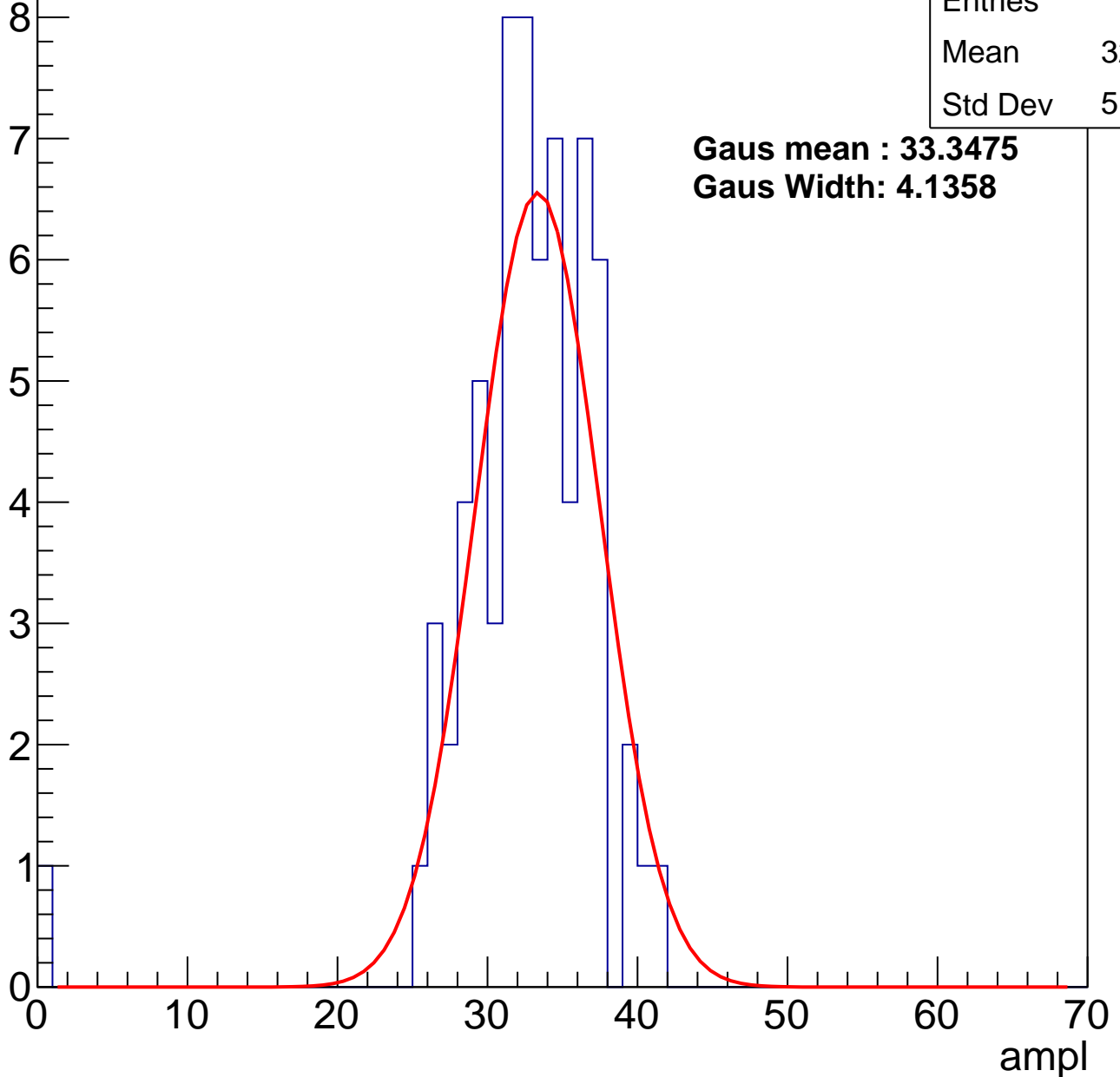
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	32.13
Std Dev	5.294

Gaus mean : 33.3475

Gaus Width: 4.1358



B1L103S, U17-ch86, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	39.04
Std Dev	6.337

Gaus mean : 40.6985

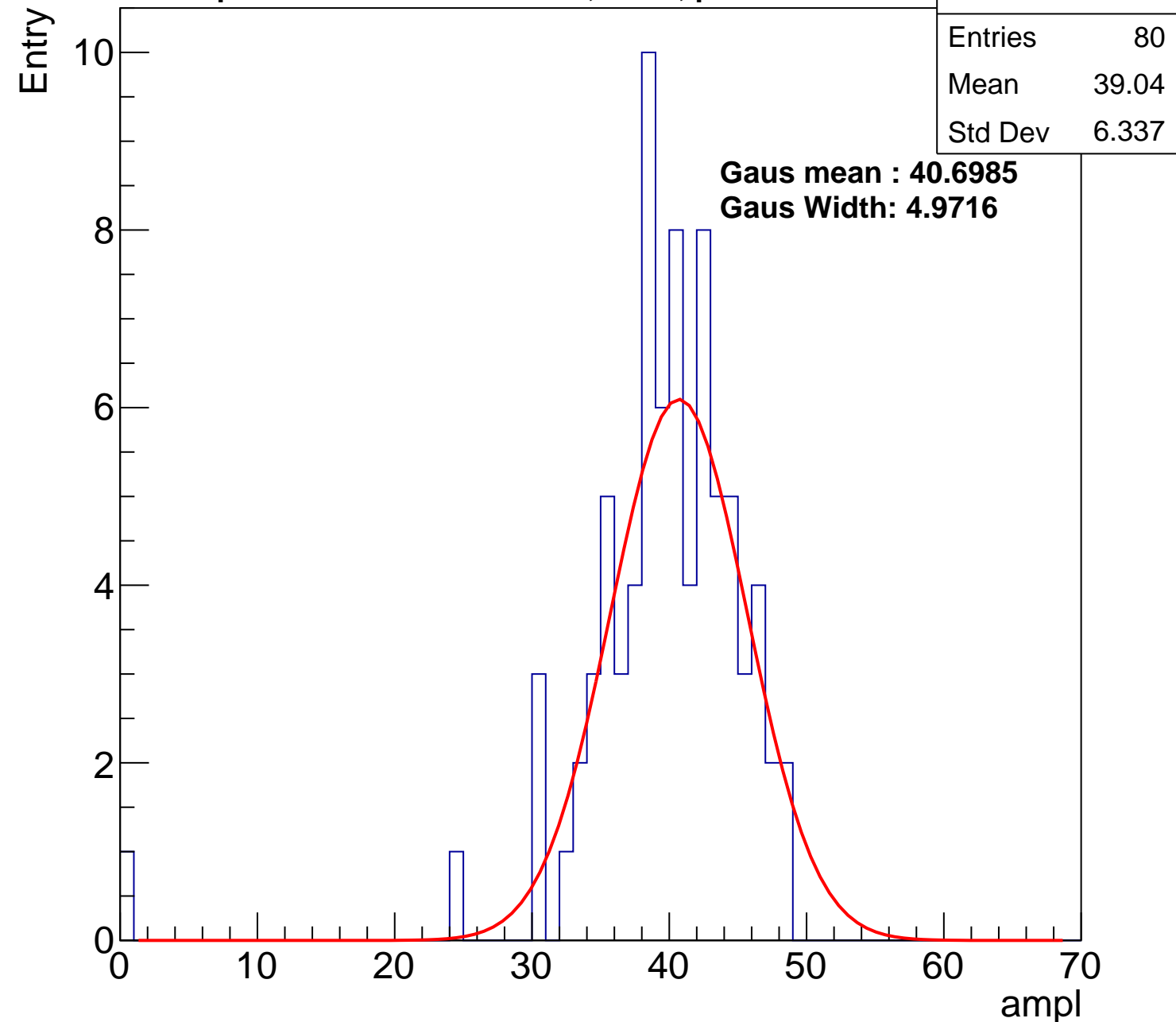
Gaus Width: 4.9716

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

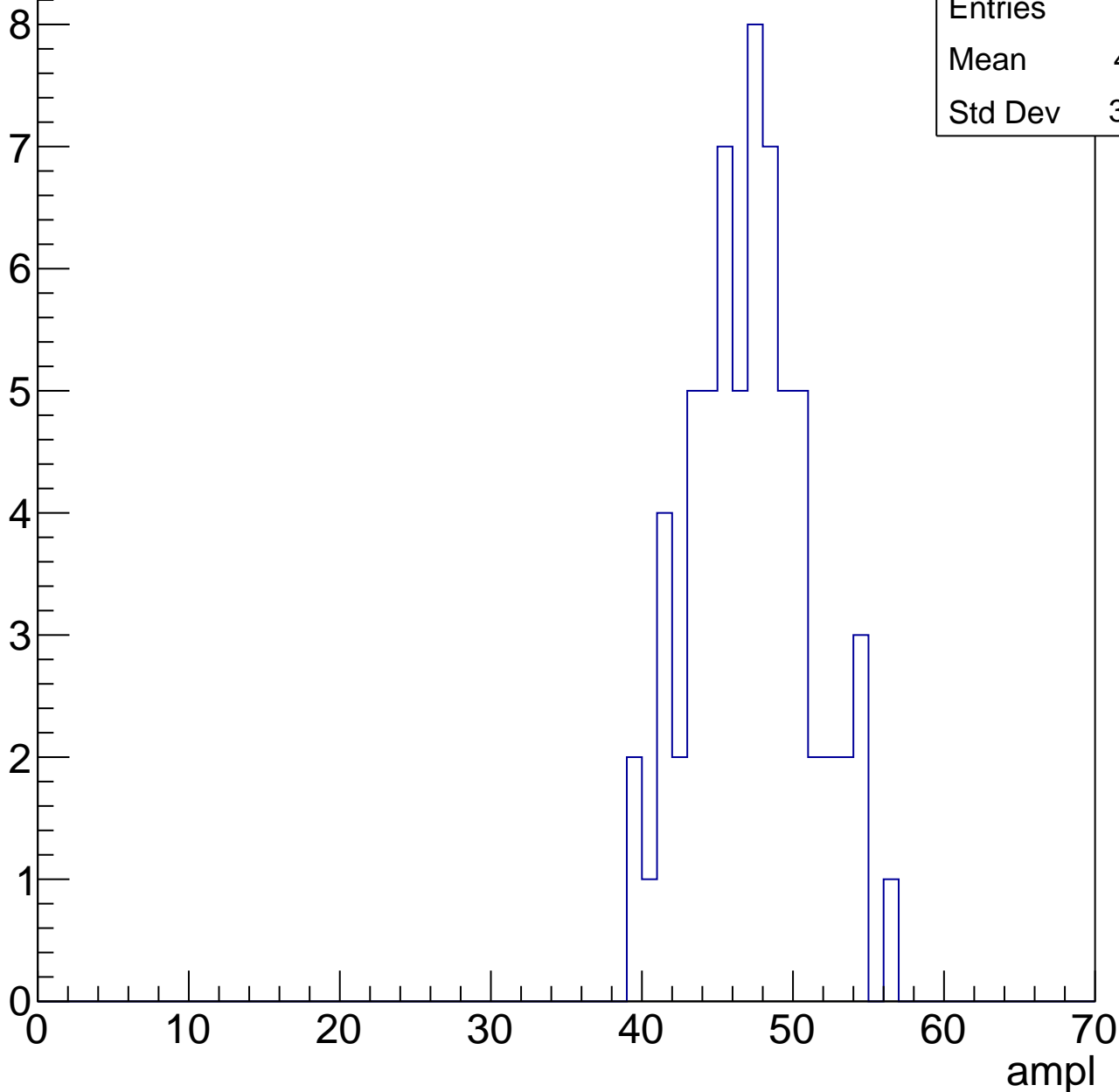


B1L103S, U17-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	46.71
Std Dev	3.849

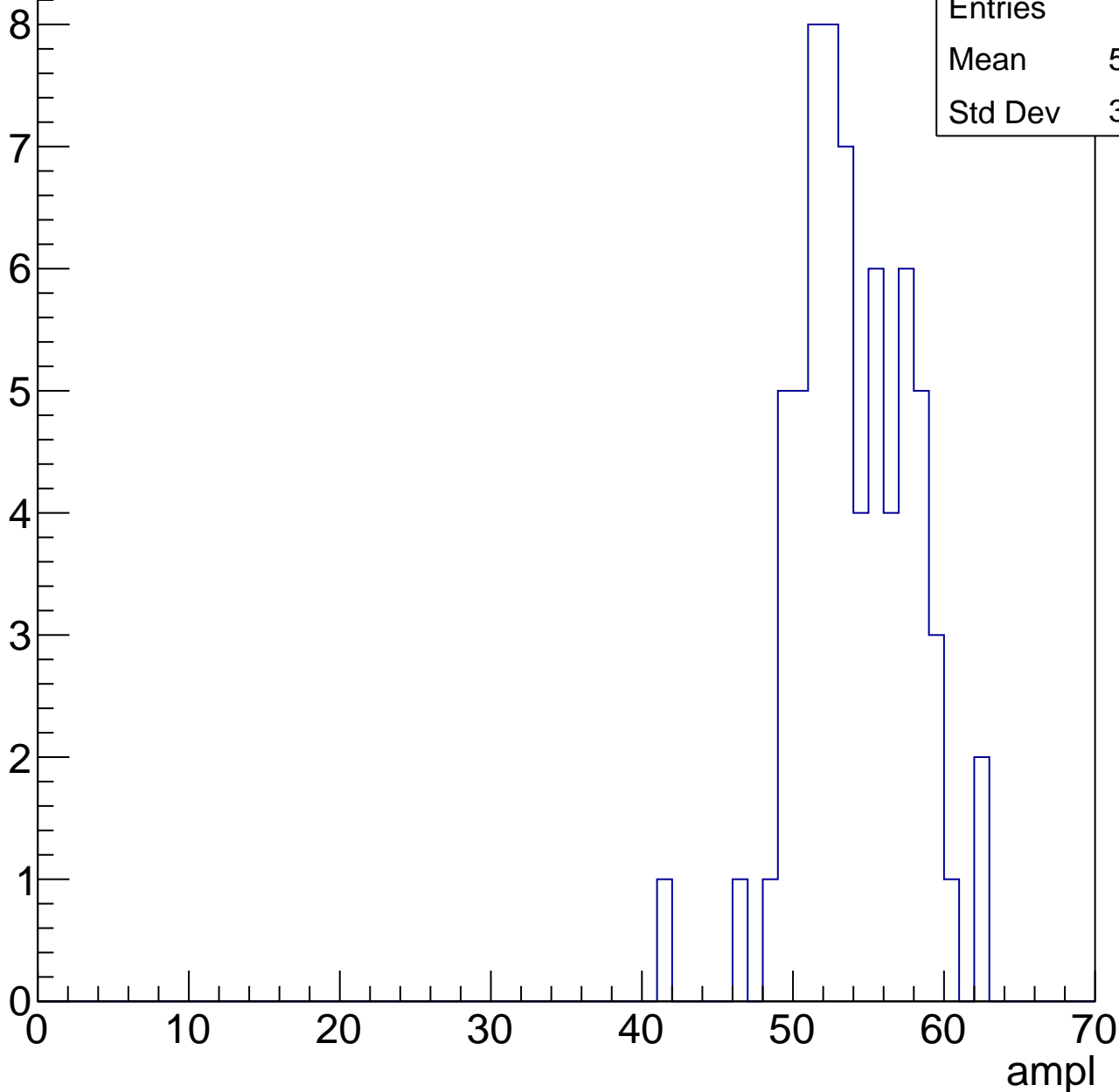


B1L103S, U17-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	53.55
Std Dev	3.798



B1L103S, U17-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

52

Mean

57.71

Std Dev

8.513

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

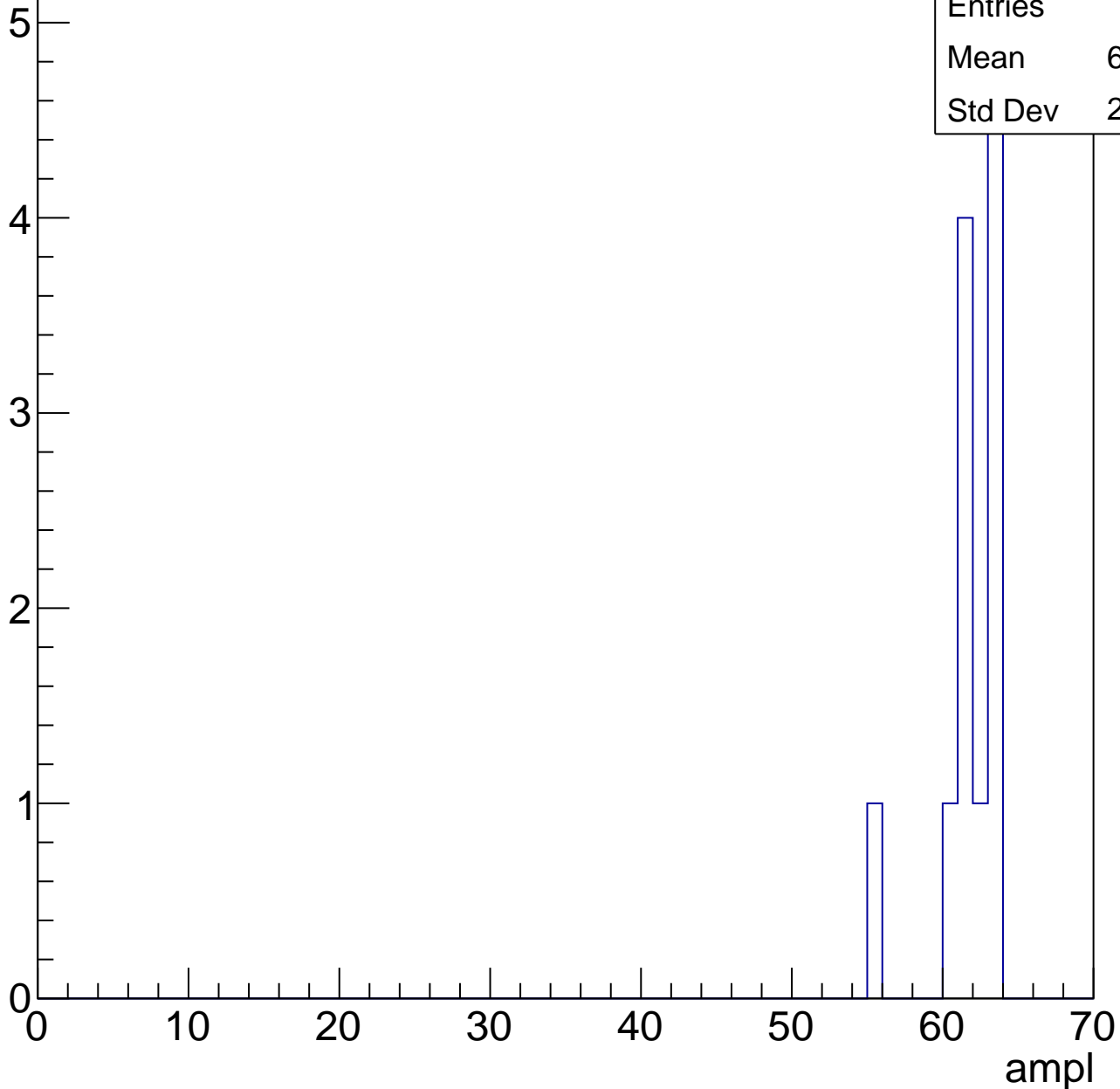
7

B1L103S, U17-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

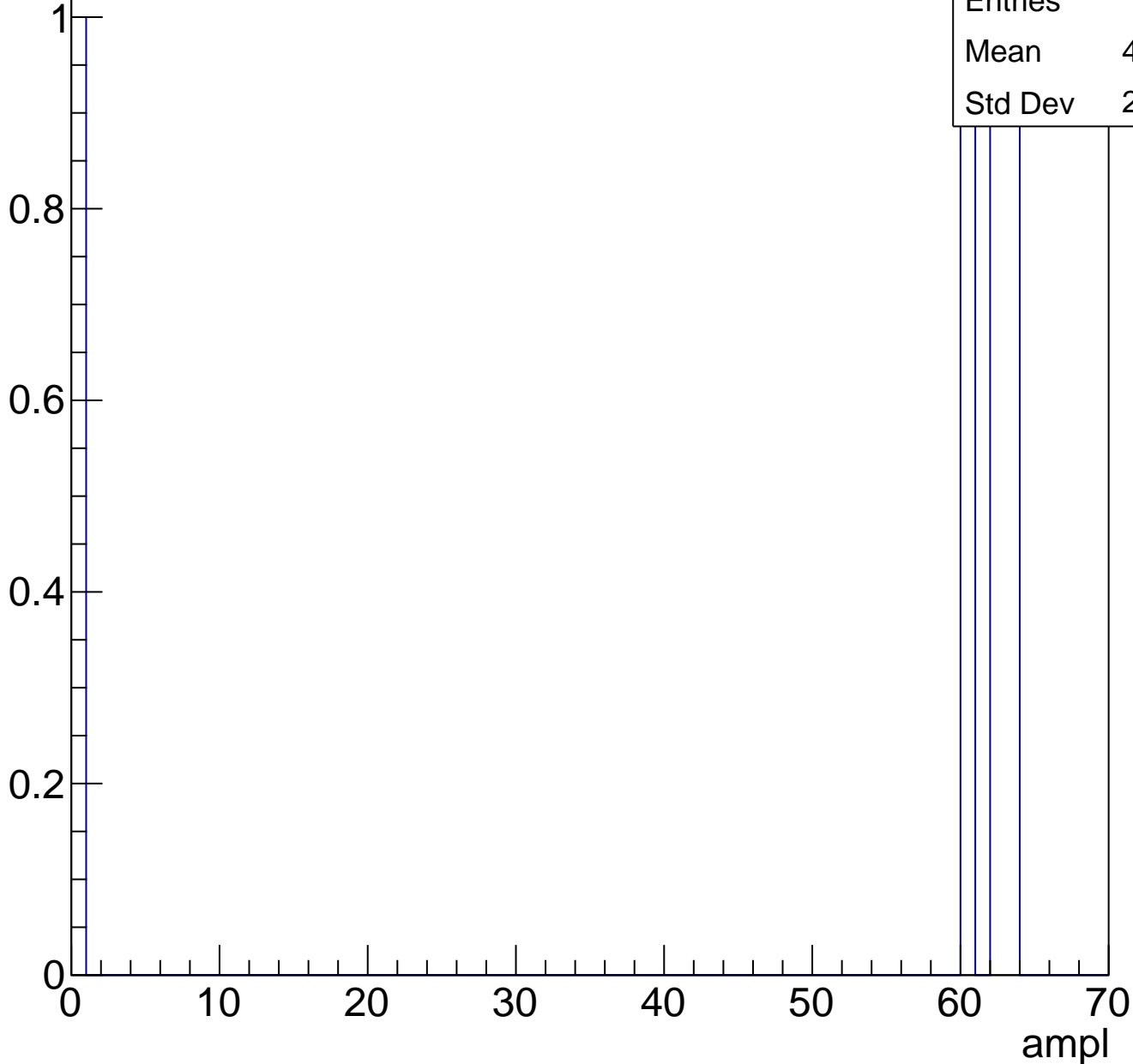
Entries	12
Mean	61.33
Std Dev	2.173



B1L103S, U17-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch87, adc0

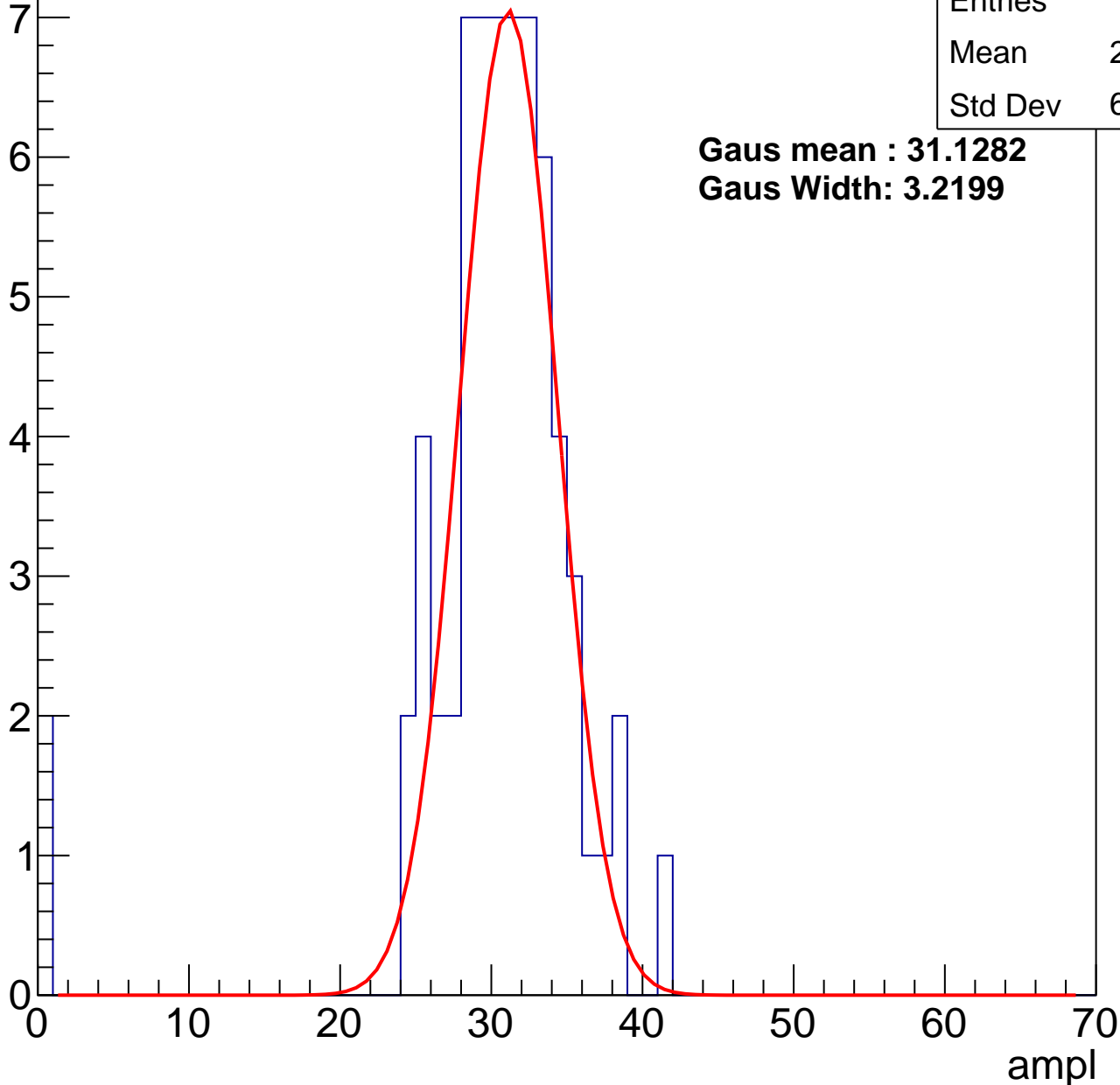
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.74
Std Dev	6.342

Gaus mean : 31.1282

Gaus Width: 3.2199



B1L103S, U17-ch87, adc1

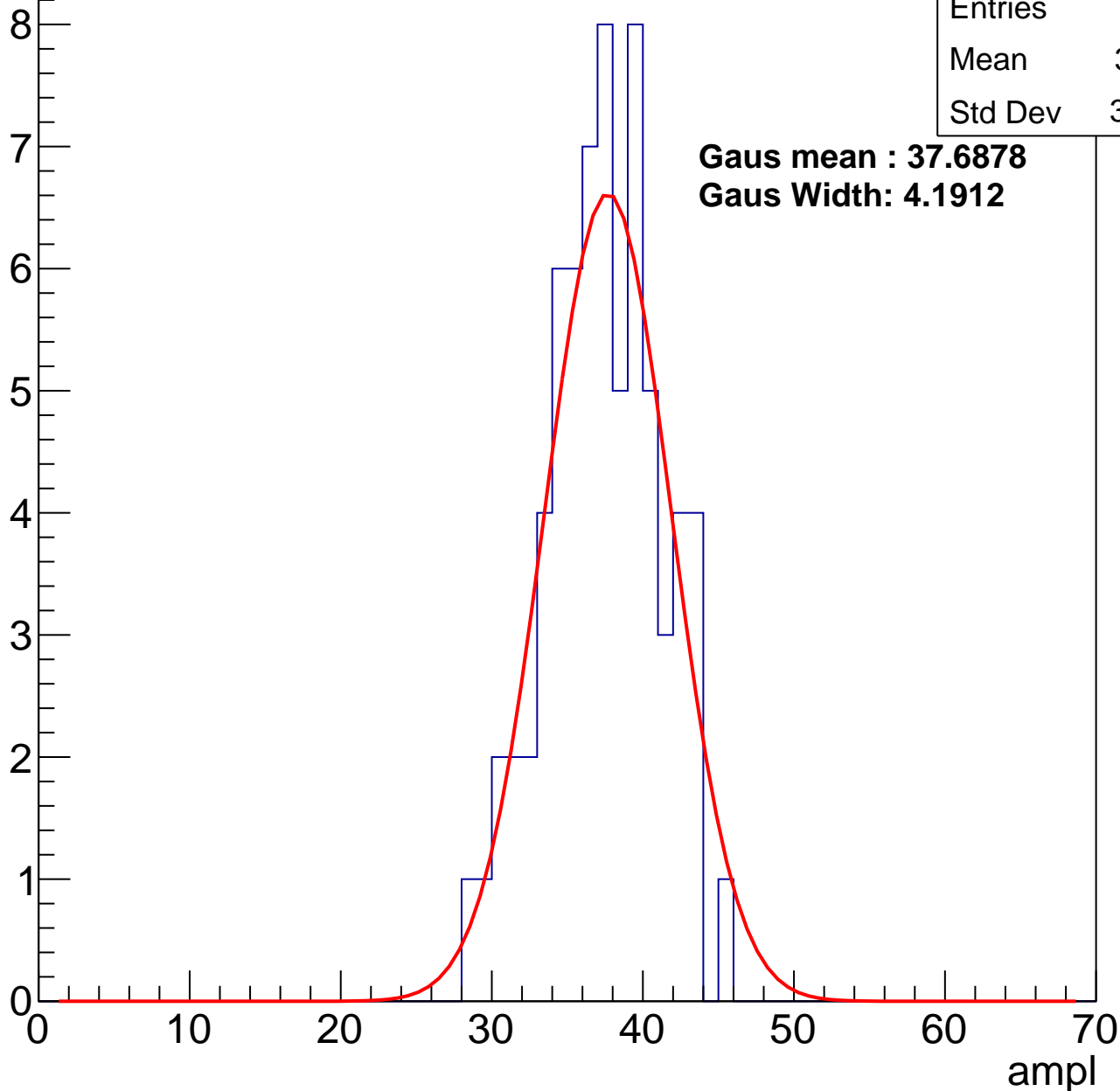
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.91
Std Dev	3.713

Gaus mean : 37.6878

Gaus Width: 4.1912



B1L103S, U17-ch87, adc2

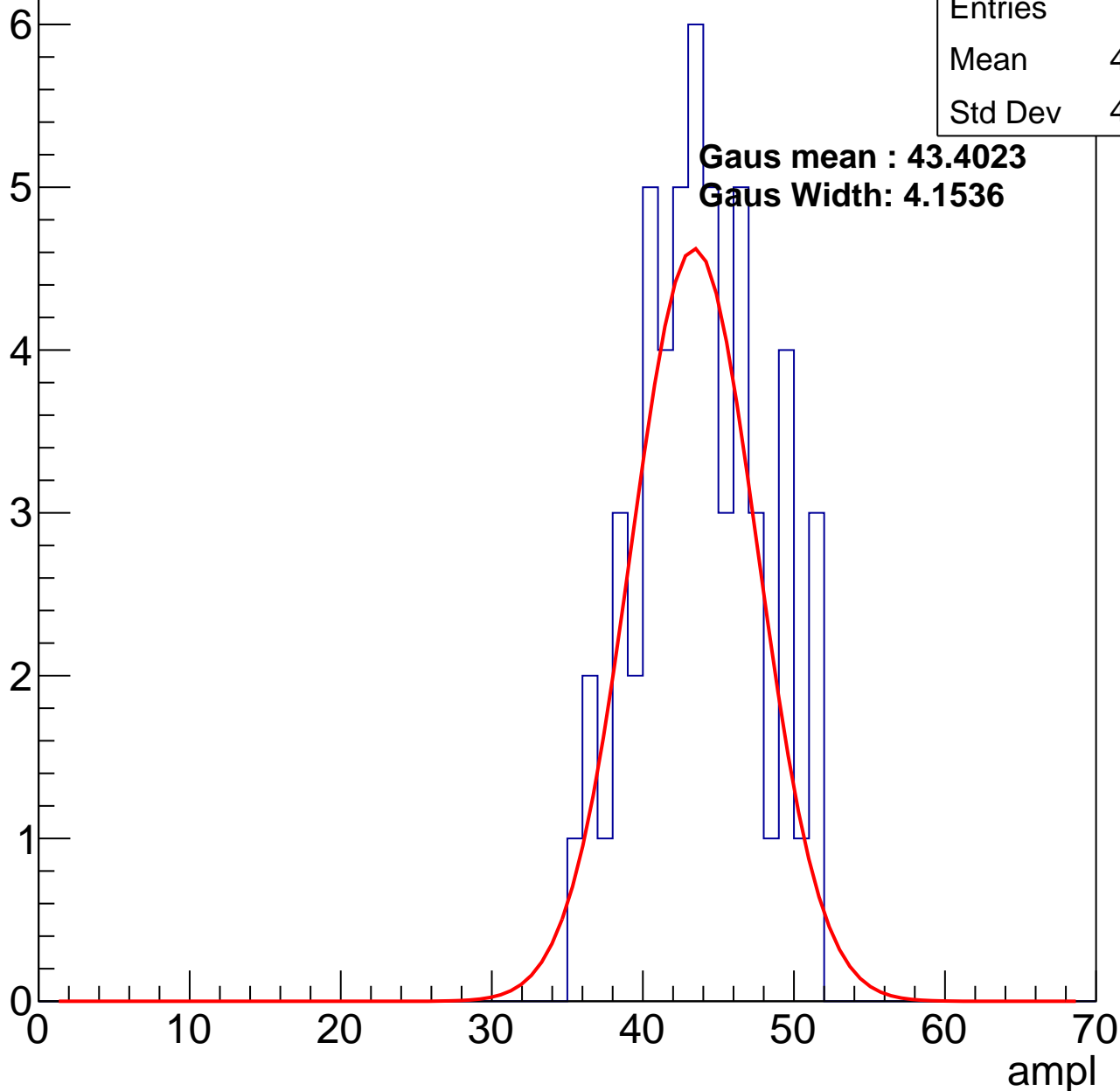
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	43.35
Std Dev	4.047

Gaus mean : 43.4023

Gaus Width: 4.1536

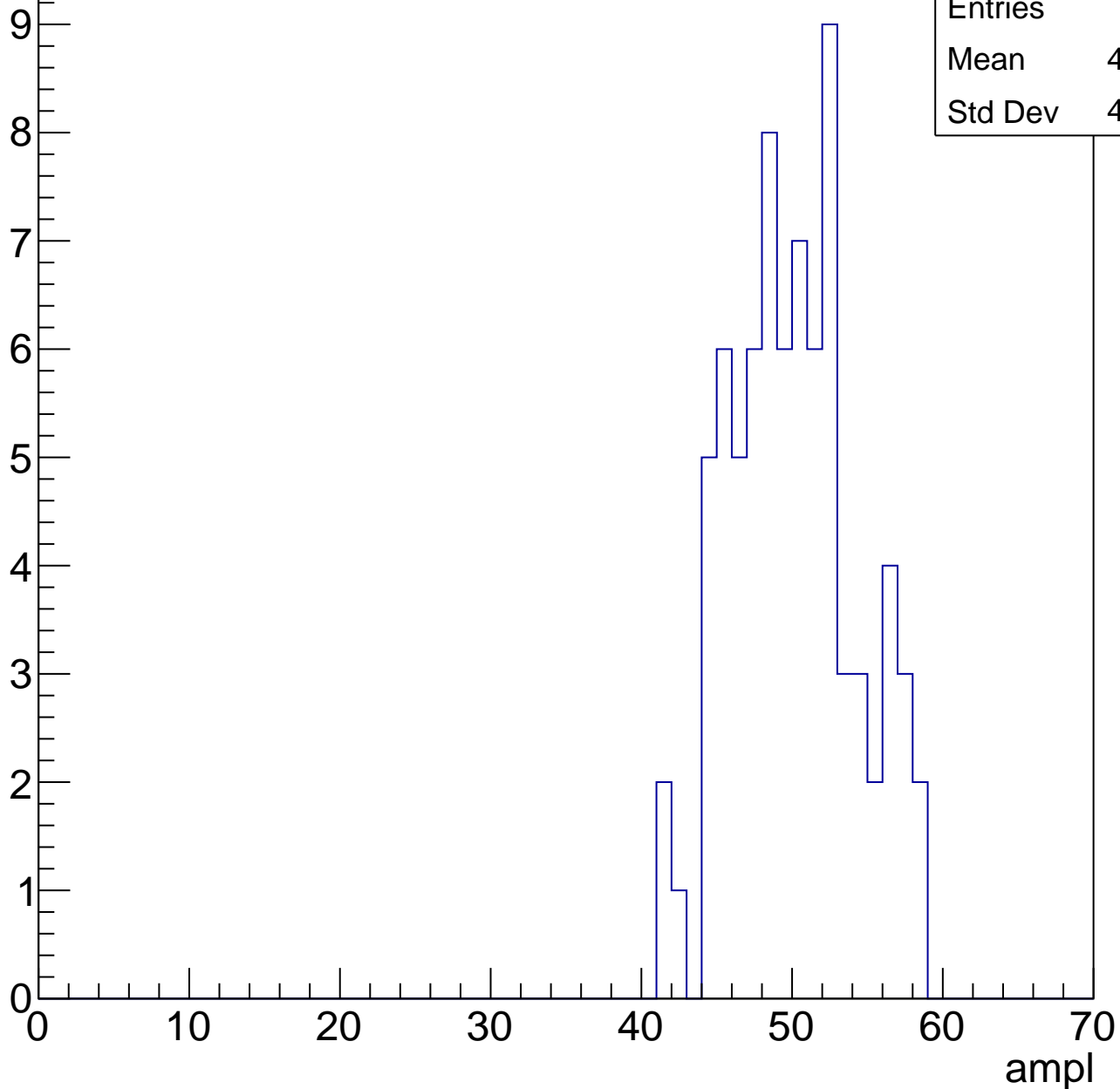


B1L103S, U17-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	49.62
Std Dev	4.086

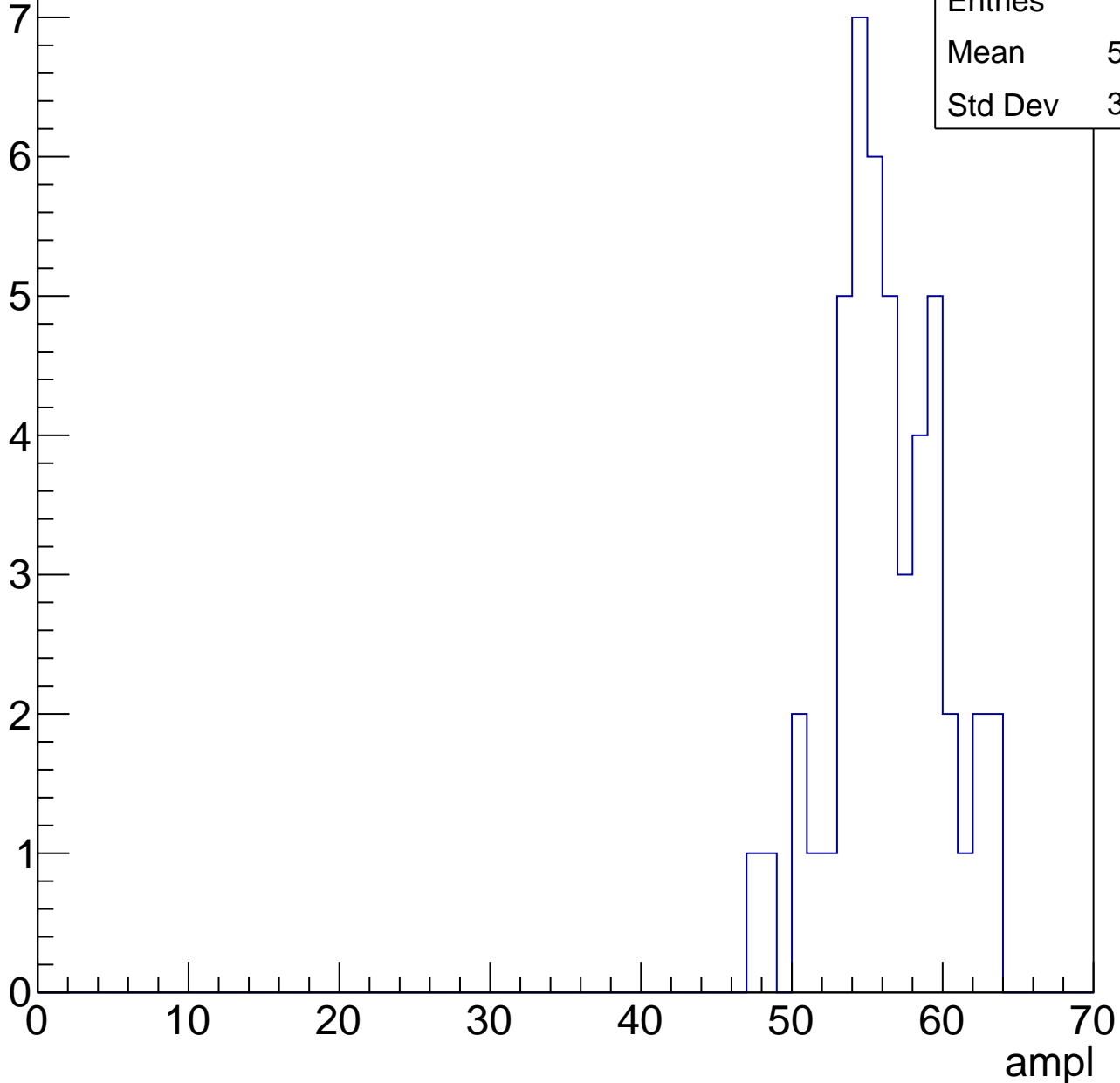


B1L103S, U17-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	55.83
Std Dev	3.613



B1L103S, U17-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	43
Mean	59.56
Std Dev	2.591

ampl

0

10

20

30

40

50

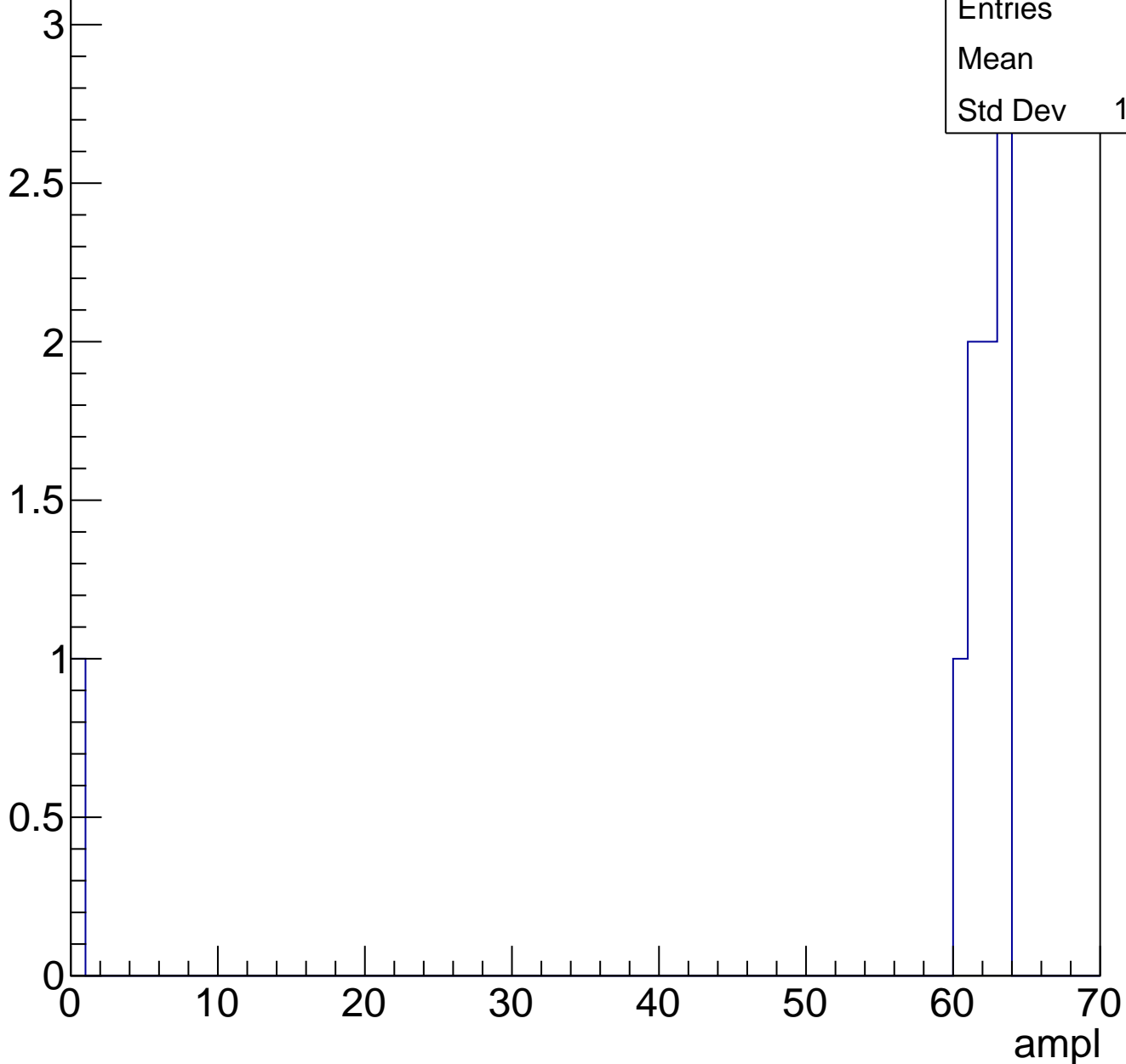
60

70

B1L103S, U17-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch88, adc0

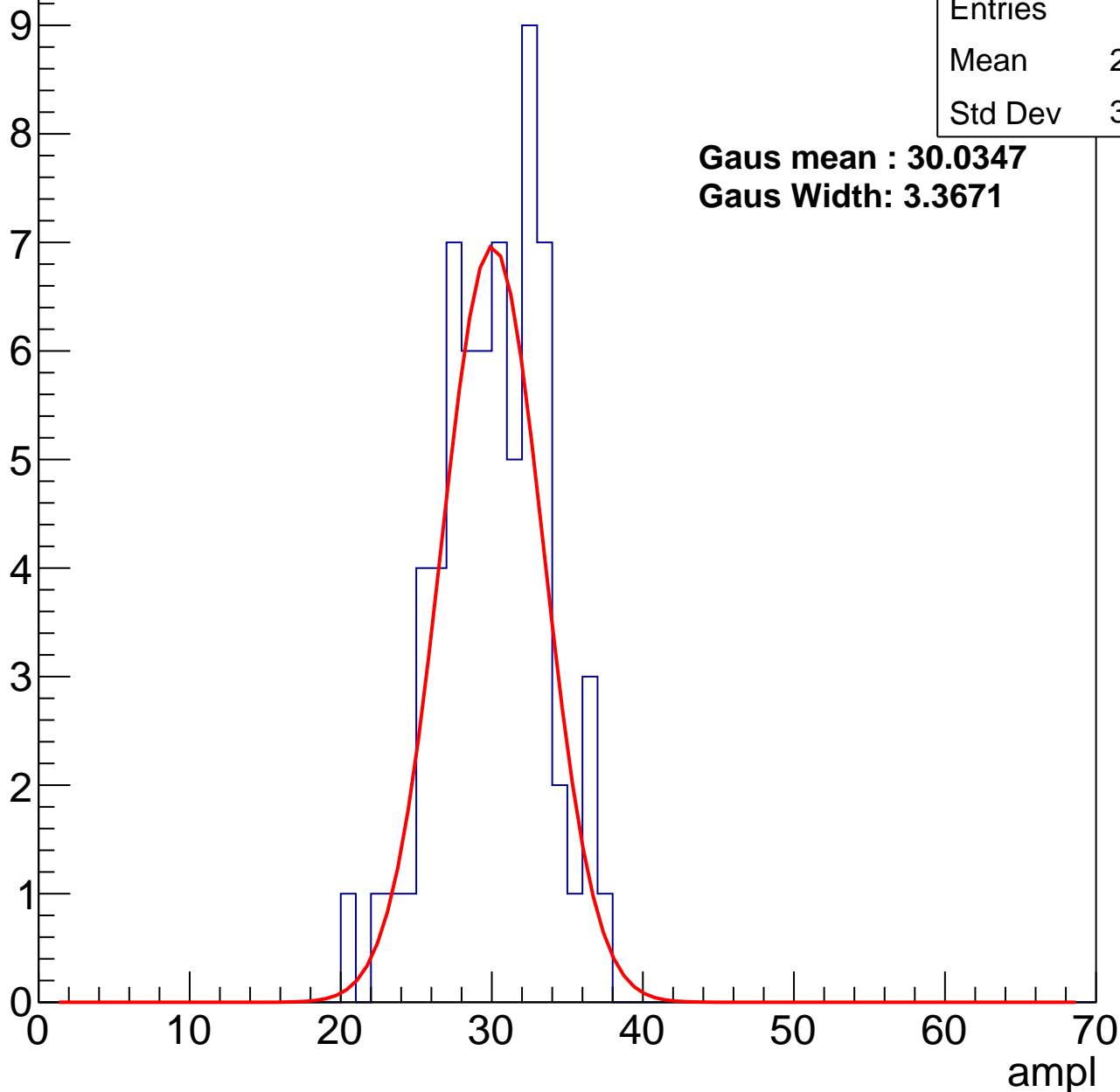
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.64
Std Dev	3.515

Gaus mean : 30.0347

Gaus Width: 3.3671



B1L103S, U17-ch88, adc1

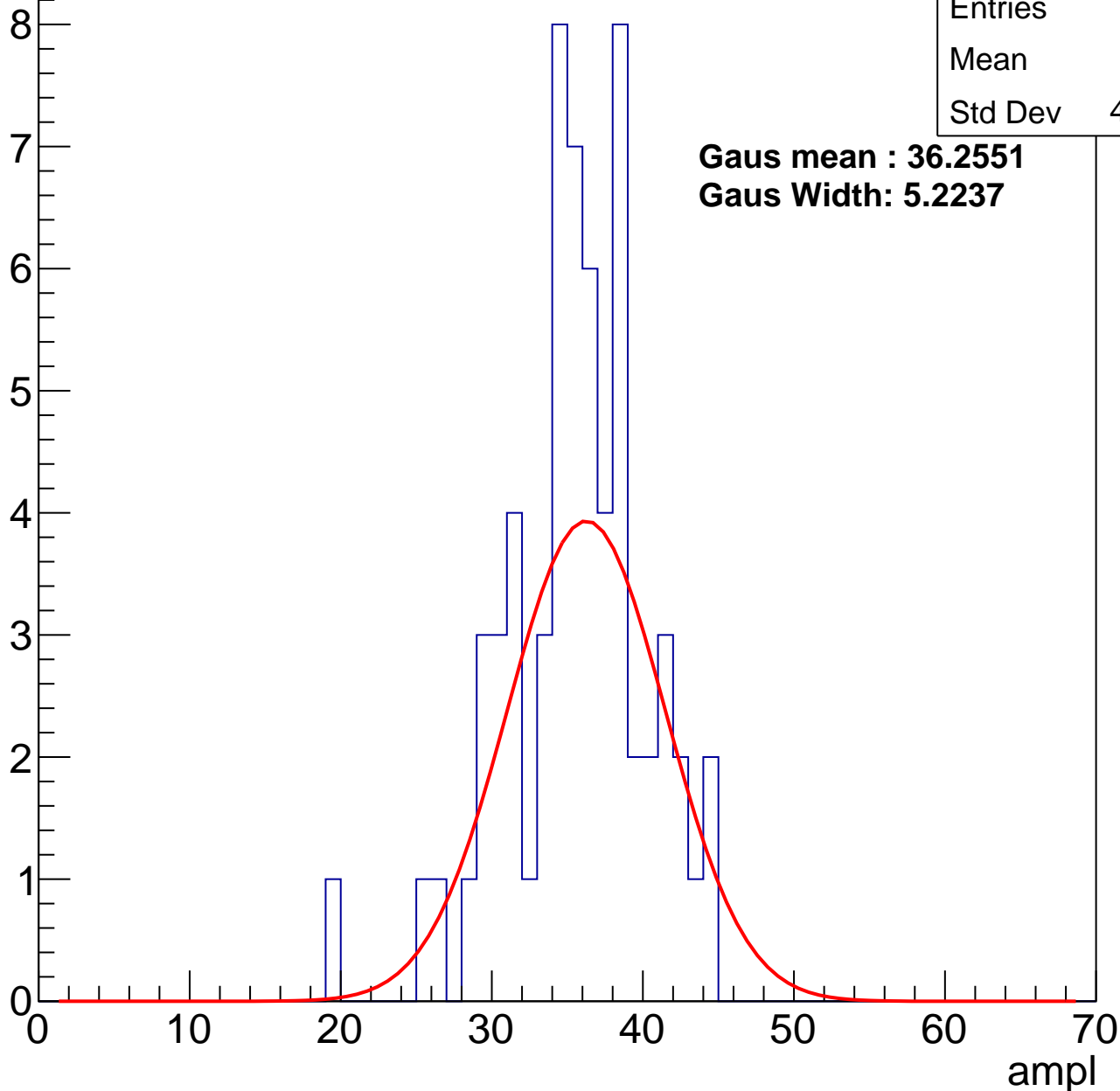
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.1
Std Dev	4.662

Gaus mean : 36.2551

Gaus Width: 5.2237



B1L103S, U17-ch88, adc2

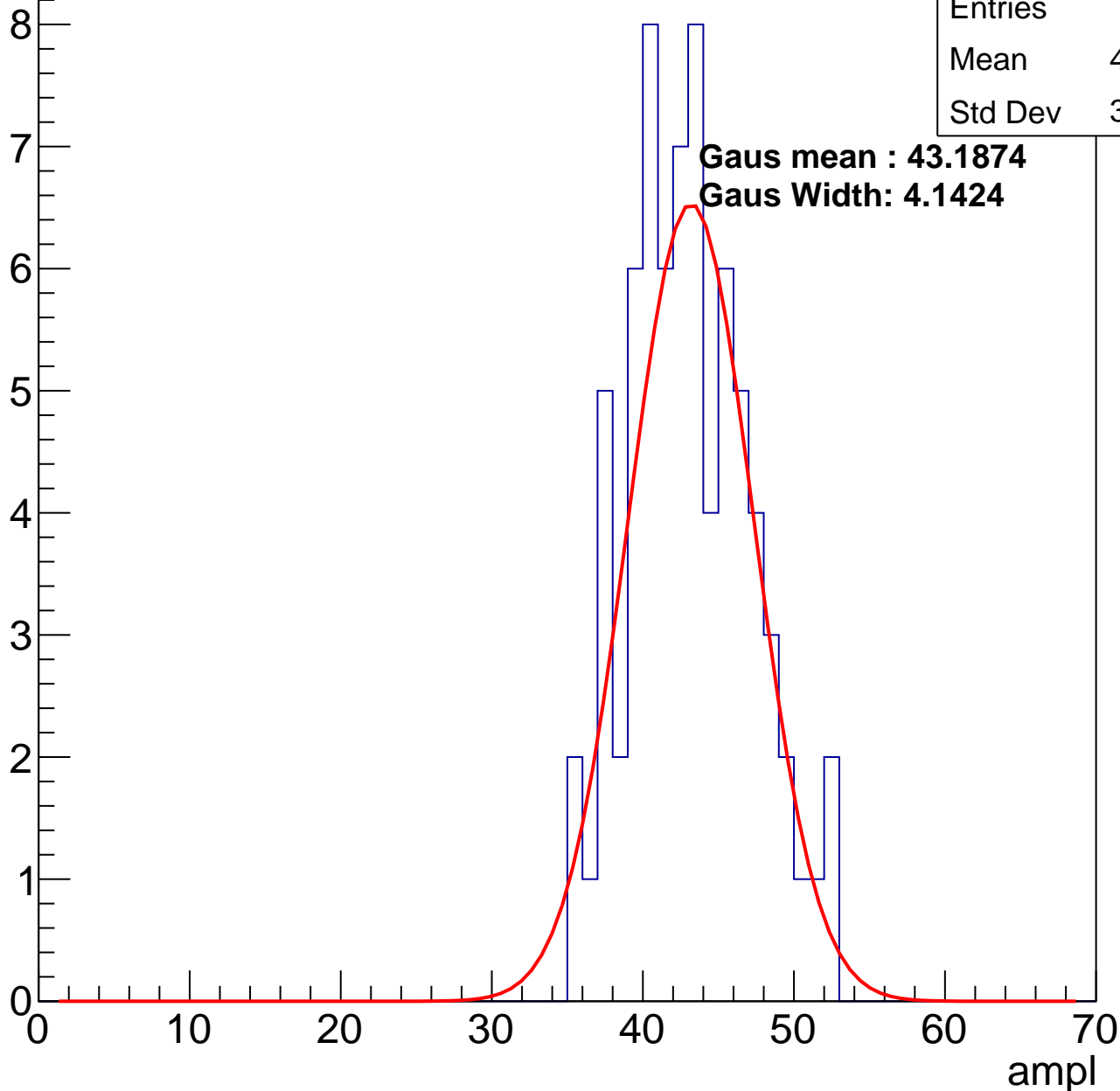
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.68
Std Dev	3.982

Gaus mean : 43.1874

Gaus Width: 4.1424

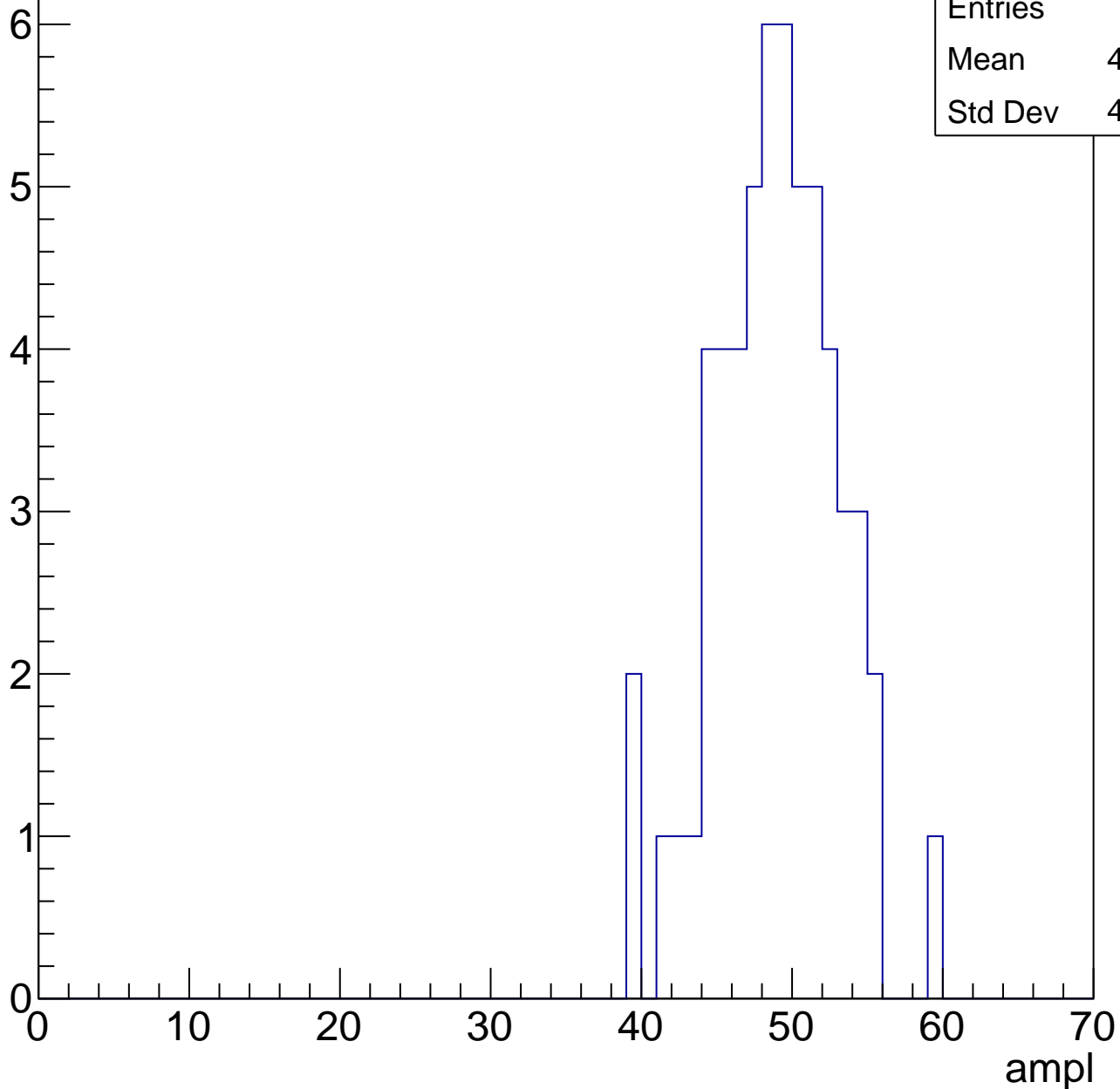


B1L103S, U17-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	48.49
Std Dev	4.009

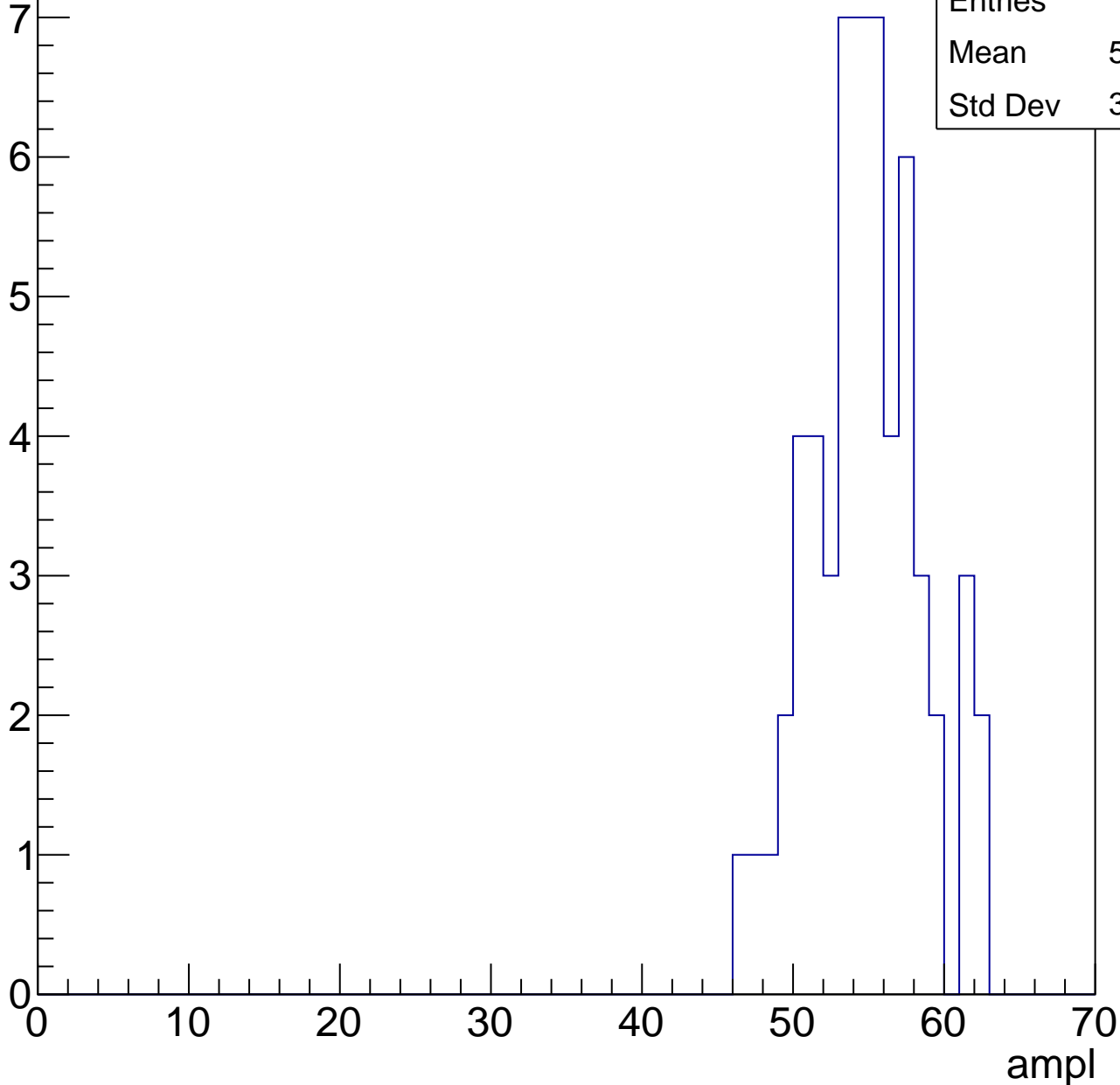


B1L103S, U17-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.35
Std Dev	3.654

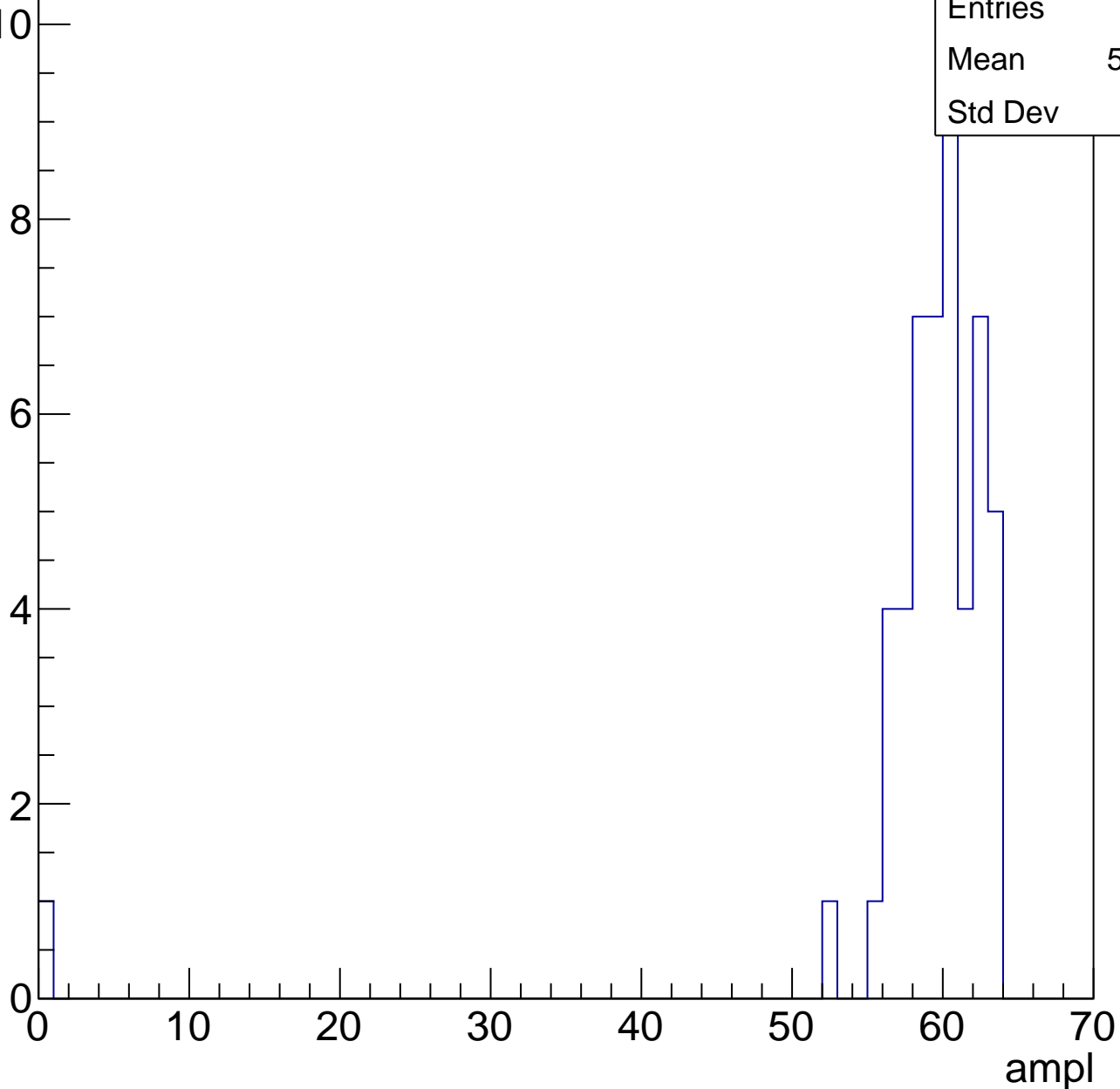


B1L103S, U17-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.25
Std Dev	8.57

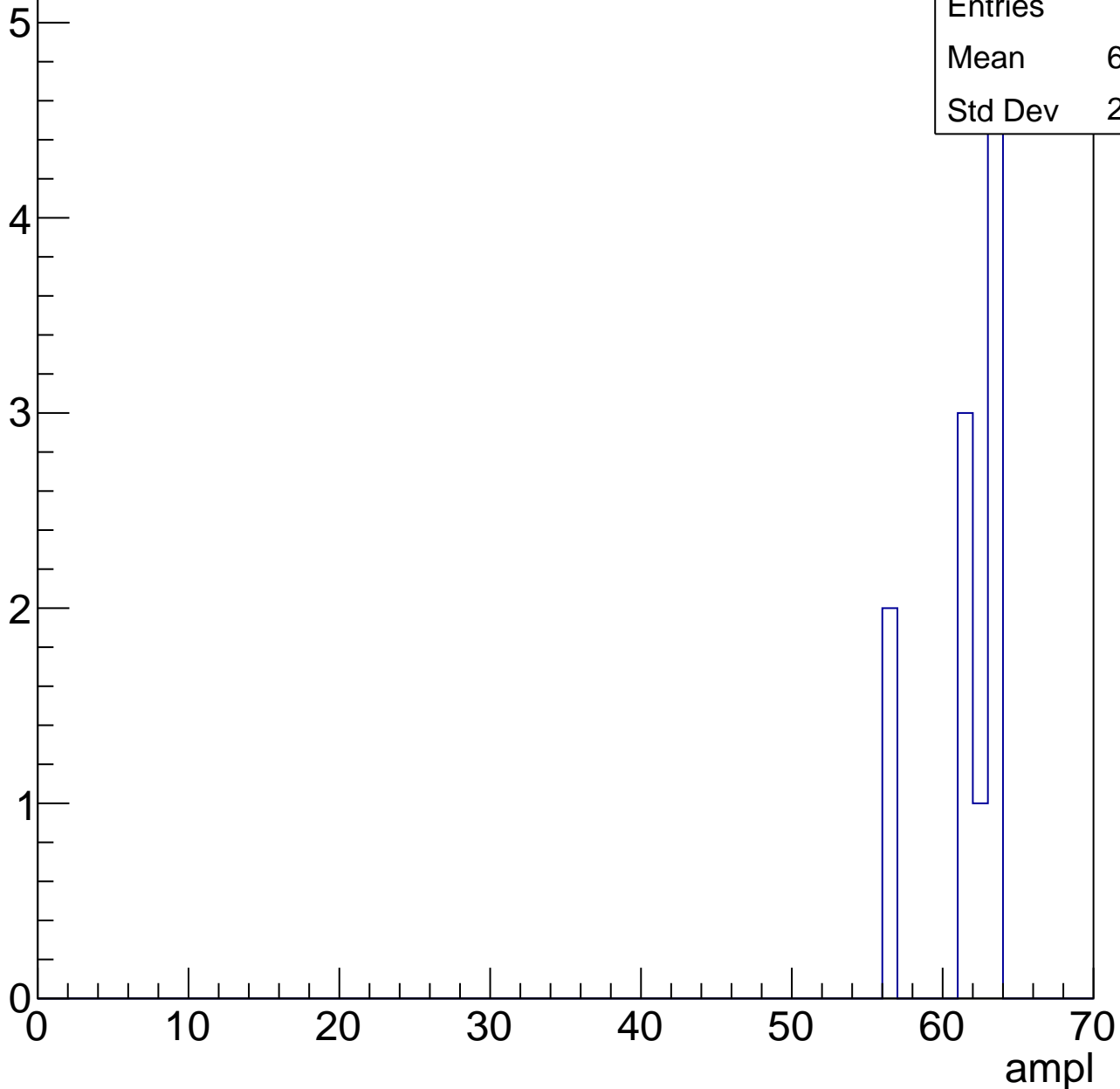


B1L103S, U17-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	61.09
Std Dev	2.539



B1L103S, U17-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch89, adc0

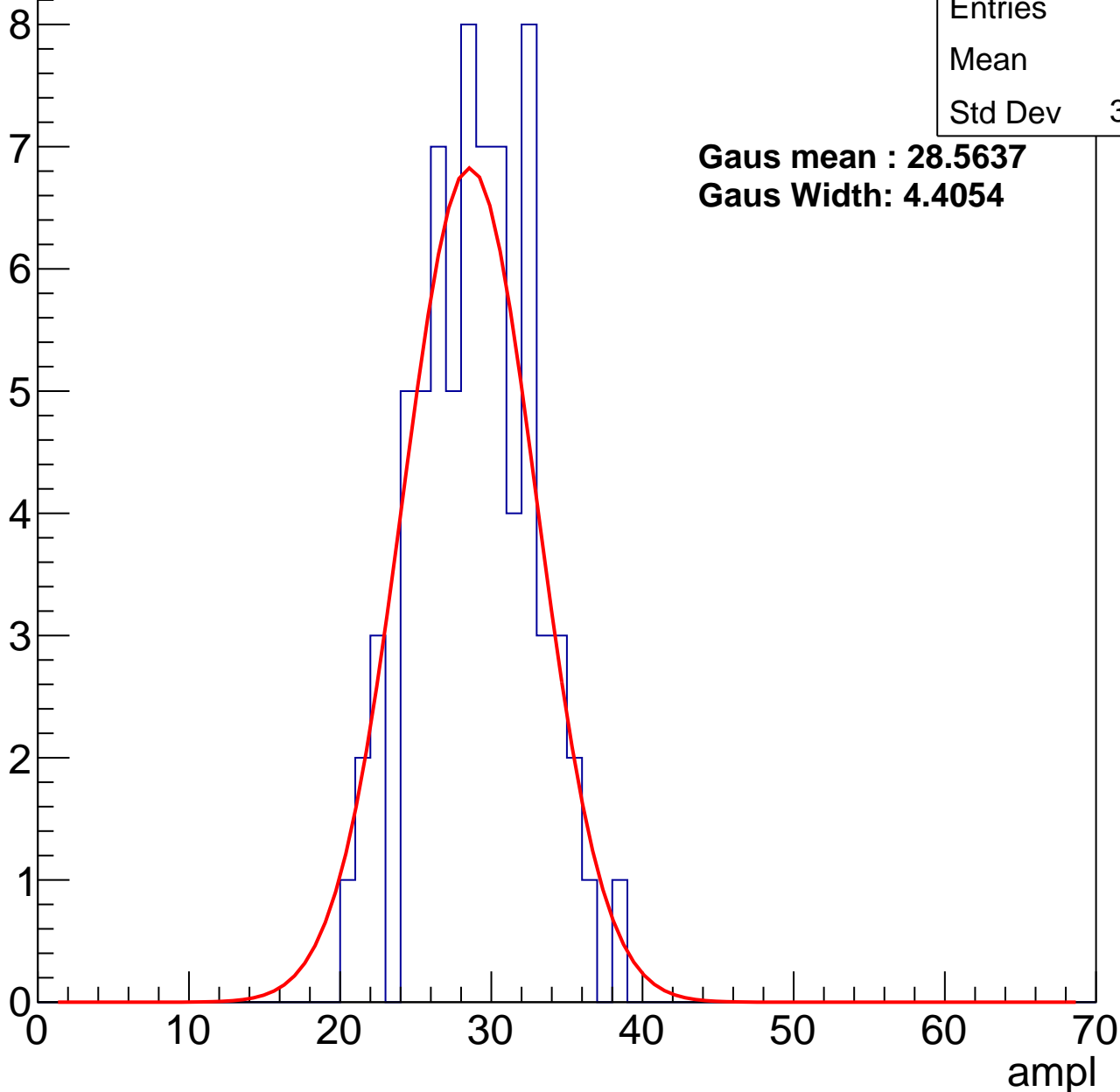
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.5
Std Dev	3.826

Gaus mean : 28.5637

Gaus Width: 4.4054



B1L103S, U17-ch89, adc1

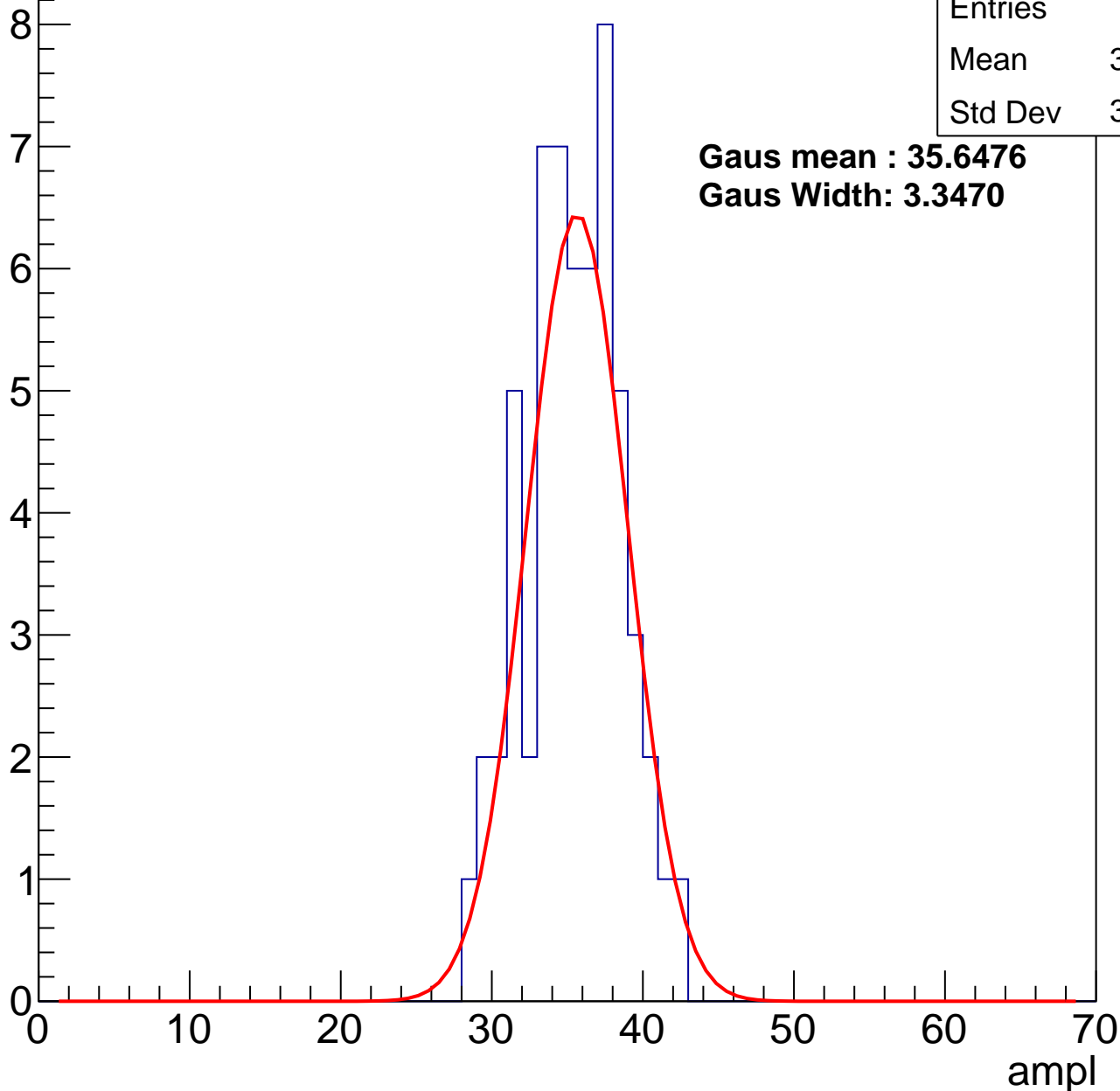
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	34.93
Std Dev	3.134

Gaus mean : 35.6476

Gaus Width: 3.3470



B1L103S, U17-ch89, adc2

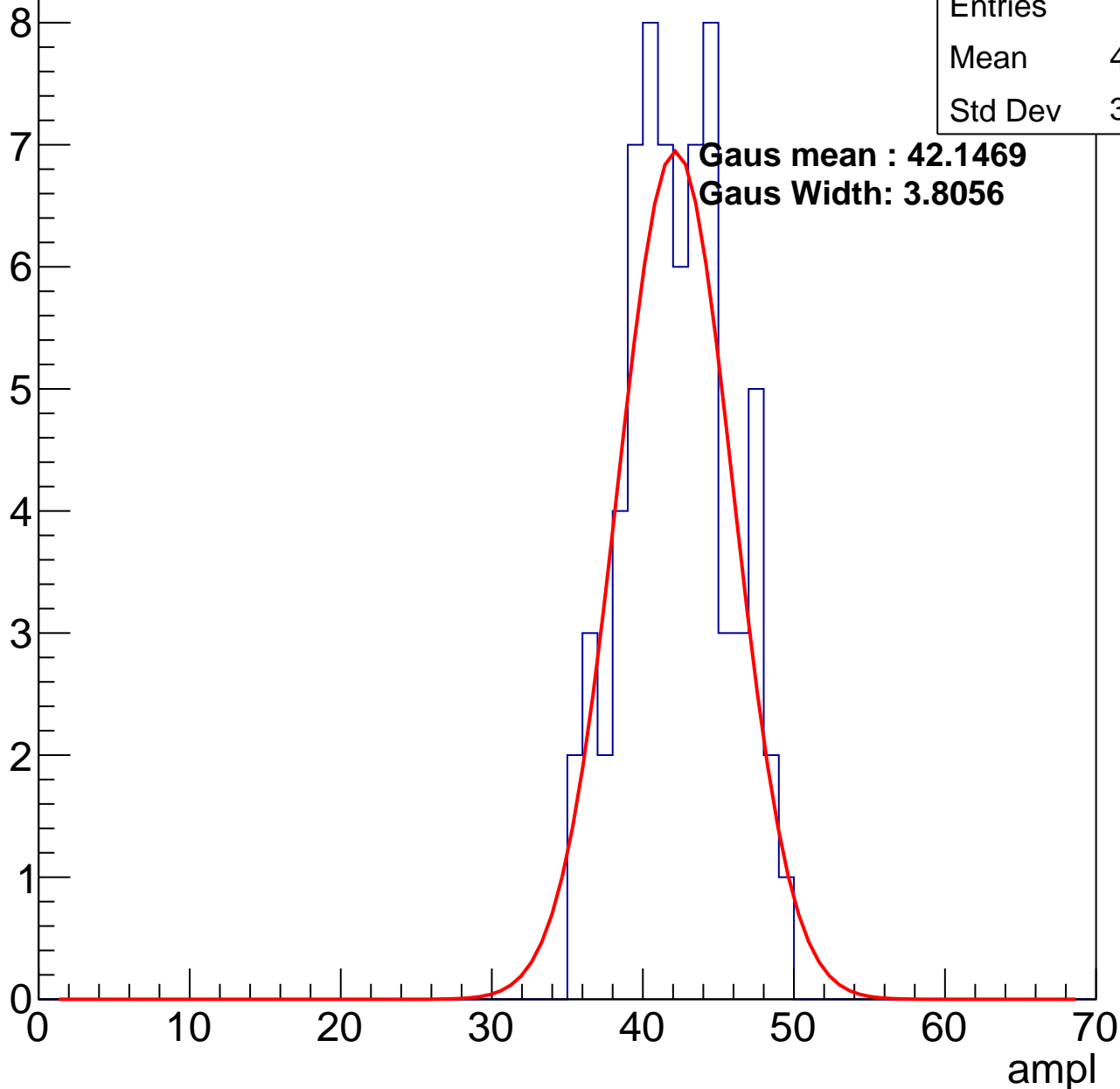
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	41.79
Std Dev	3.385

Gaus mean : 42.1469

Gaus Width: 3.8056

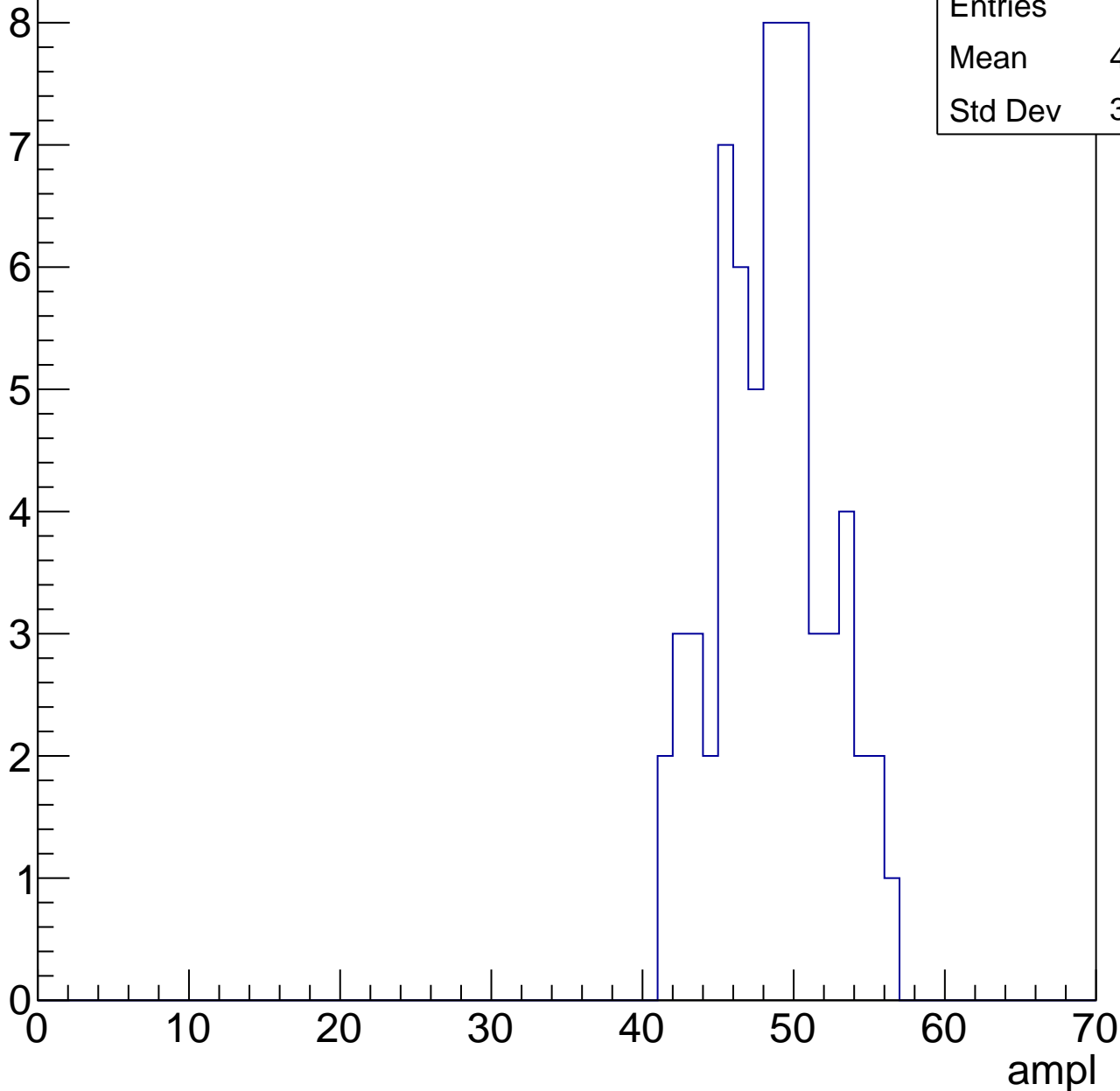


B1L103S, U17-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.09
Std Dev	3.569

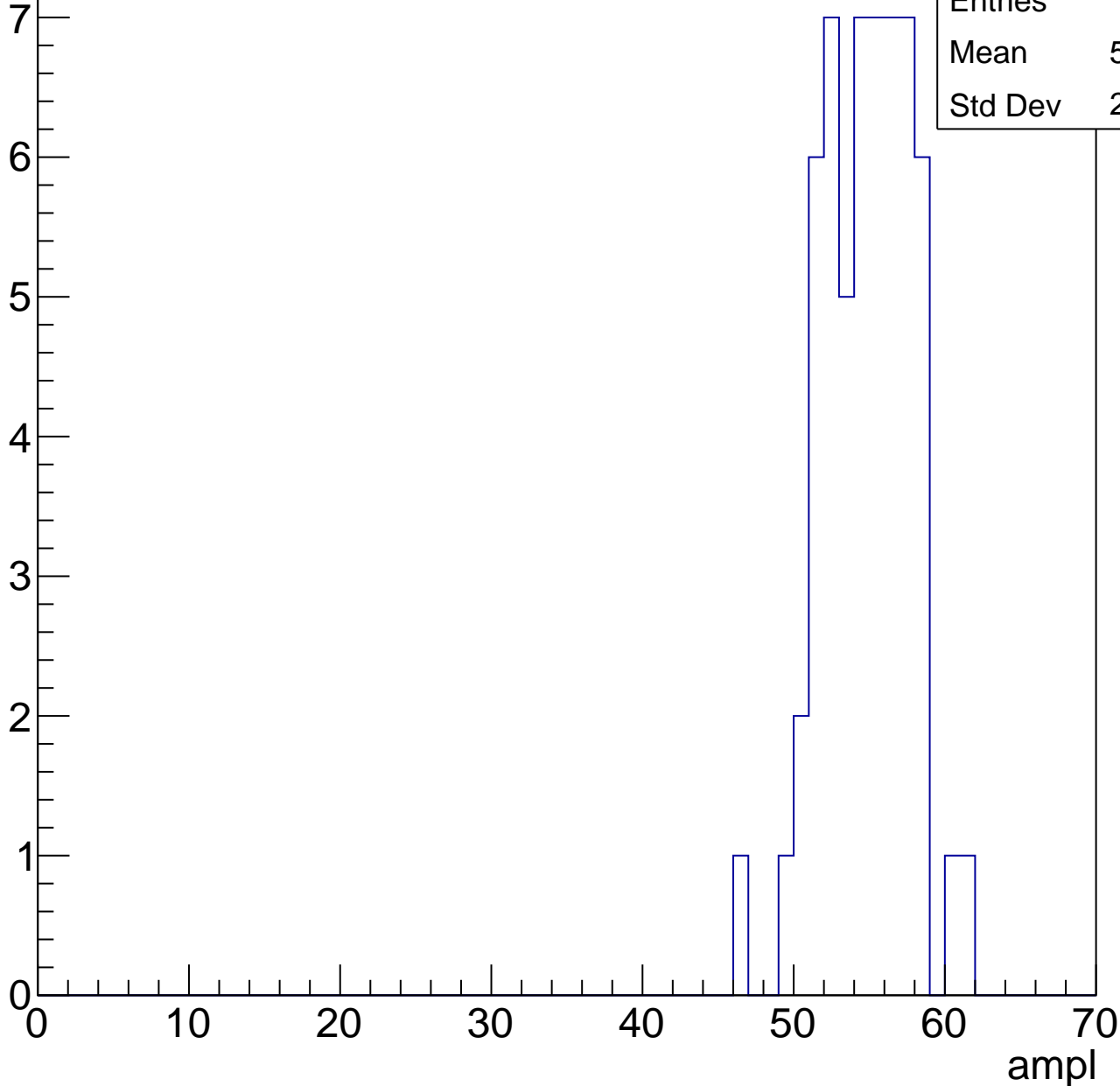


B1L103S, U17-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.36
Std Dev	2.875



B1L103S, U17-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 54

Mean 59.78

Std Dev 2.699

8

6

4

2

0

0

10

20

30

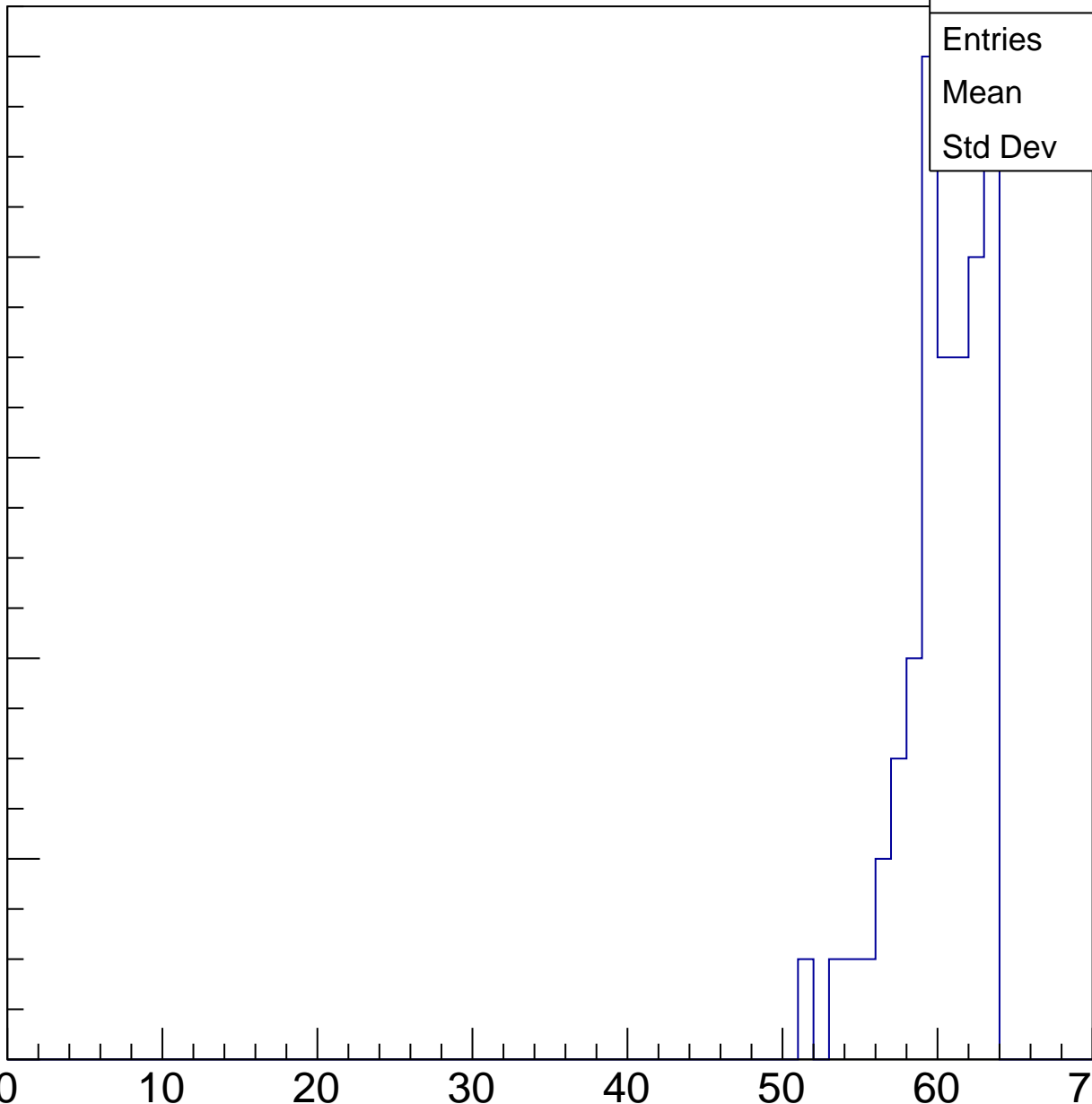
40

50

60

70

ampl



B1L103S, U17-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

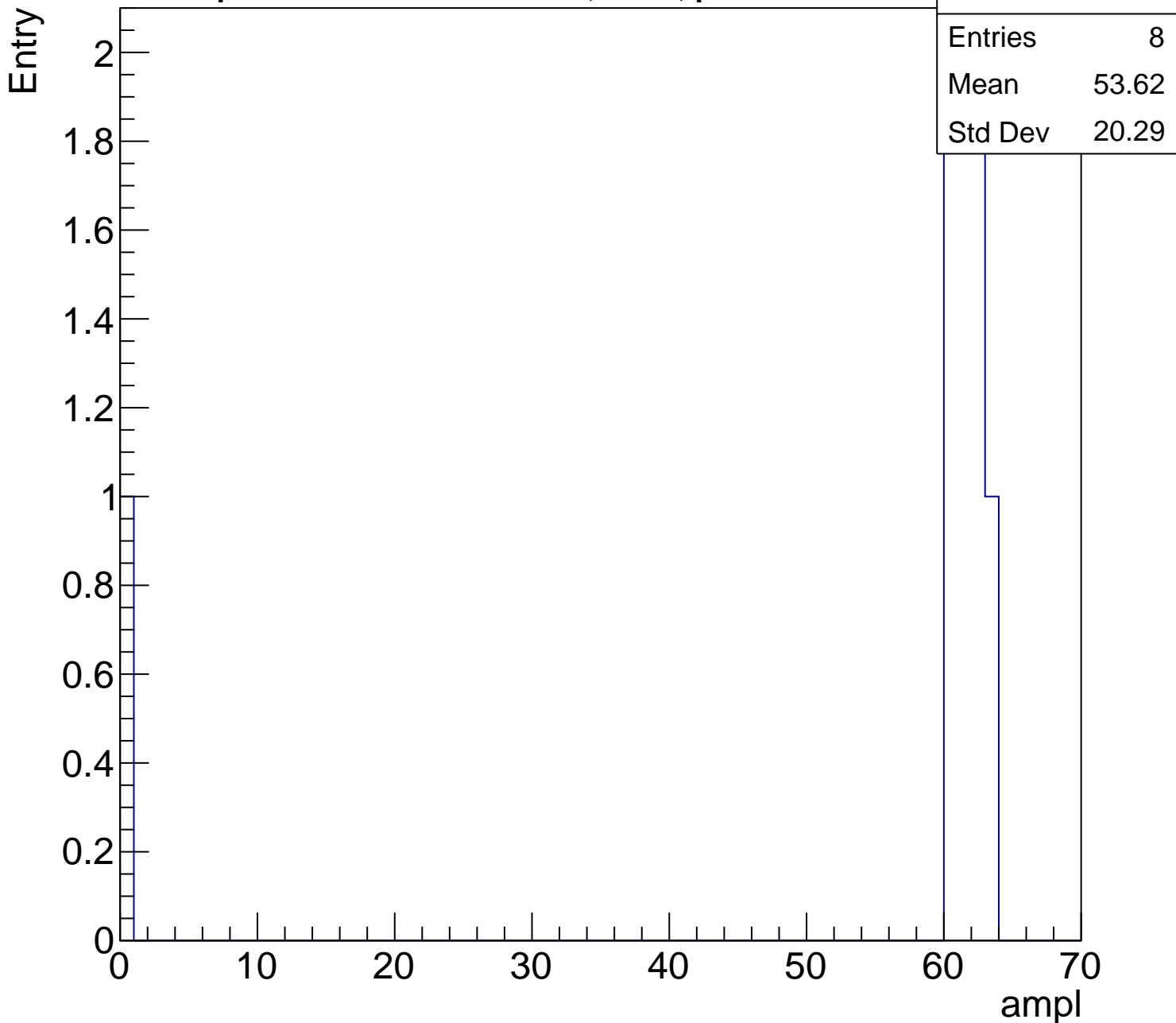
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.62
Std Dev	20.29

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L103S, U17-ch90, adc0

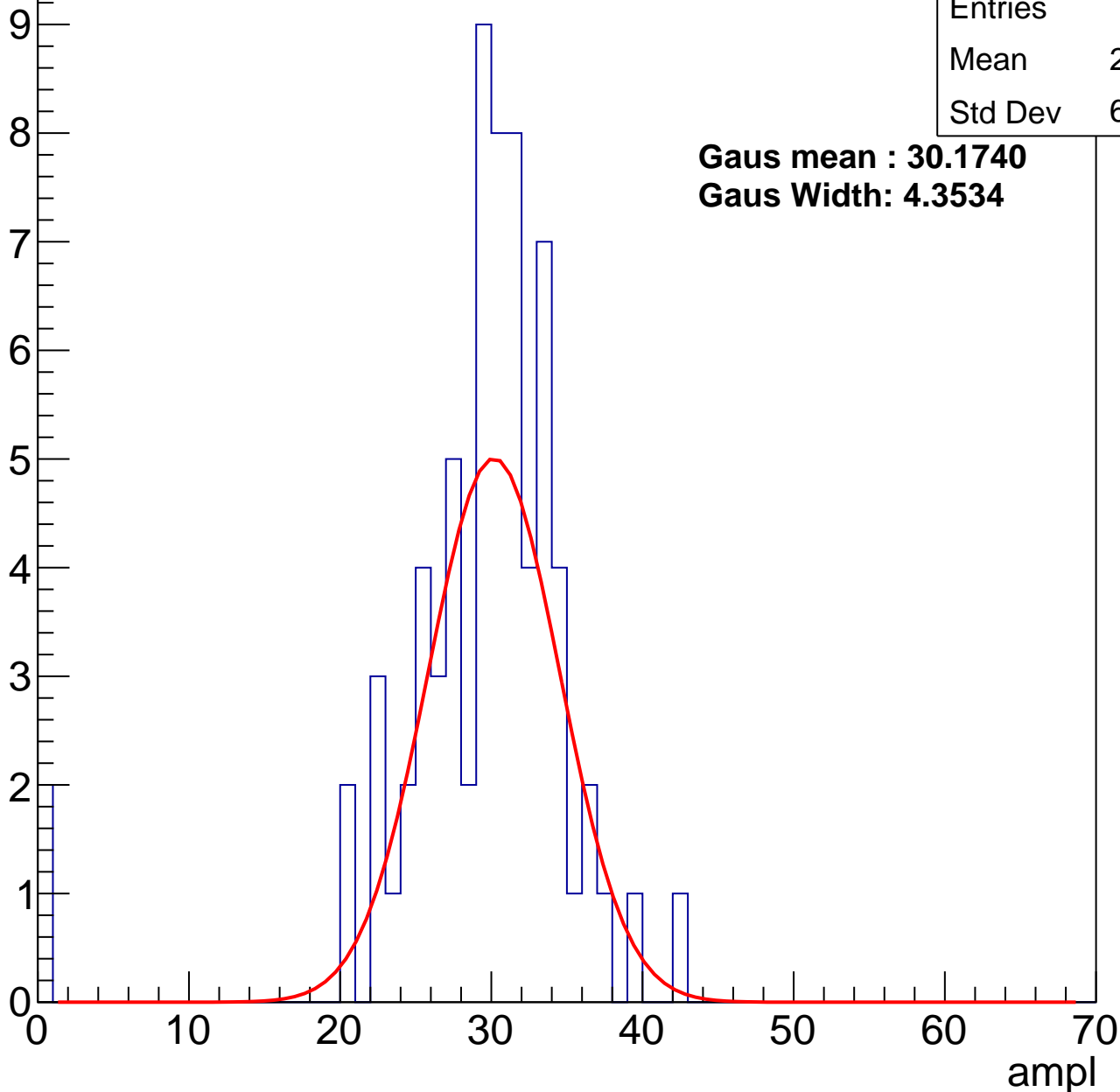
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.79
Std Dev	6.483

Gaus mean : 30.1740

Gaus Width: 4.3534



B1L103S, U17-ch90, adc1

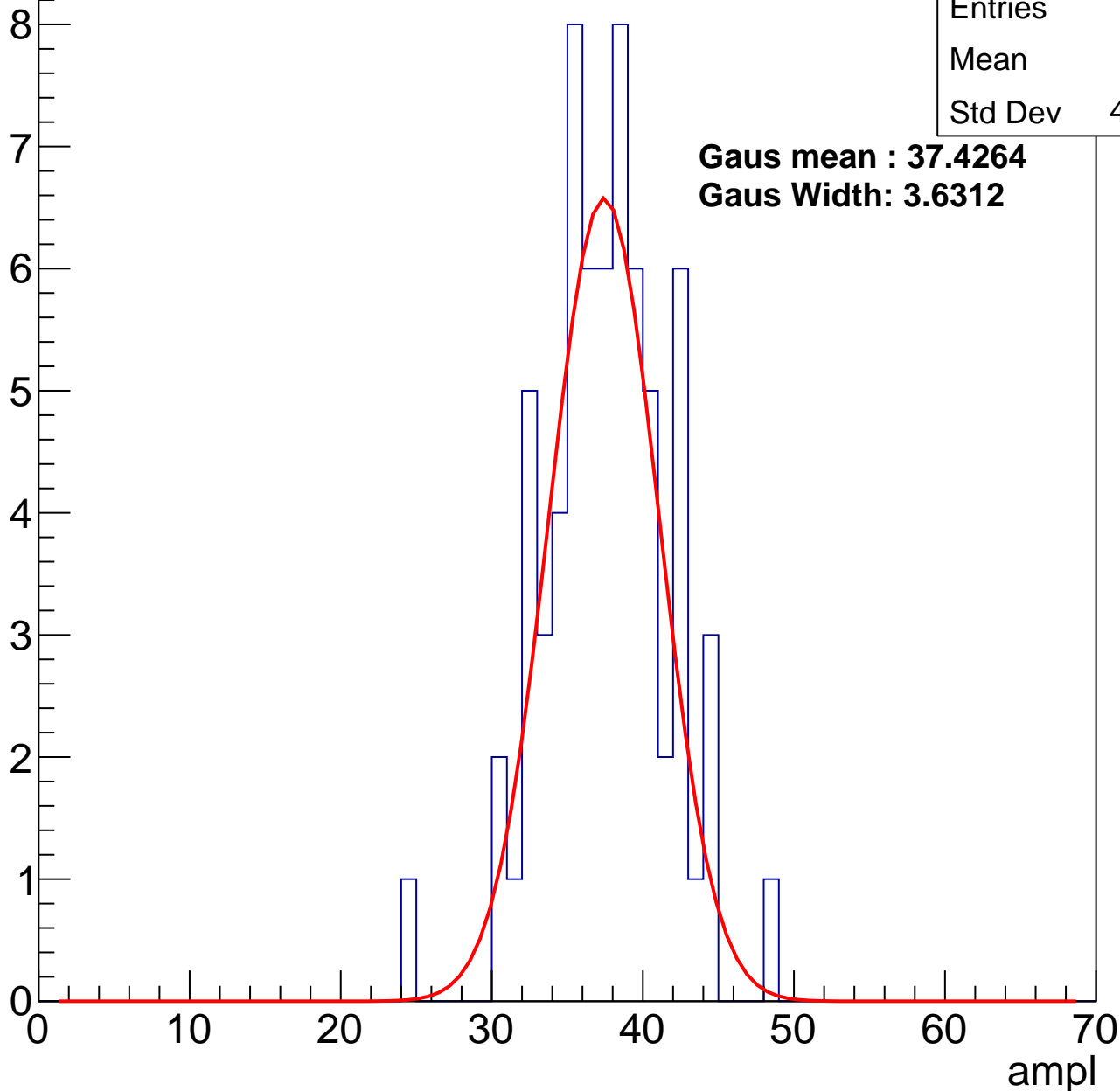
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.1
Std Dev	4.044

Gaus mean : 37.4264

Gaus Width: 3.6312



B1L103S, U17-ch90, adc2

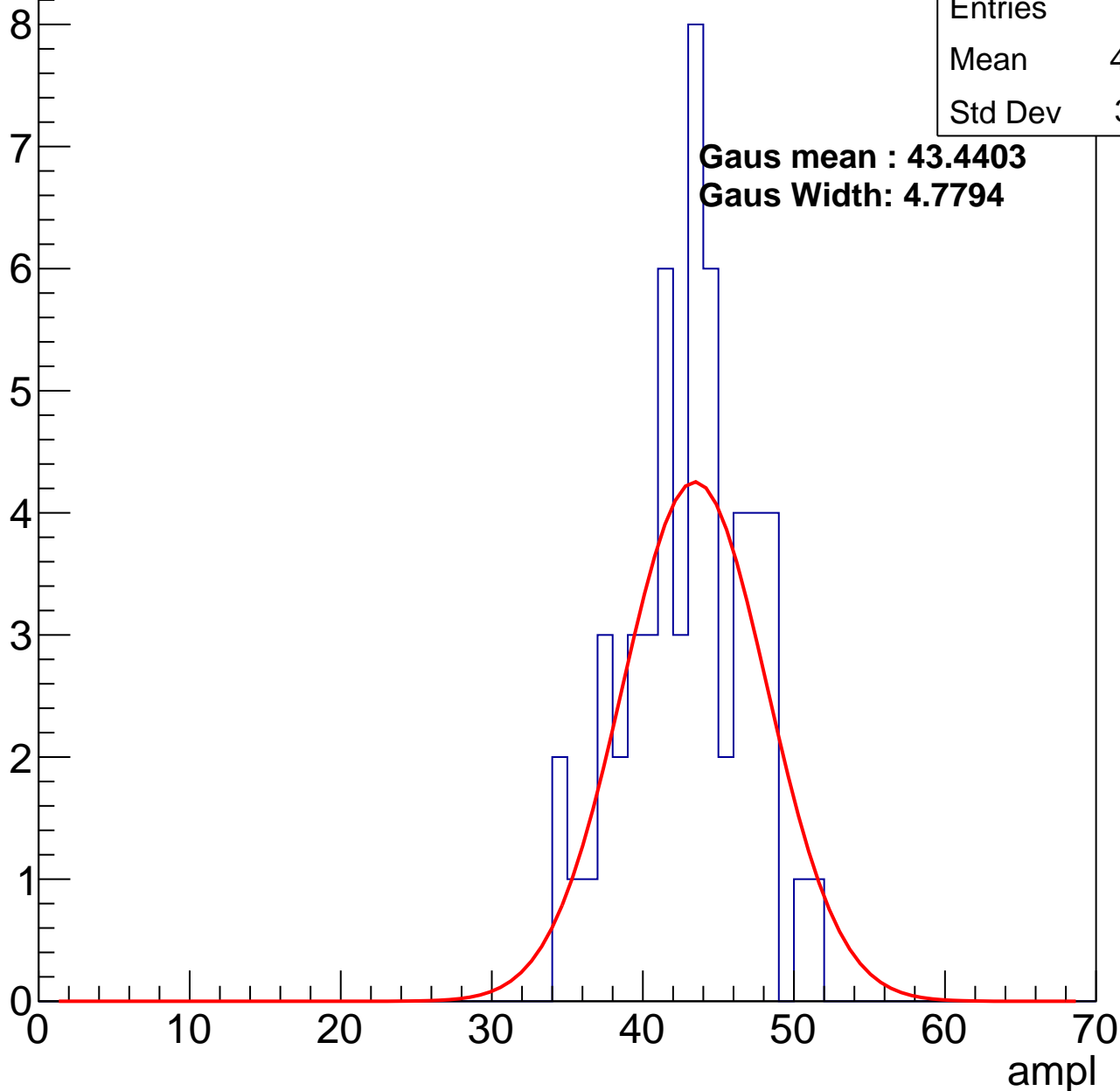
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	42.56
Std Dev	3.971

Gaus mean : 43.4403

Gaus Width: 4.7794

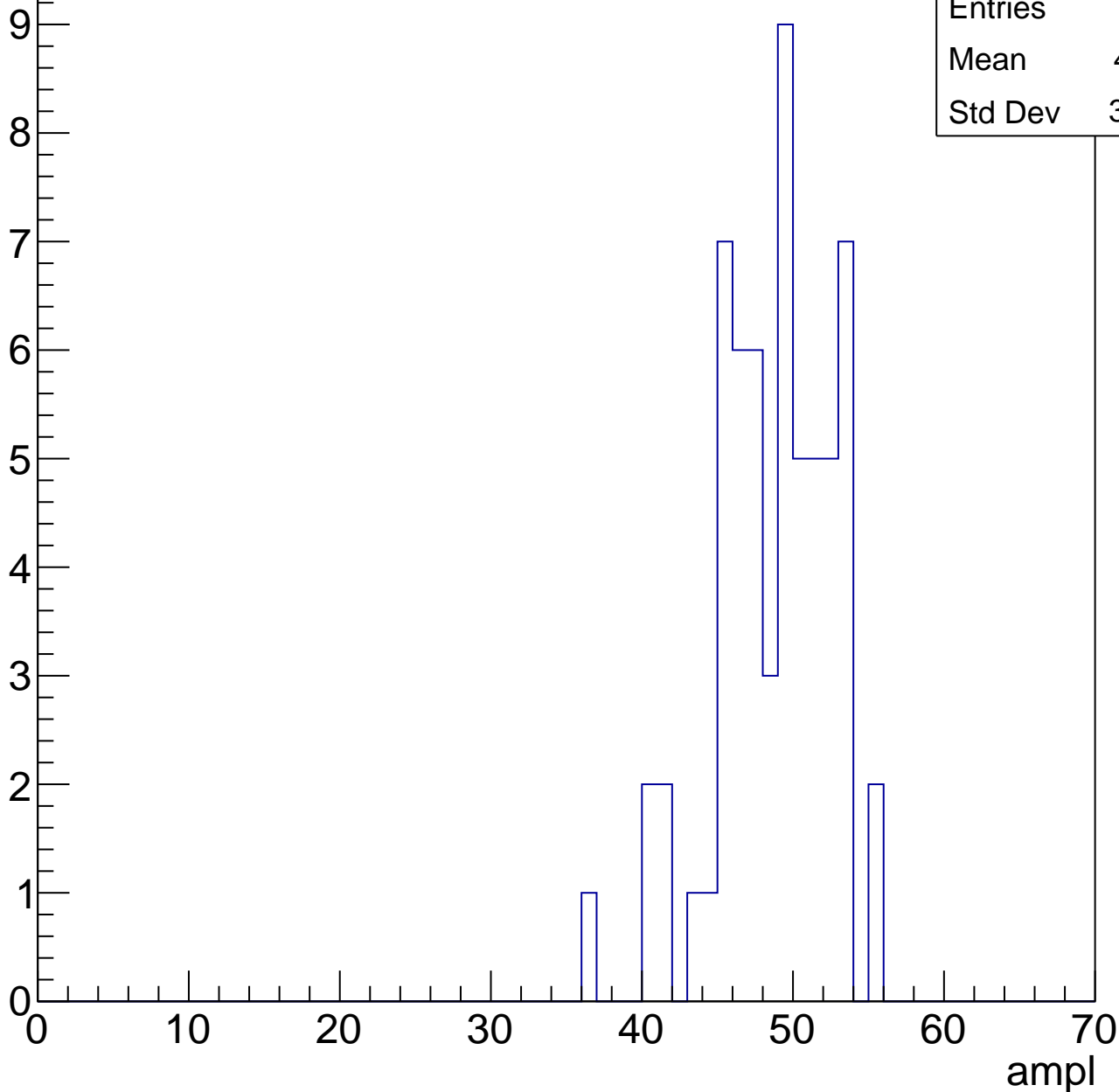


B1L103S, U17-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	48.21
Std Dev	3.869

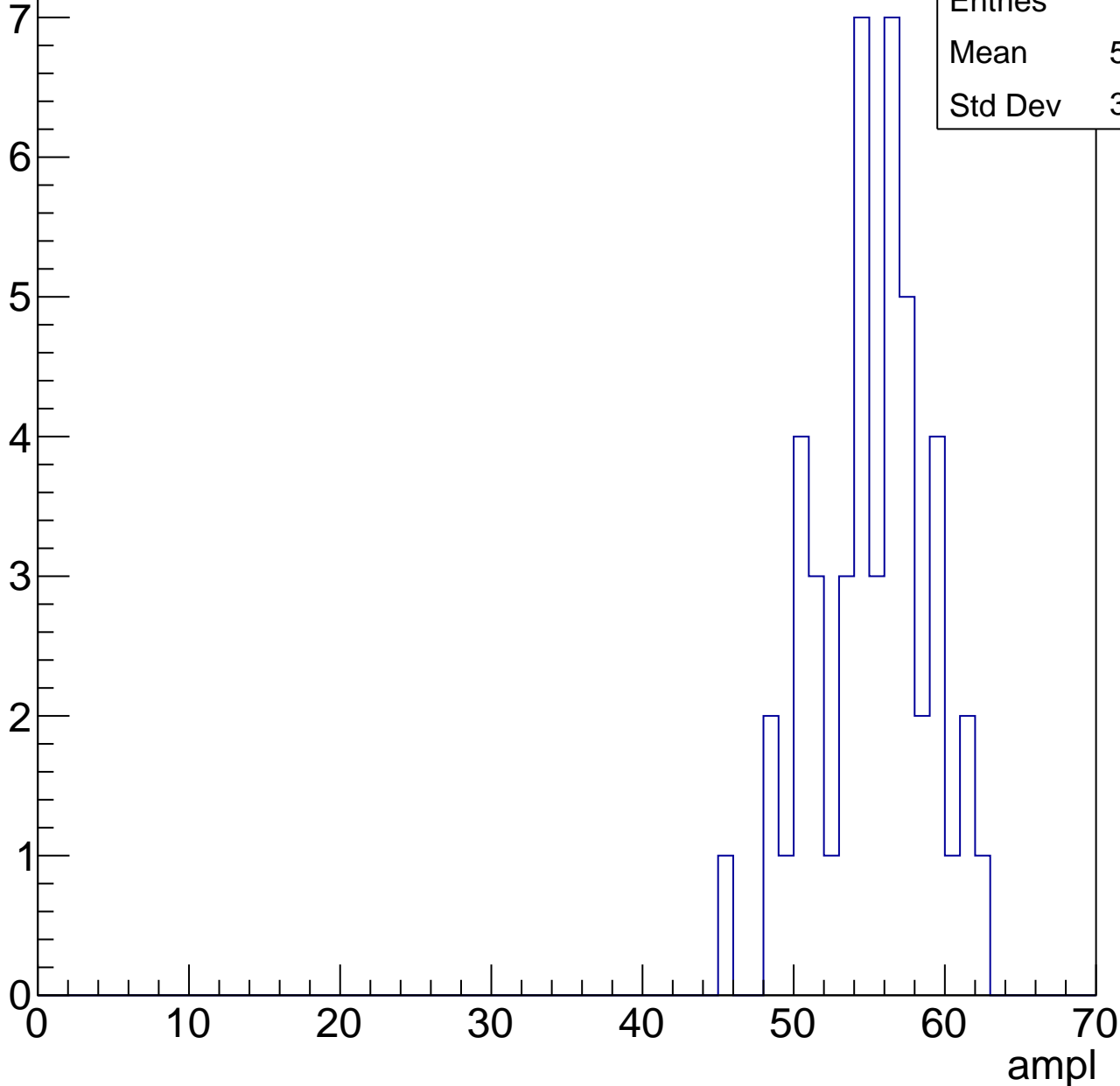


B1L103S, U17-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	54.68
Std Dev	3.748

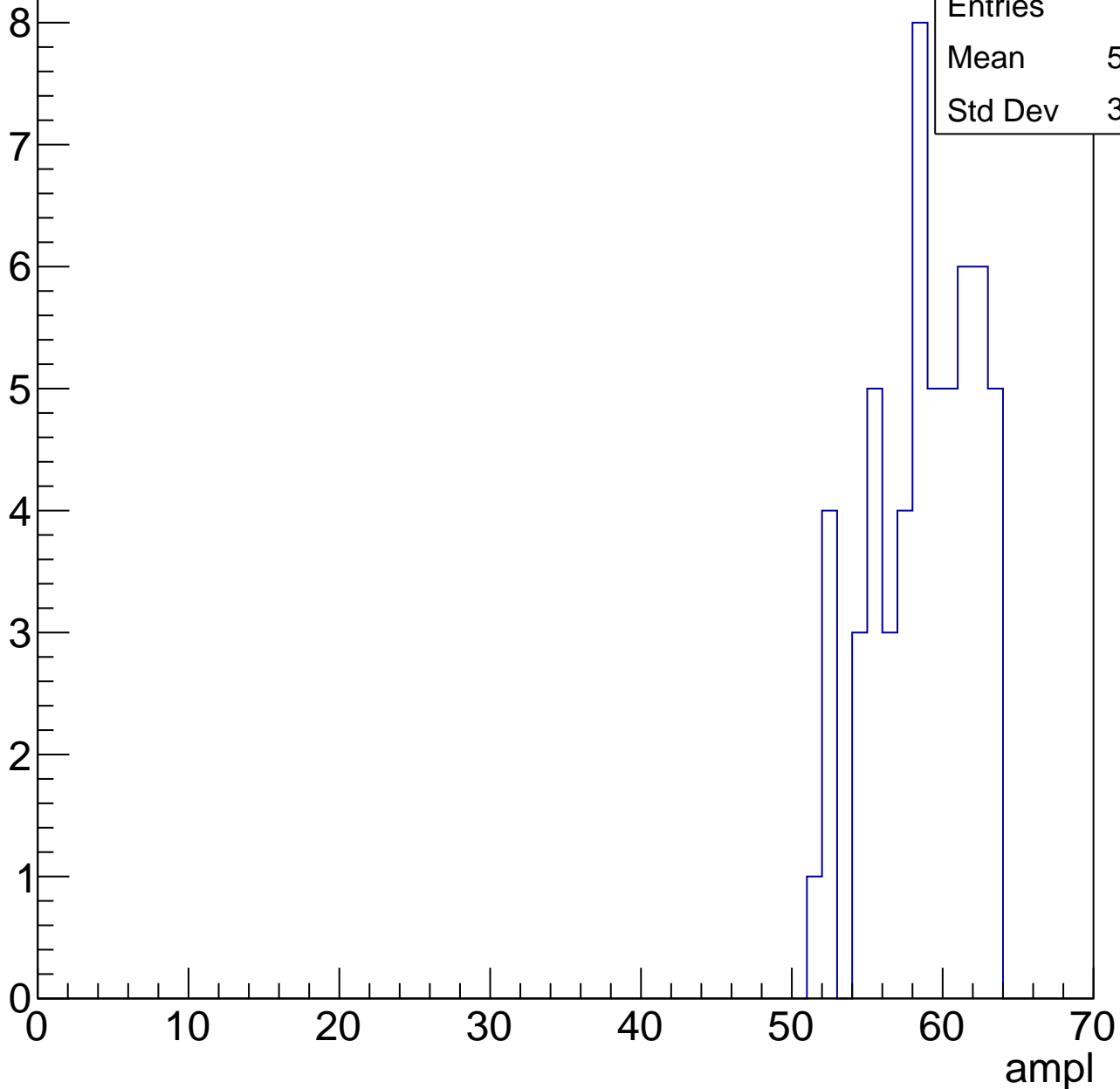


B1L103S, U17-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.25
Std Dev	3.299

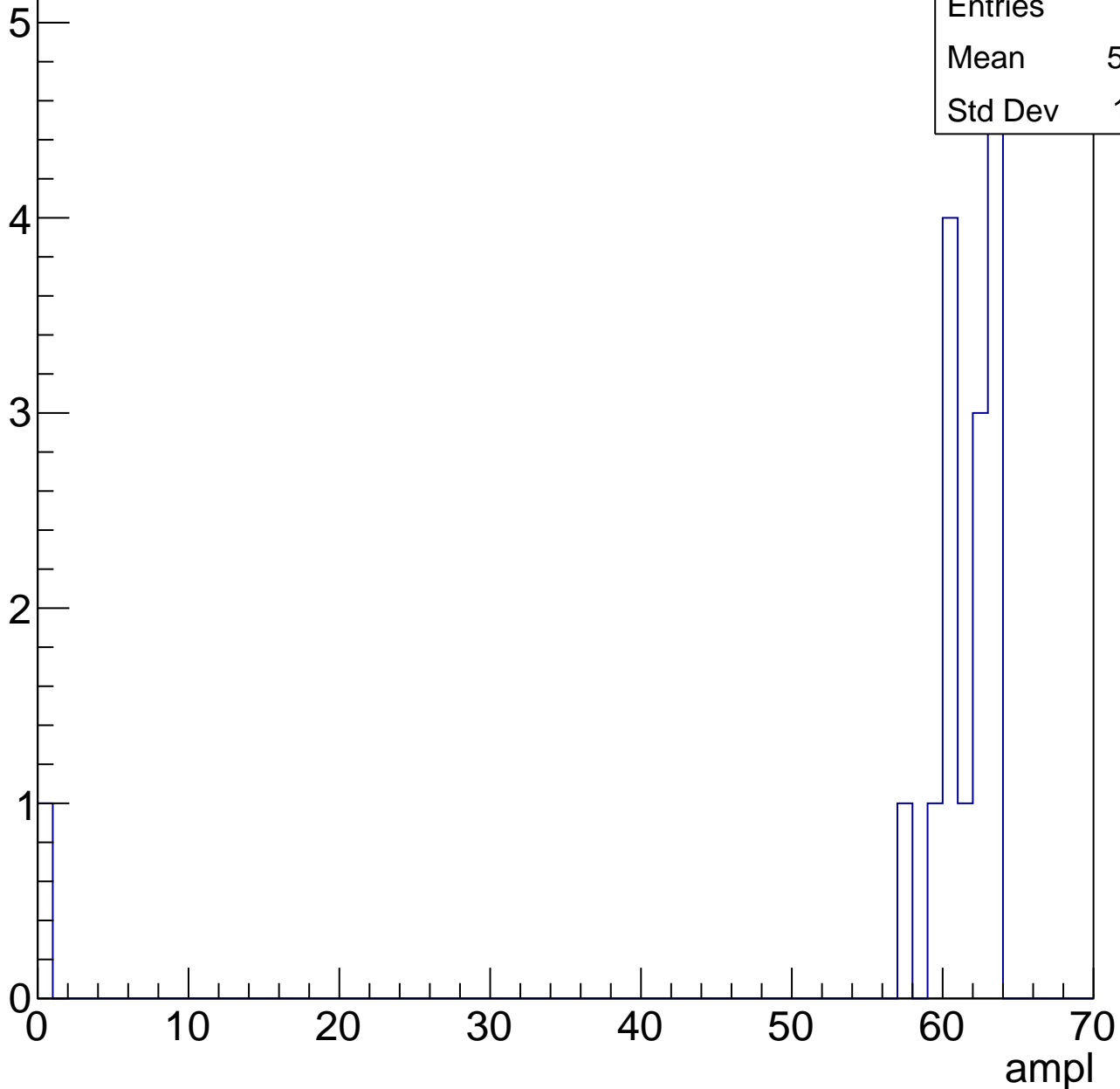


B1L103S, U17-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.38
Std Dev	14.91



B1L103S, U17-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch91, adc0

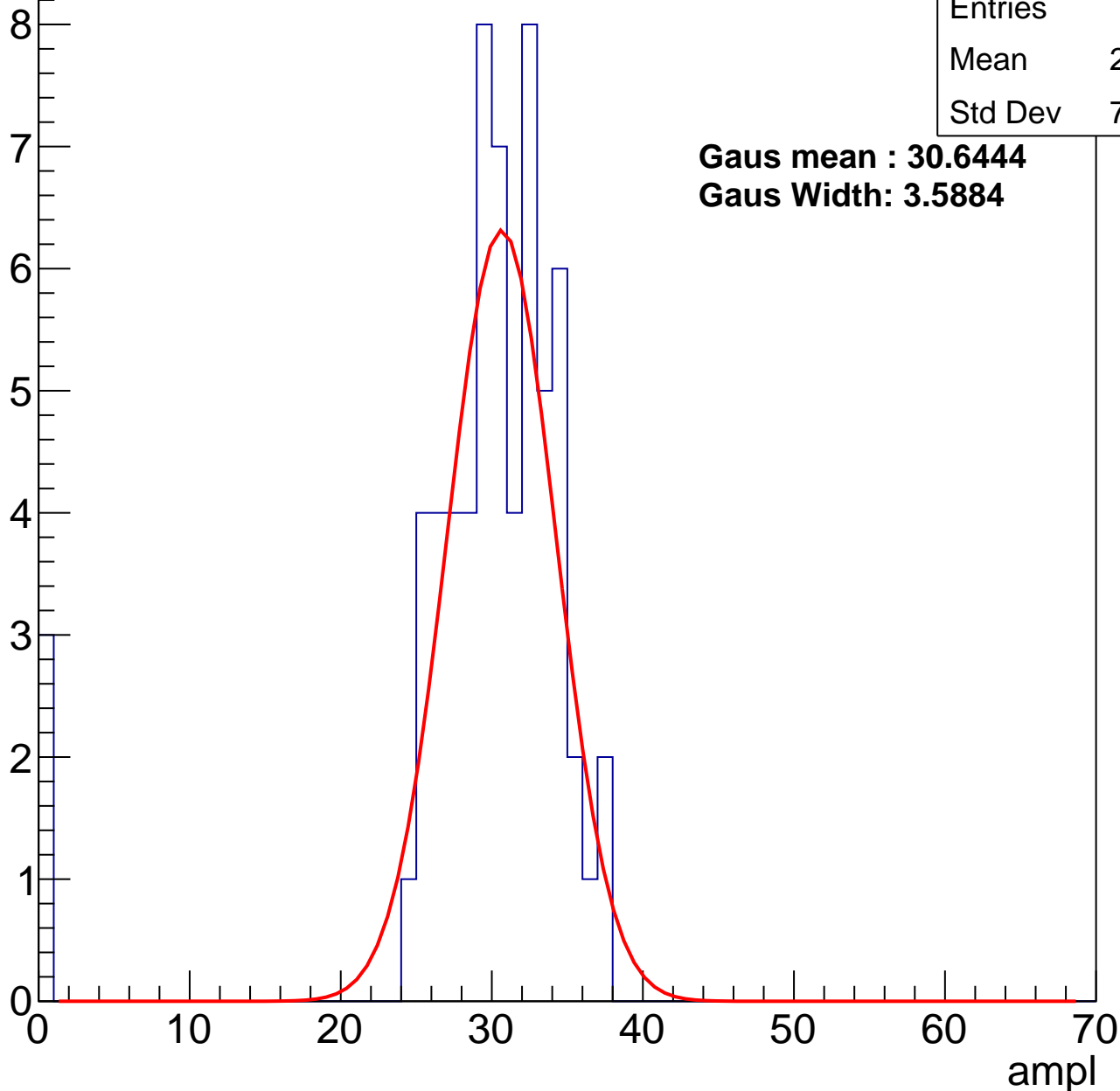
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.87
Std Dev	7.172

Gaus mean : 30.6444

Gaus Width: 3.5884



B1L103S, U17-ch91, adc1

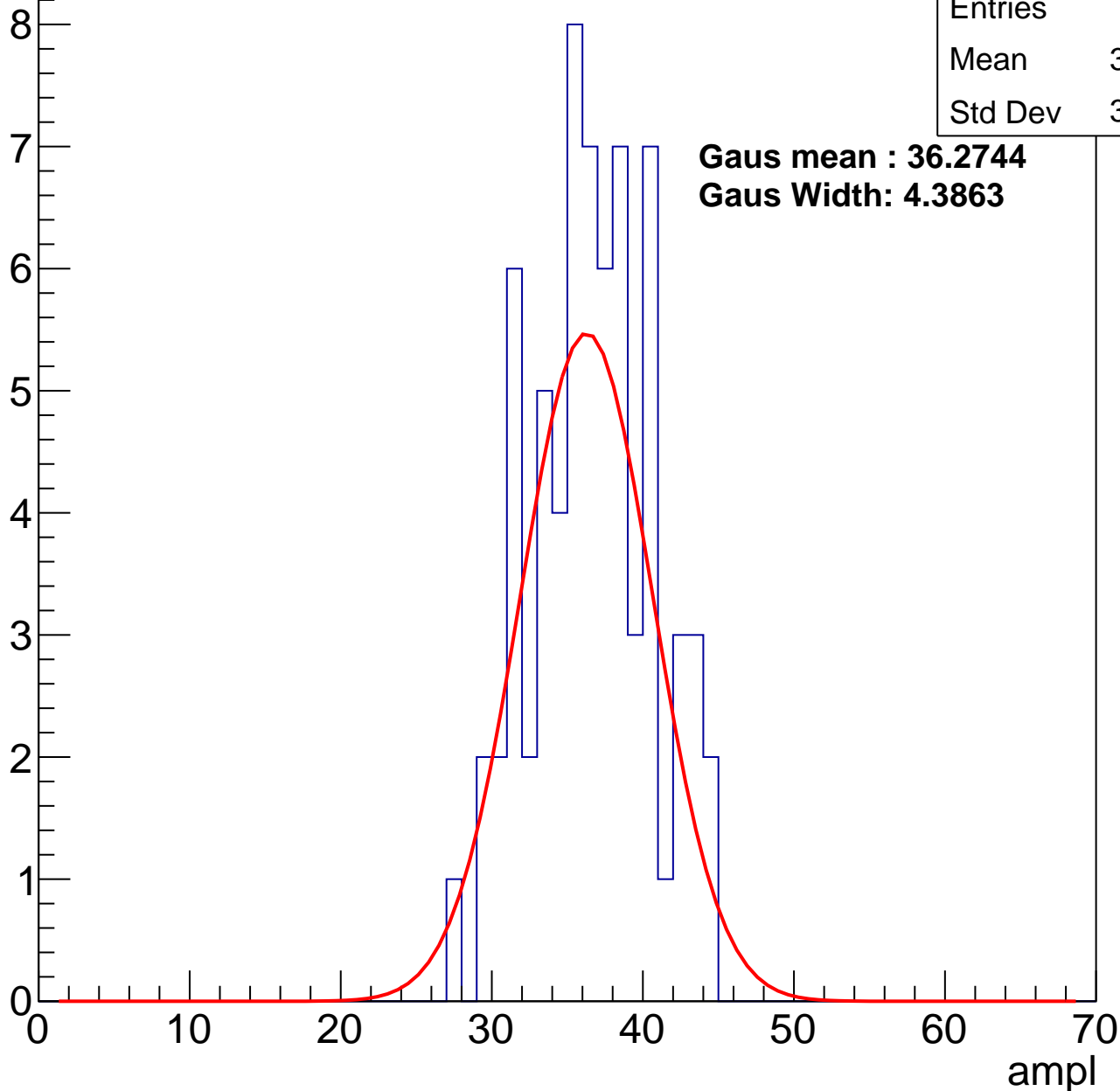
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.19
Std Dev	3.957

Gaus mean : 36.2744

Gaus Width: 4.3863



B1L103S, U17-ch91, adc2

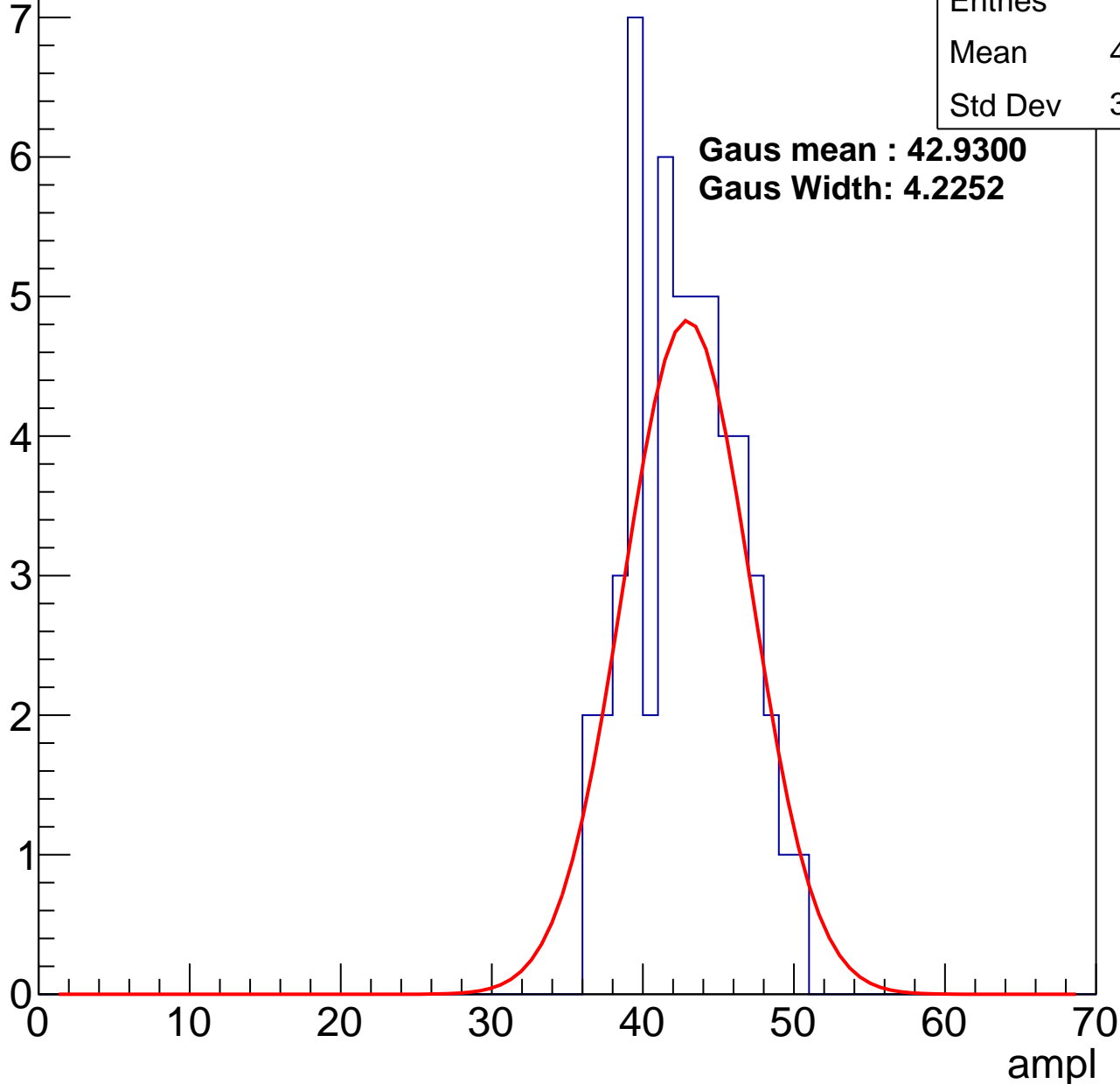
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.38
Std Dev	3.465

Gaus mean : 42.9300

Gaus Width: 4.2252

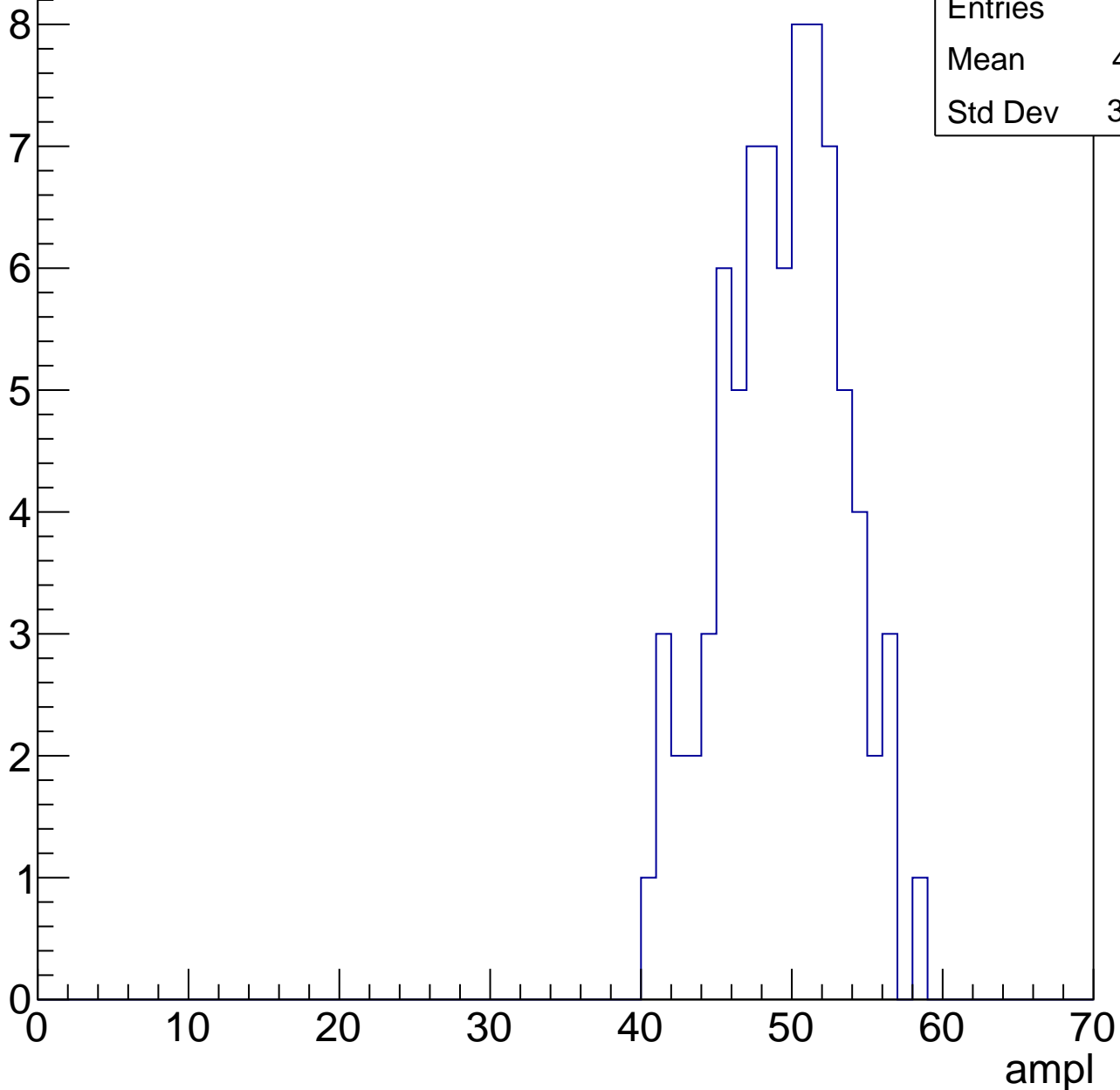


B1L103S, U17-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	48.91
Std Dev	3.994

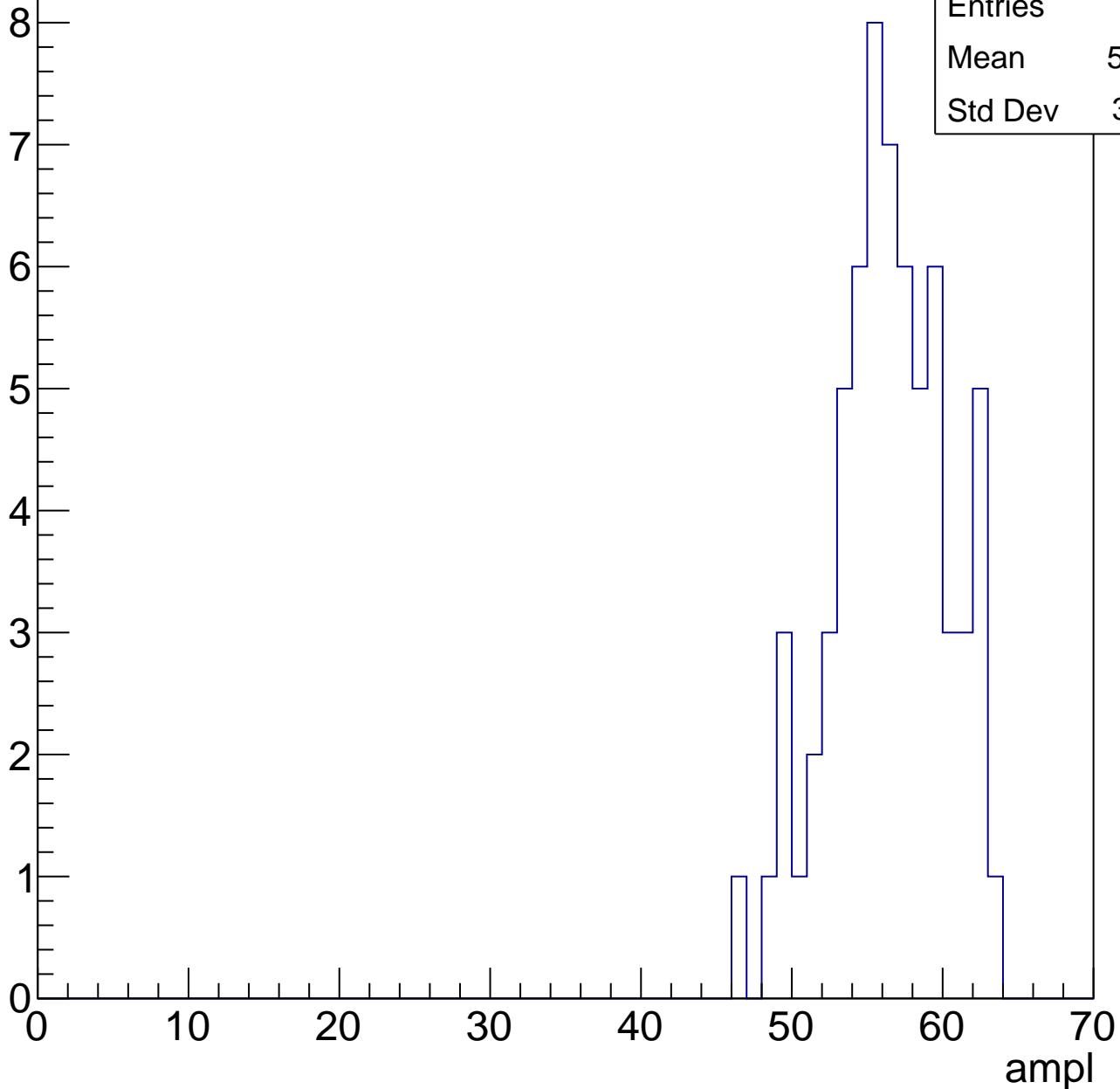


B1L103S, U17-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.94
Std Dev	3.801

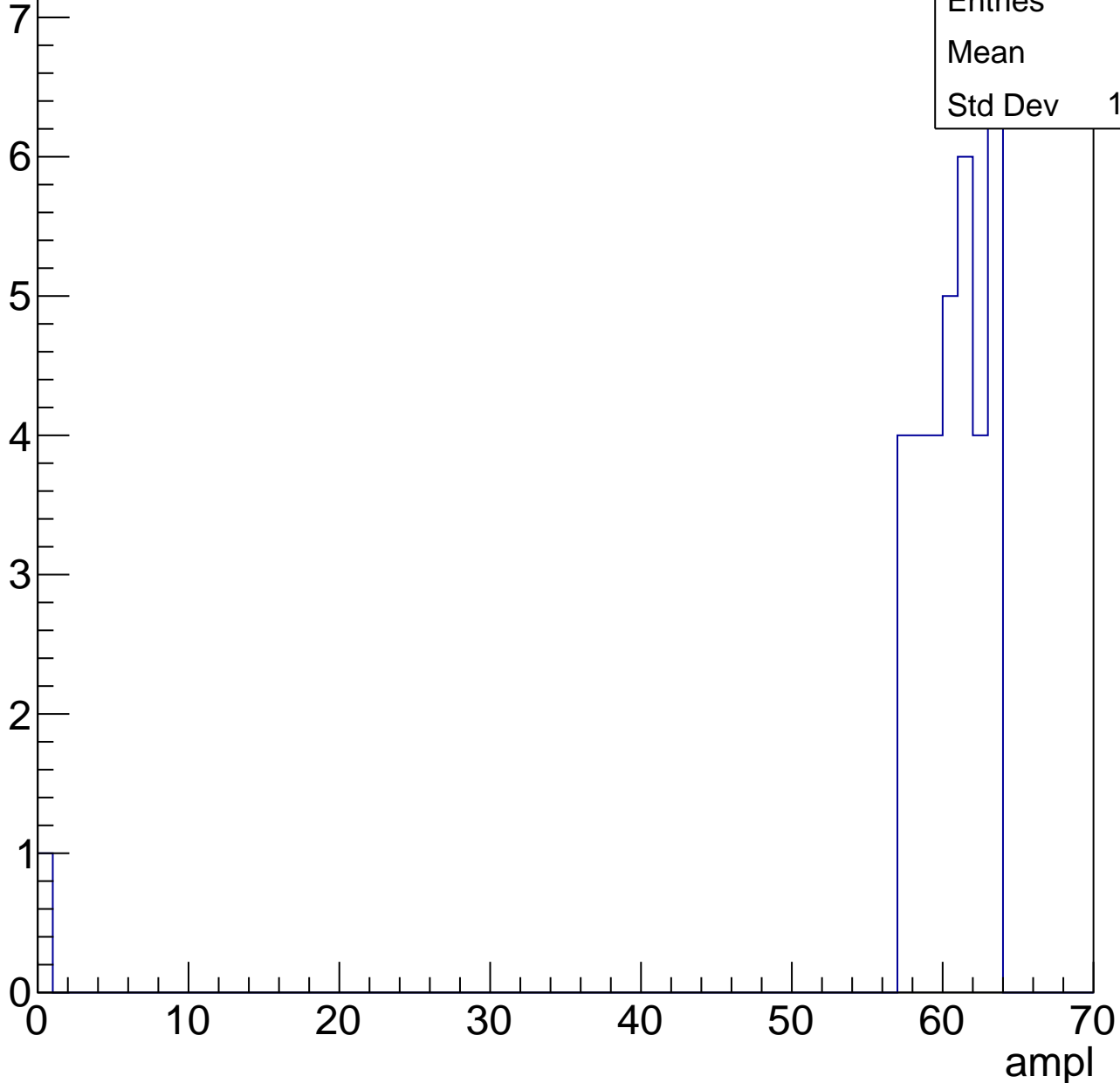


B1L103S, U17-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.6
Std Dev	10.24



B1L103S, U17-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

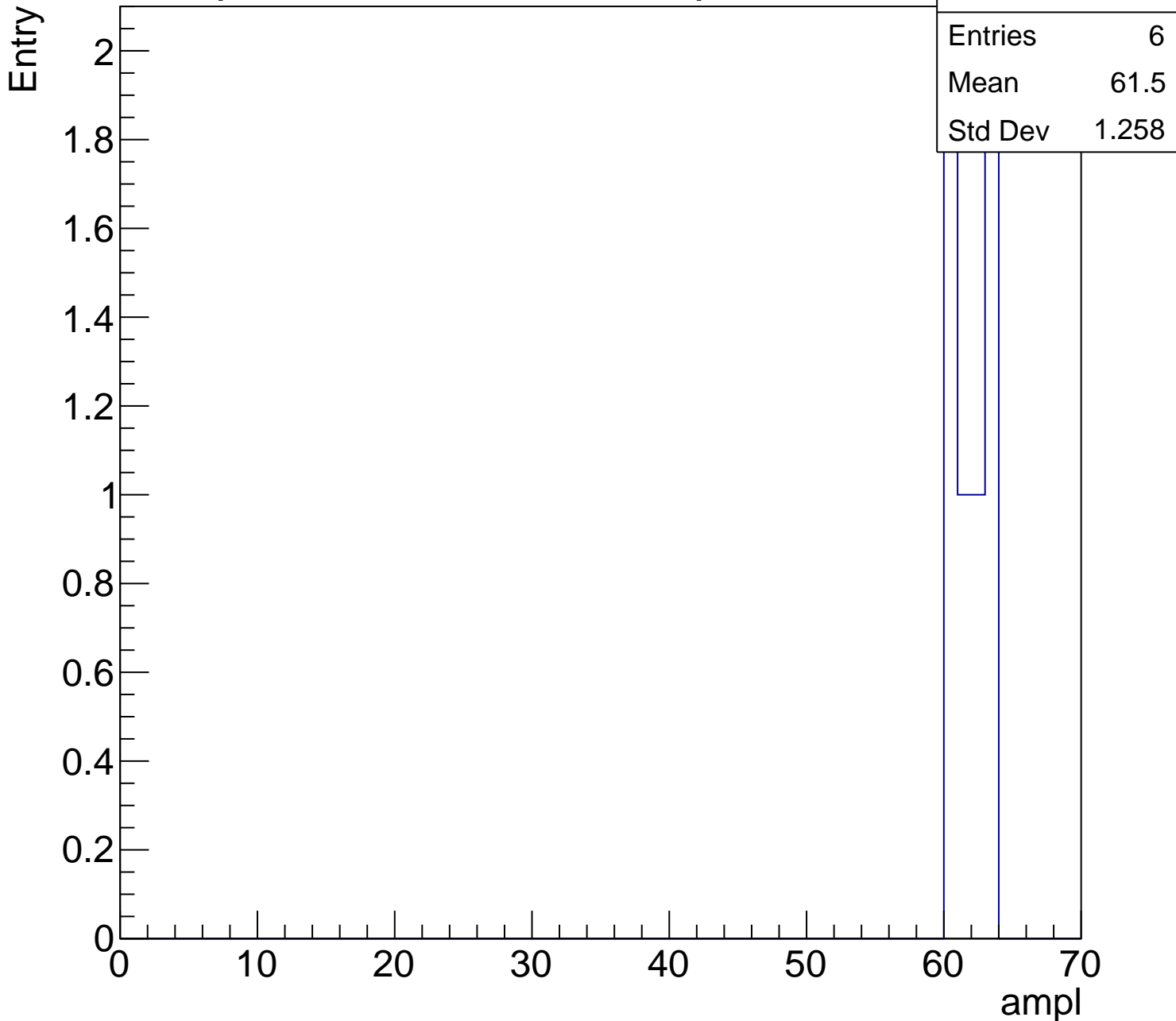
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch92, adc0

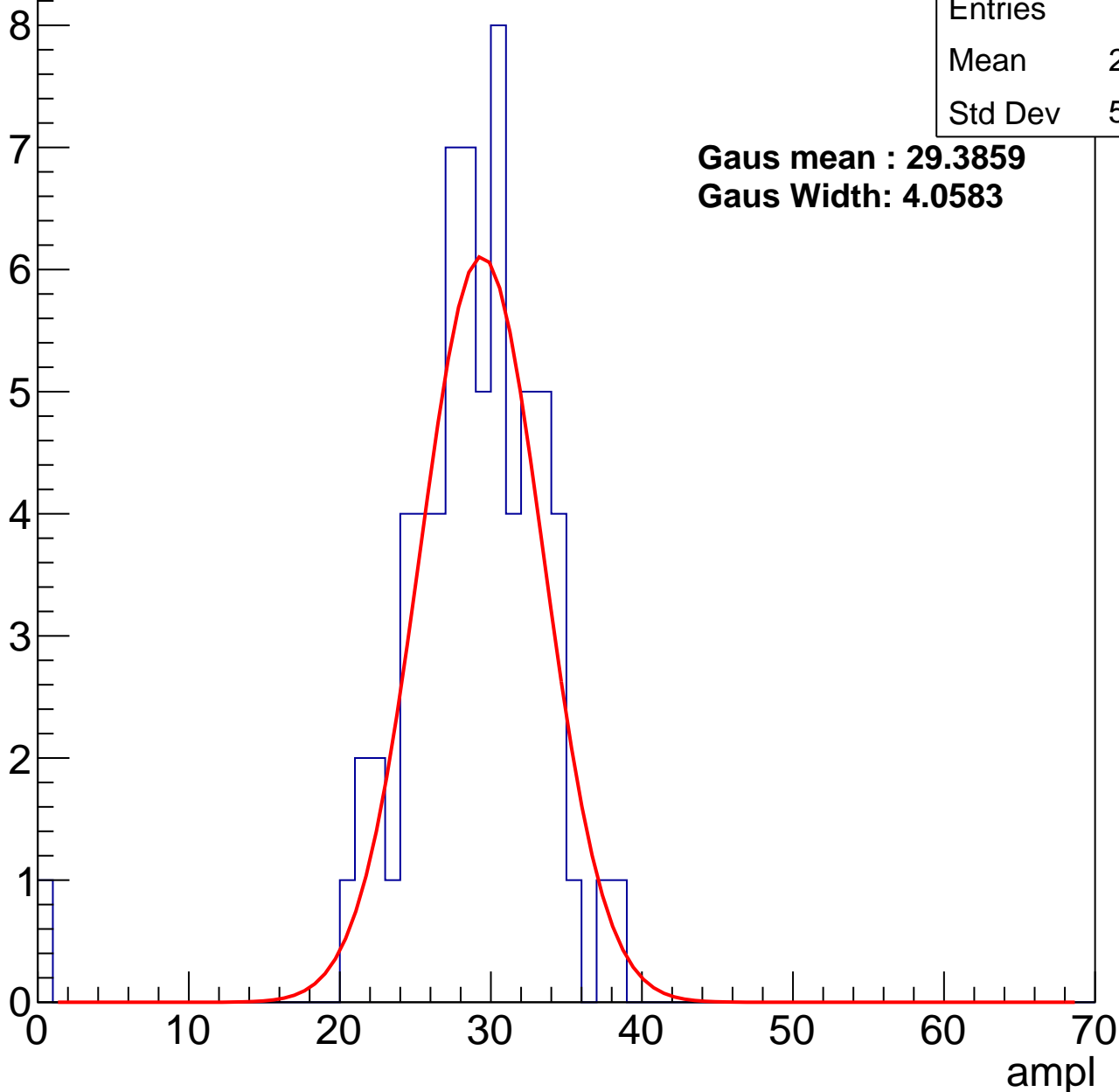
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.27
Std Dev	5.202

Gaus mean : 29.3859

Gaus Width: 4.0583



B1L103S, U17-ch92, adc1

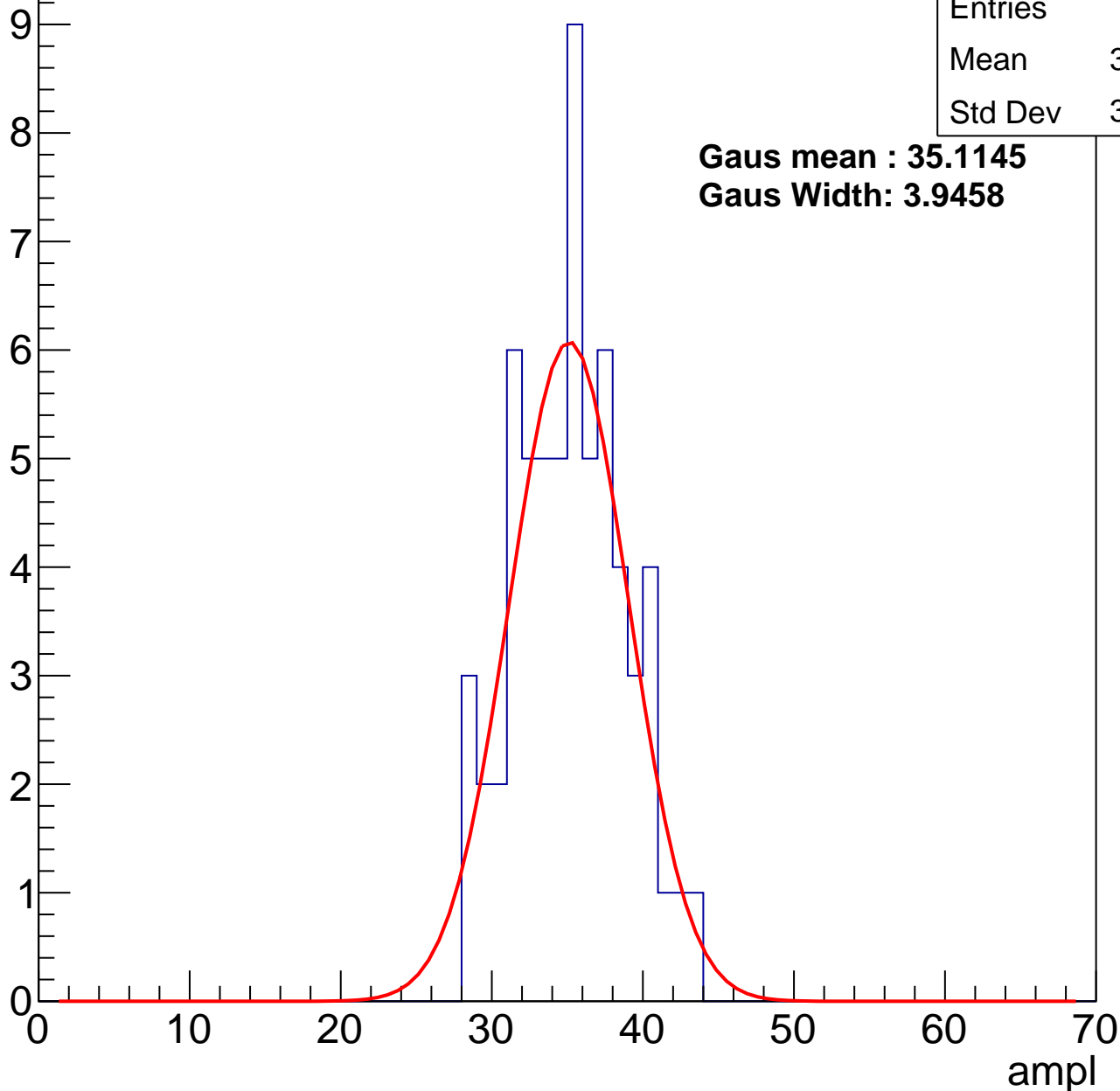
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	34.76
Std Dev	3.577

Gaus mean : 35.1145

Gaus Width: 3.9458



B1L103S, U17-ch92, adc2

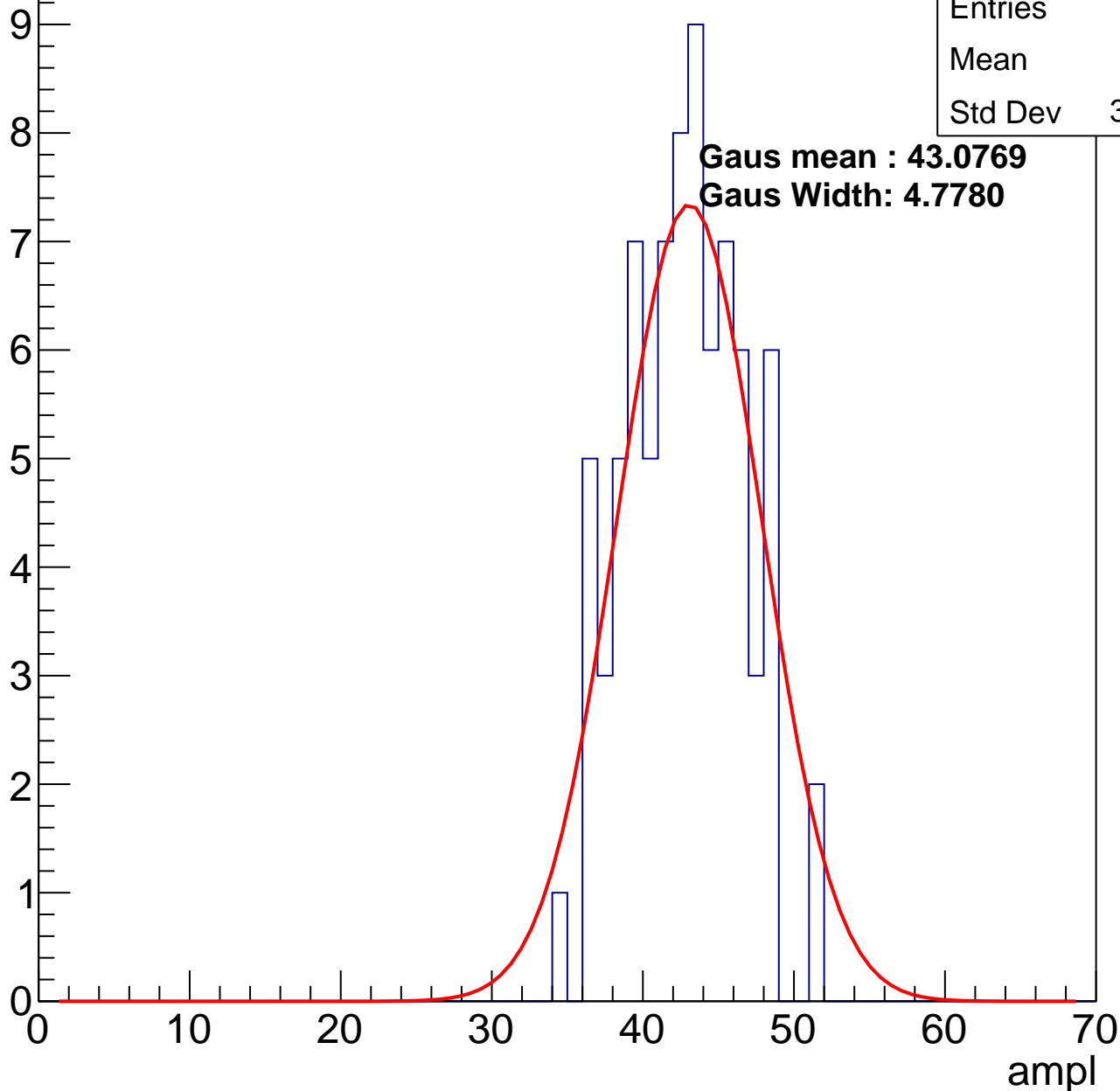
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	42.3
Std Dev	3.753

Gaus mean : 43.0769

Gaus Width: 4.7780

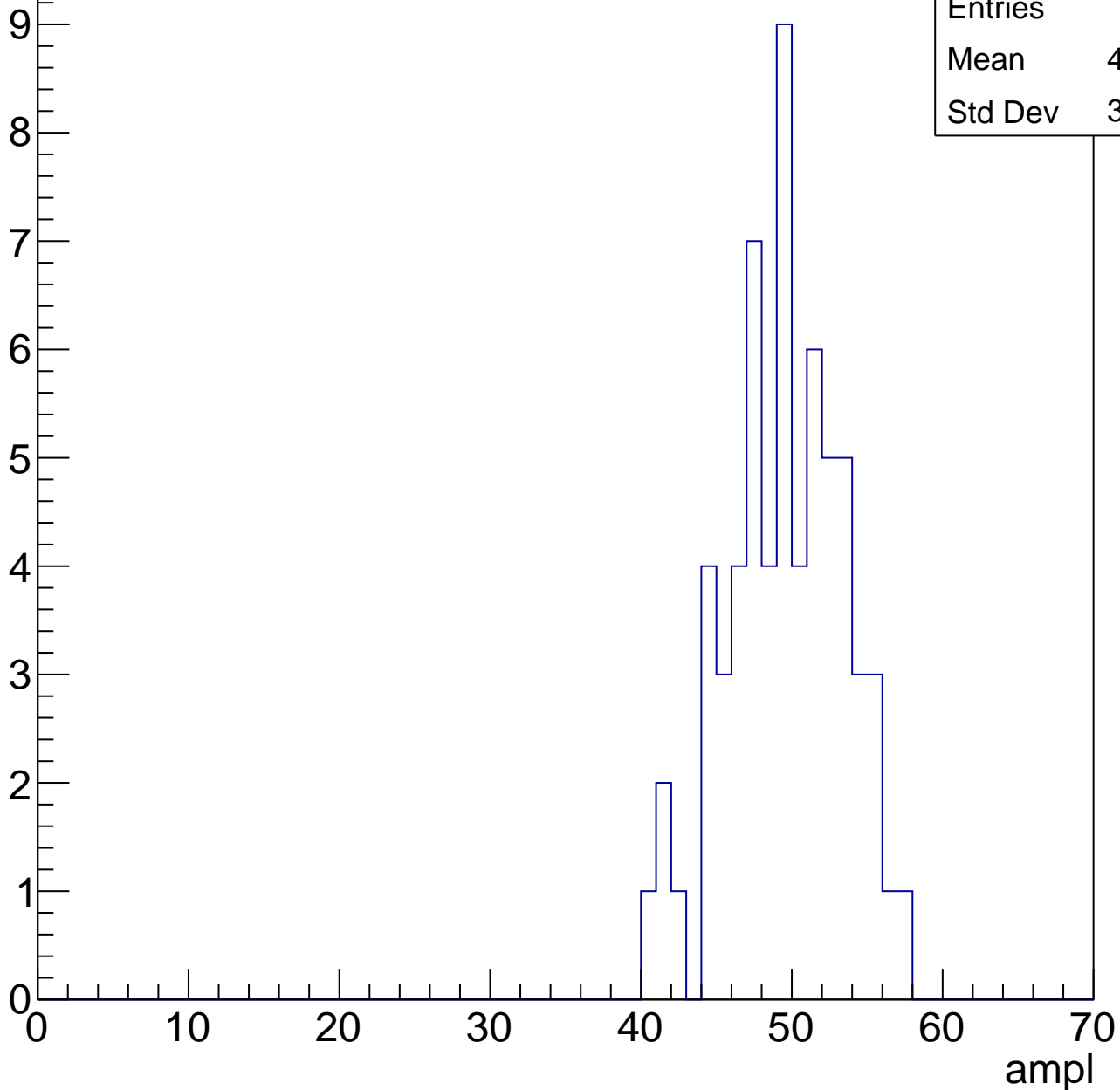


B1L103S, U17-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.08
Std Dev	3.827

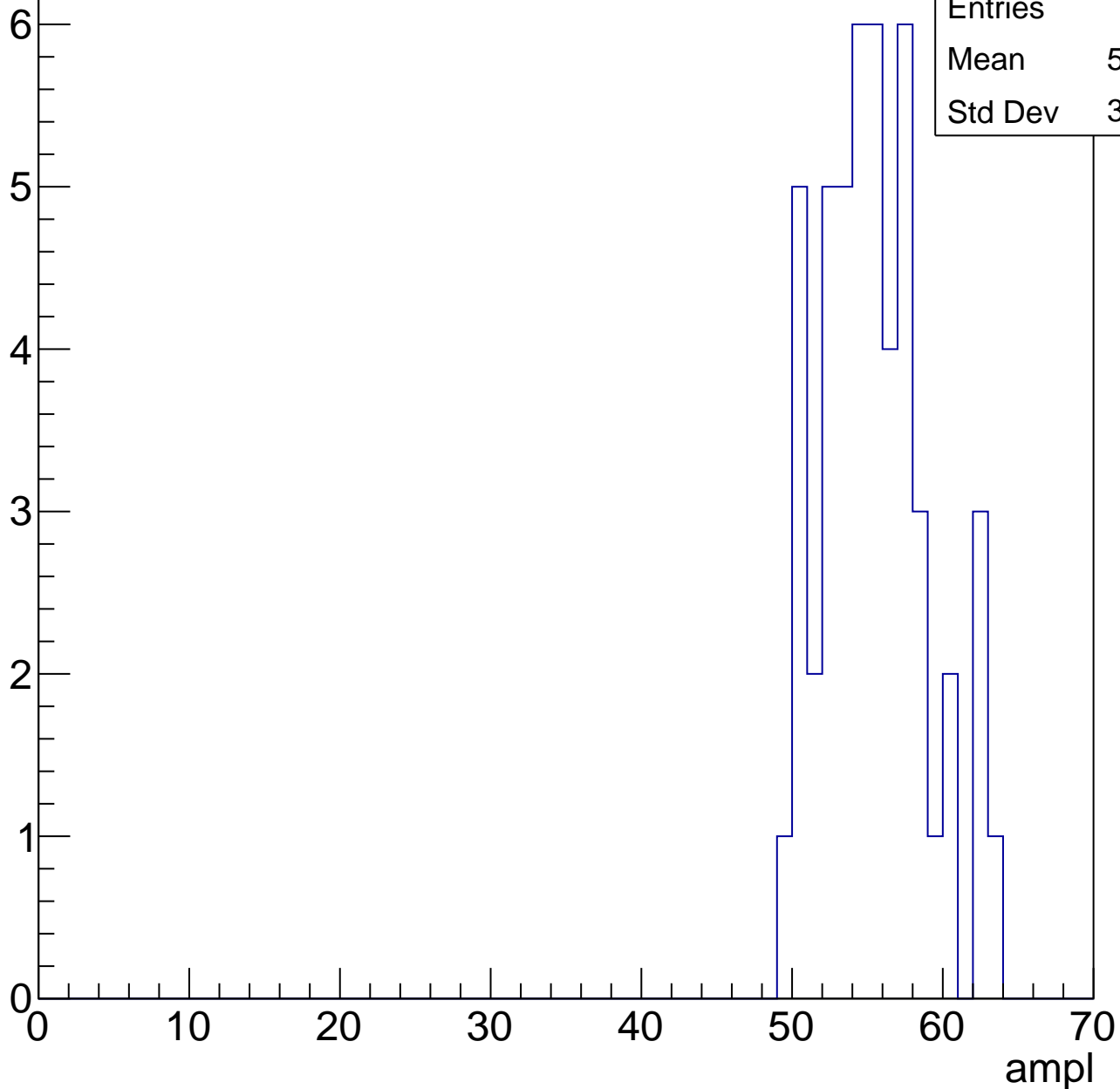


B1L103S, U17-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.96
Std Dev	3.452

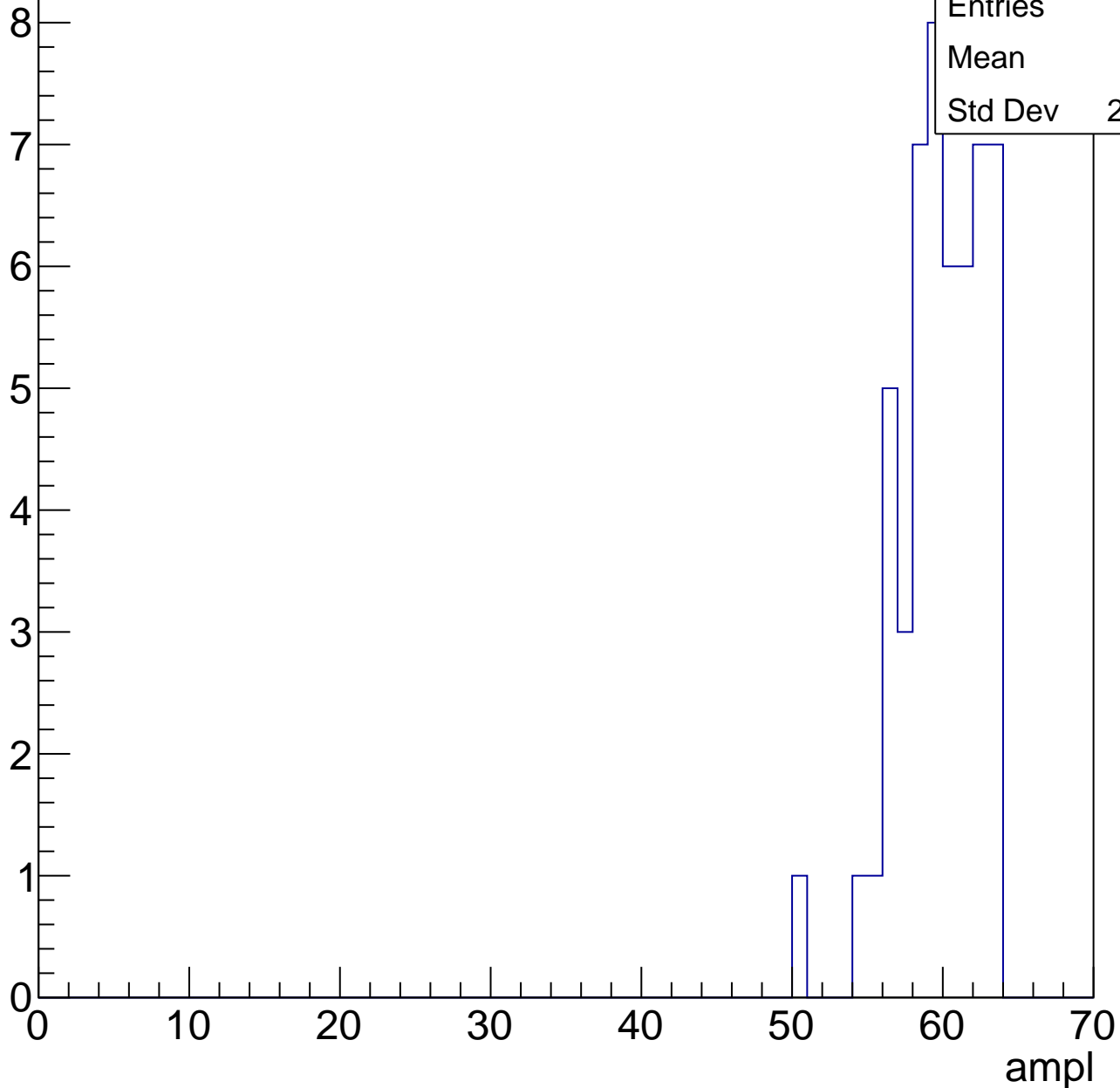


B1L103S, U17-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

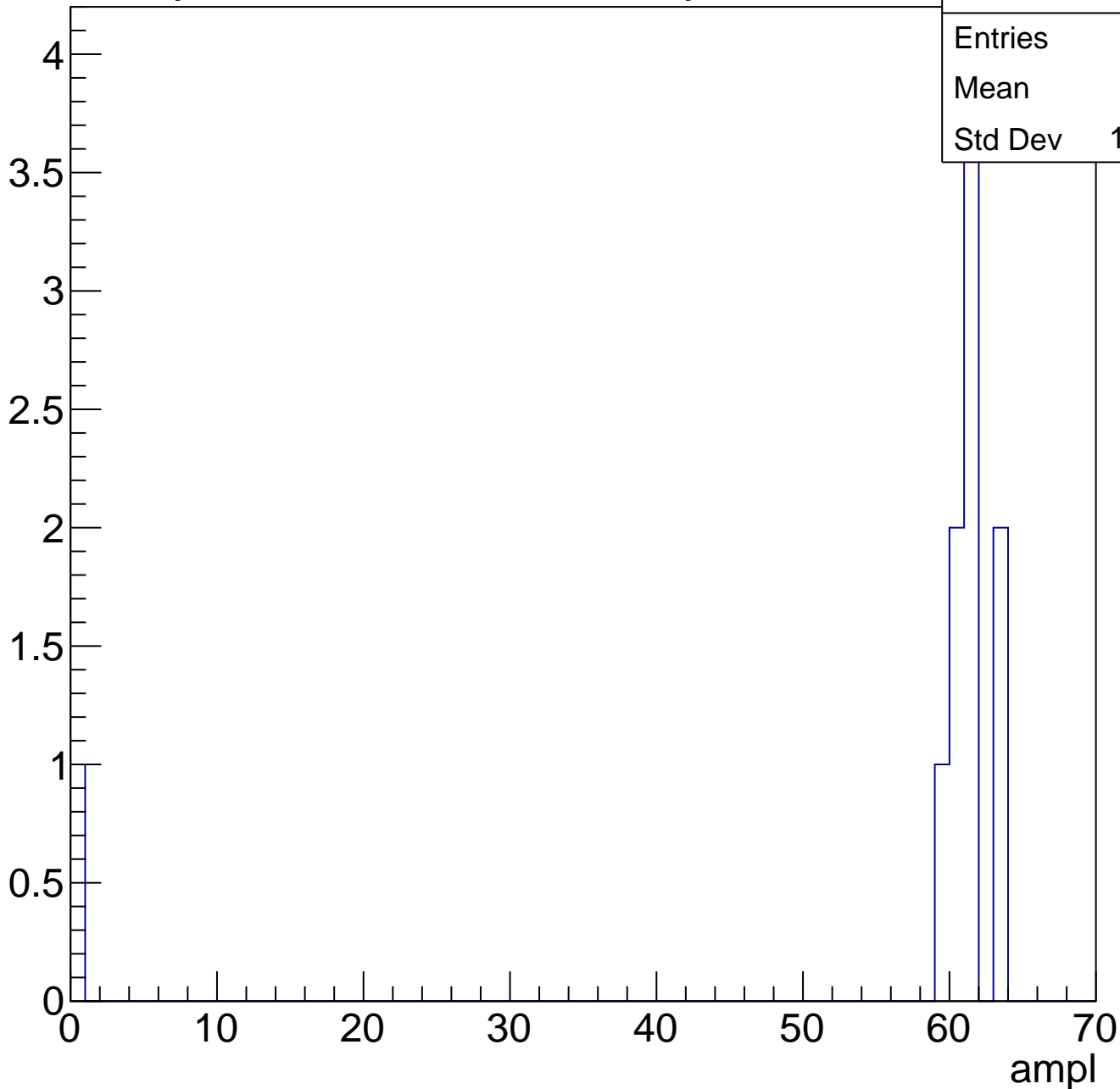
Entries	52
Mean	59.4
Std Dev	2.712



B1L103S, U17-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch93, adc0

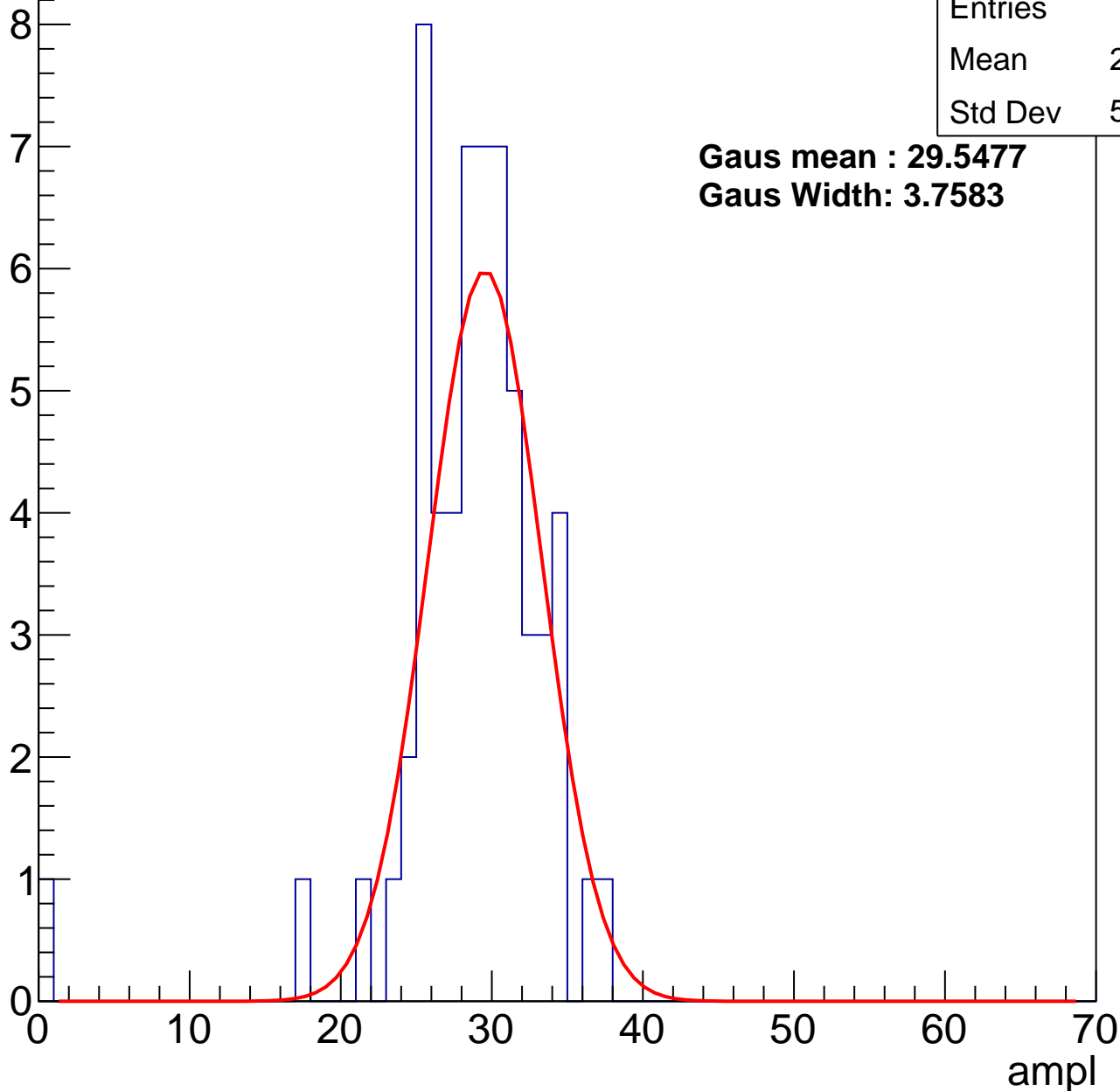
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.15
Std Dev	5.154

Gaus mean : 29.5477

Gaus Width: 3.7583



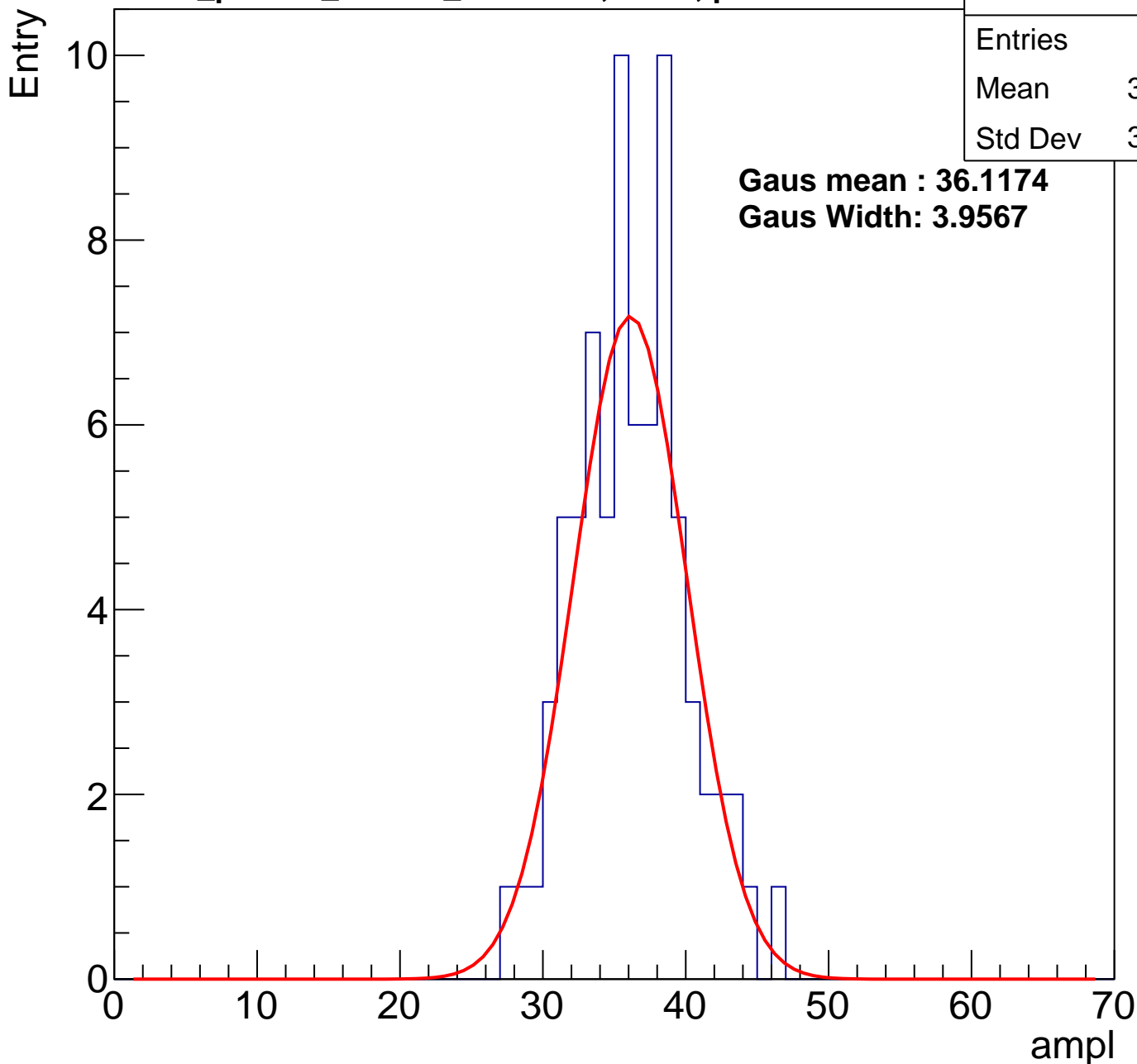
B1L103S, U17-ch93, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	35.72
Std Dev	3.834

Gaus mean : 36.1174

Gaus Width: 3.9567



B1L103S, U17-ch93, adc2

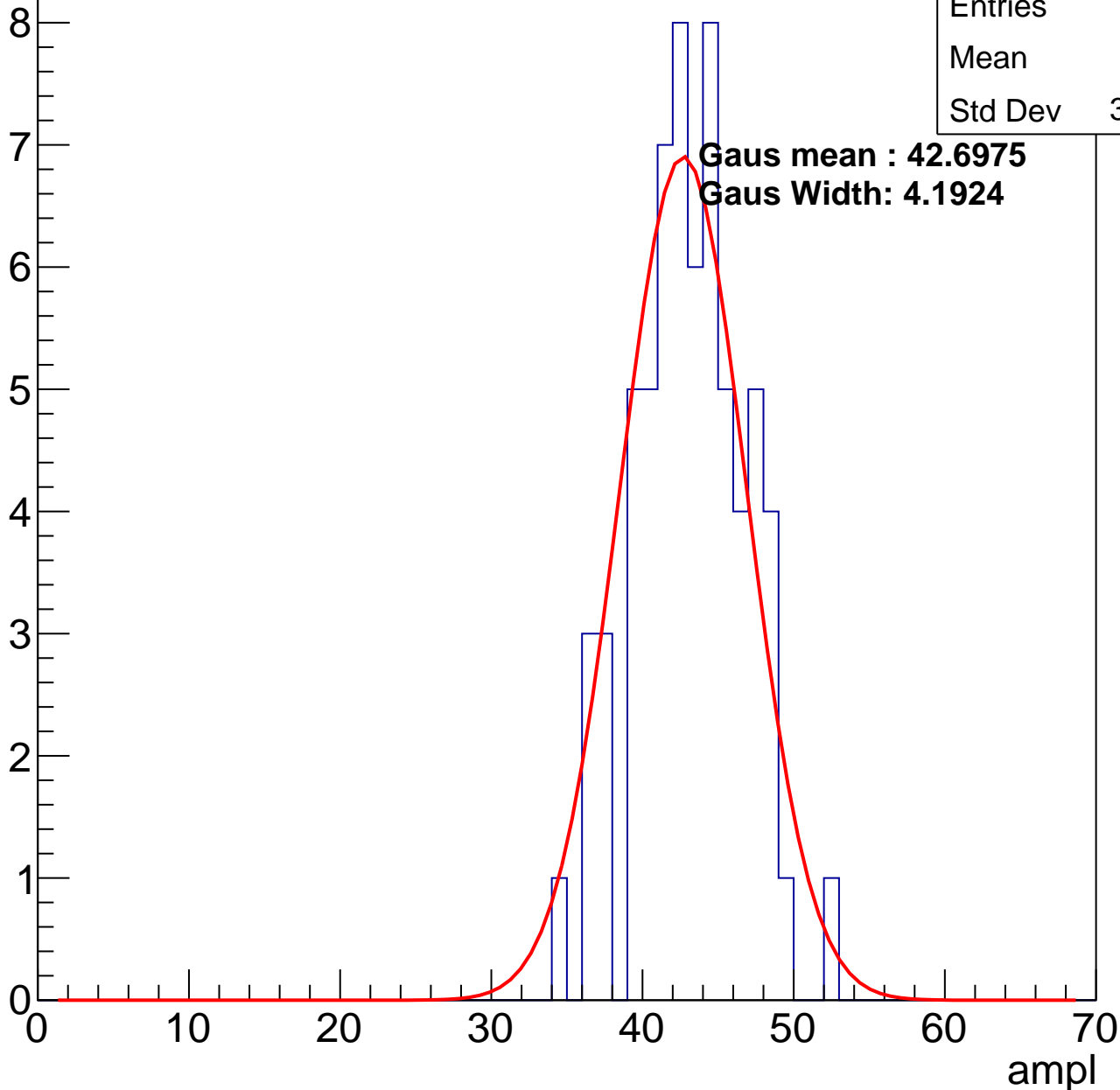
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.7
Std Dev	3.593

Gaus mean : 42.6975

Gaus Width: 4.1924

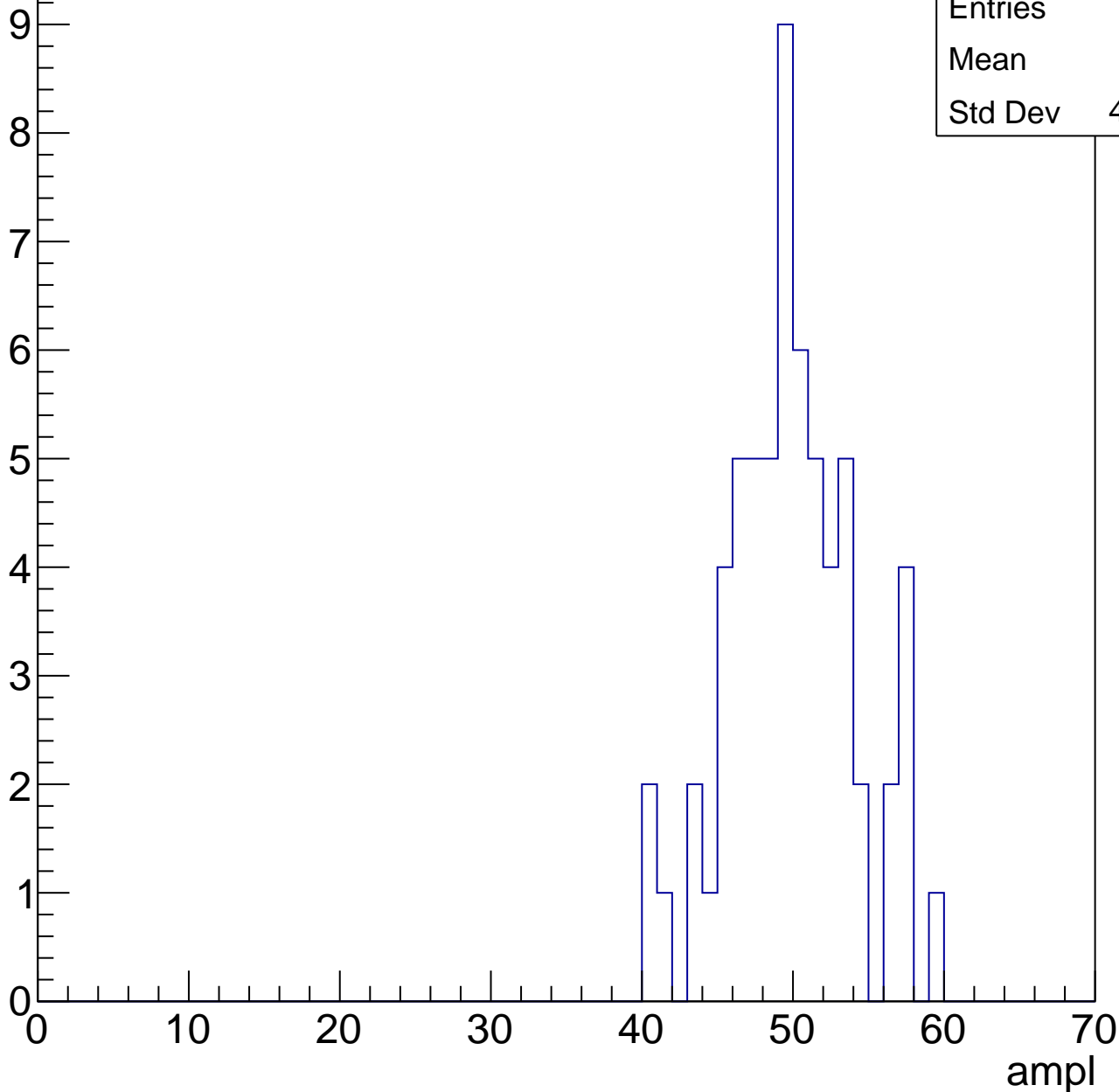


B1L103S, U17-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.4
Std Dev	4.165

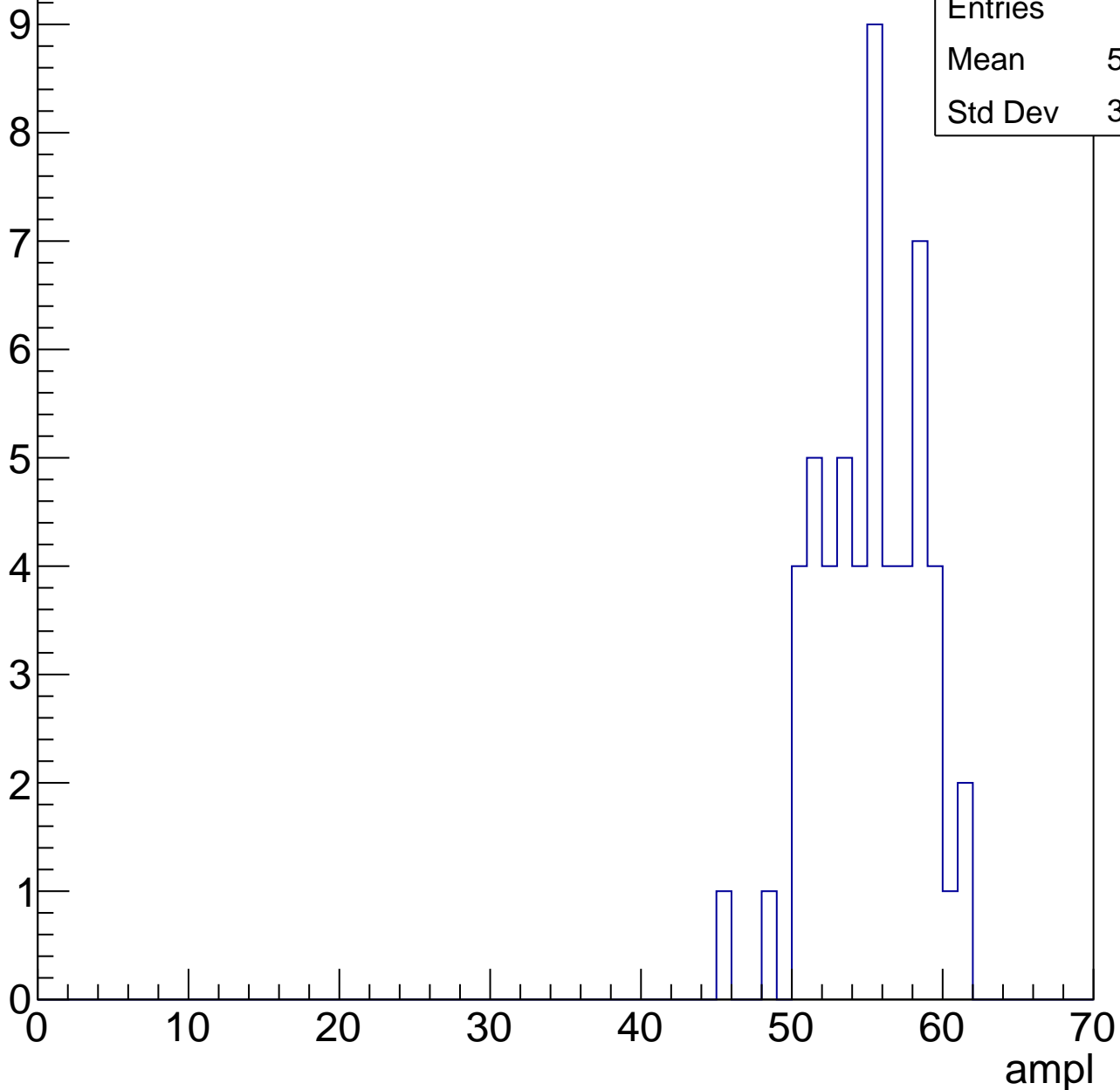


B1L103S, U17-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.69
Std Dev	3.378

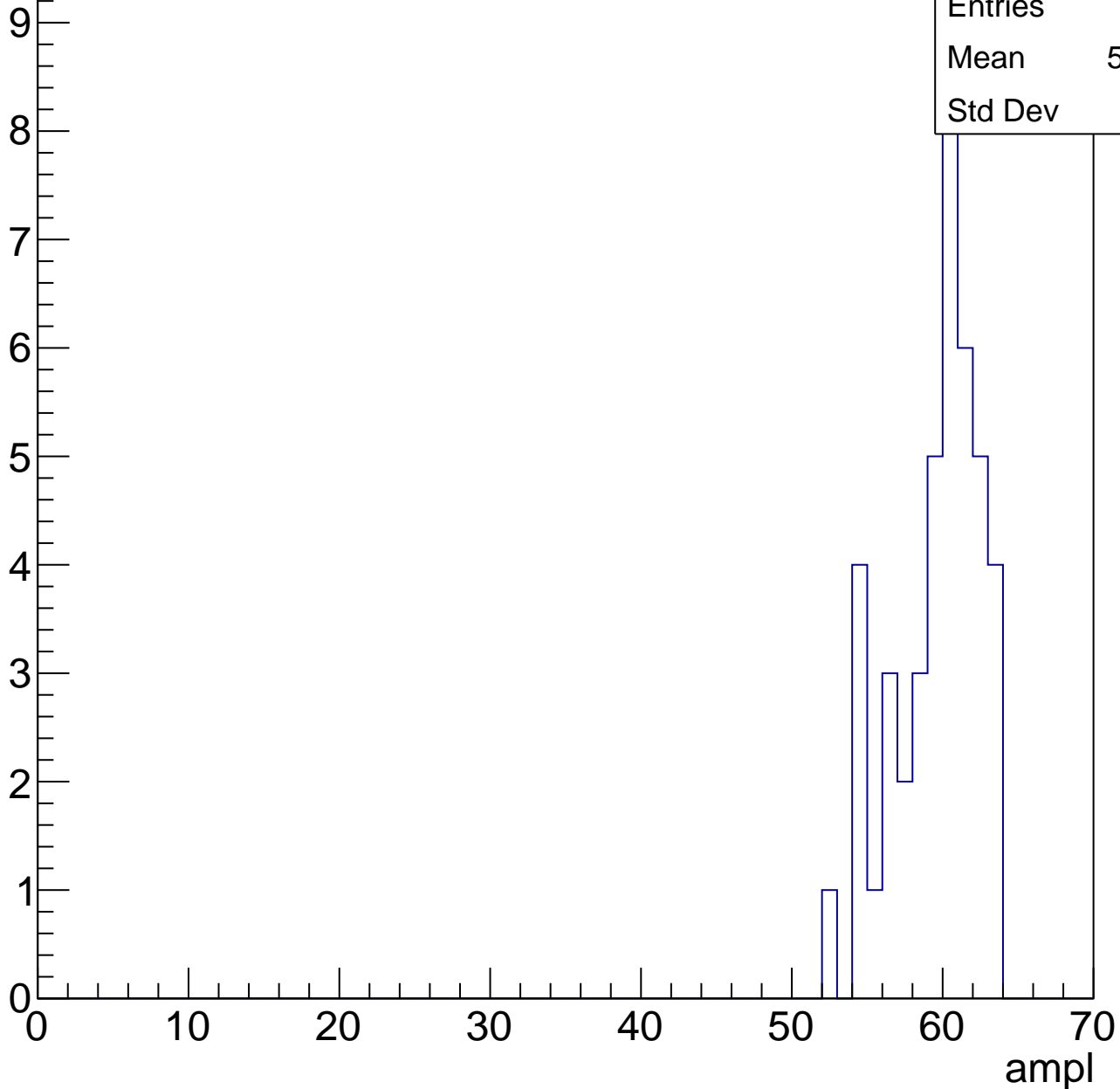


B1L103S, U17-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	59.12
Std Dev	2.83

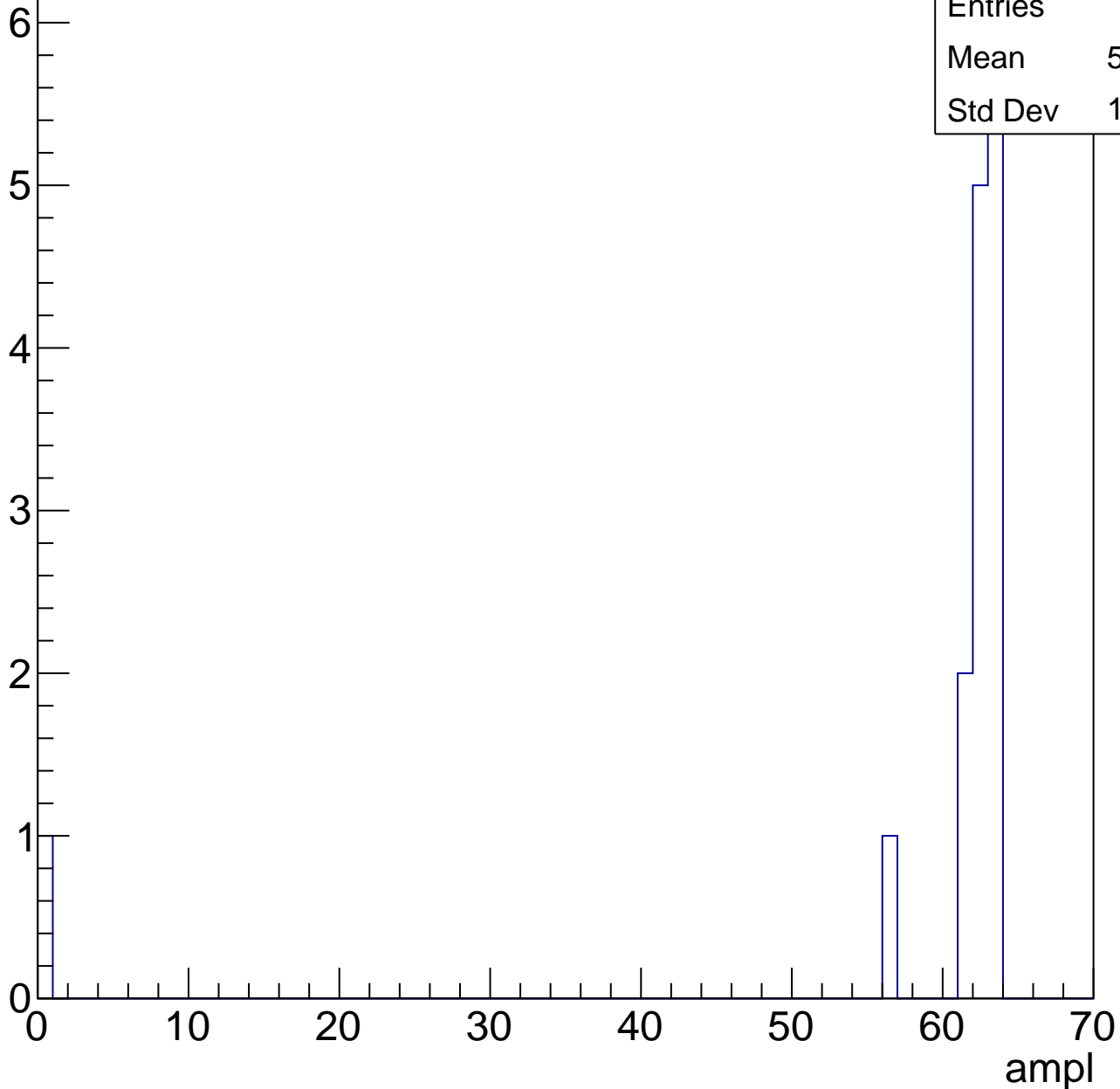


B1L103S, U17-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	57.73
Std Dev	15.52



B1L103S, U17-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch94, adc0

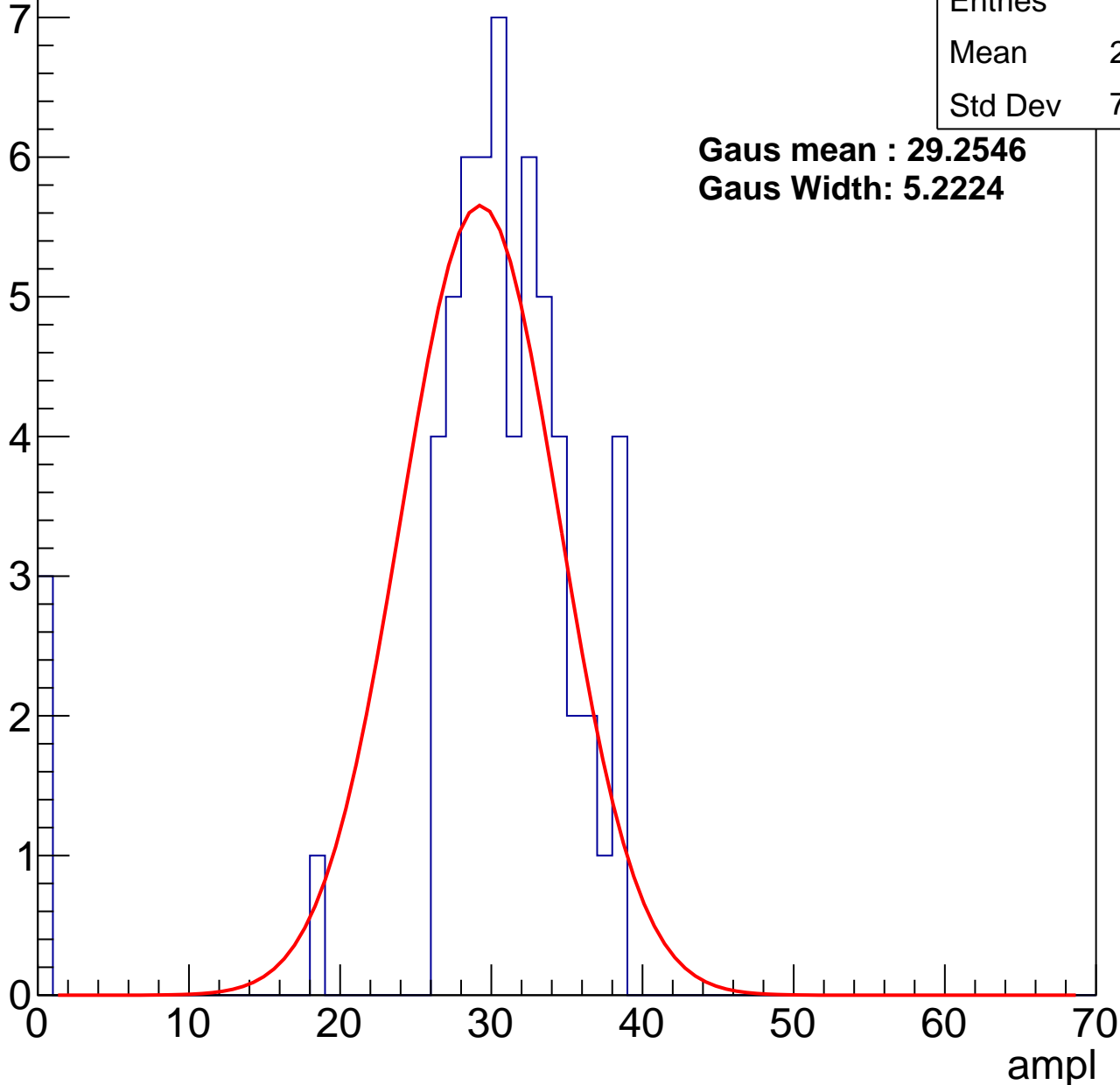
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	29.28
Std Dev	7.655

Gaus mean : 29.2546

Gaus Width: 5.2224



B1L103S, U17-ch94, adc1

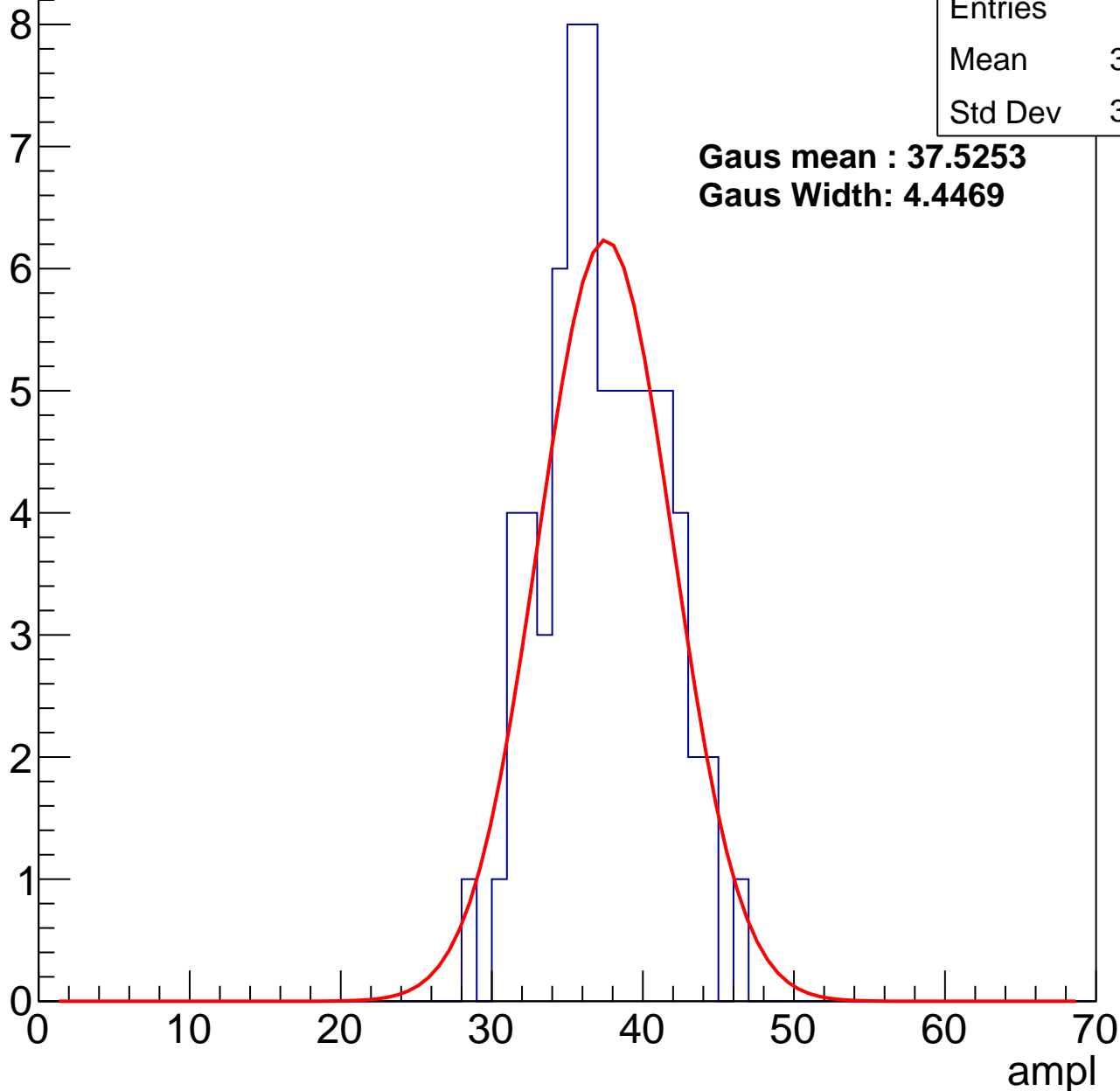
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.87
Std Dev	3.837

Gaus mean : 37.5253

Gaus Width: 4.4469



B1L103S, U17-ch94, adc2

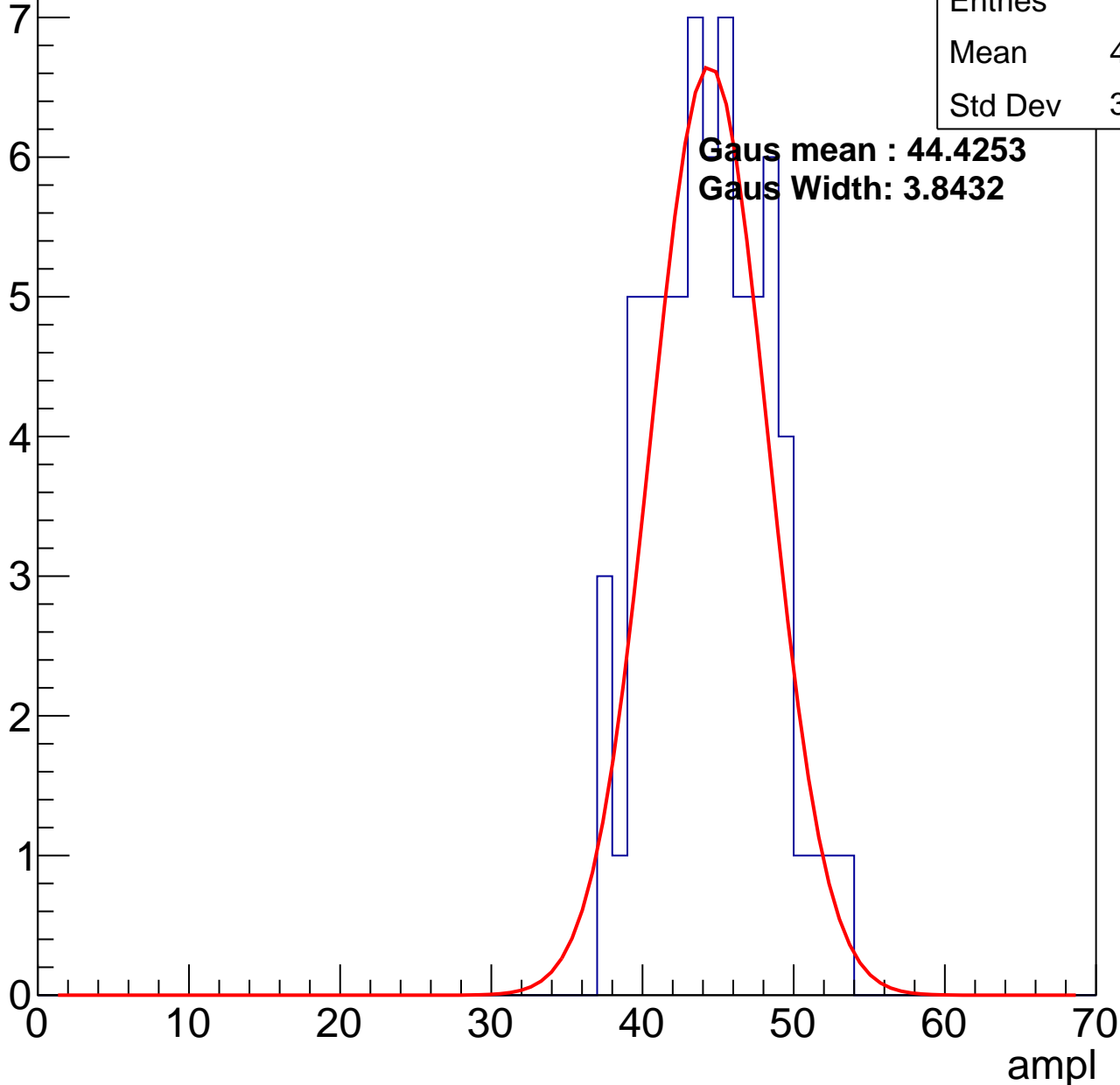
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	44.03
Std Dev	3.753

Gaus mean : 44.4253

Gaus Width: 3.8432

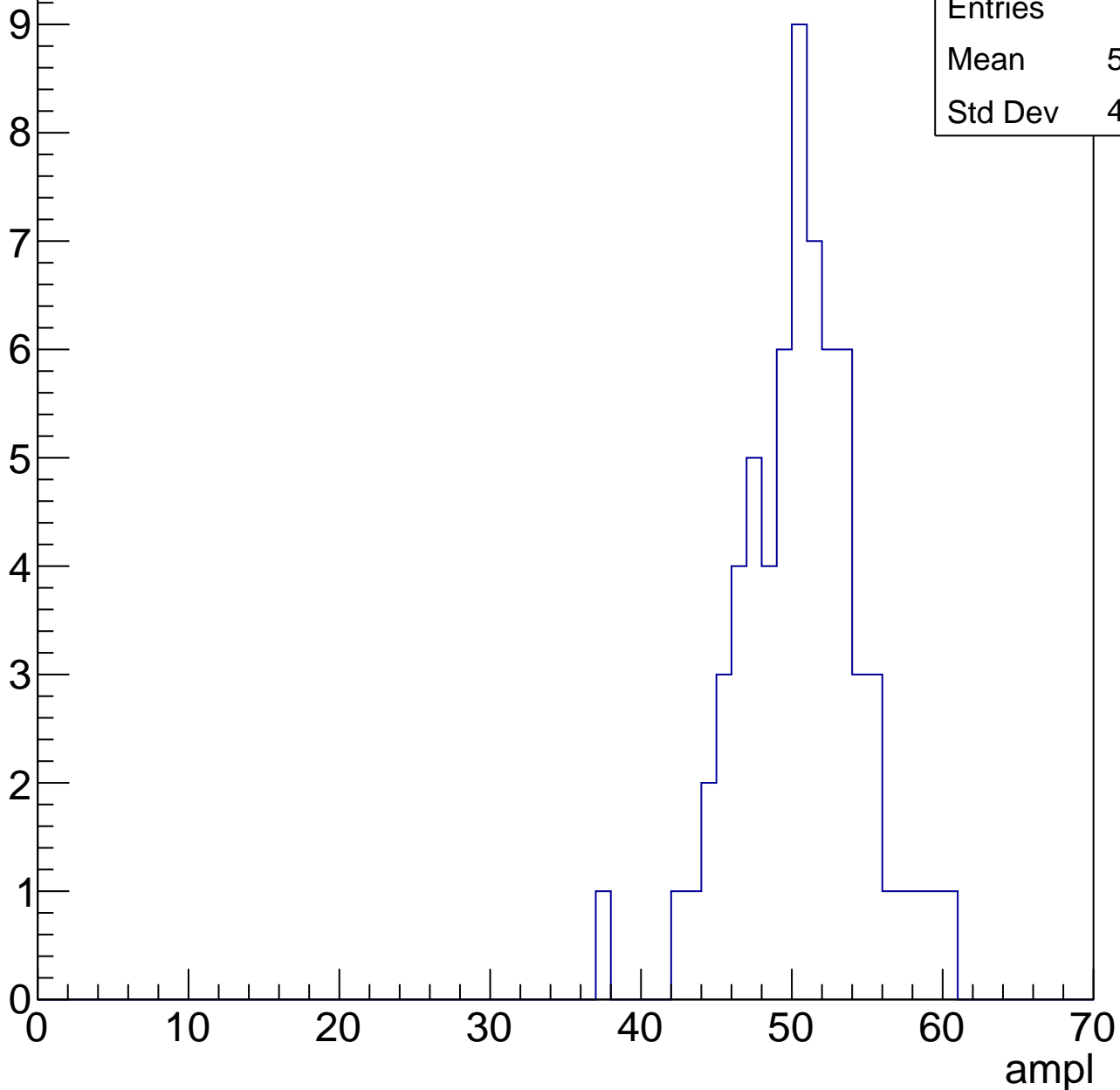


B1L103S, U17-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	50.06
Std Dev	4.078

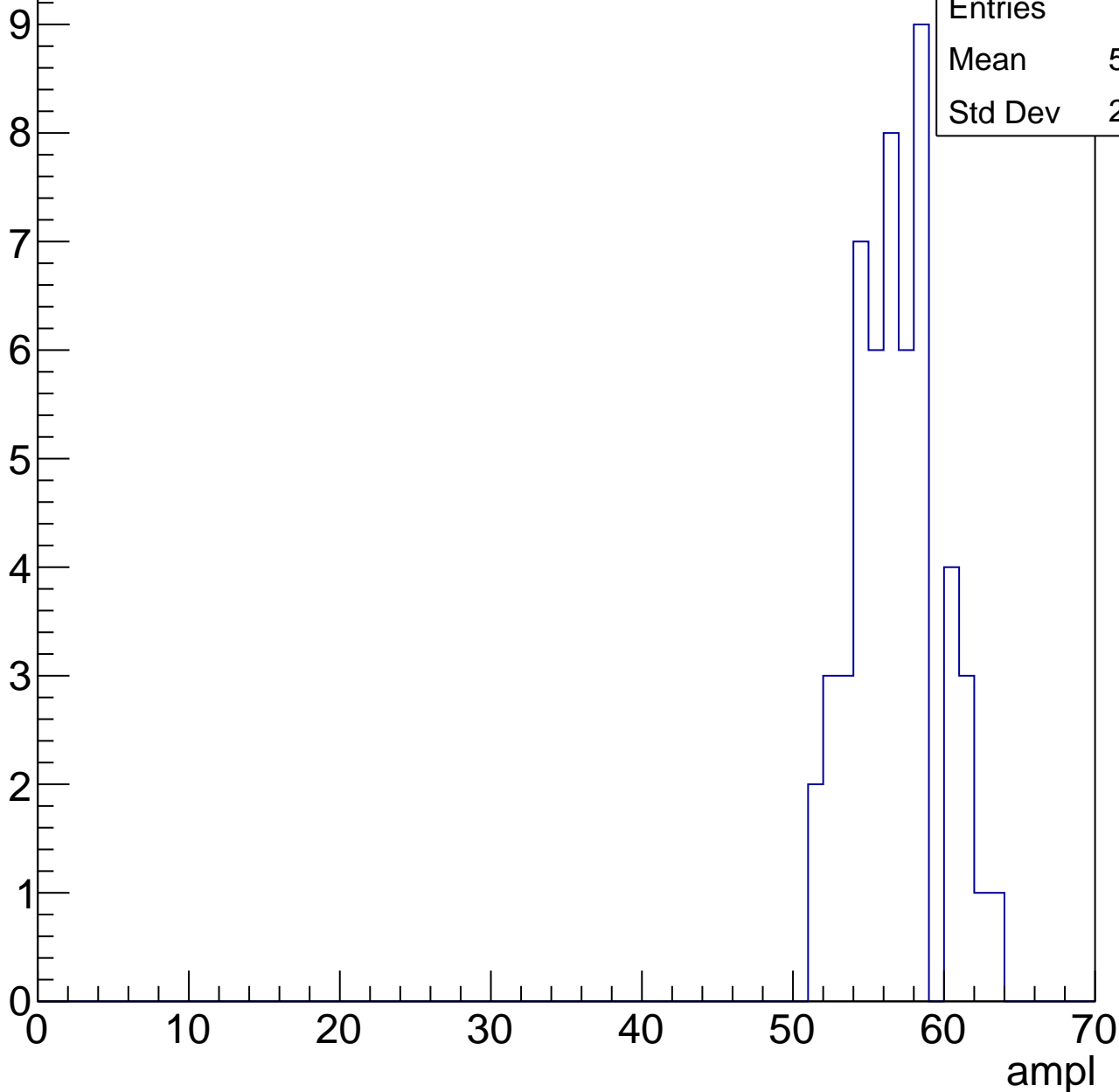


B1L103S, U17-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	56.32
Std Dev	2.814

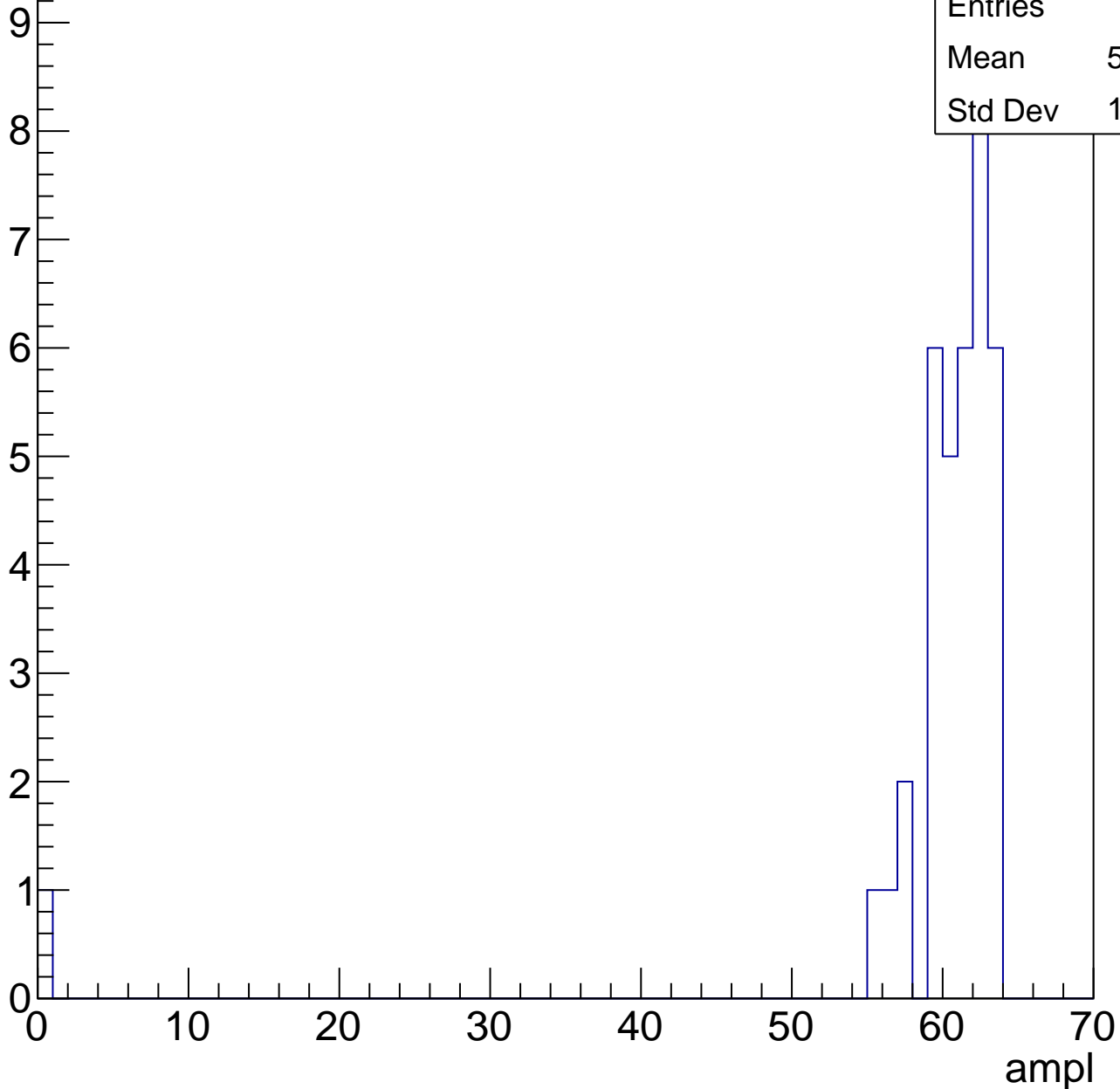


B1L103S, U17-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.95
Std Dev	10.03



B1L103S, U17-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

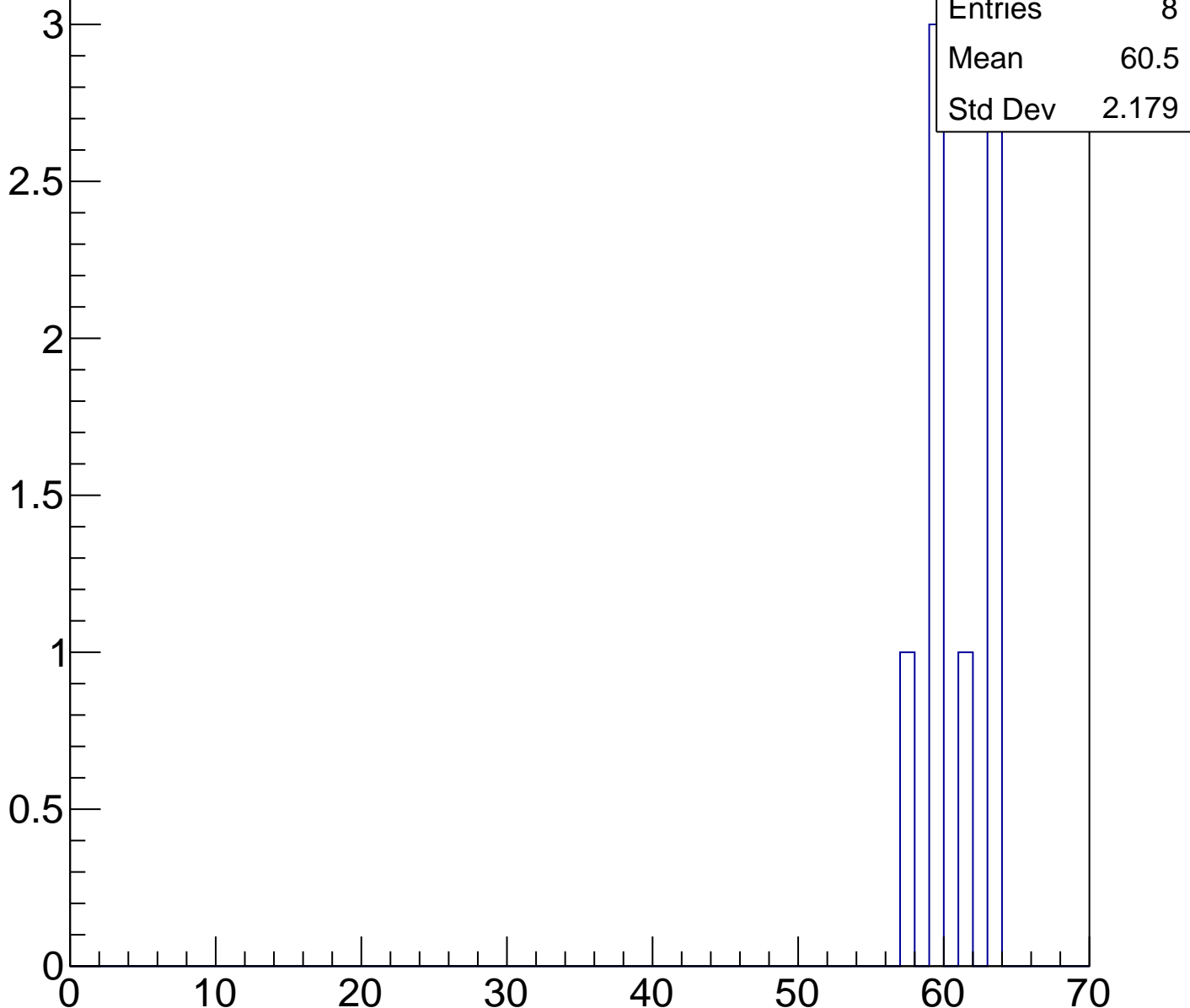
8

Mean

60.5

Std Dev

2.179



B1L103S, U17-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch95, adc0

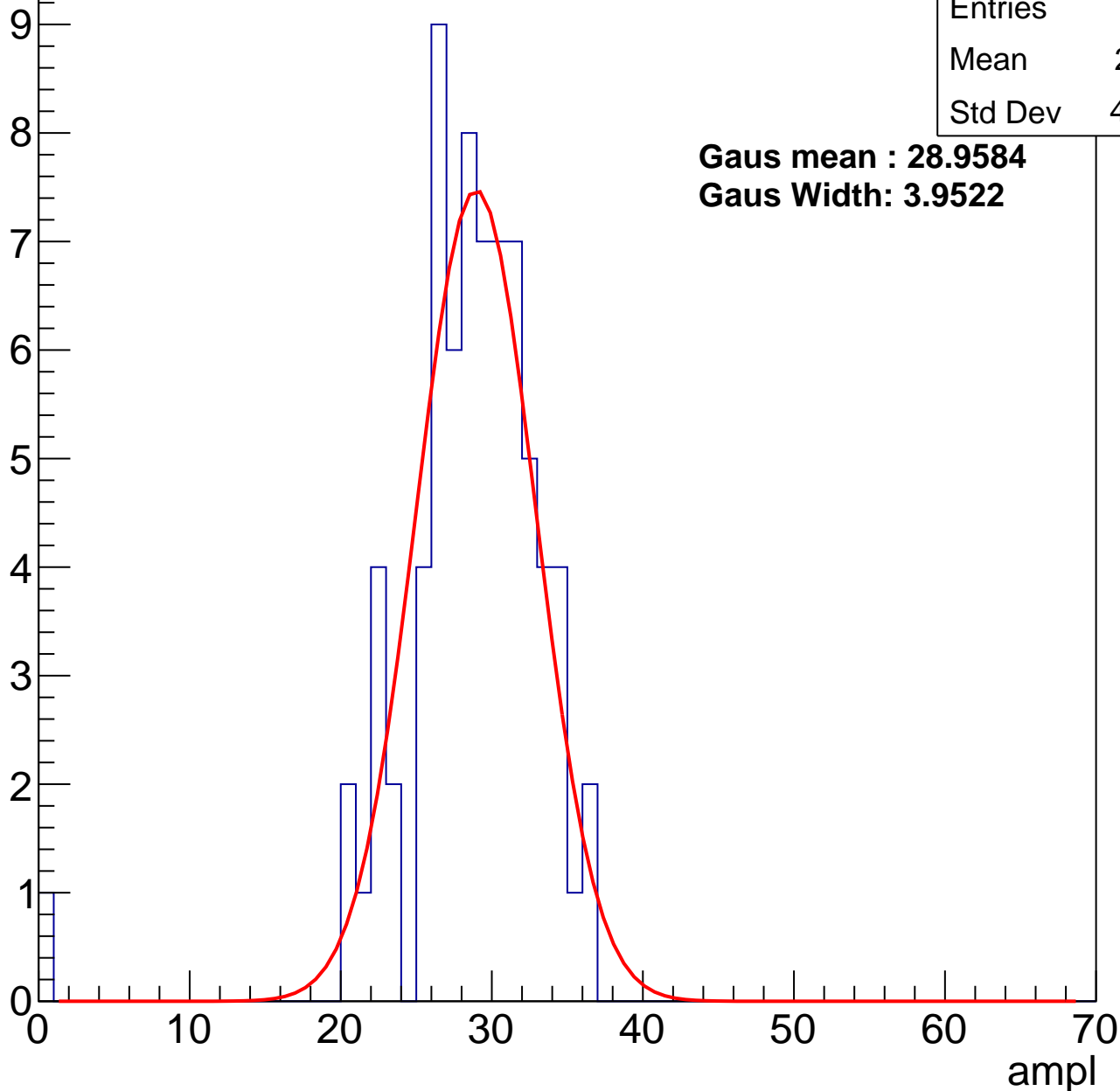
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.11
Std Dev	4.974

Gaus mean : 28.9584

Gaus Width: 3.9522



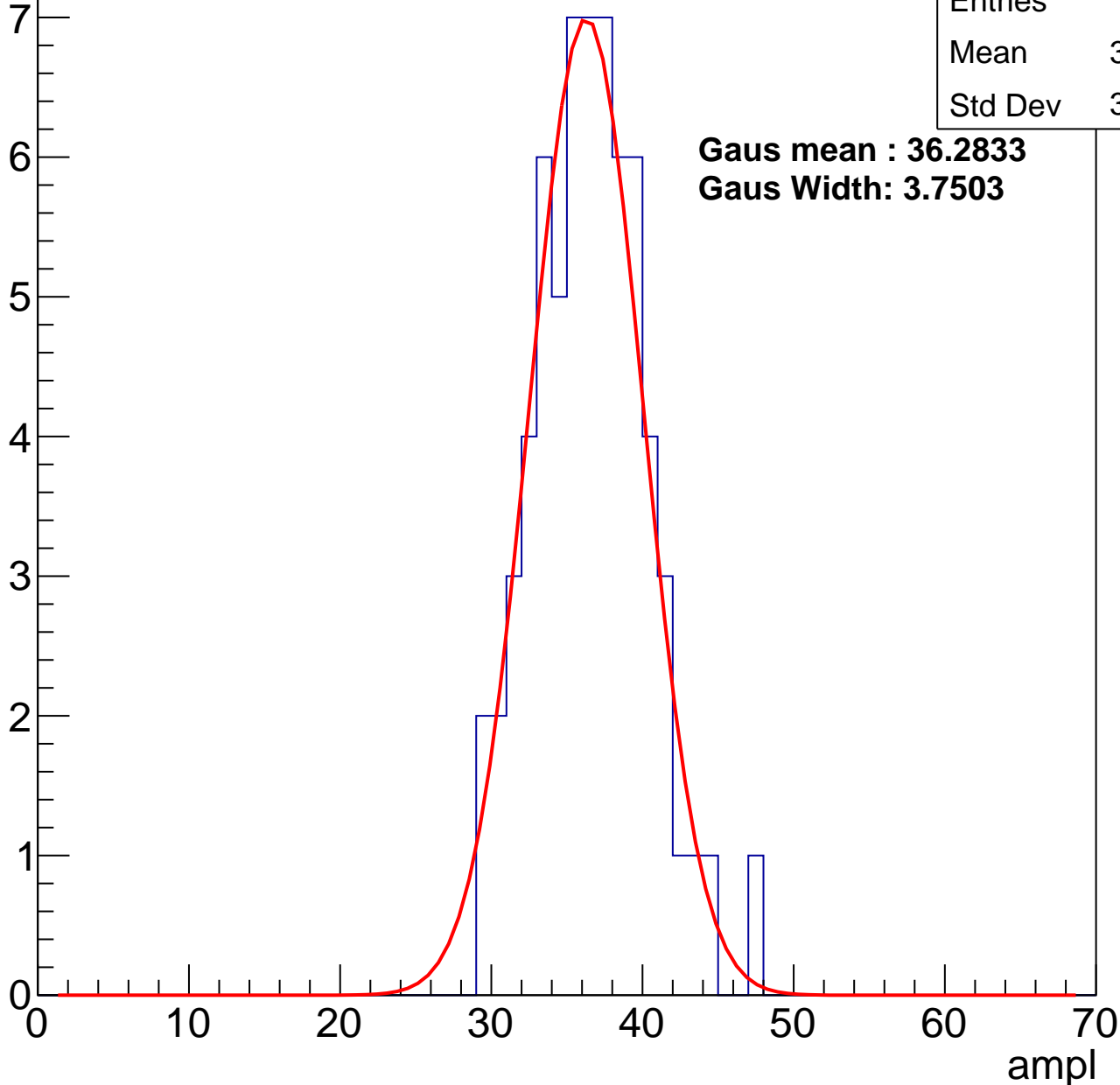
B1L103S, U17-ch95, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.12
Std Dev	3.658

Gaus mean : 36.2833
Gaus Width: 3.7503



B1L103S, U17-ch95, adc2

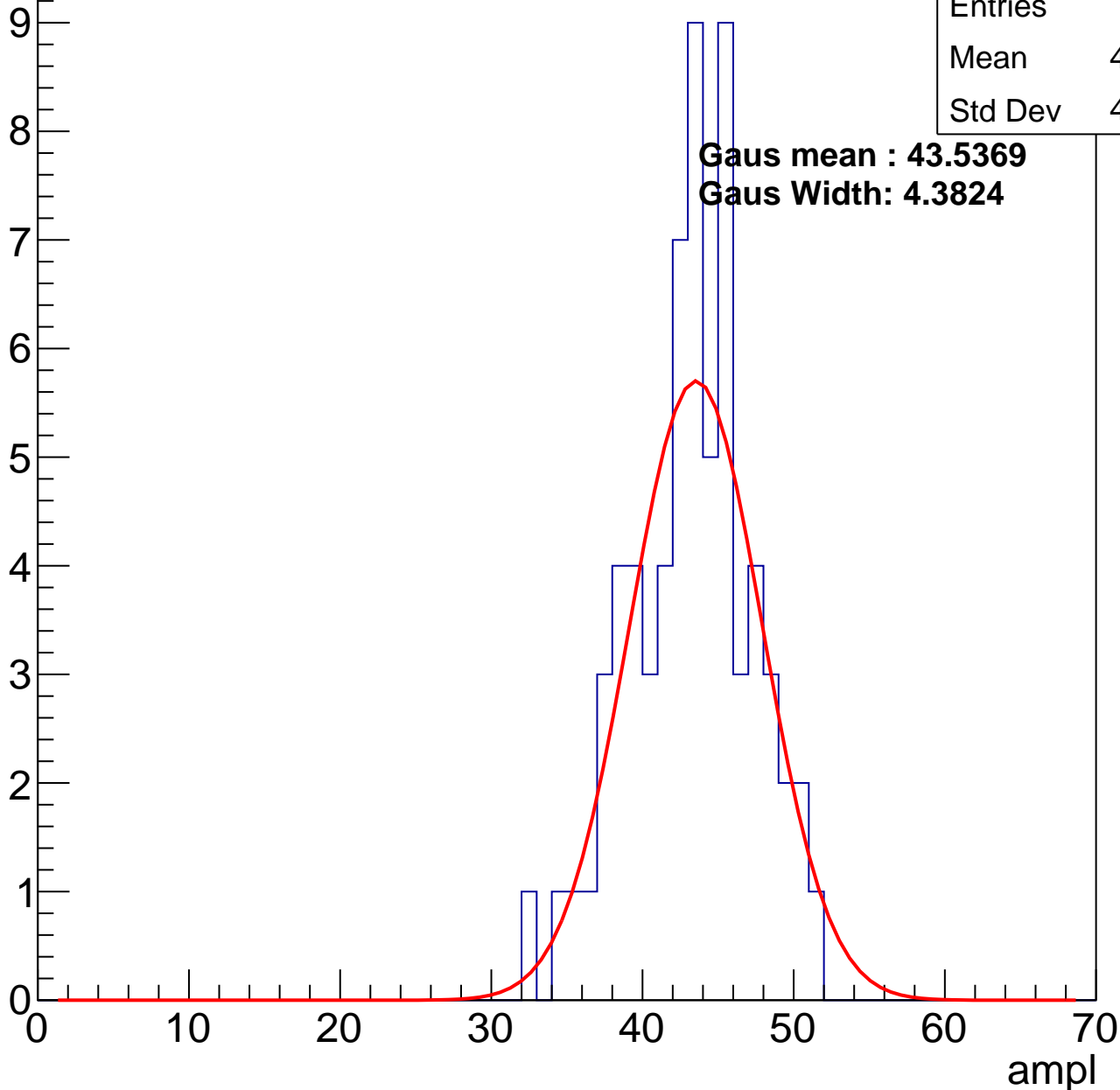
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.76
Std Dev	4.004

Gaus mean : 43.5369

Gaus Width: 4.3824

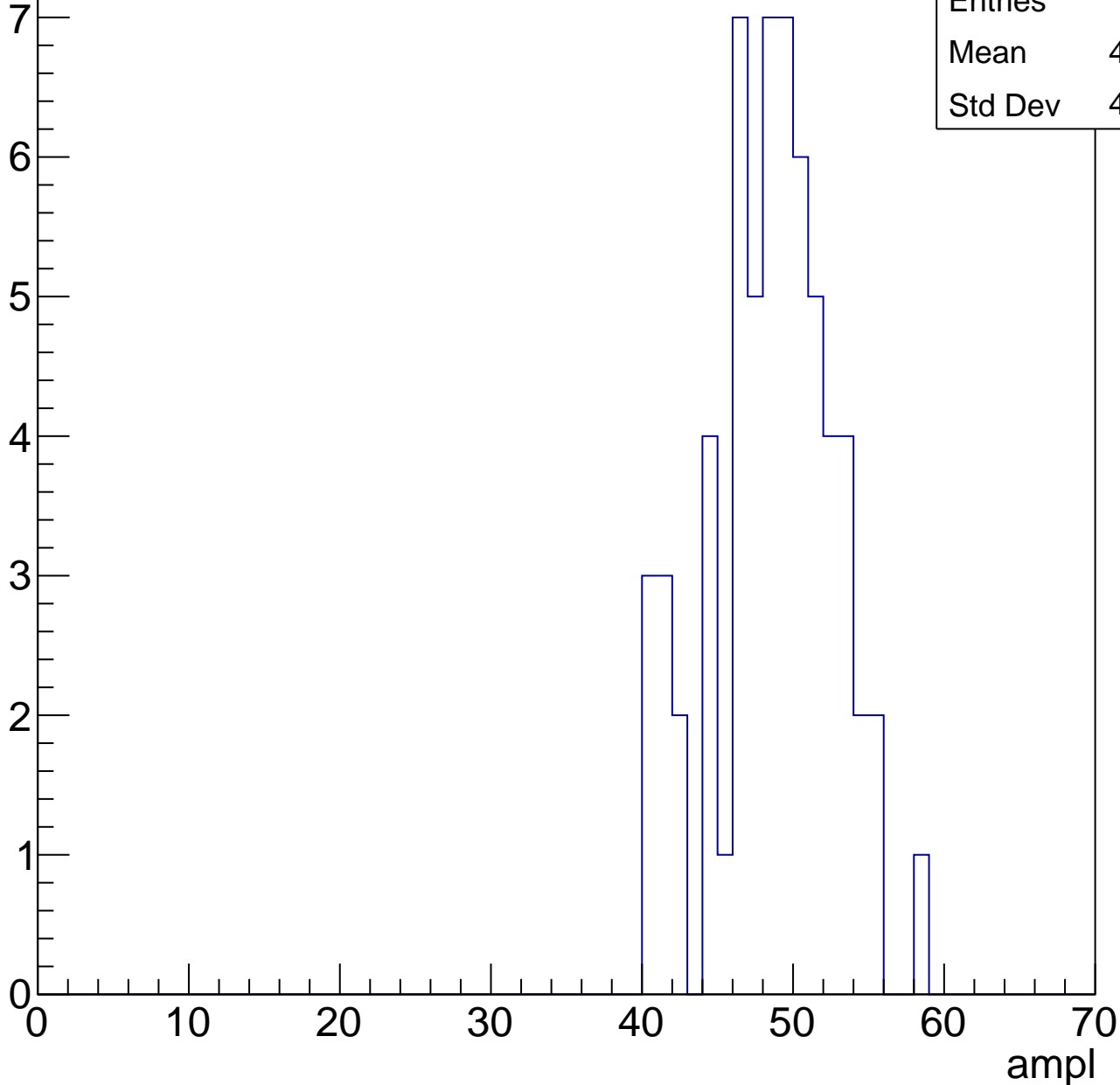


B1L103S, U17-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.17
Std Dev	4.034

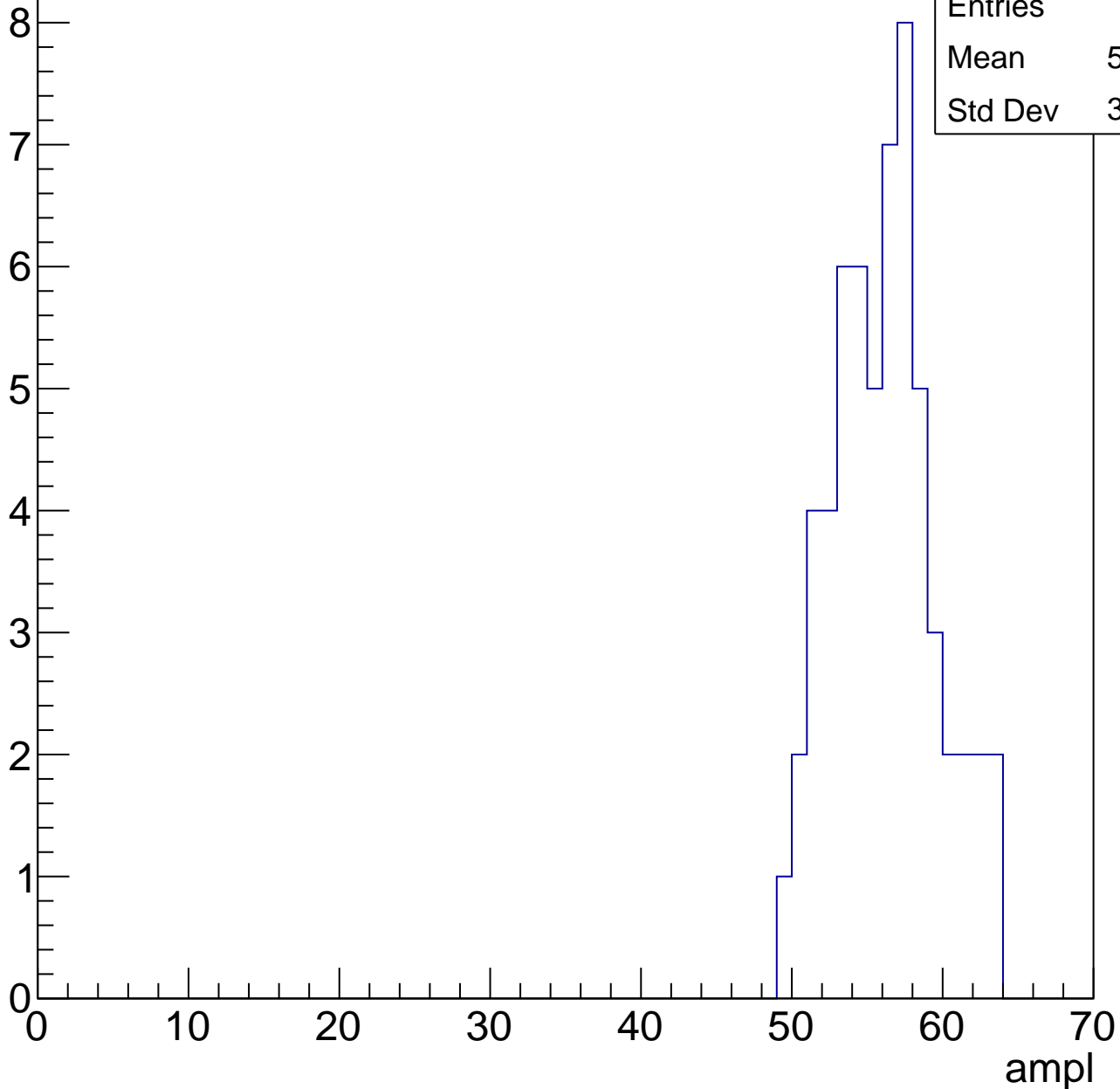


B1L103S, U17-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.68
Std Dev	3.367



B1L103S, U17-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 45

Mean 57.78

Std Dev 9.211

ampl

0

10

20

30

40

50

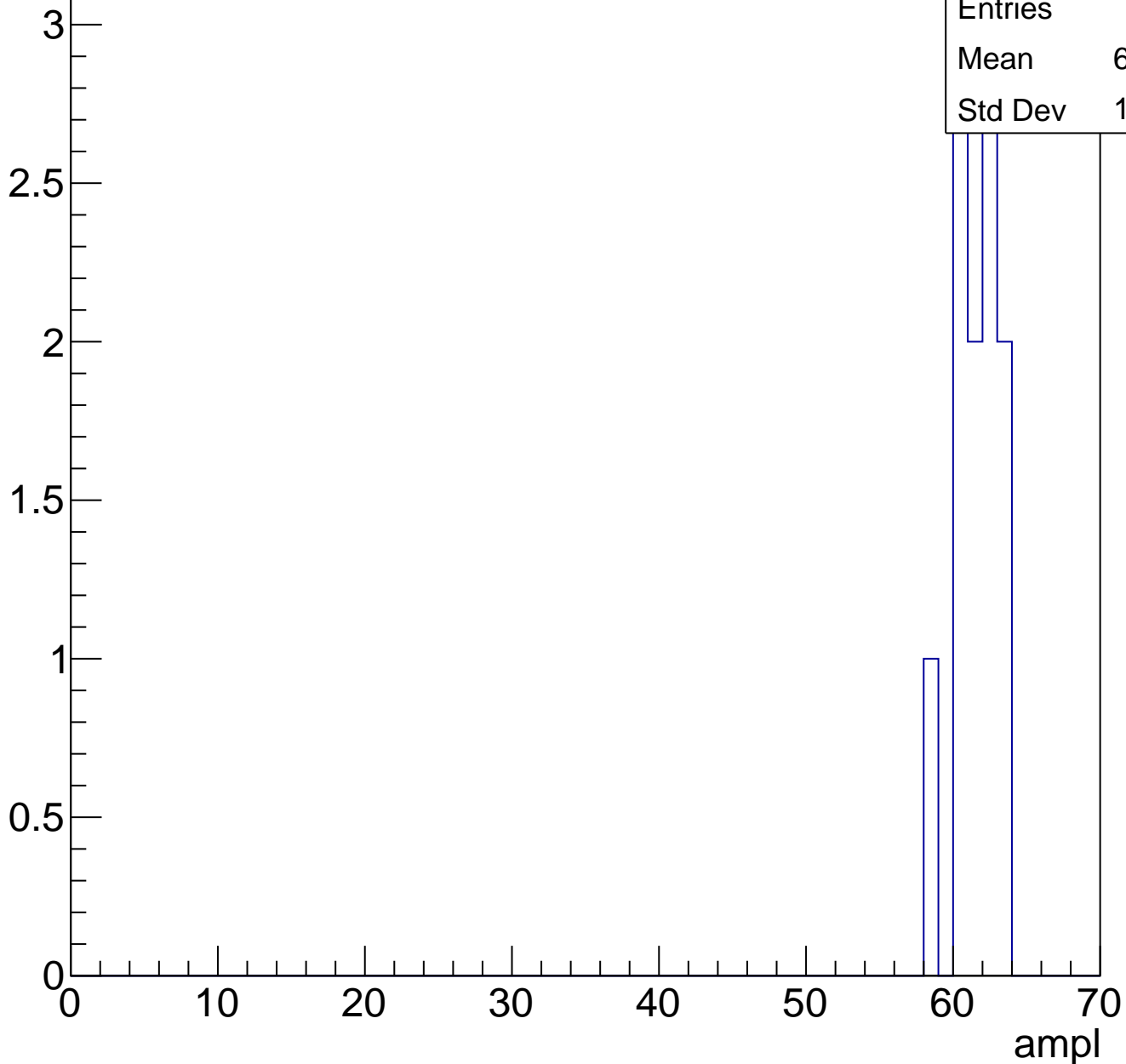
60

70

B1L103S, U17-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch96, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	29.18
Std Dev	4.845

Gaus mean : 29.8725

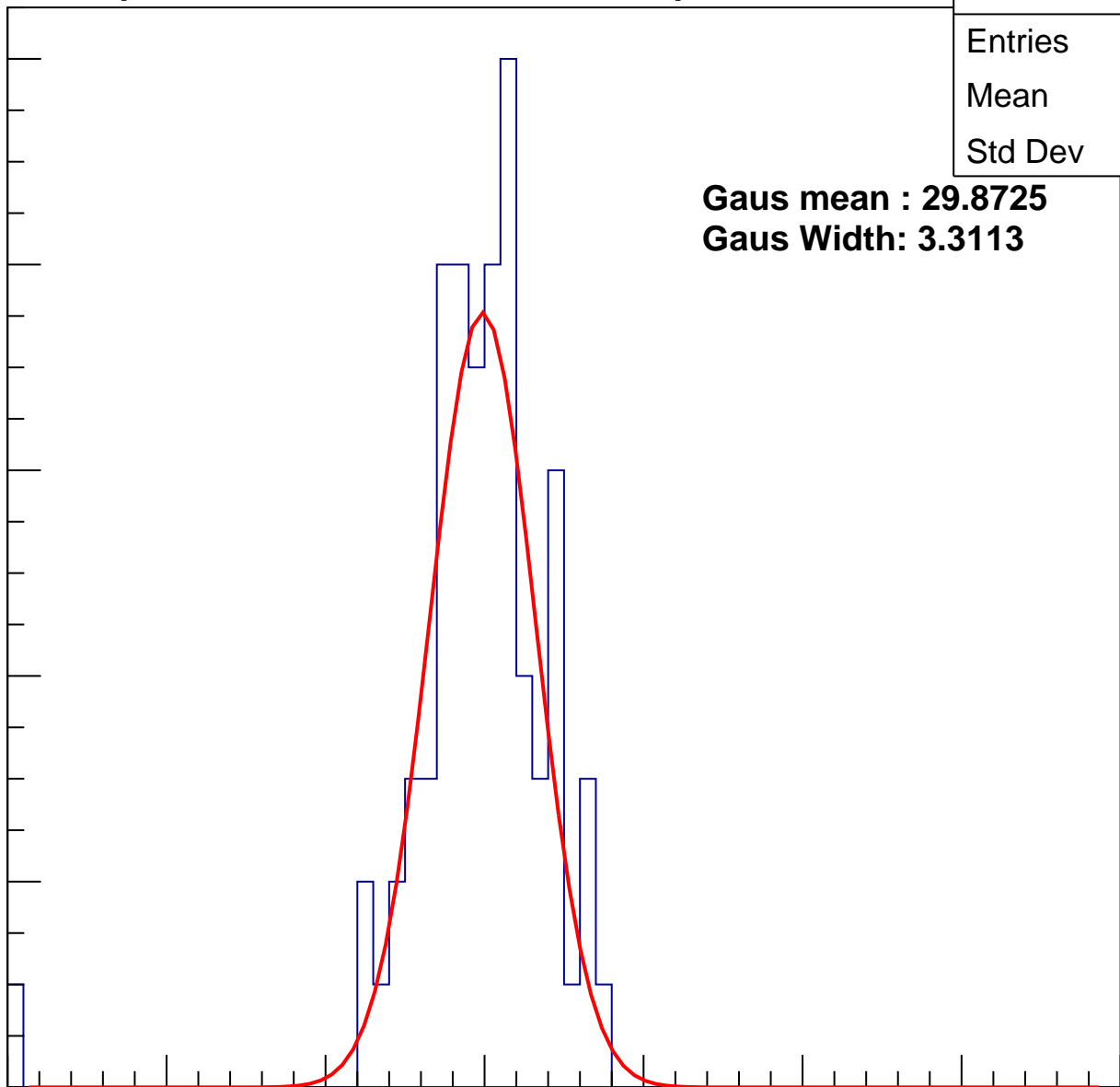
Gaus Width: 3.3113

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch96, adc1

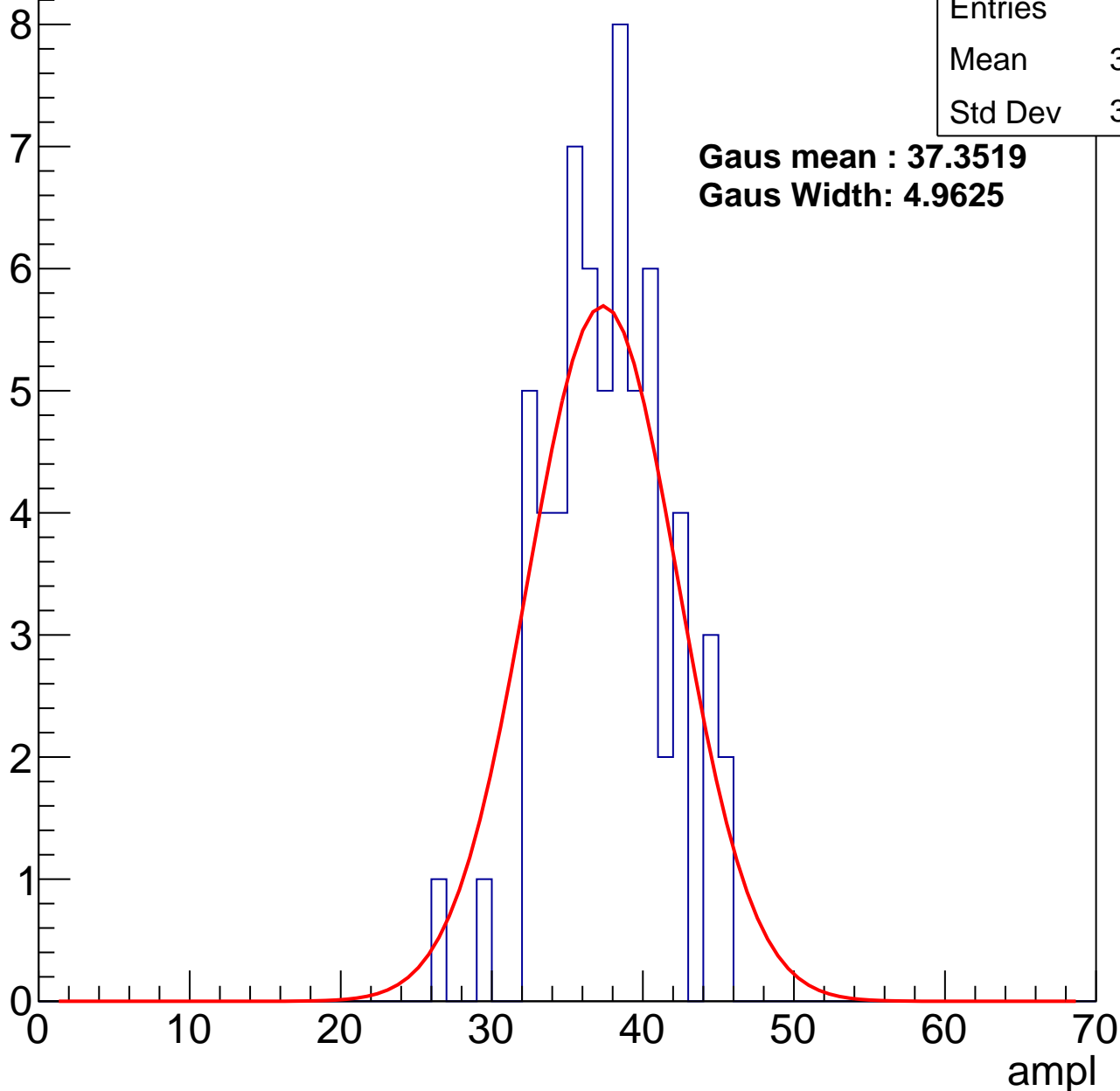
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.14
Std Dev	3.846

Gaus mean : 37.3519

Gaus Width: 4.9625



B1L103S, U17-ch96, adc2

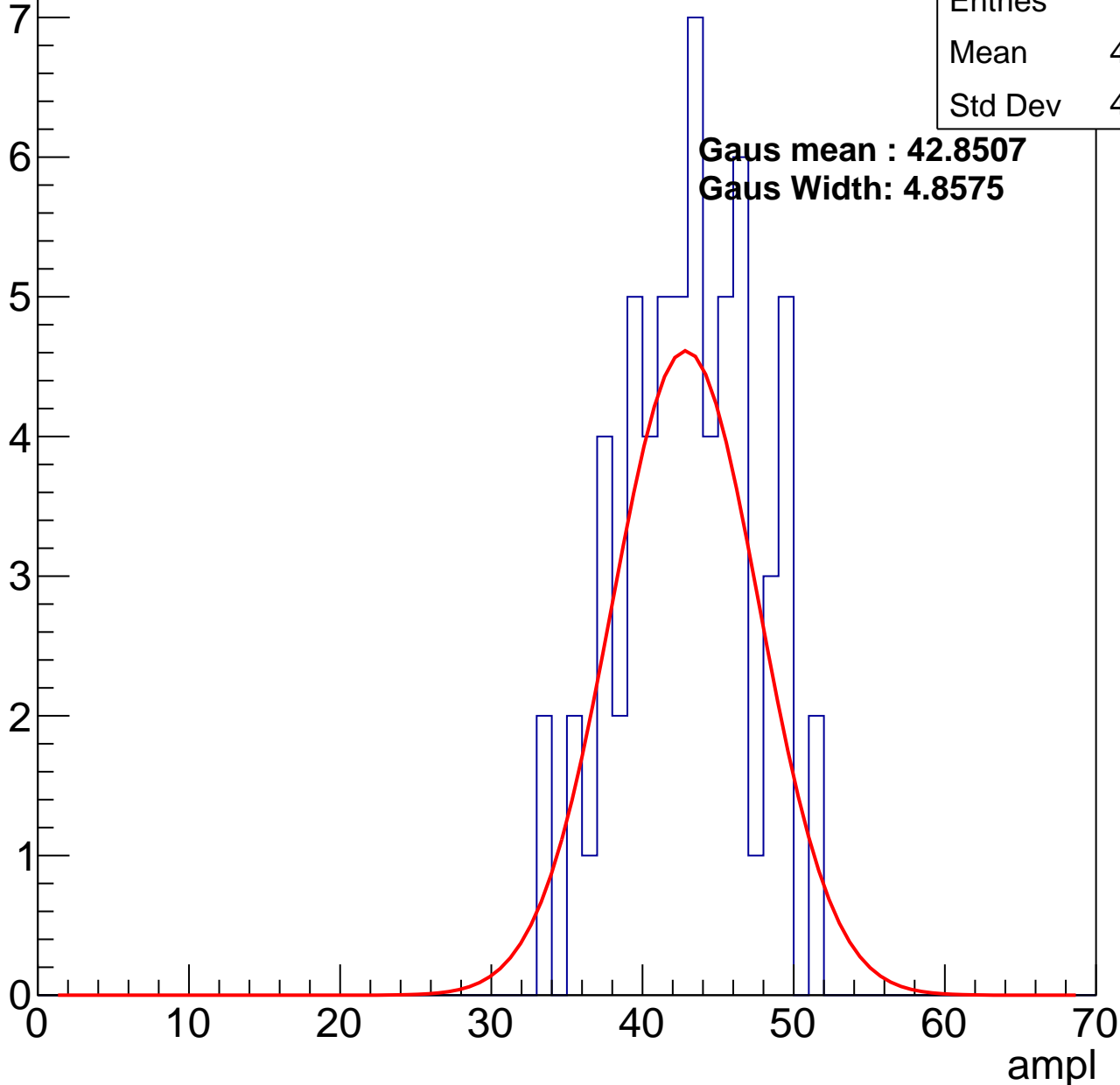
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.57
Std Dev	4.323

Gaus mean : 42.8507

Gaus Width: 4.8575

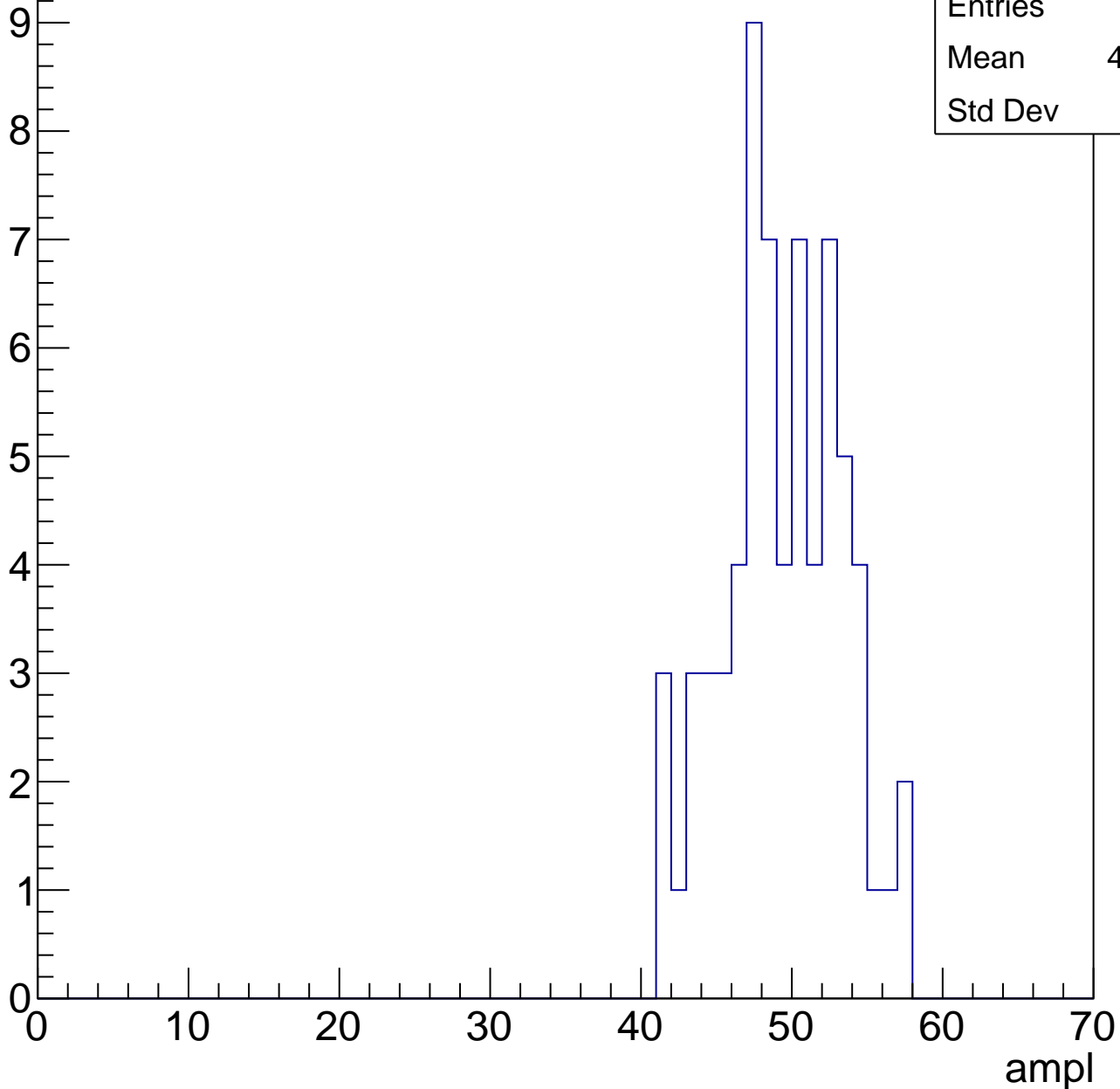


B1L103S, U17-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

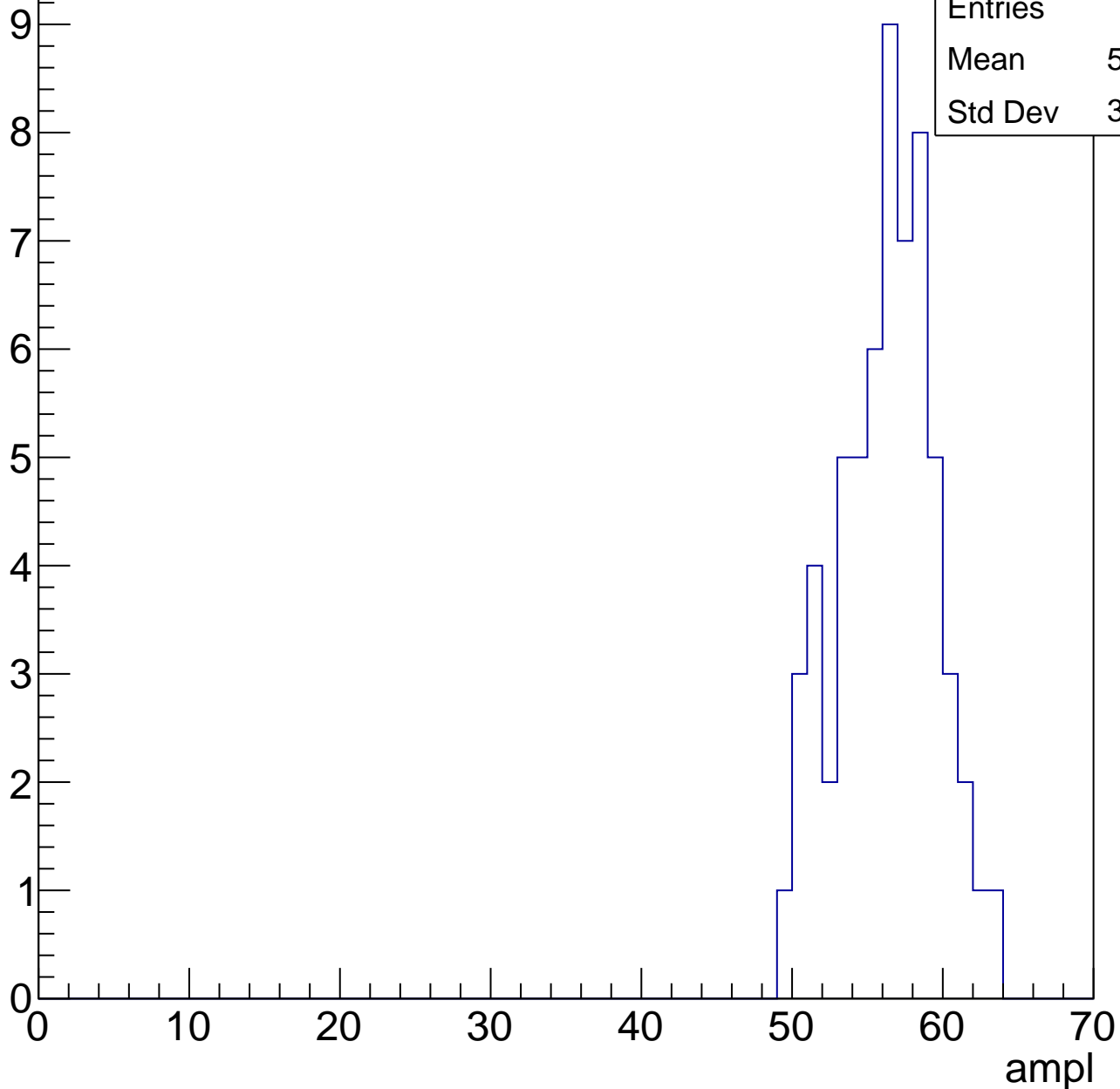
Entries	68
Mean	48.88
Std Dev	3.89



B1L103S, U17-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

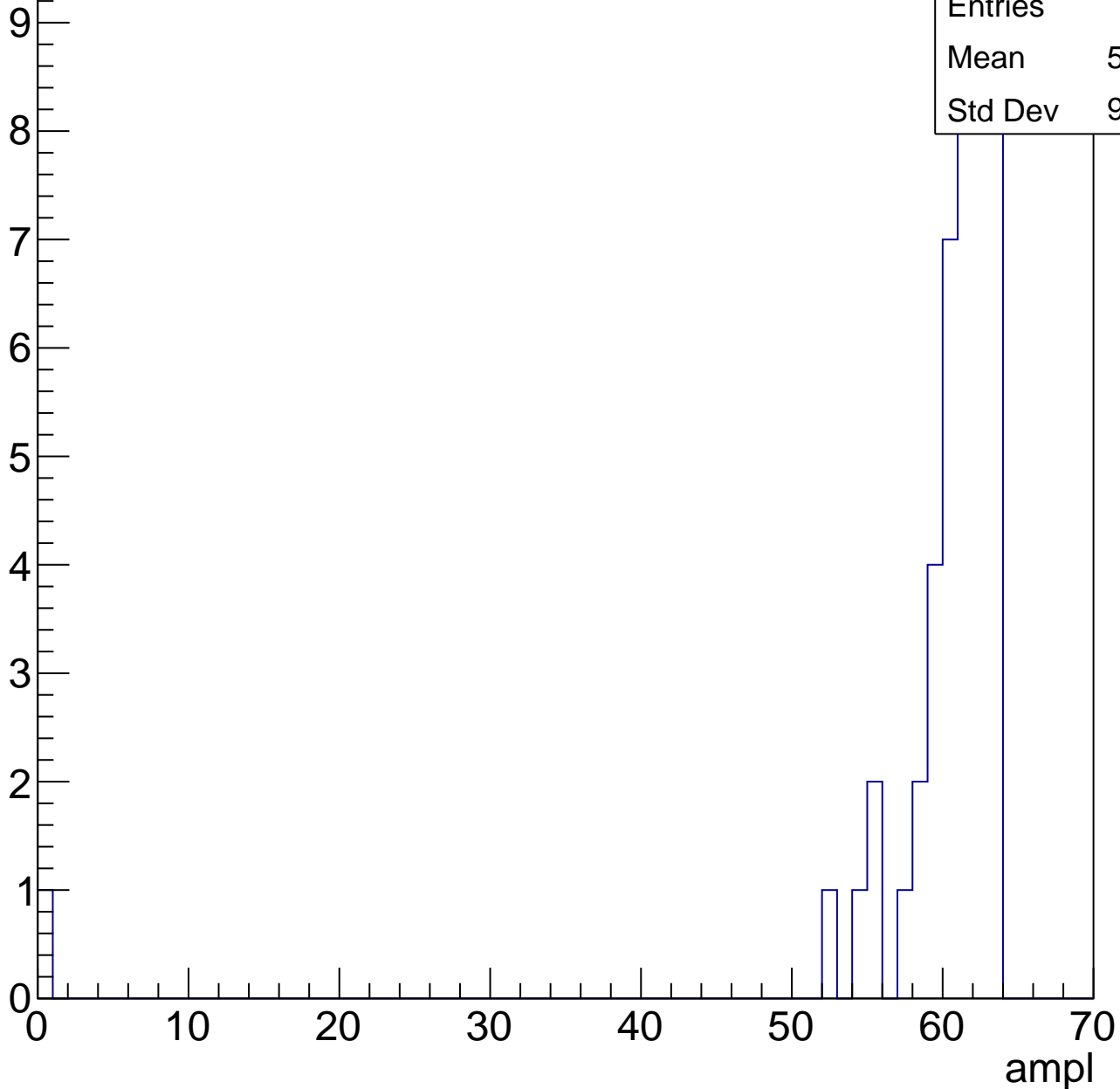


B1L103S, U17-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.98
Std Dev	9.343



B1L103S, U17-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	3
Mean	59
Std Dev	3.266

B1L103S, U17-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch97, adc0

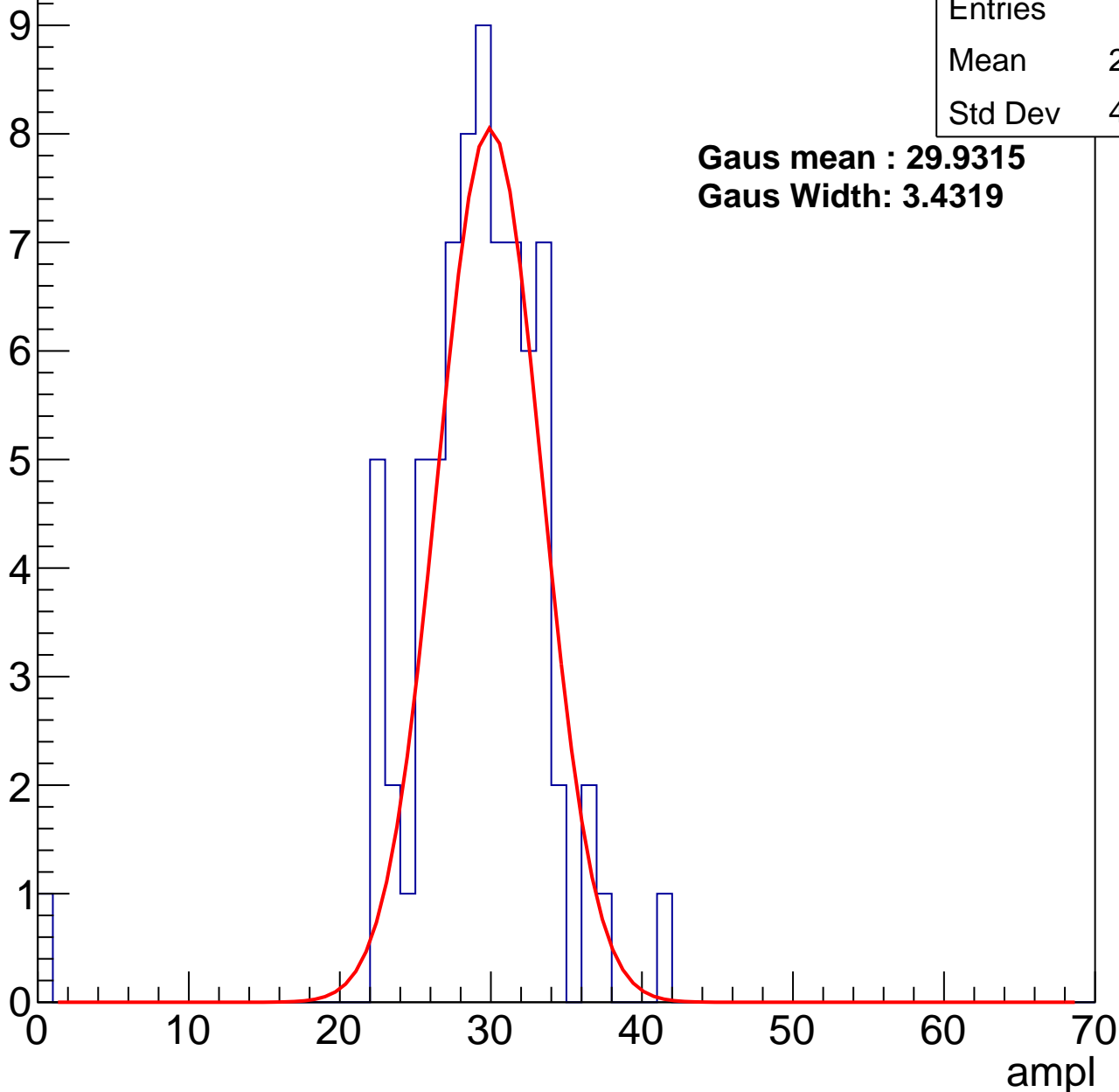
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.64
Std Dev	4.989

Gaus mean : 29.9315

Gaus Width: 3.4319



B1L103S, U17-ch97, adc1

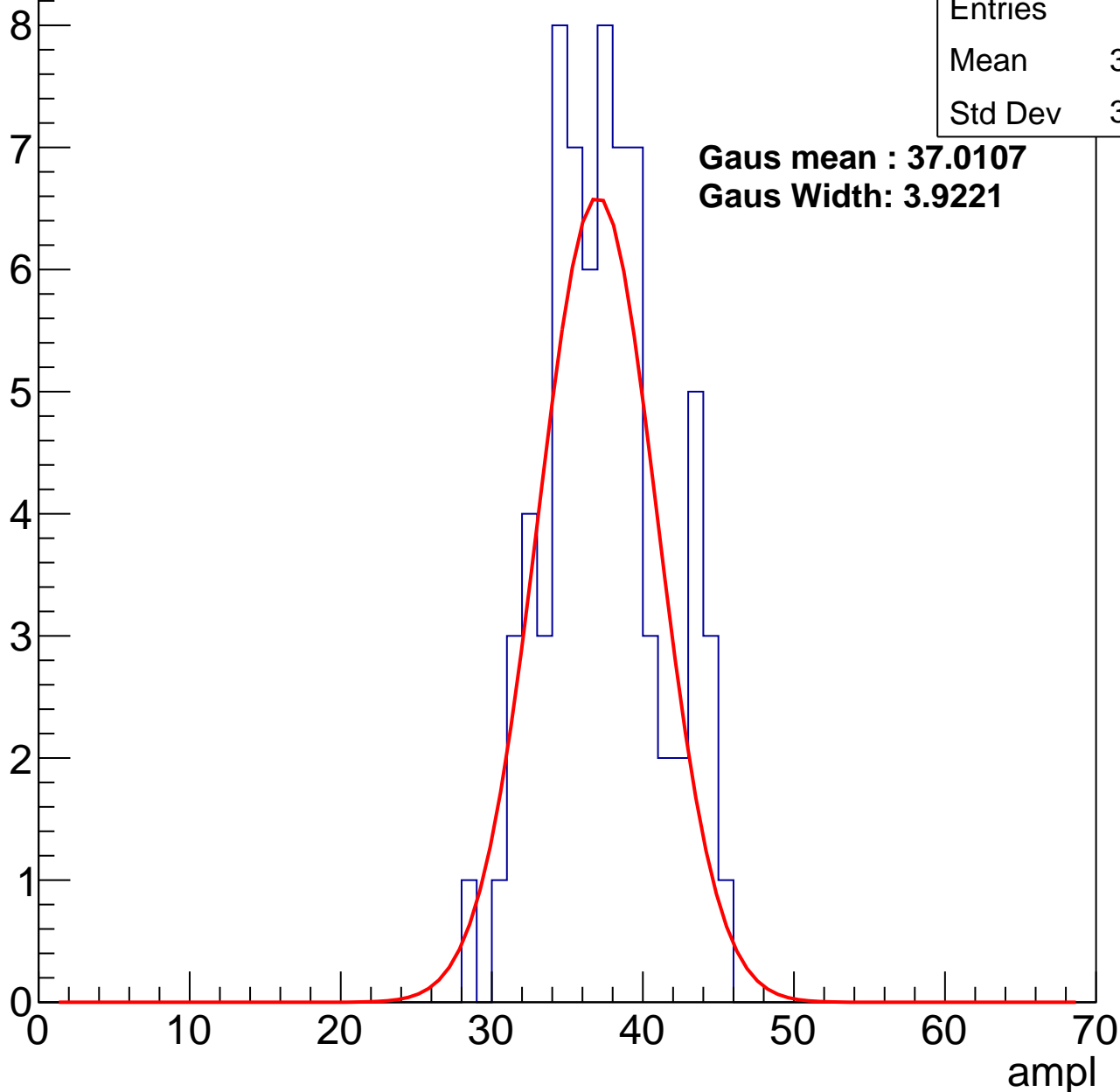
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.96
Std Dev	3.803

Gaus mean : 37.0107

Gaus Width: 3.9221



B1L103S, U17-ch97, adc2

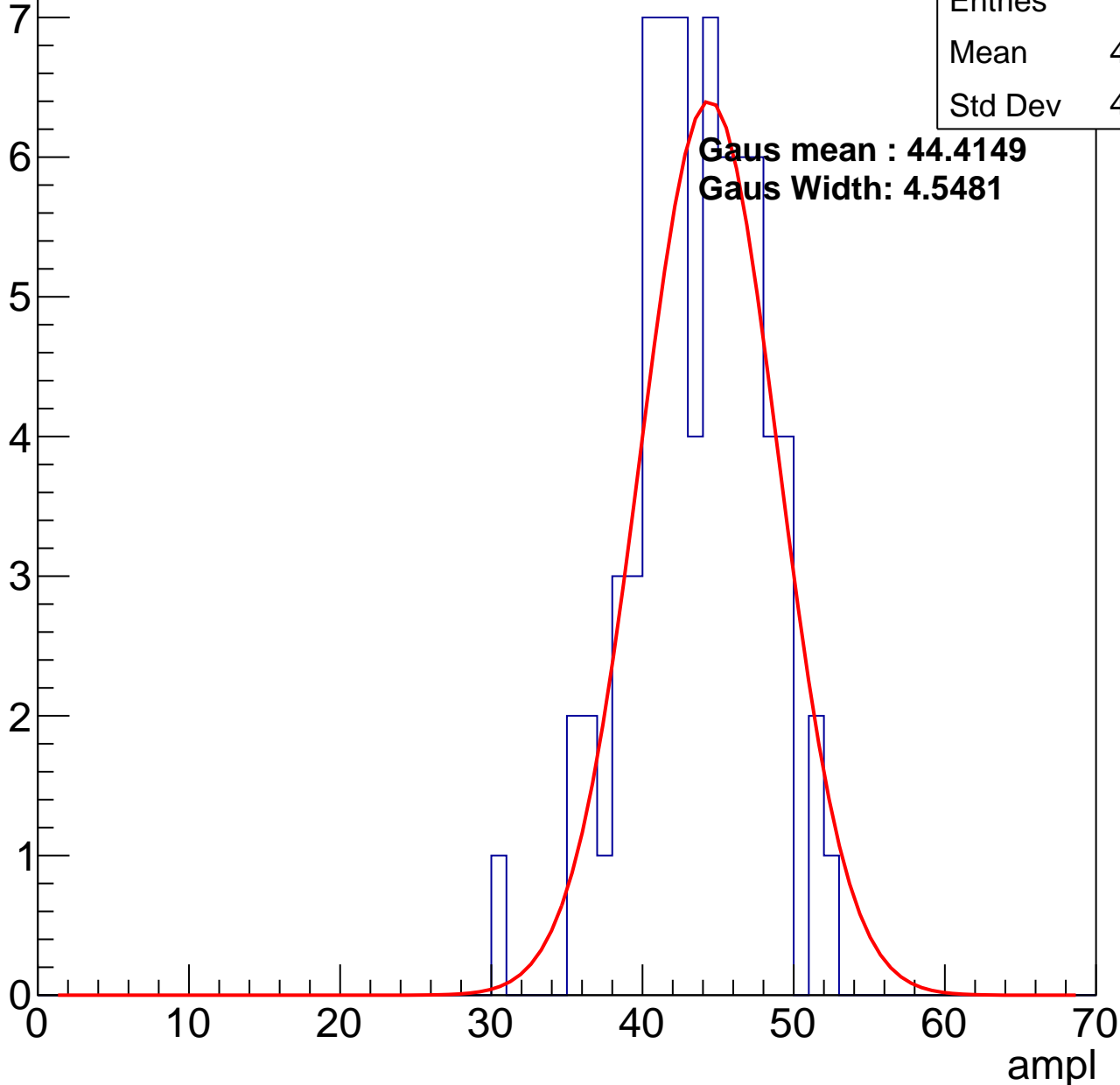
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.16
Std Dev	4.194

Gaus mean : 44.4149

Gaus Width: 4.5481

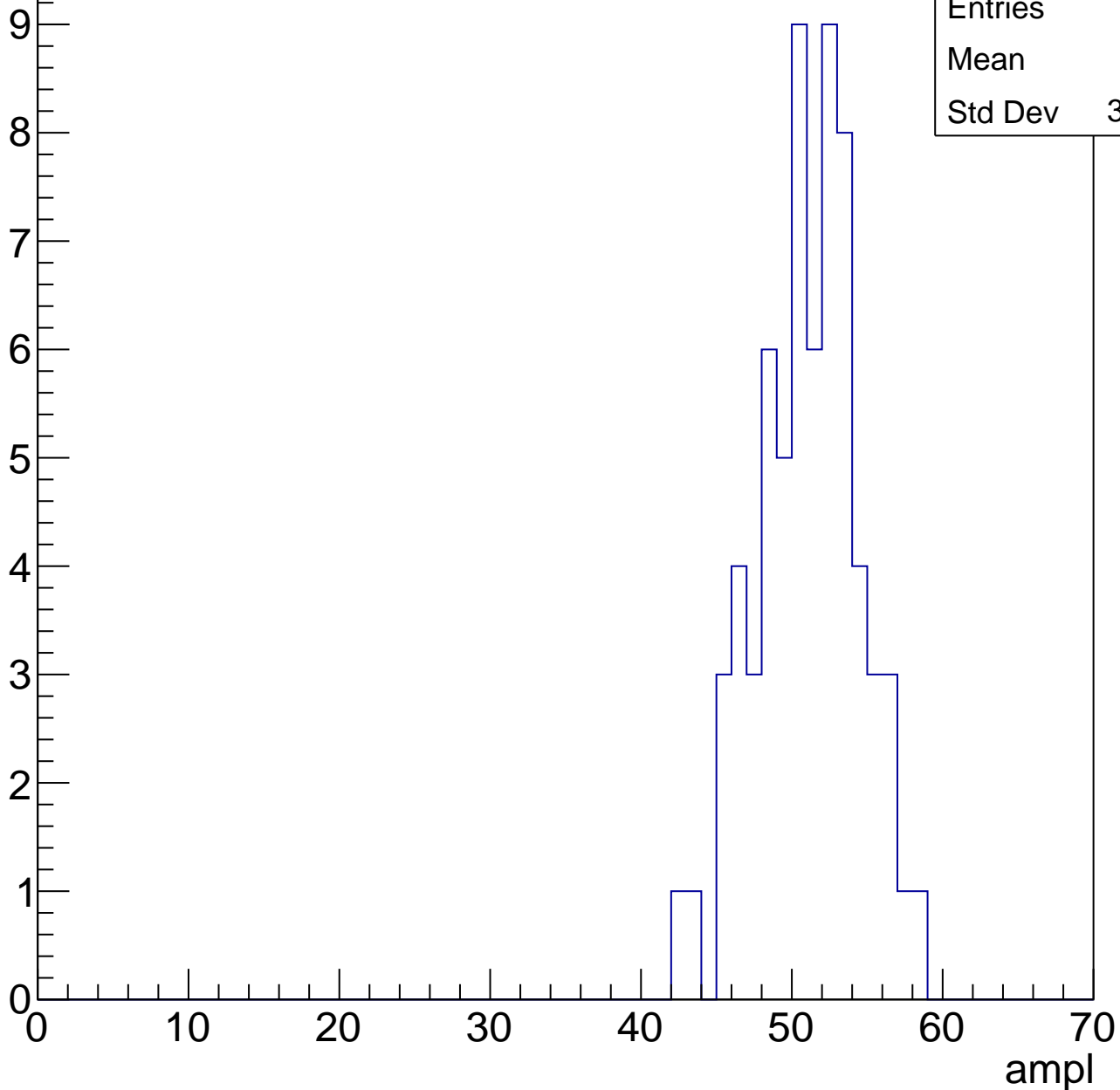


B1L103S, U17-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	50.6
Std Dev	3.364

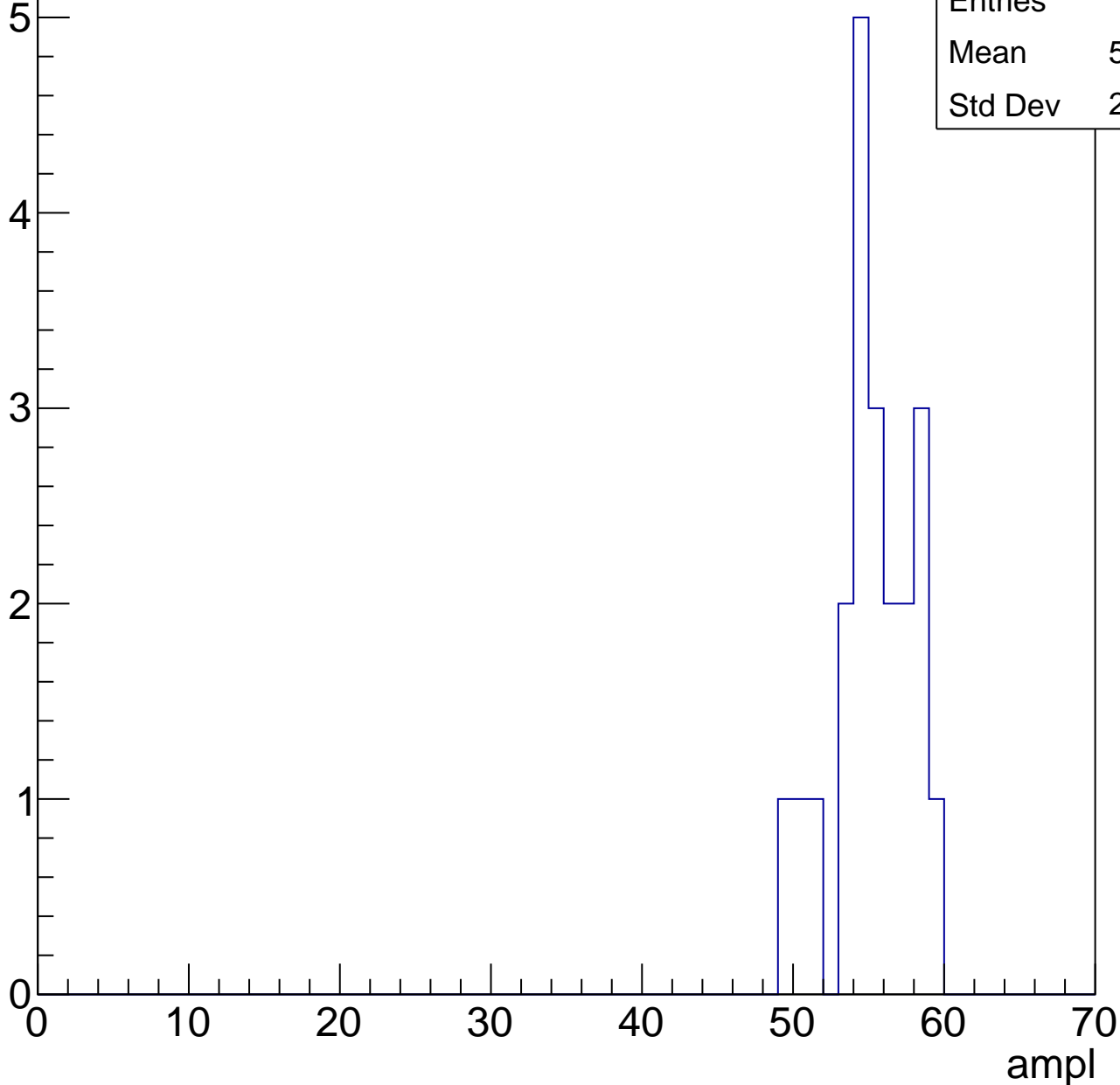


B1L103S, U17-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	54.76
Std Dev	2.599

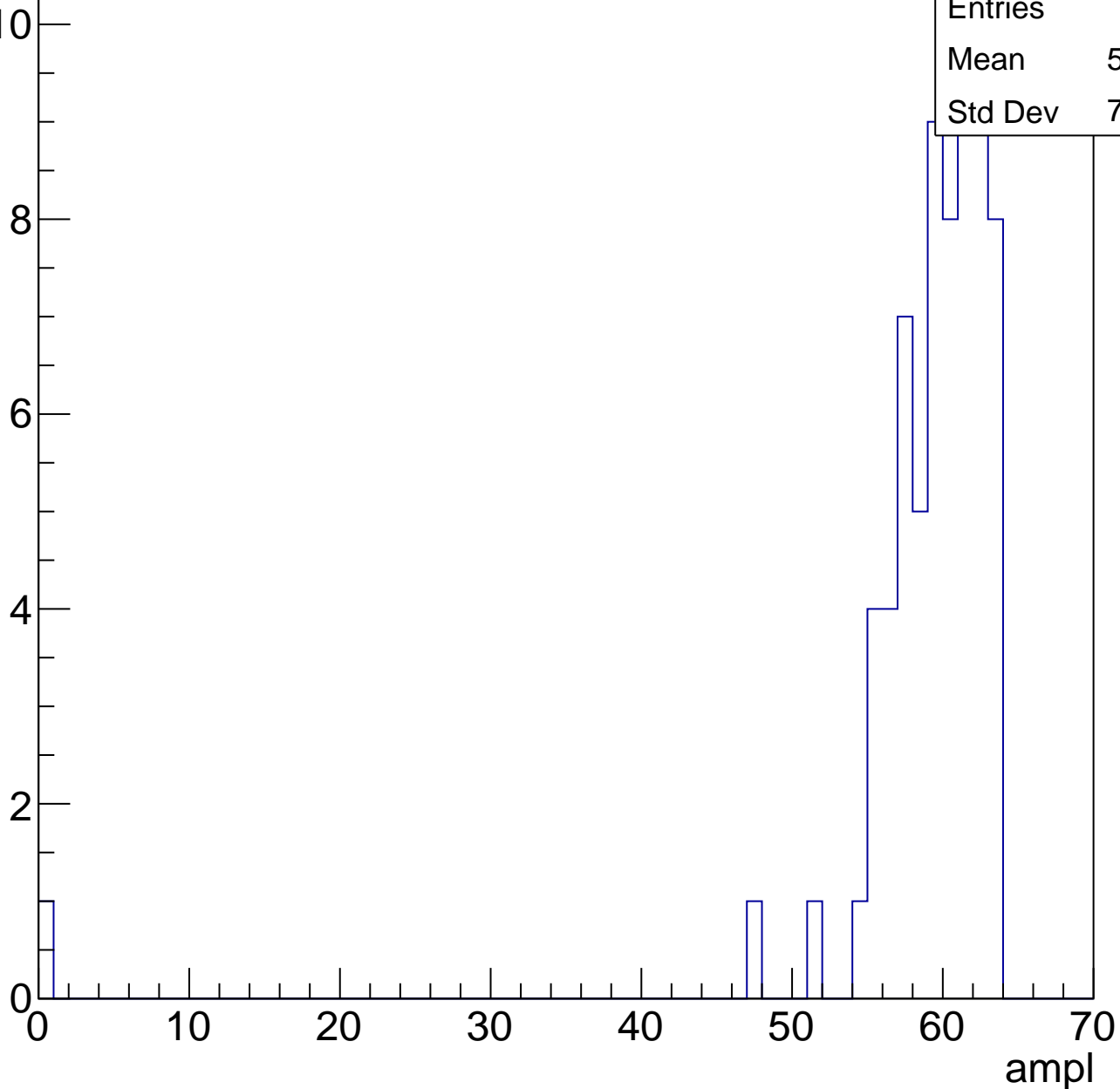


B1L103S, U17-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	58.37
Std Dev	7.748



B1L103S, U17-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.14
Std Dev	2.356

ampl

0 10 20 30 40 50 60 70

B1L103S, U17-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch98, adc0

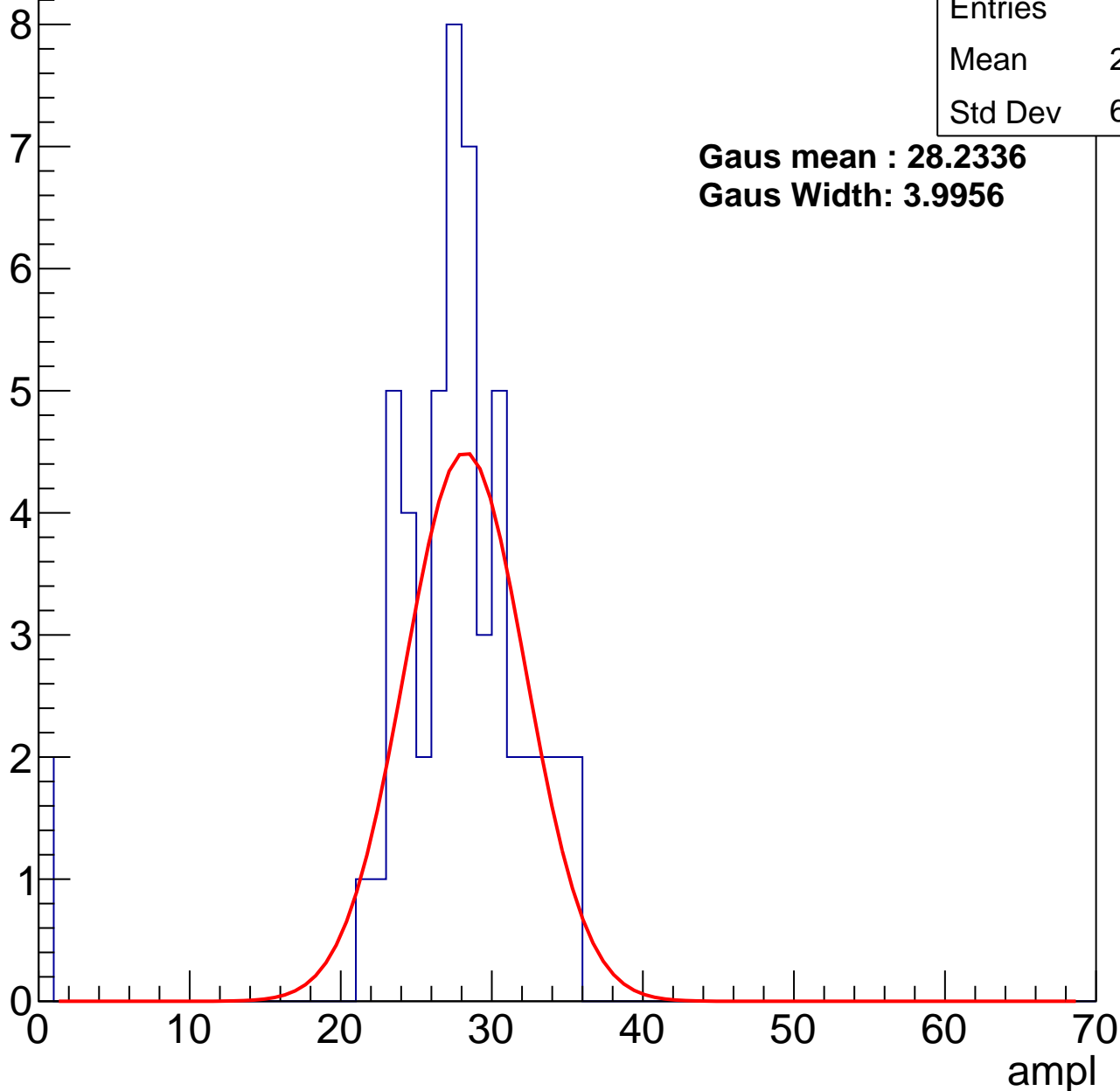
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	26.66
Std Dev	6.273

Gaus mean : 28.2336

Gaus Width: 3.9956



B1L103S, U17-ch98, adc1

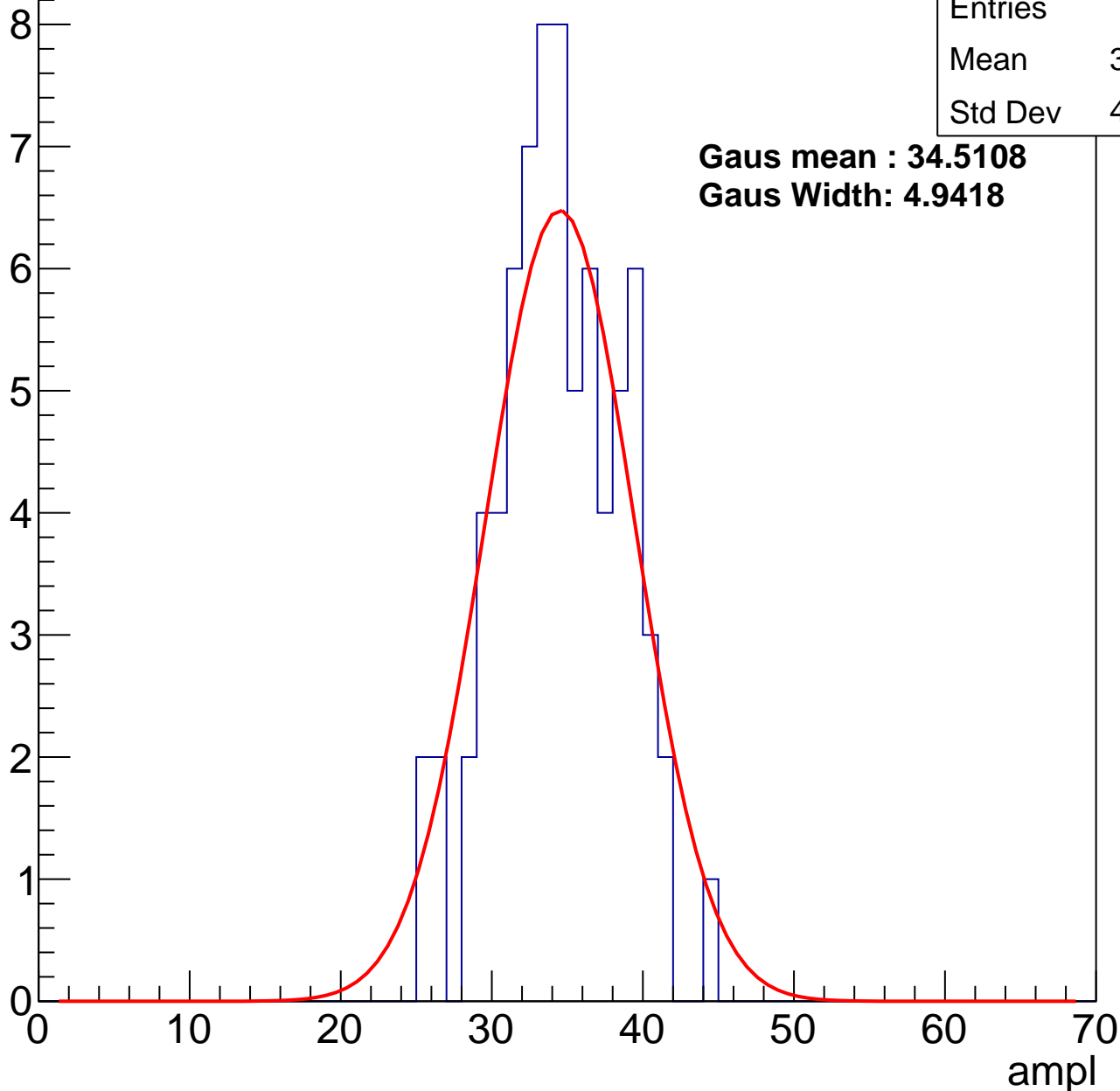
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	33.99
Std Dev	4.028

Gaus mean : 34.5108

Gaus Width: 4.9418



B1L103S, U17-ch98, adc2

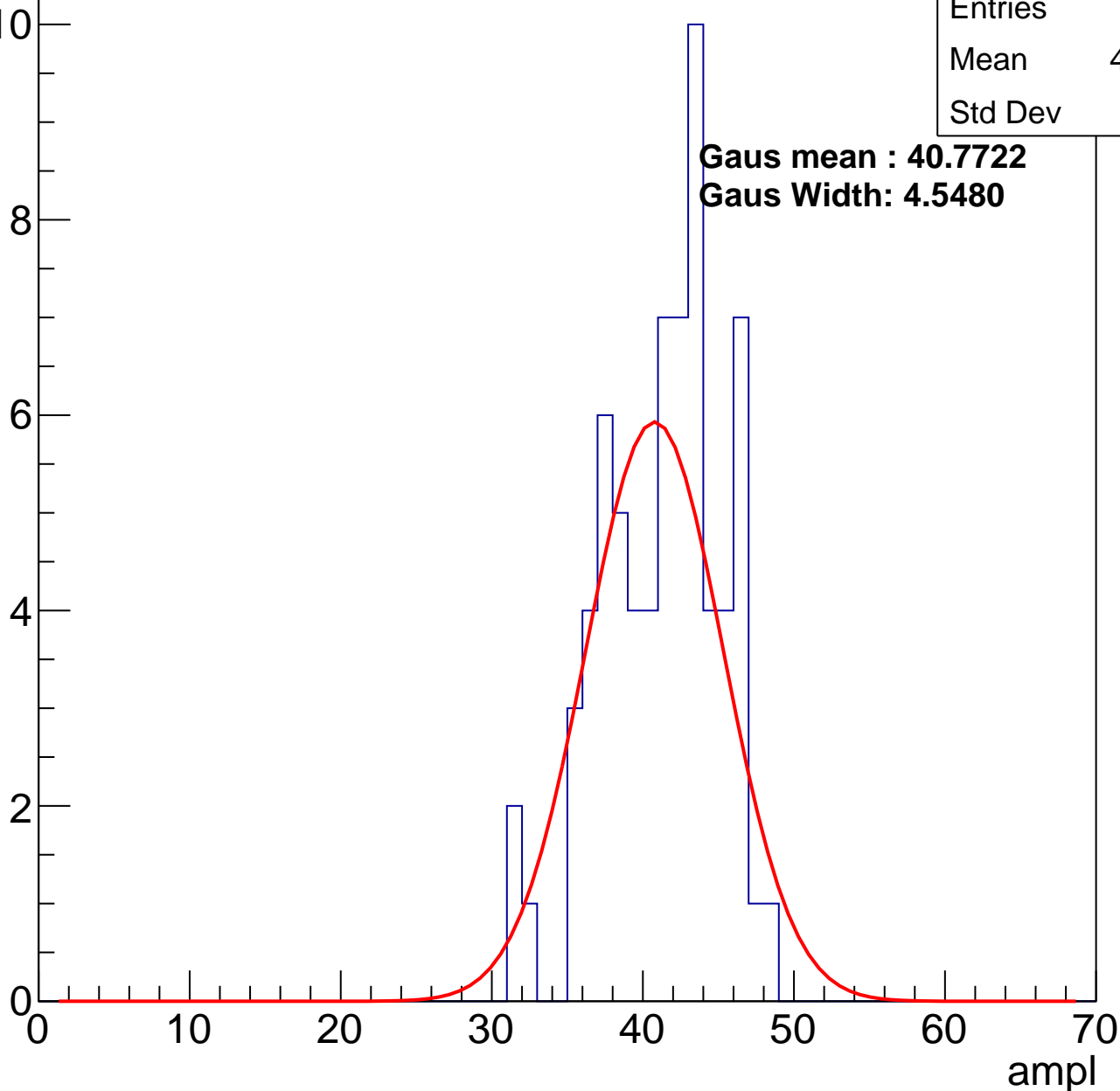
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	40.79
Std Dev	3.88

Gaus mean : 40.7722

Gaus Width: 4.5480



B1L103S, U17-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	47.77
Std Dev	3.854

Entry

10

8

6

4

2

0

0

10

20

30

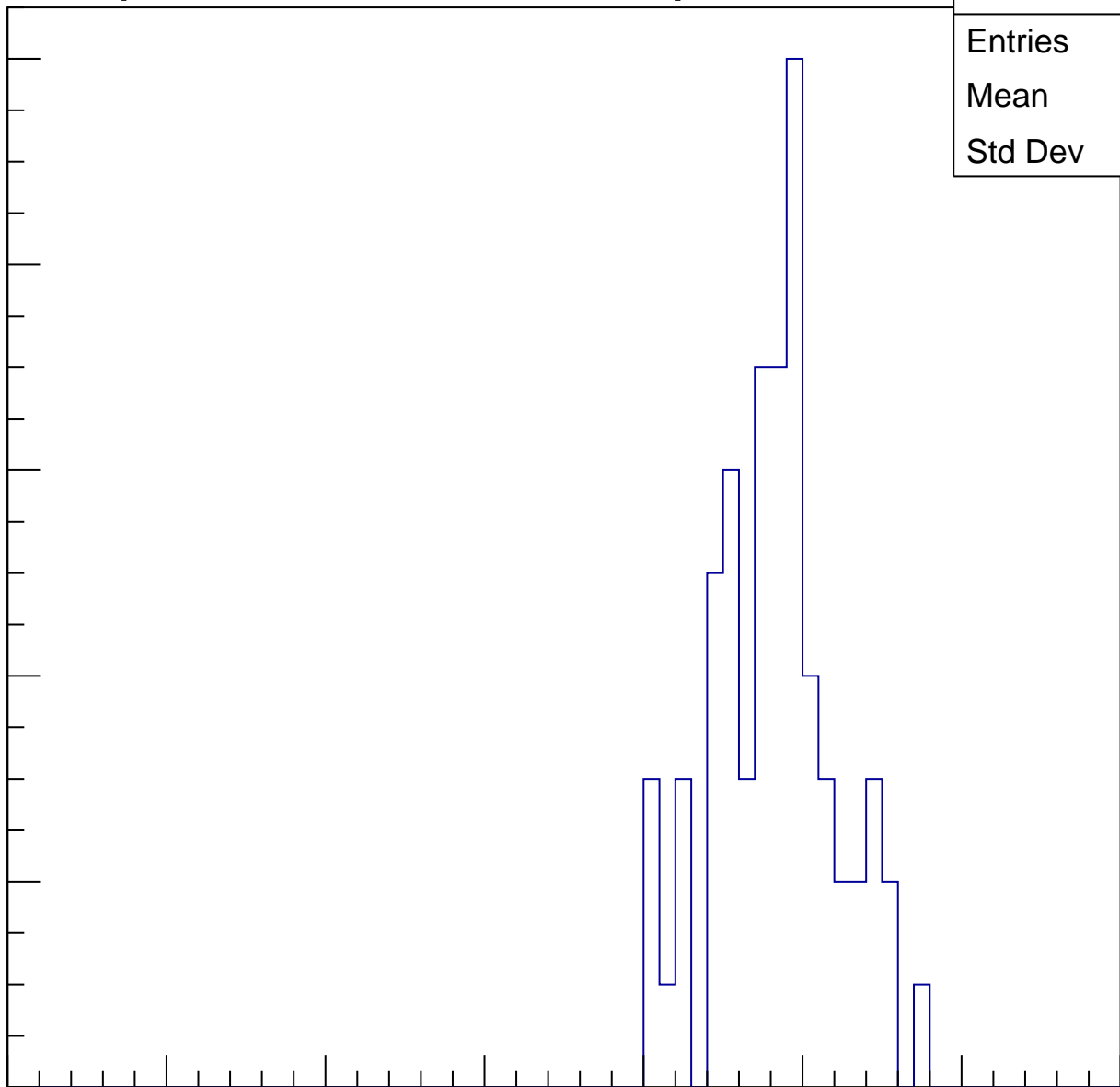
40

50

60

70

ampl

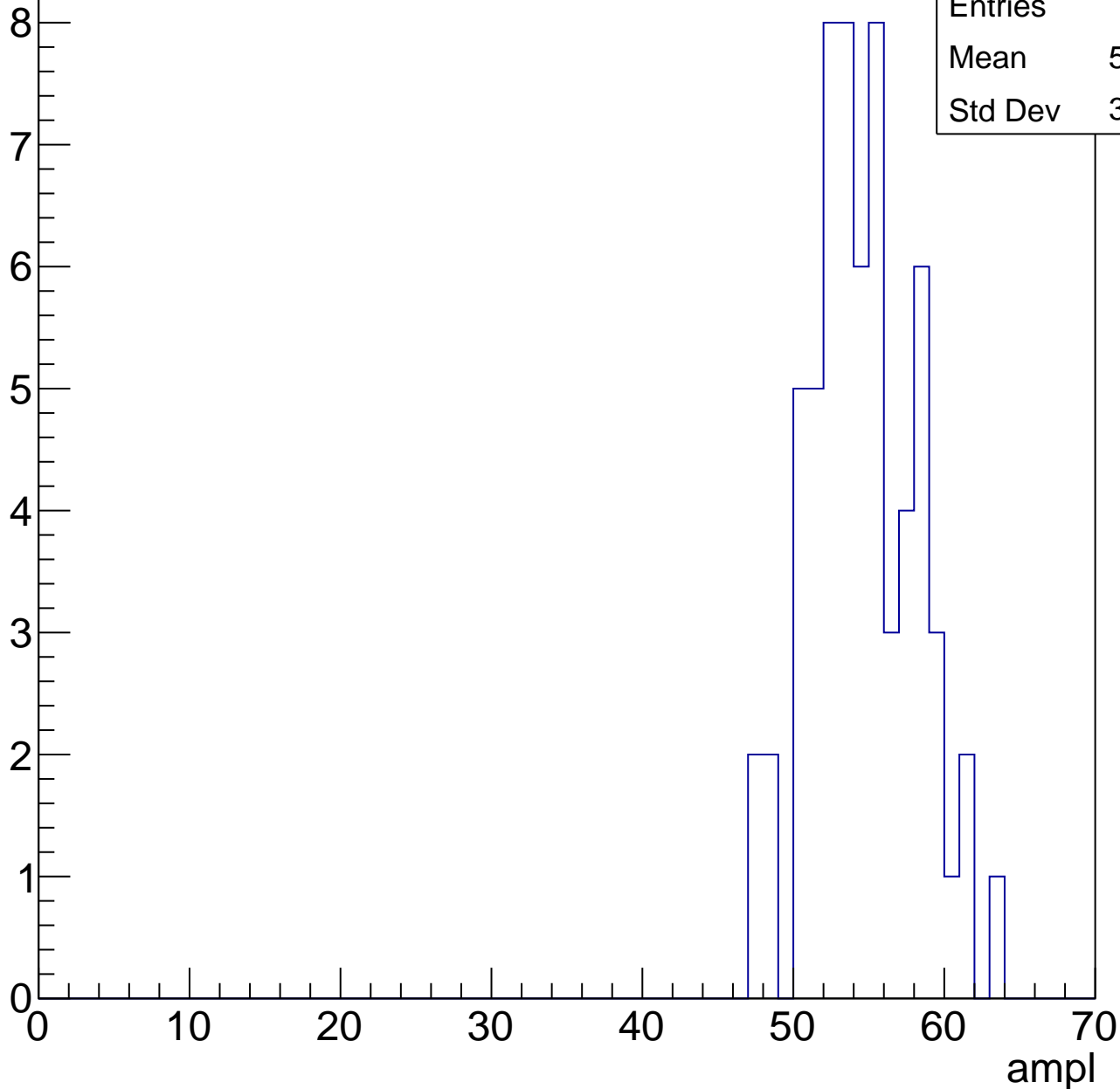


B1L103S, U17-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	54.14
Std Dev	3.482

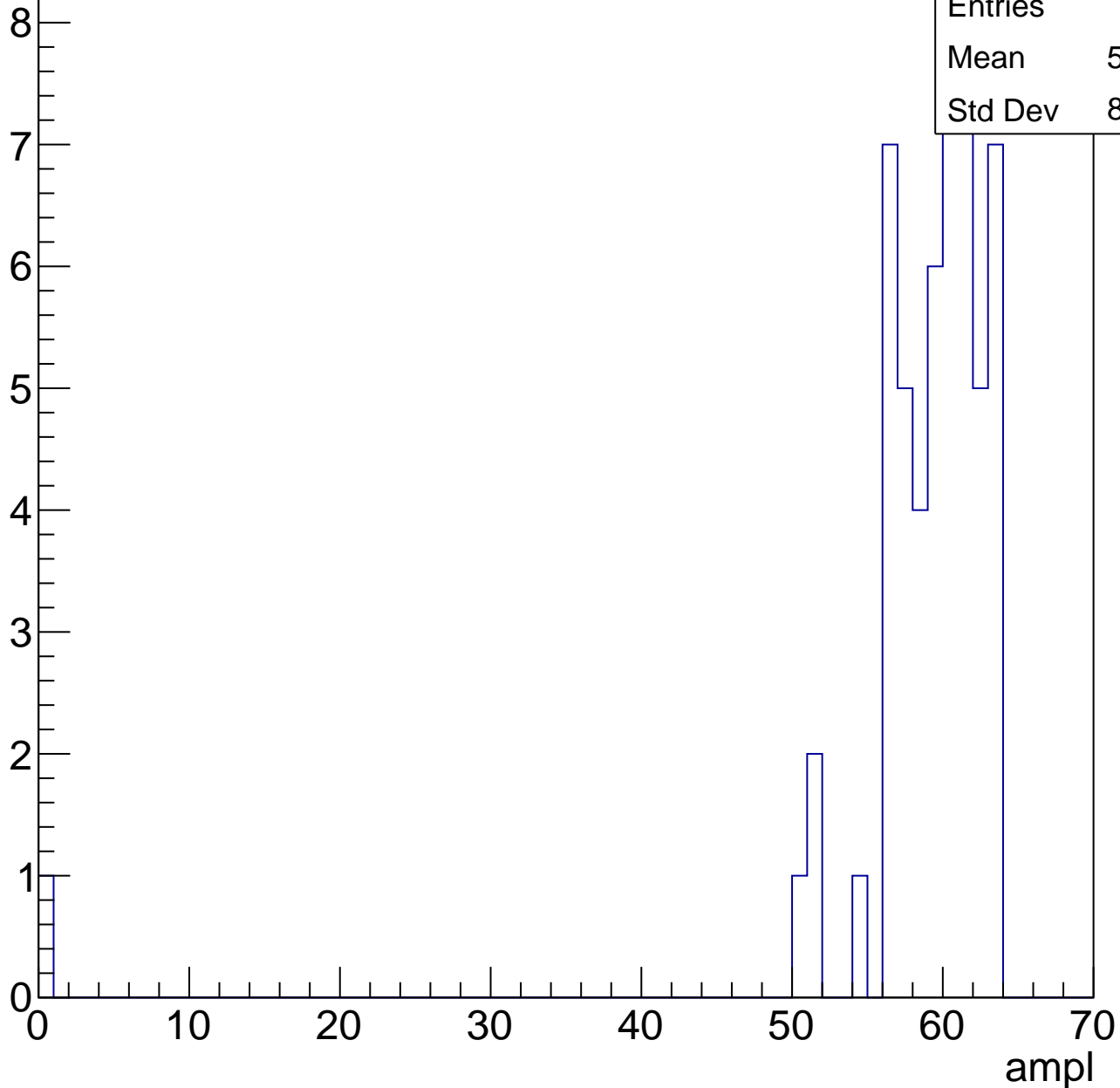


B1L103S, U17-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.96
Std Dev	8.464

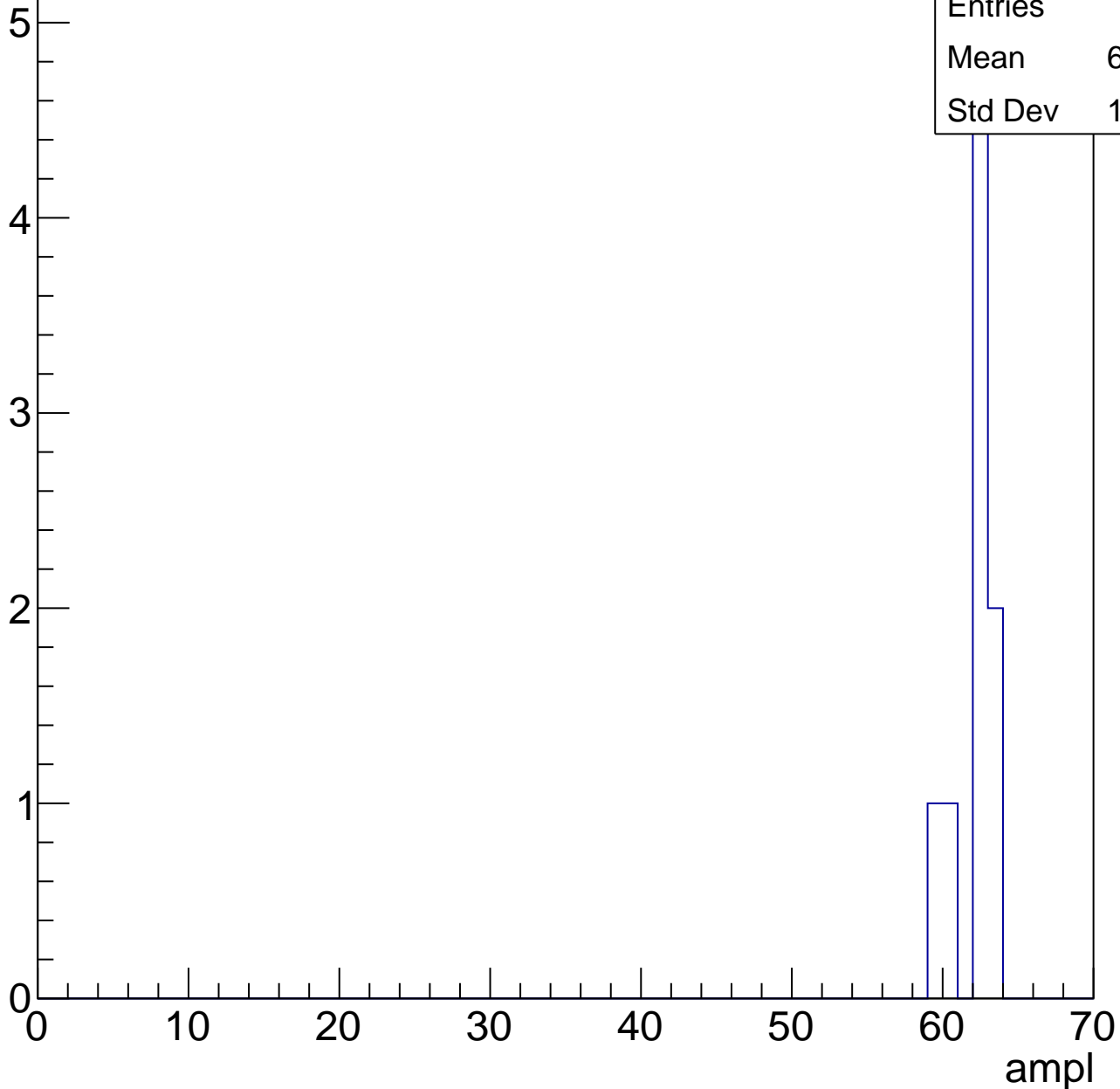


B1L103S, U17-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.67
Std Dev	1.247



B1L103S, U17-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch99, adc0

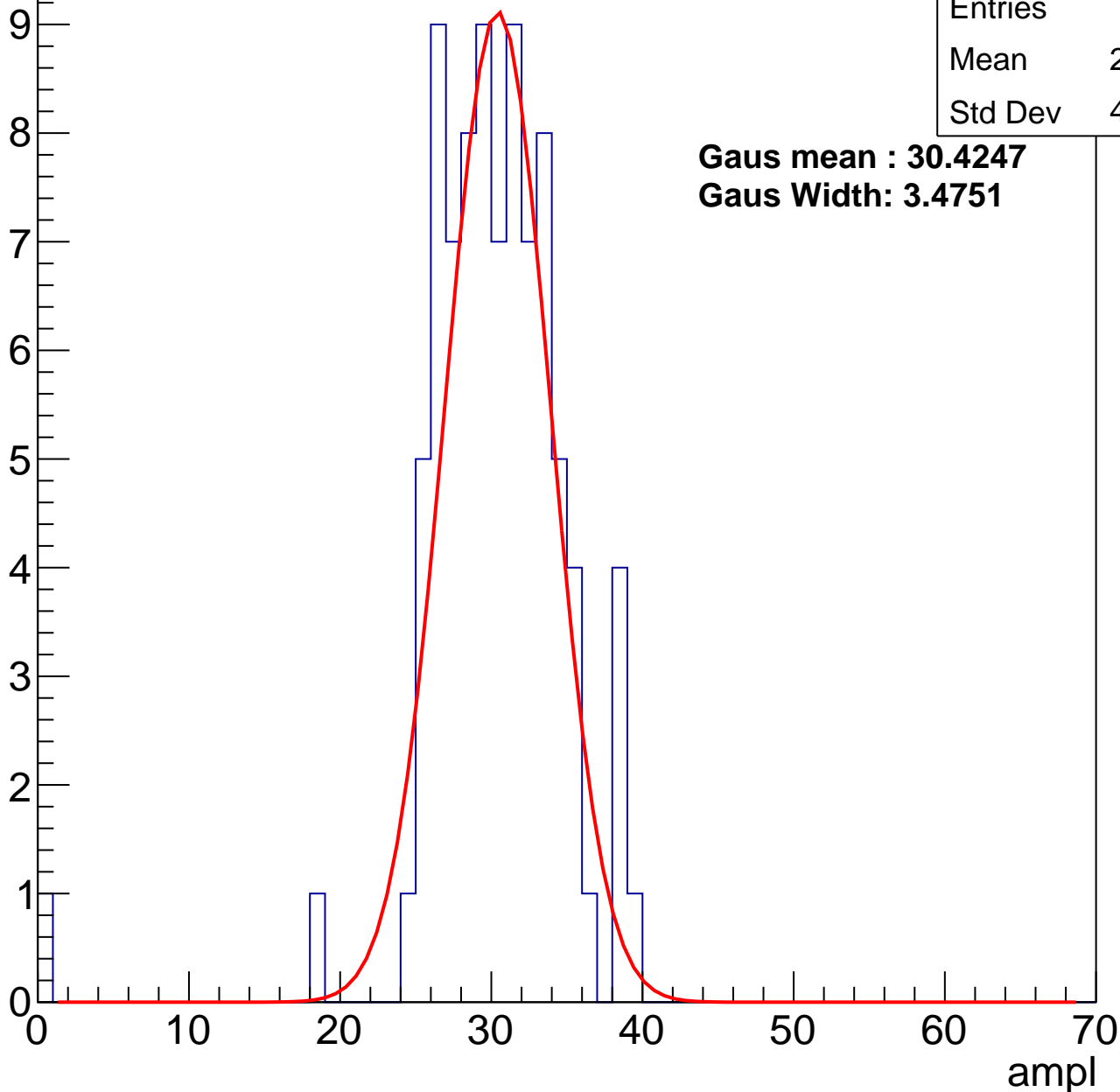
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	29.76
Std Dev	4.925

Gaus mean : 30.4247

Gaus Width: 3.4751



B1L103S, U17-ch99, adc1

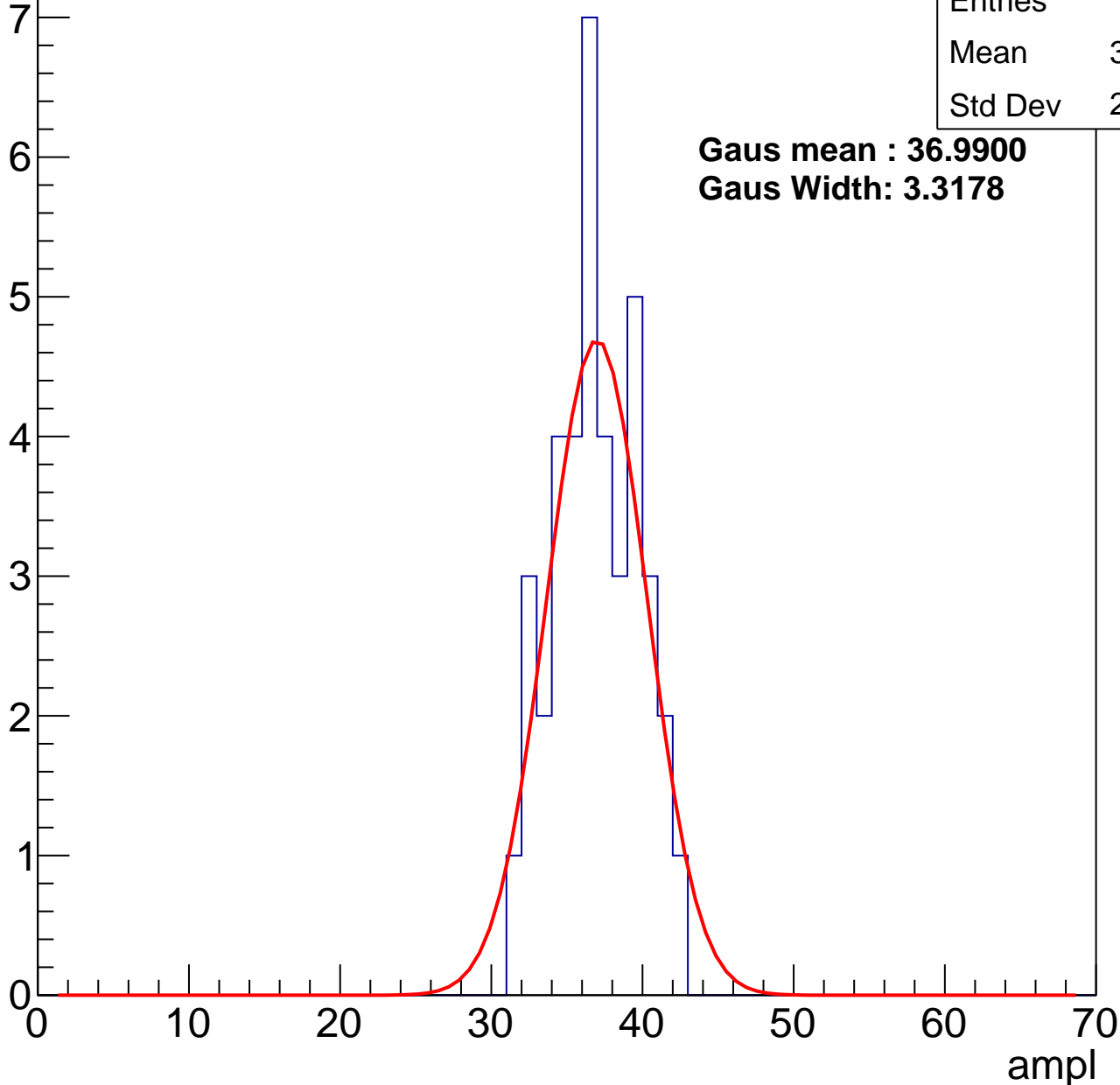
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	36.46
Std Dev	2.763

Gaus mean : 36.9900

Gaus Width: 3.3178



B1L103S, U17-ch99, adc2

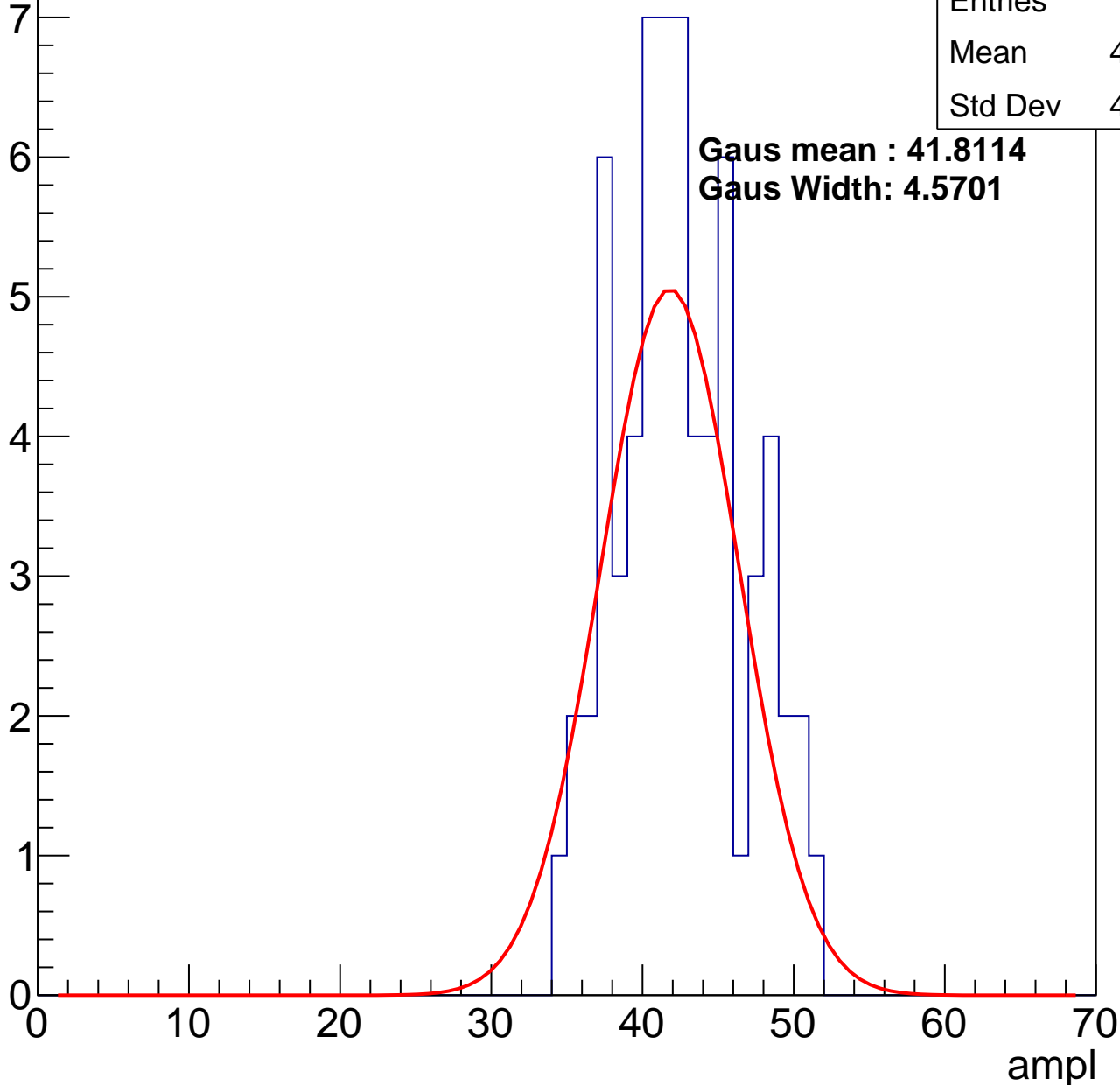
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.05
Std Dev	4.117

Gaus mean : 41.8114

Gaus Width: 4.5701

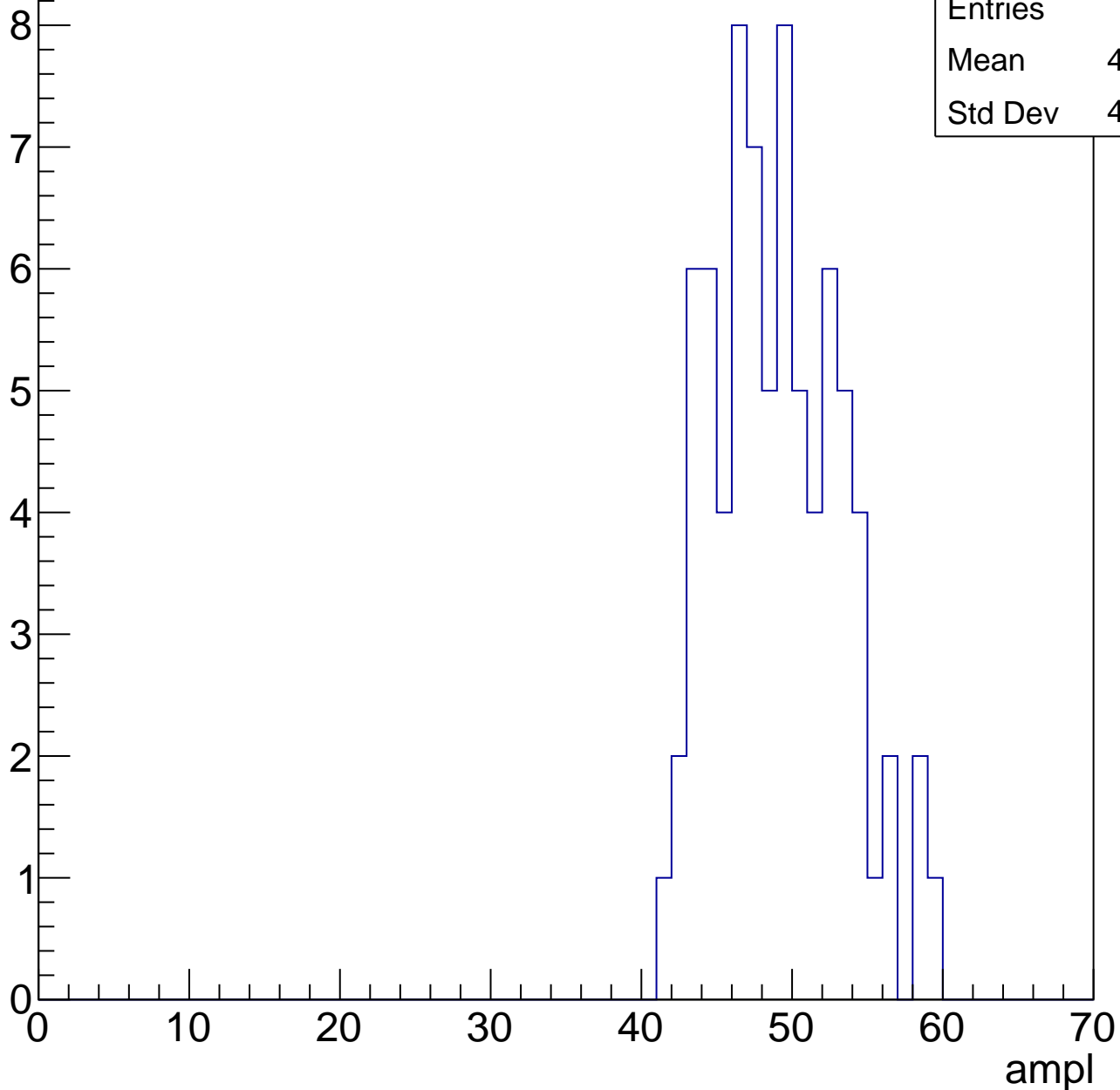


B1L103S, U17-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	48.64
Std Dev	4.168

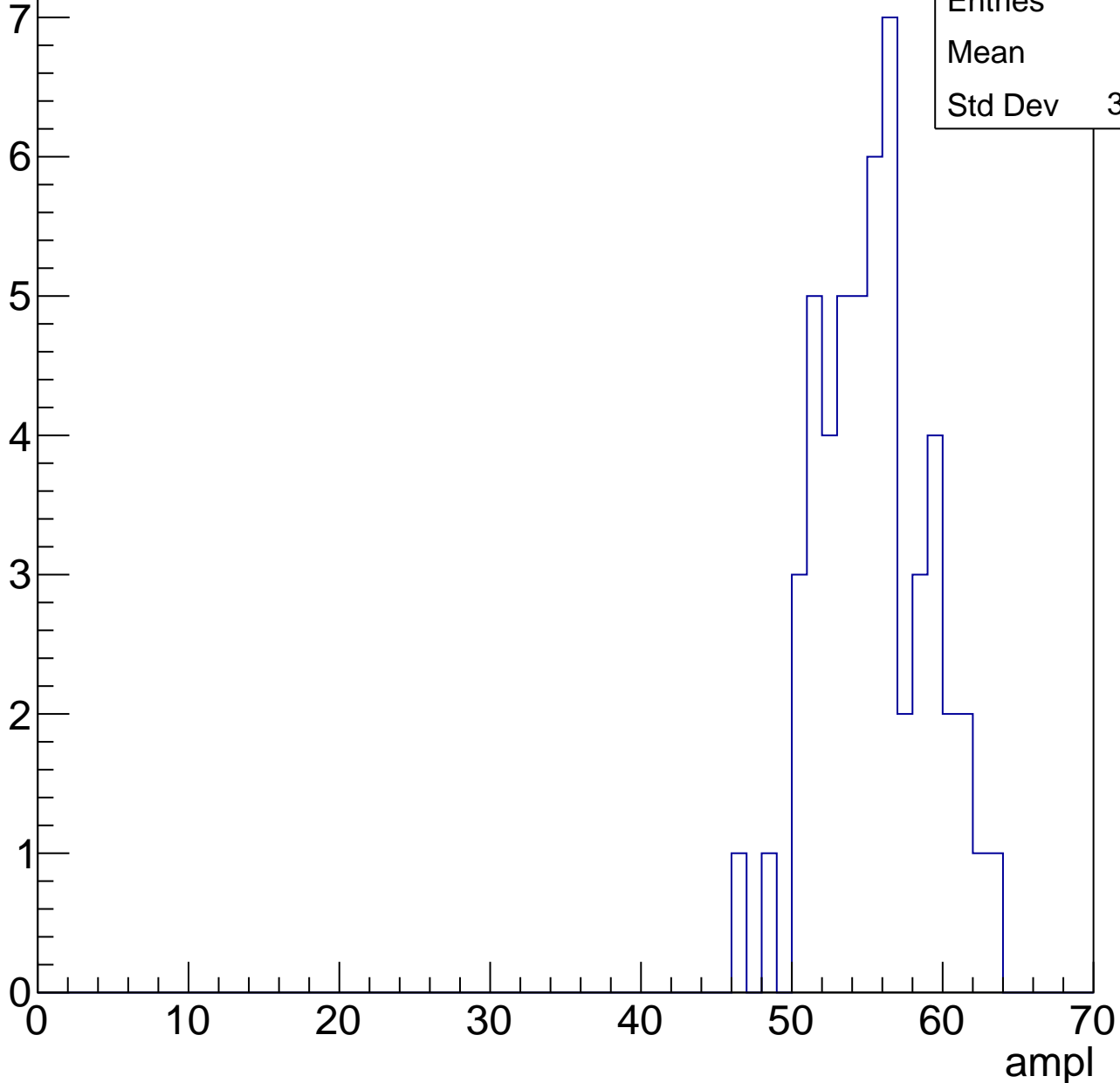


B1L103S, U17-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.9
Std Dev	3.633



B1L103S, U17-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.63
Std Dev	2.449

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L103S, U17-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

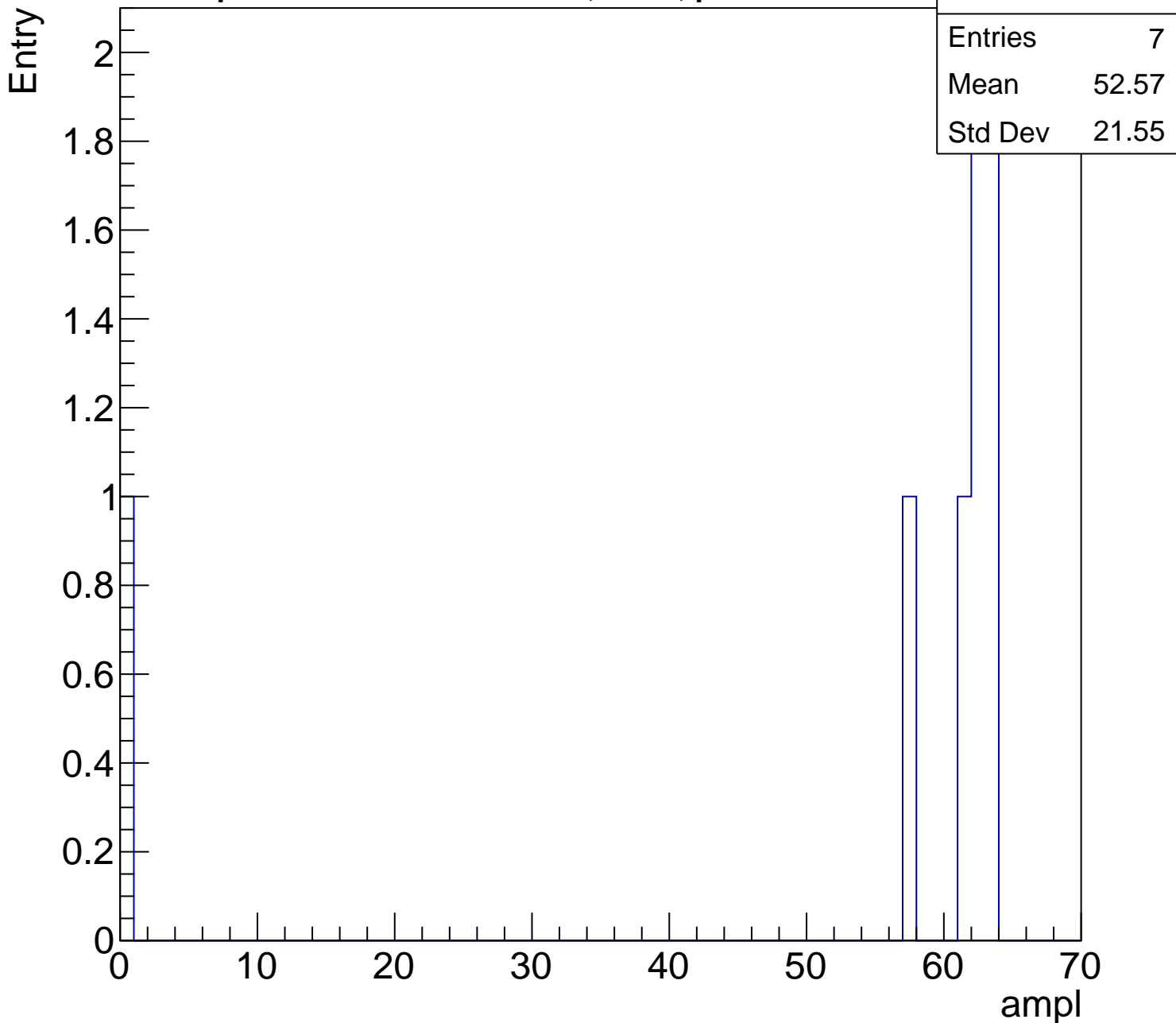
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.57
Std Dev	21.55

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch100, adc0

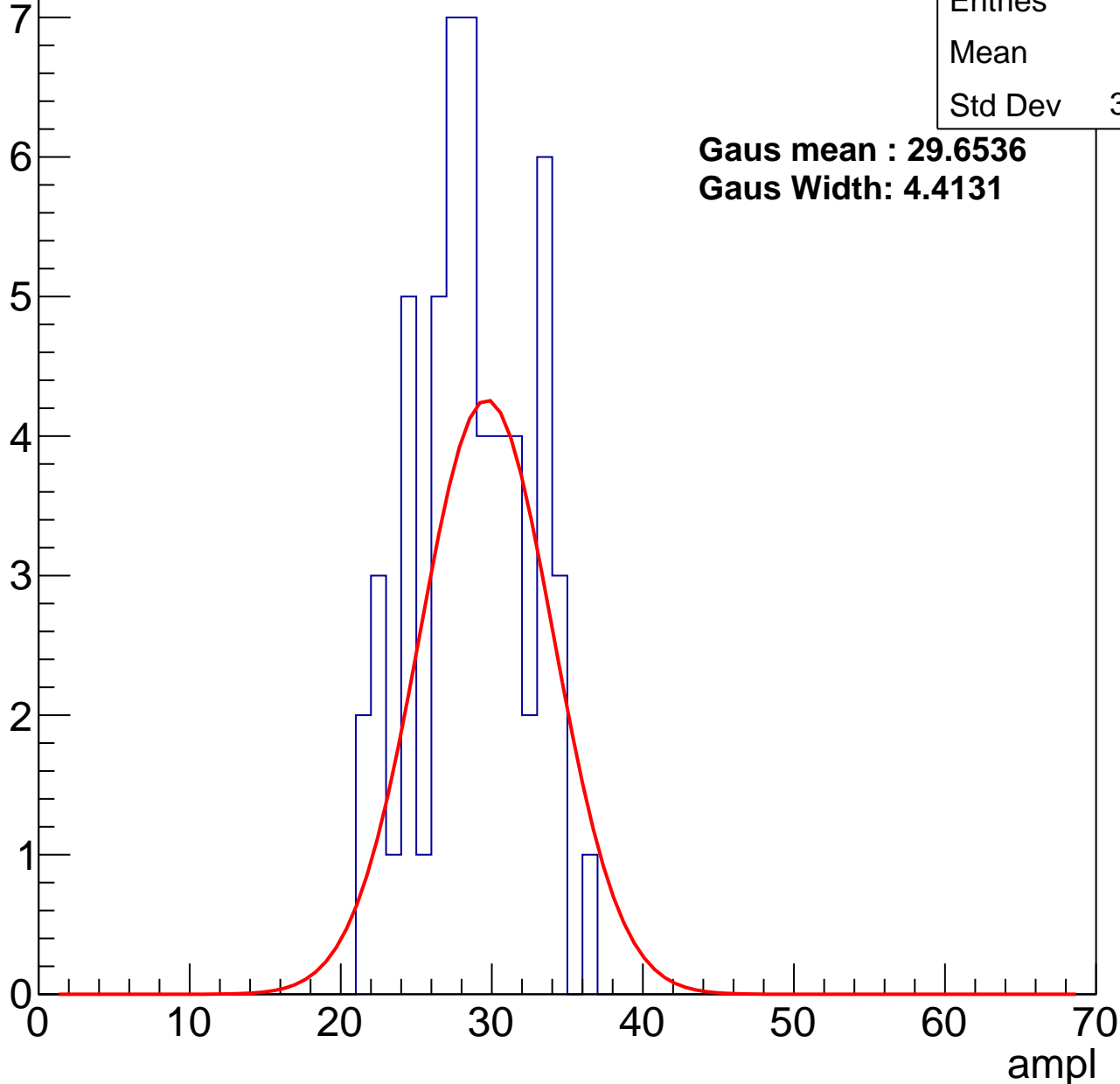
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	28.2
Std Dev	3.705

Gaus mean : 29.6536

Gaus Width: 4.4131



B1L103S, U17-ch100, adc1

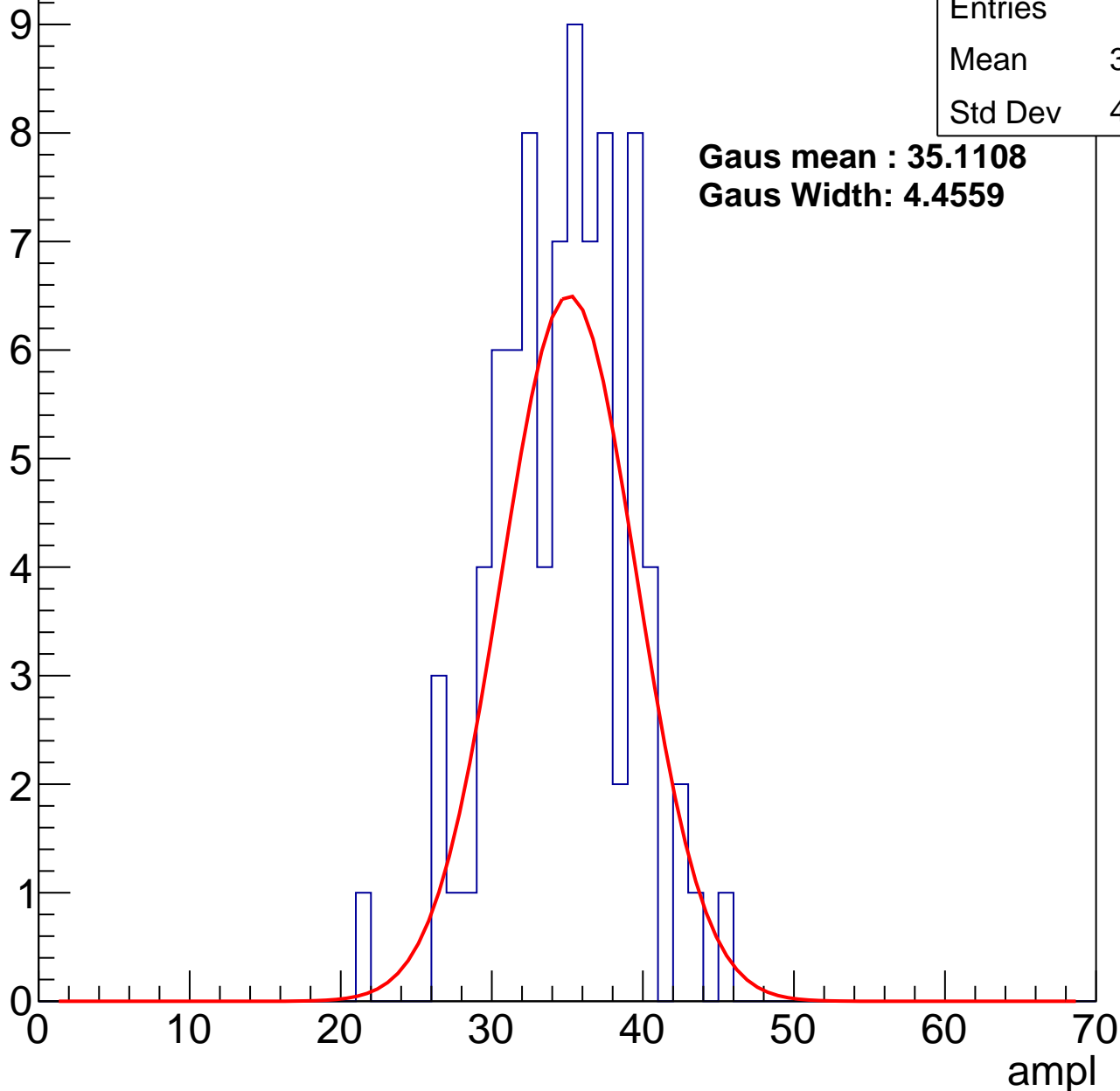
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	34.28
Std Dev	4.308

Gaus mean : 35.1108

Gaus Width: 4.4559



B1L103S, U17-ch100, adc2

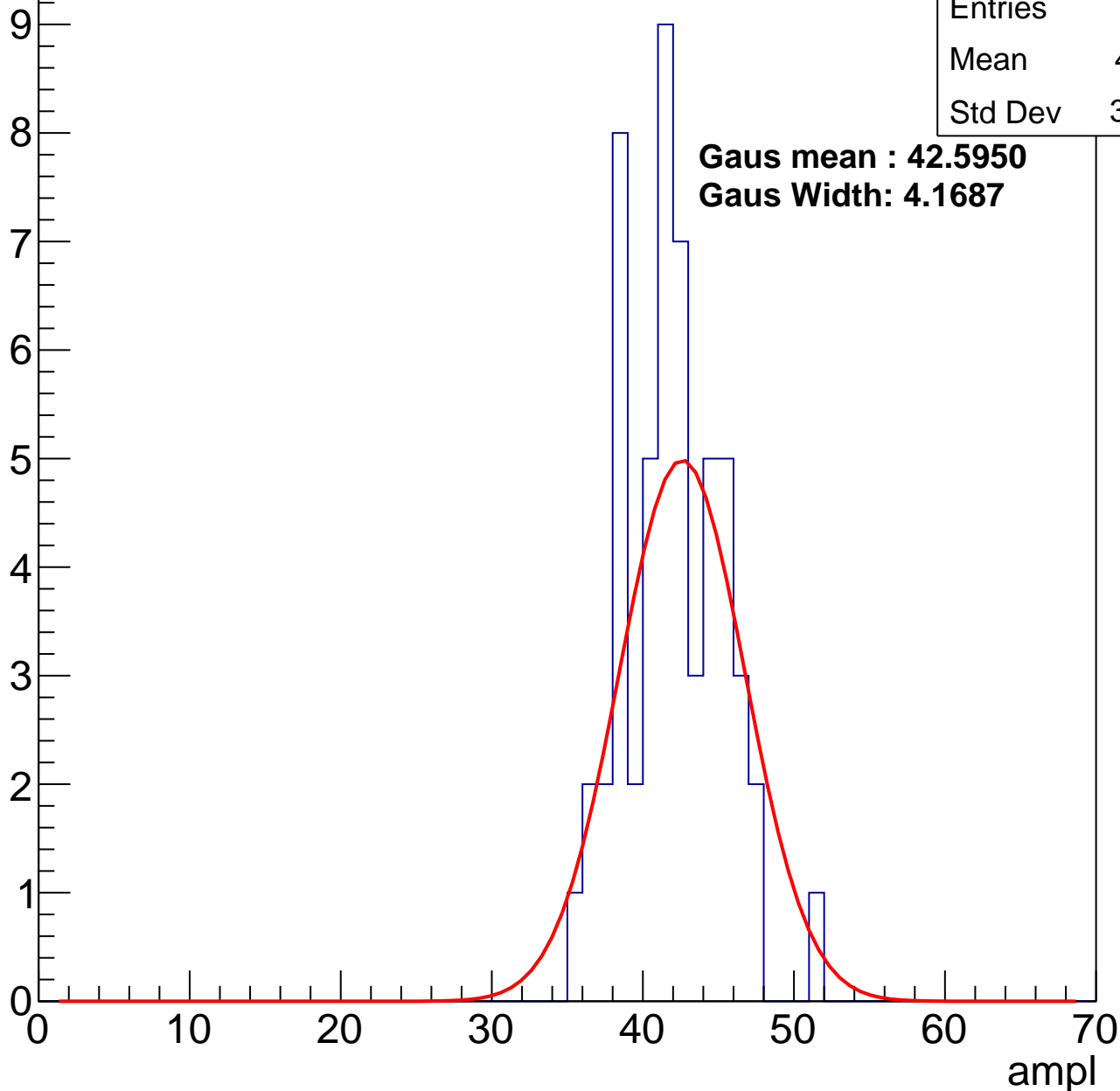
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.51
Std Dev	3.247

Gaus mean : 42.5950

Gaus Width: 4.1687

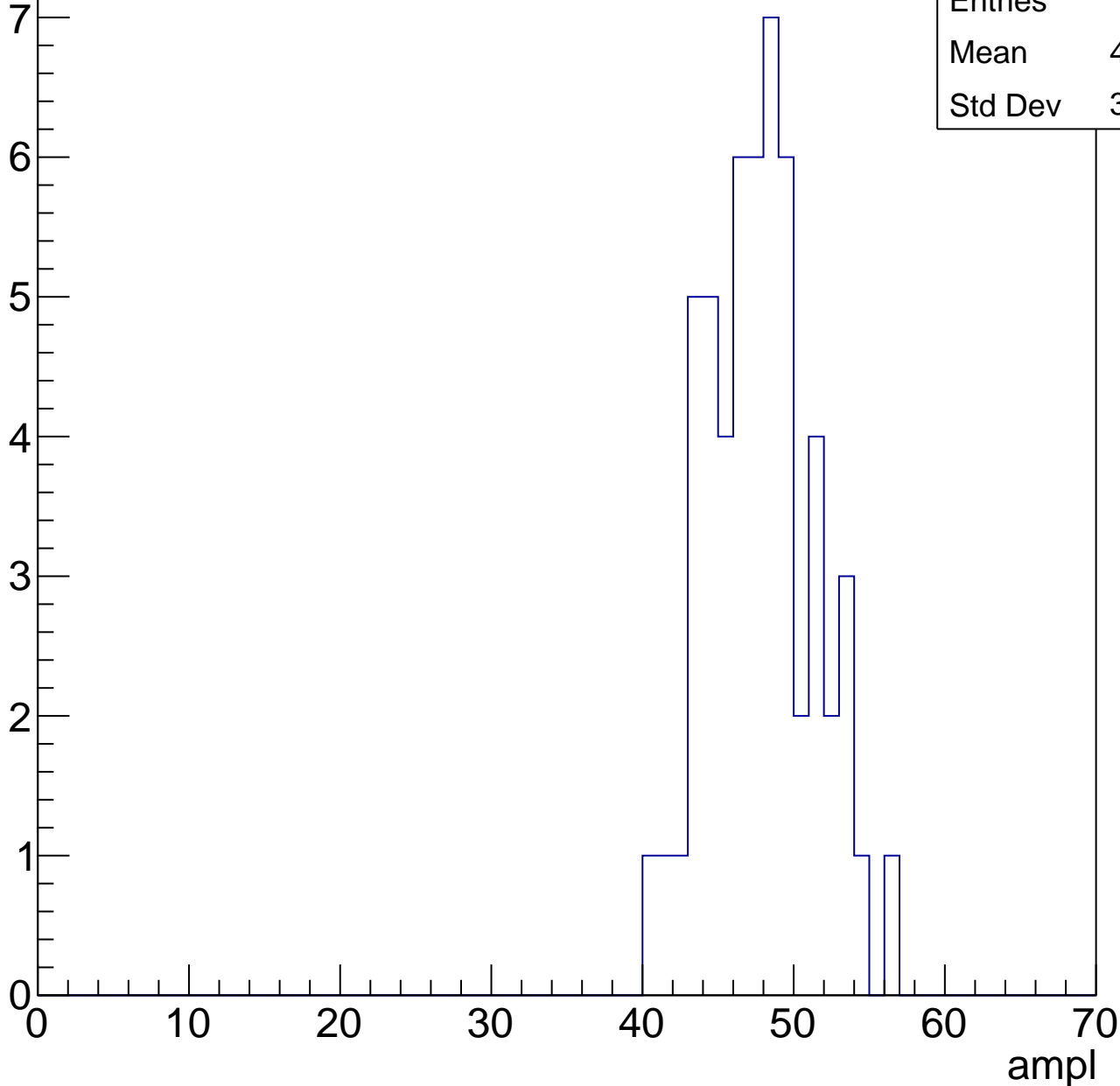


B1L103S, U17-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	47.33
Std Dev	3.443

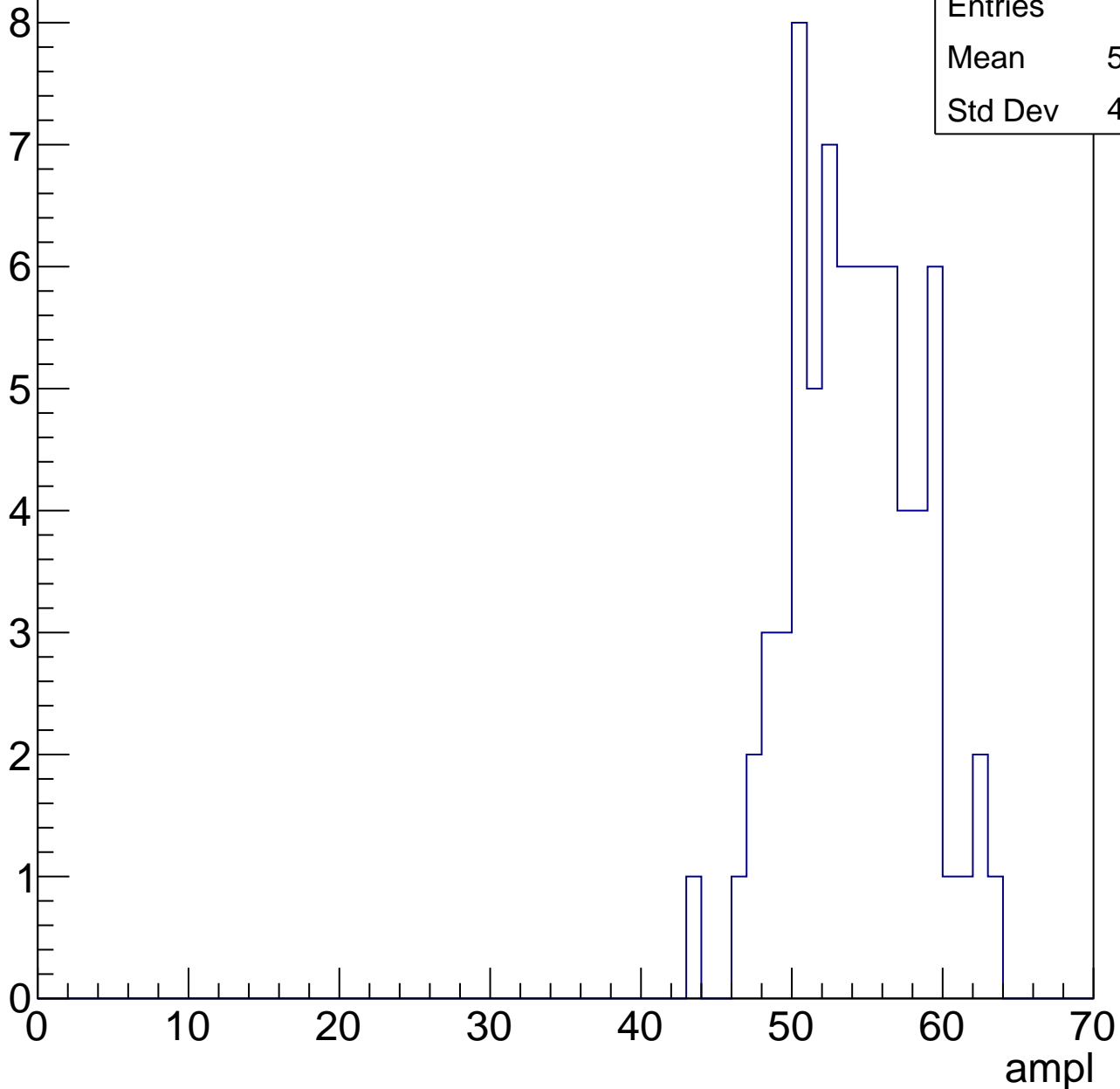


B1L103S, U17-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

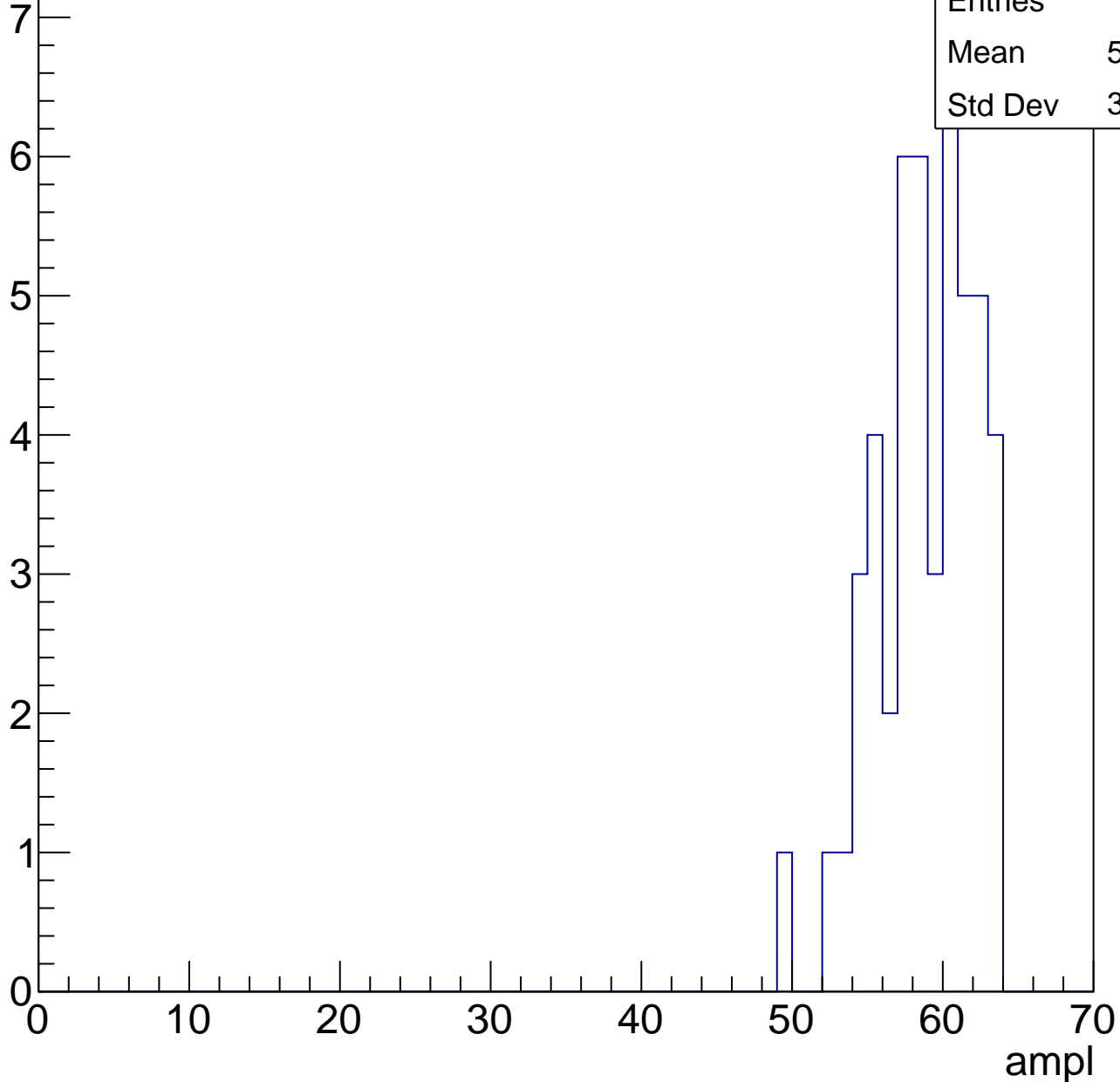
Entries	73
Mean	53.74
Std Dev	4.125



B1L103S, U17-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

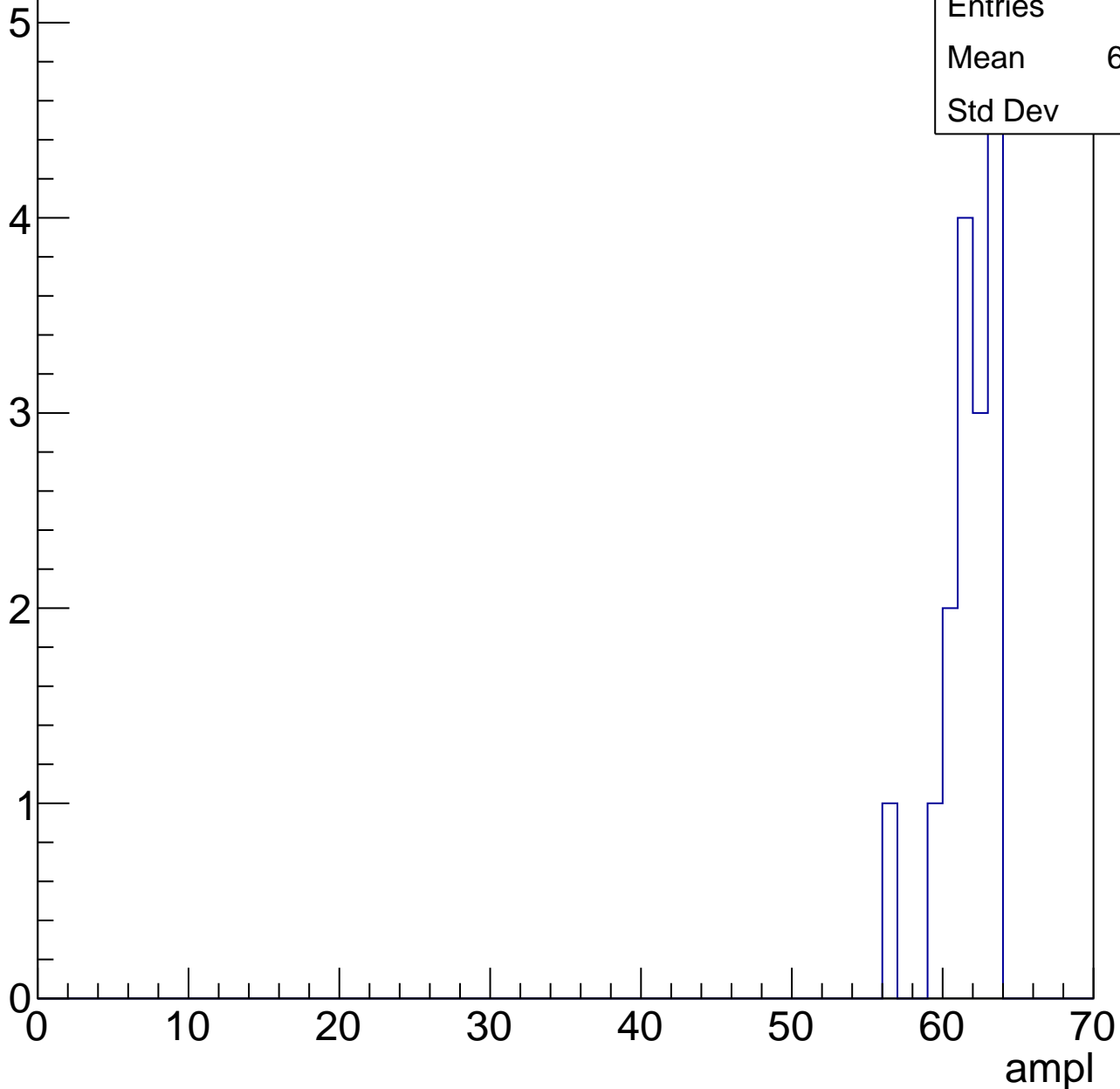


B1L103S, U17-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.25
Std Dev	1.82



B1L103S, U17-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch101, adc0

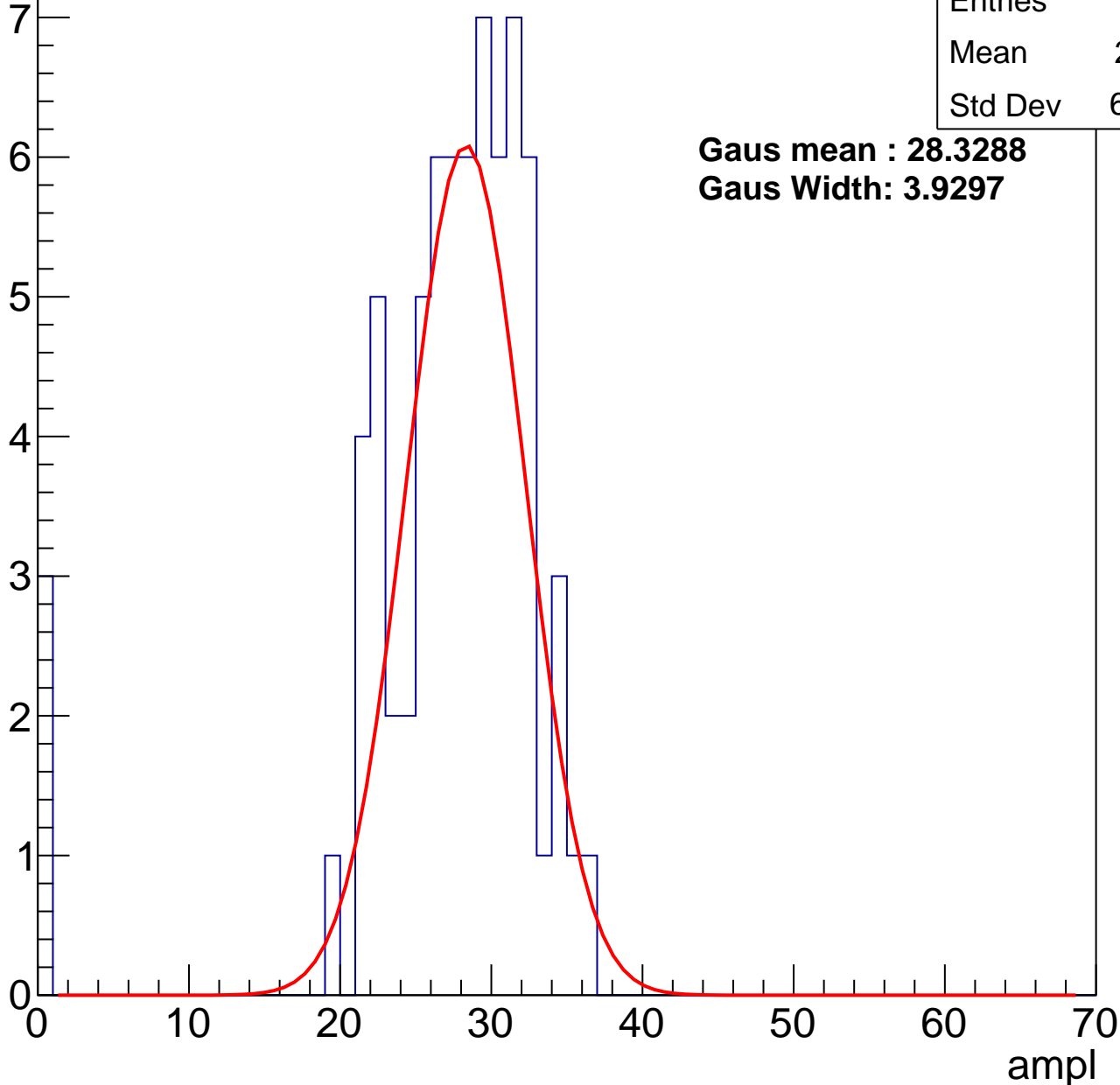
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	26.61
Std Dev	6.732

Gaus mean : 28.3288

Gaus Width: 3.9297



B1L103S, U17-ch101, adc1

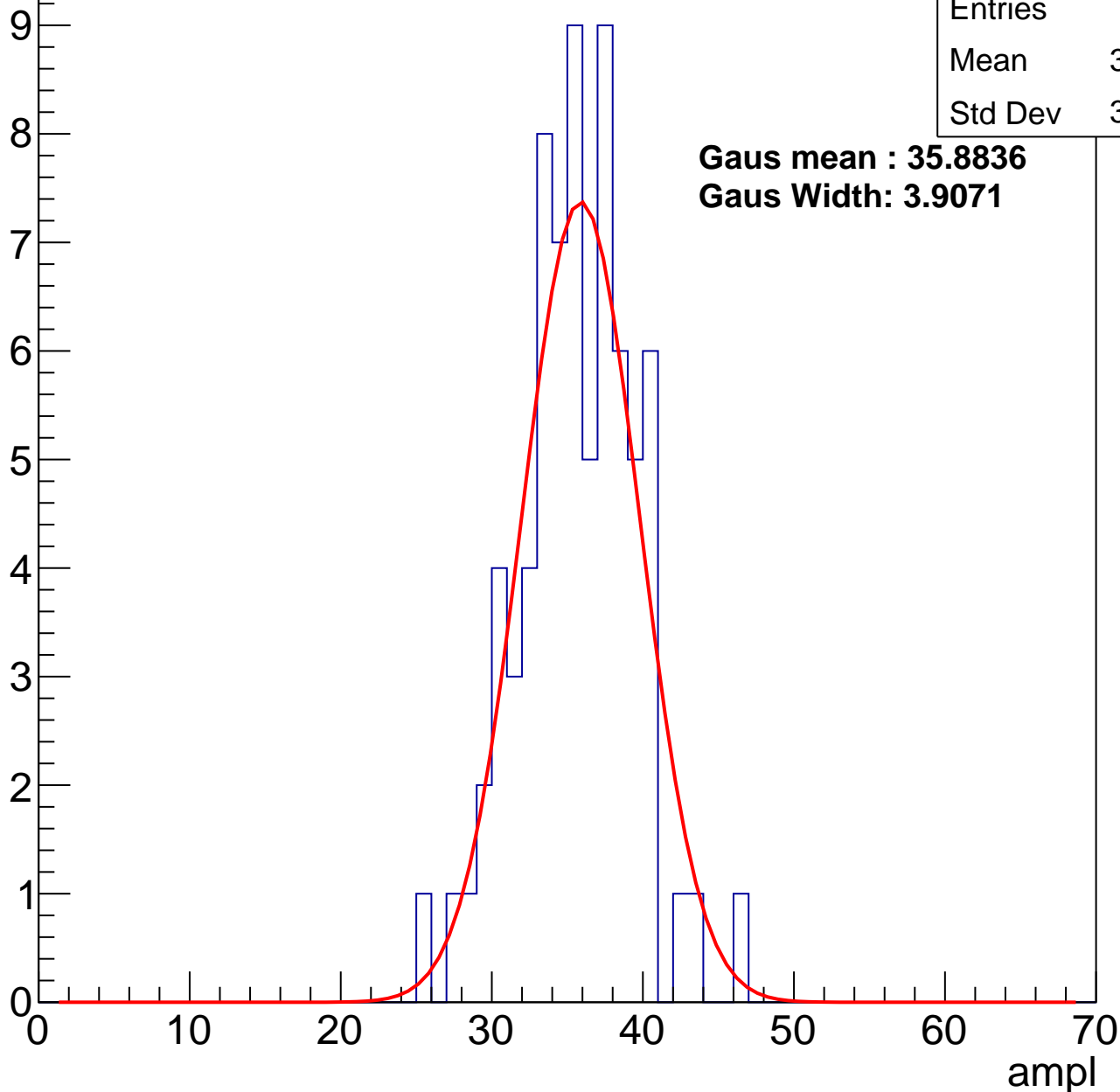
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.18
Std Dev	3.793

Gaus mean : 35.8836

Gaus Width: 3.9071



B1L103S, U17-ch101, adc2

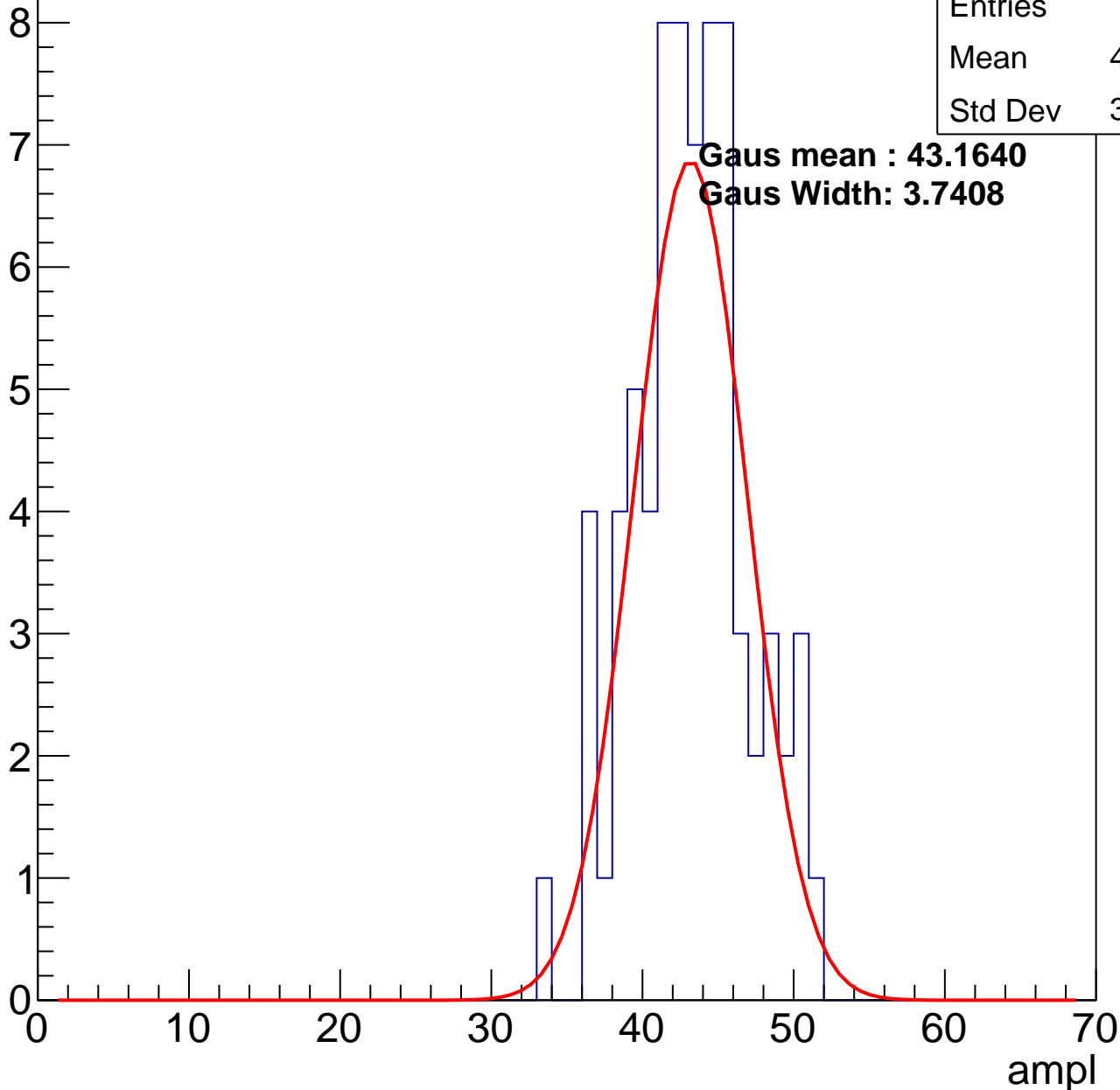
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.68
Std Dev	3.796

Gaus mean : 43.1640

Gaus Width: 3.7408

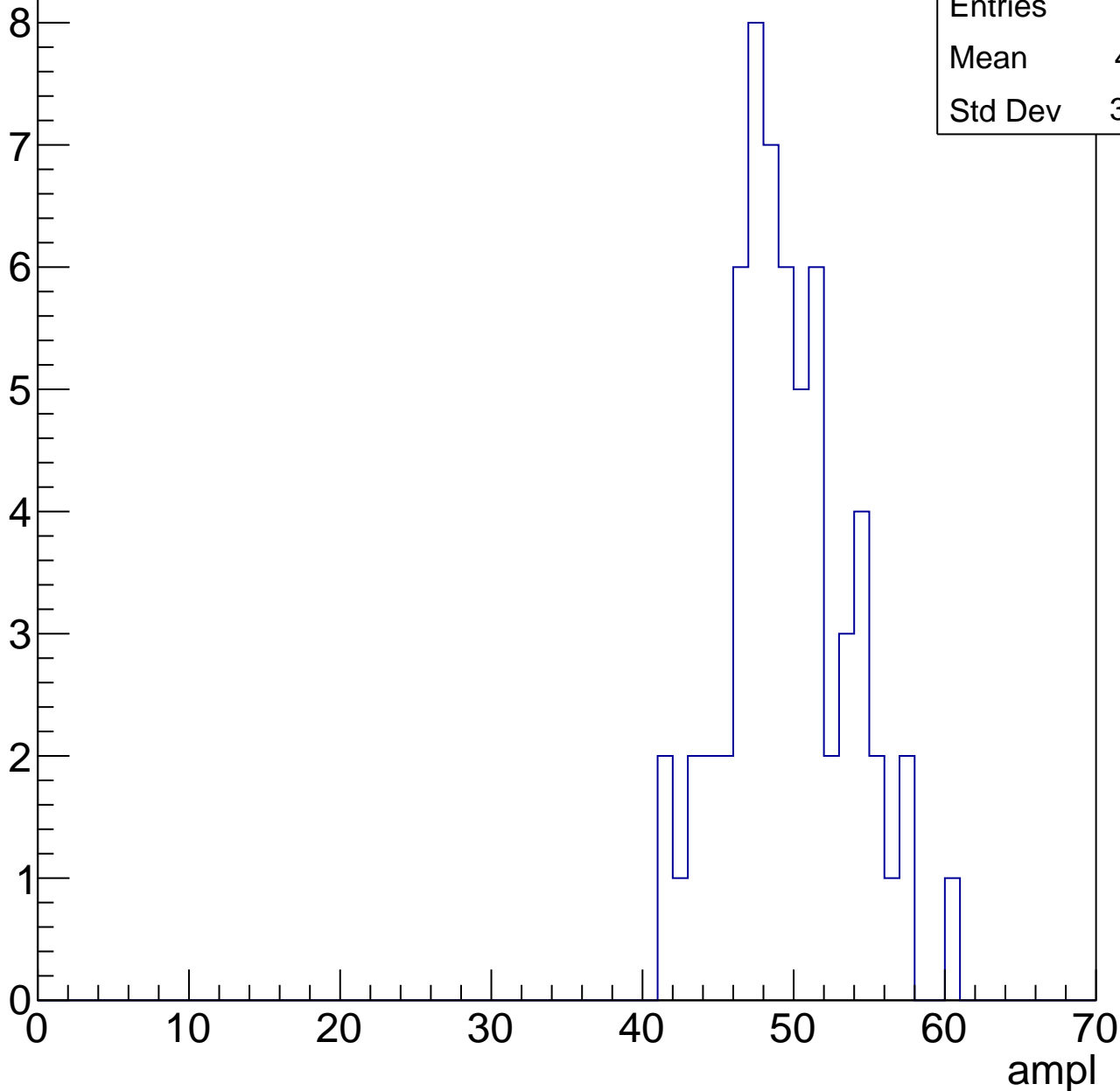


B1L103S, U17-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.11
Std Dev	3.992

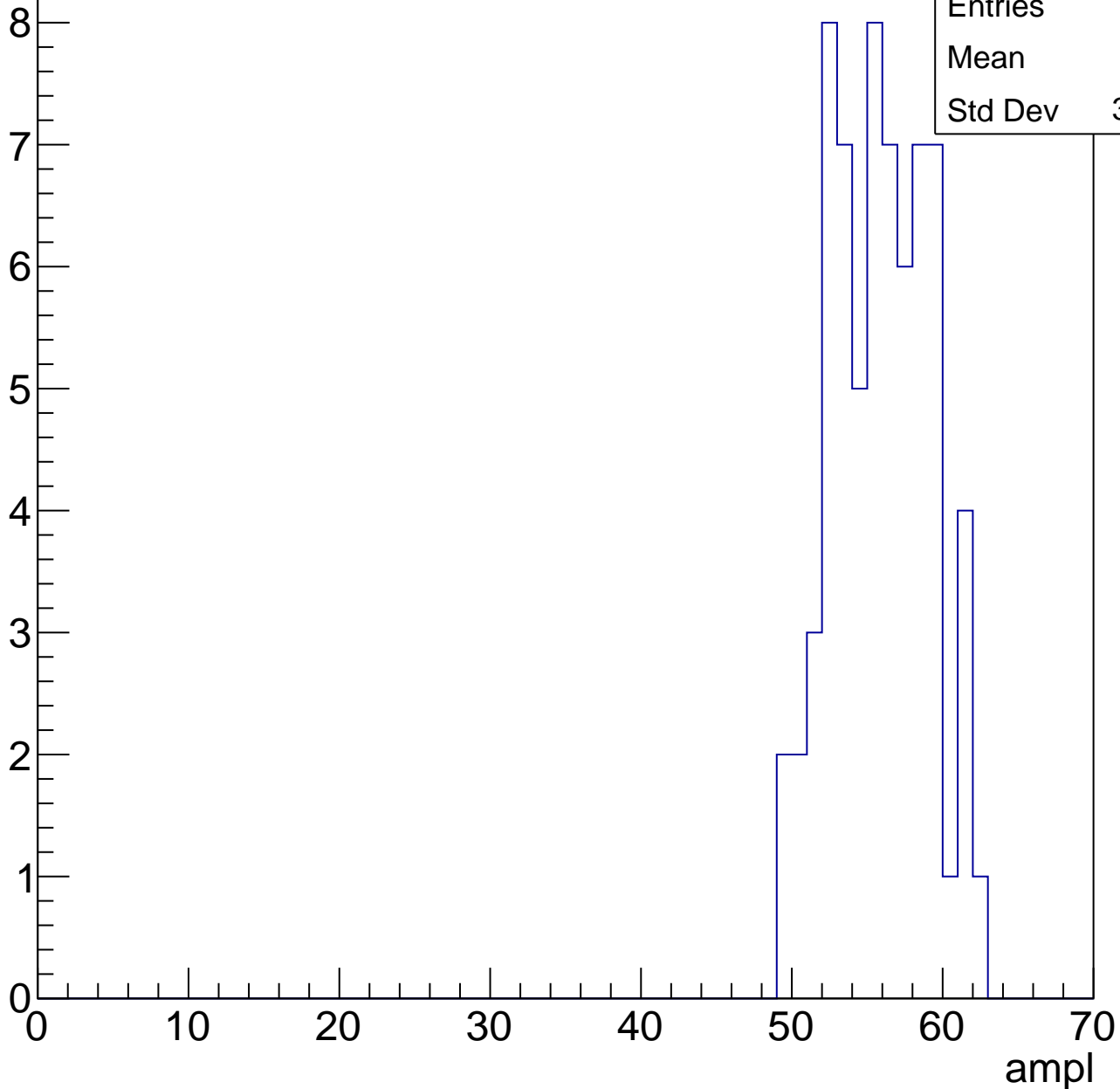


B1L103S, U17-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.4
Std Dev	3.181

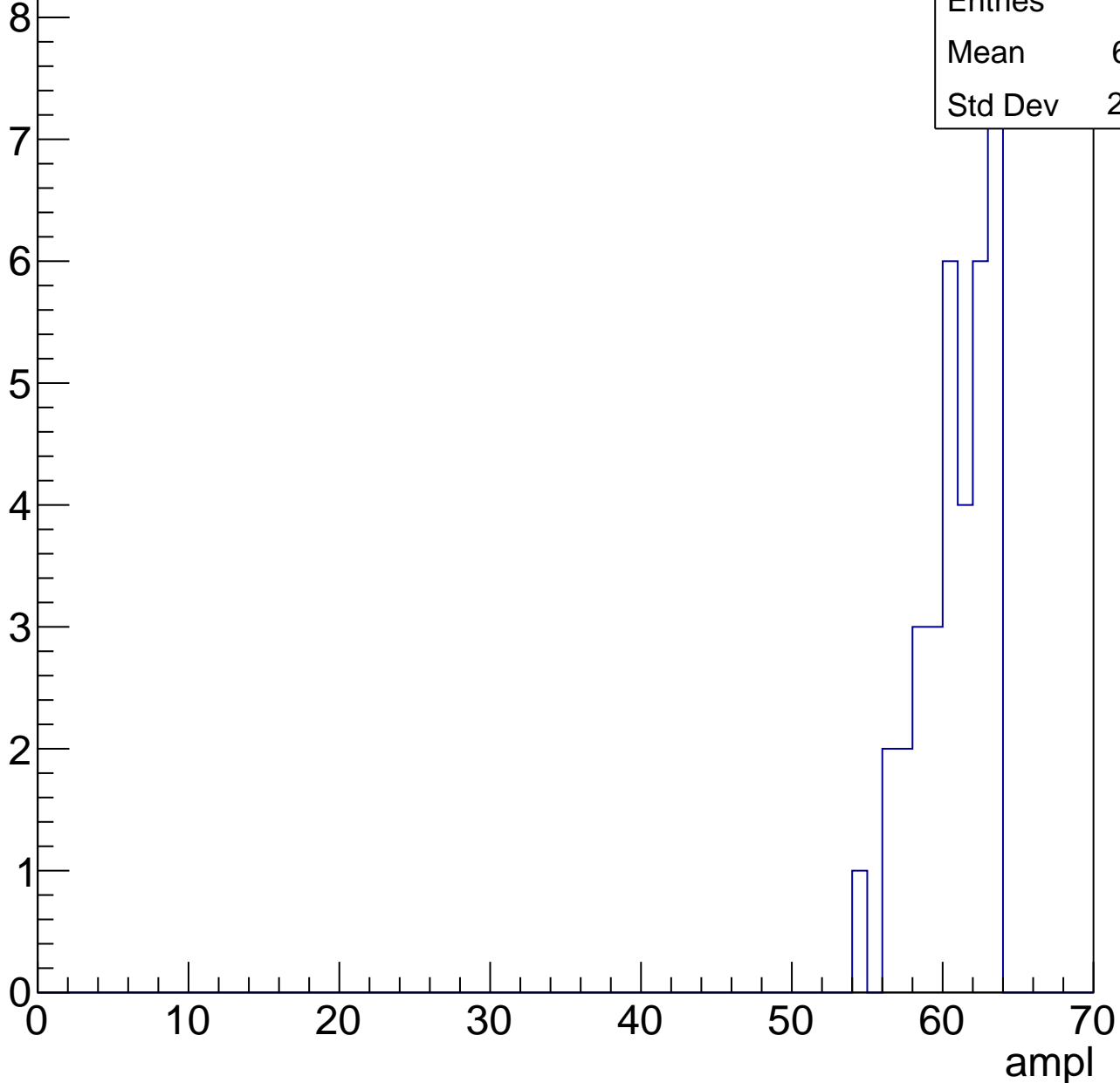


B1L103S, U17-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	60.31
Std Dev	2.376



B1L103S, U17-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

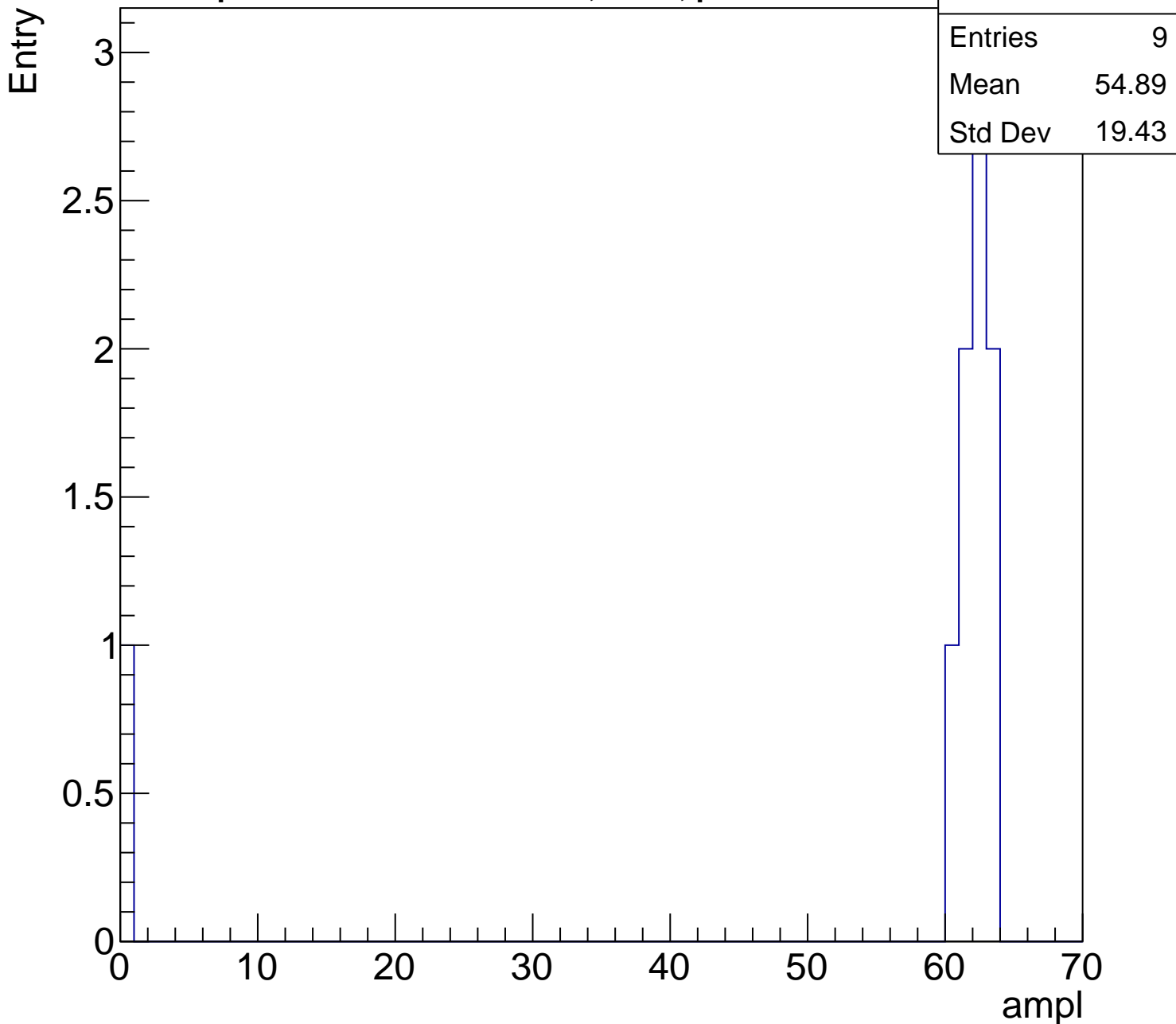
9

Mean

54.89

Std Dev

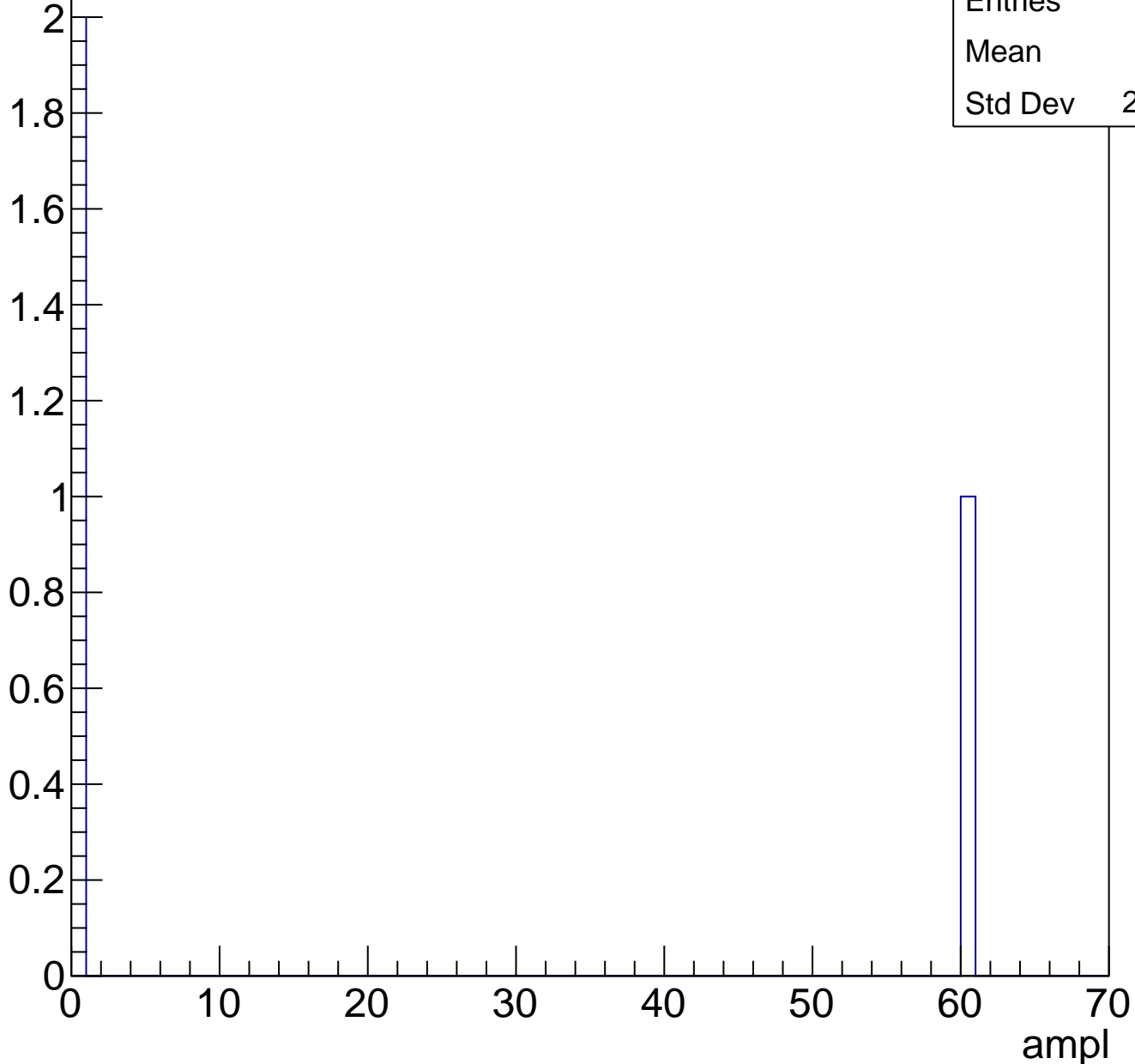
19.43



B1L103S, U17-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20
Std Dev	28.28

B1L103S, U17-ch102, adc0

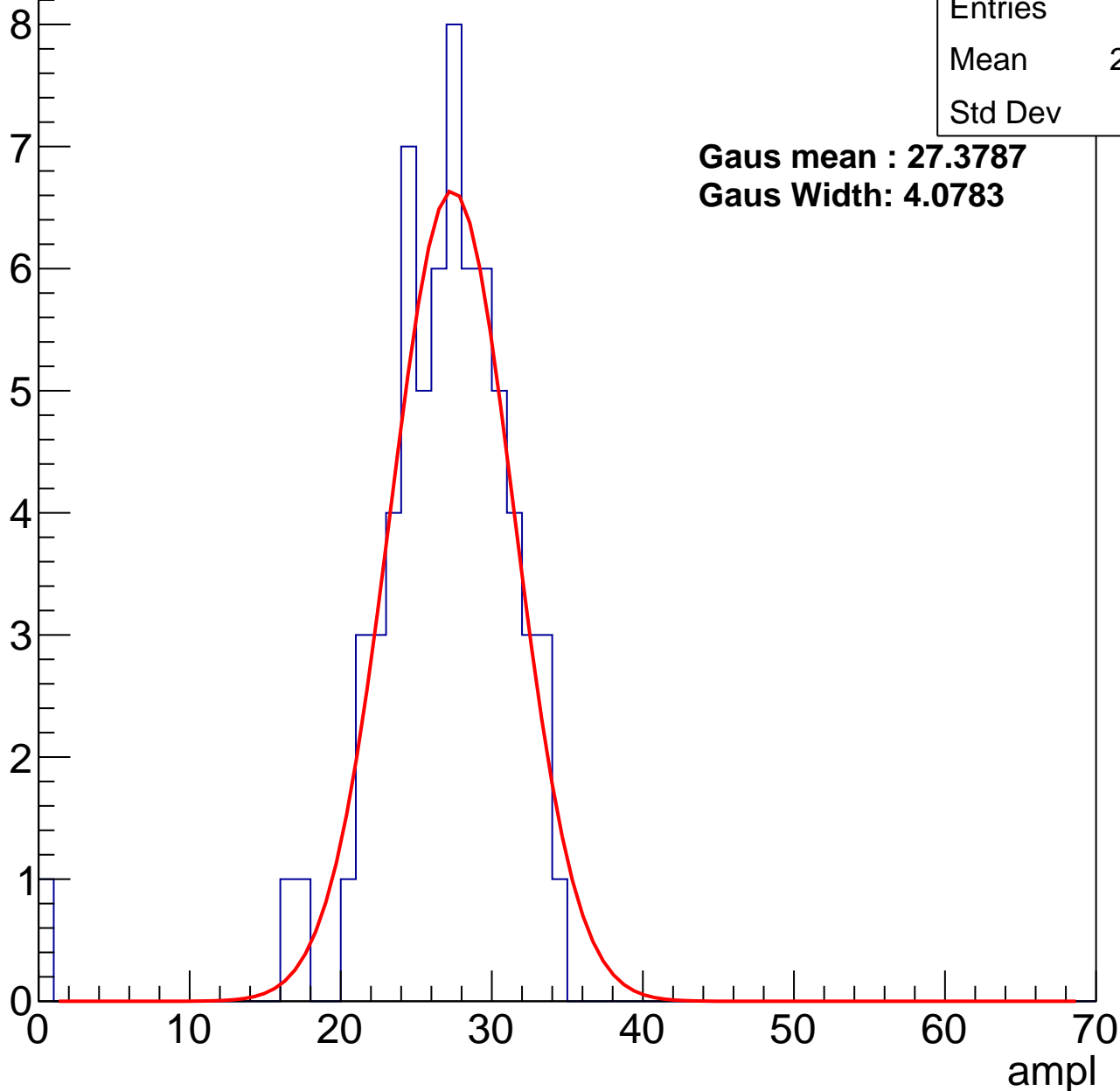
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	26.24
Std Dev	4.95

Gaus mean : 27.3787

Gaus Width: 4.0783



B1L103S, U17-ch102, adc1

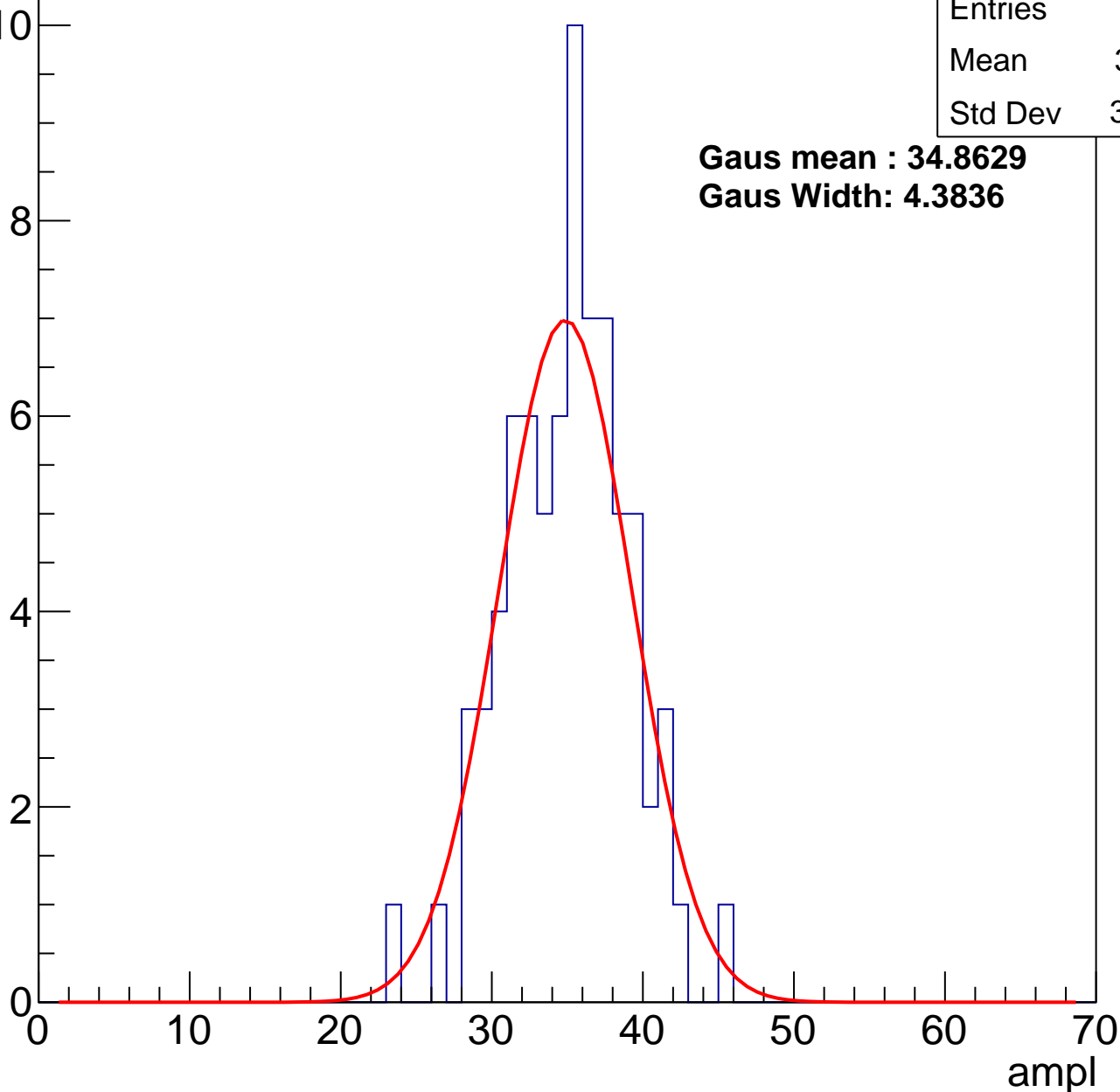
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	34.51
Std Dev	3.982

Gaus mean : 34.8629

Gaus Width: 4.3836



B1L103S, U17-ch102, adc2

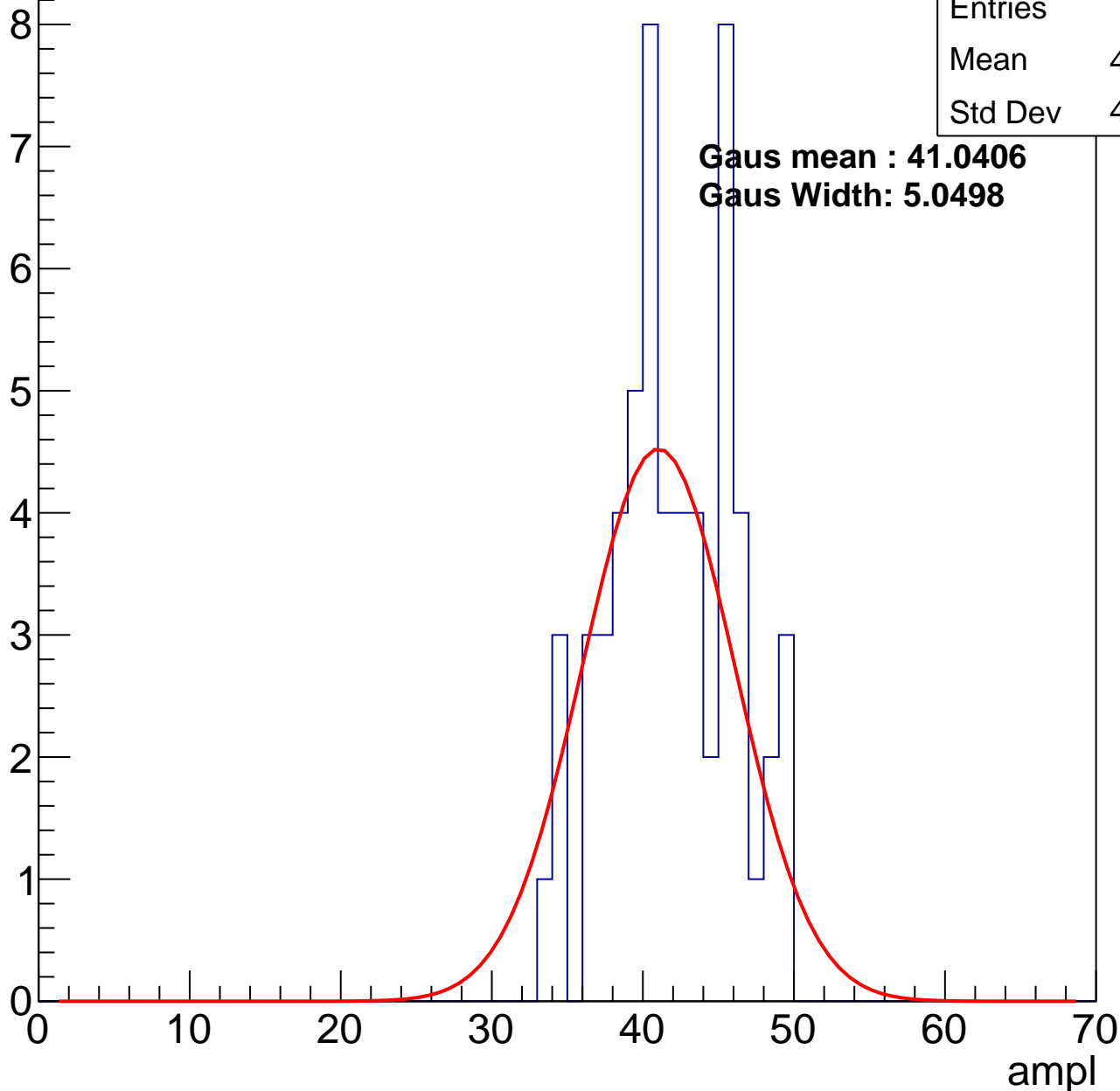
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	41.47
Std Dev	4.069

Gaus mean : 41.0406

Gaus Width: 5.0498

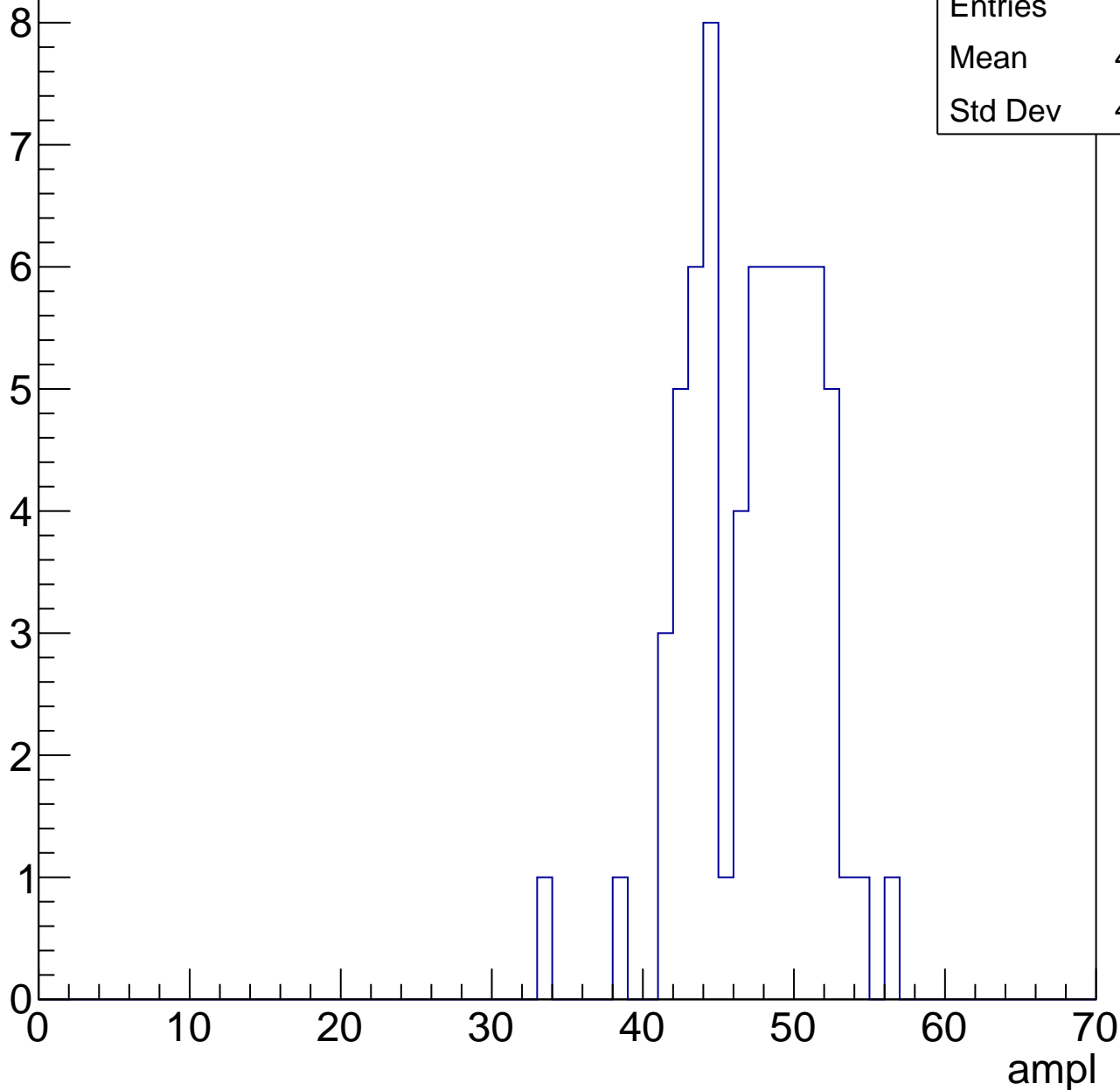


B1L103S, U17-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	46.81
Std Dev	4.151

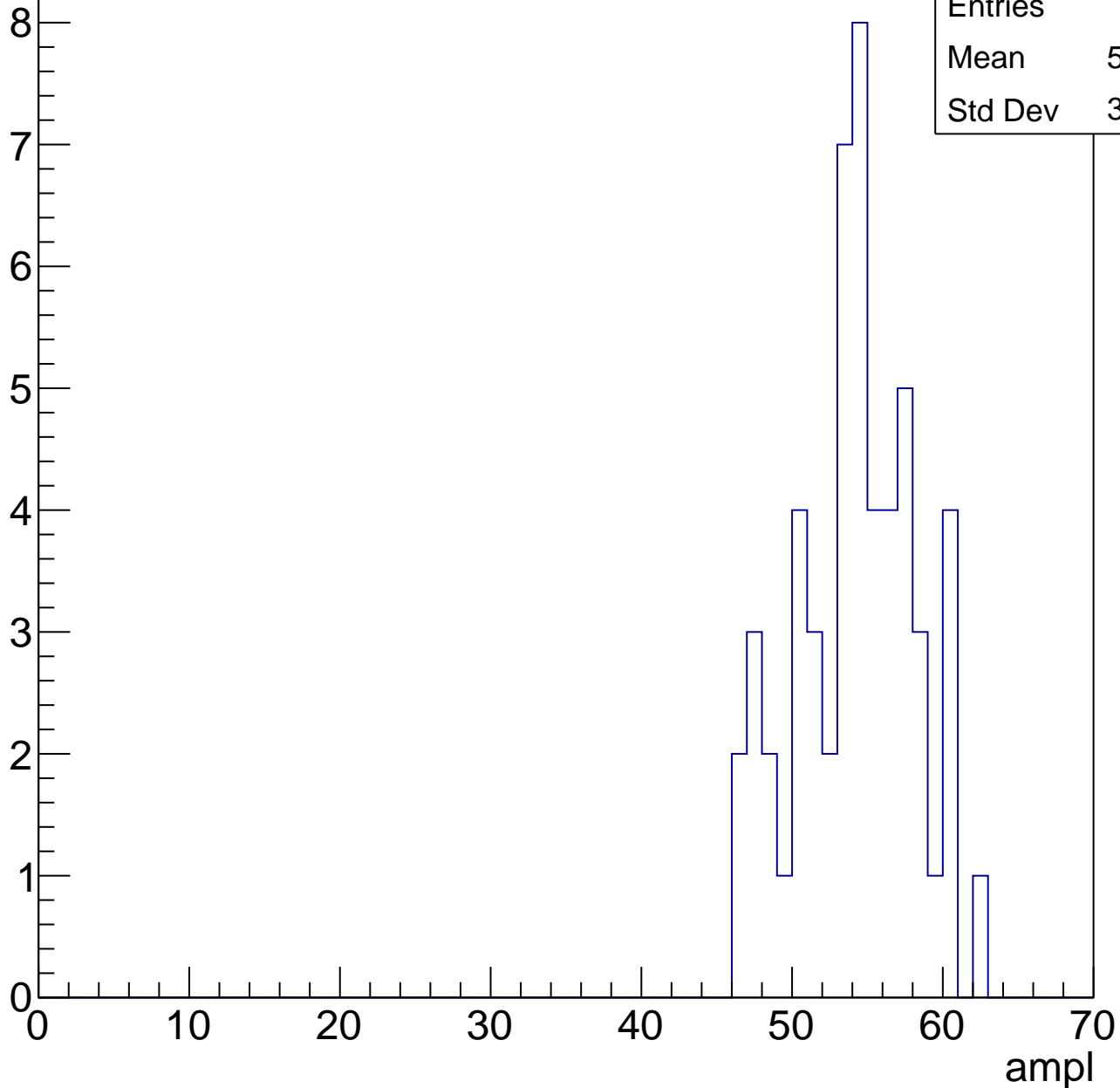


B1L103S, U17-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	53.74
Std Dev	3.898

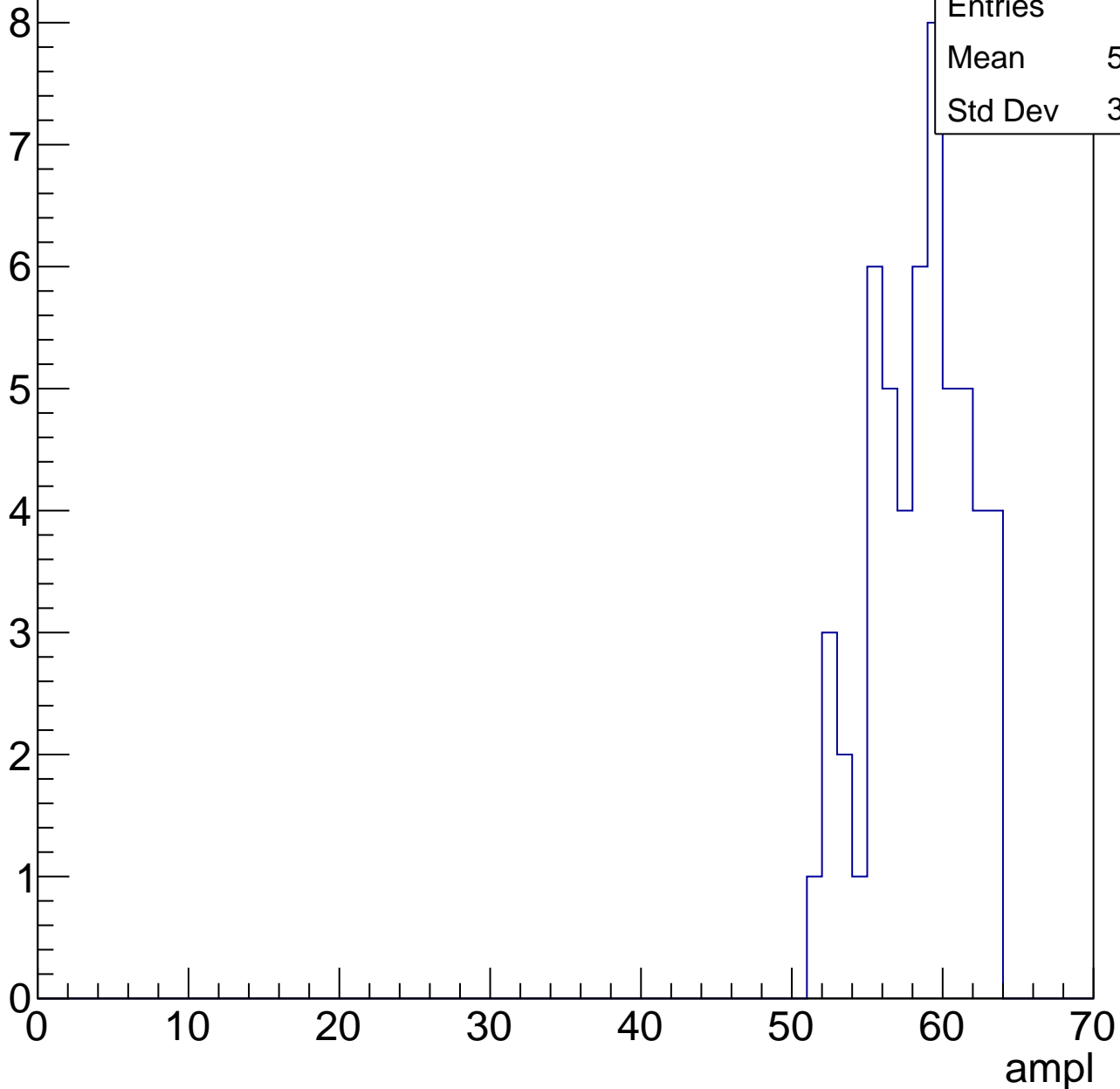


B1L103S, U17-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	57.96
Std Dev	3.156

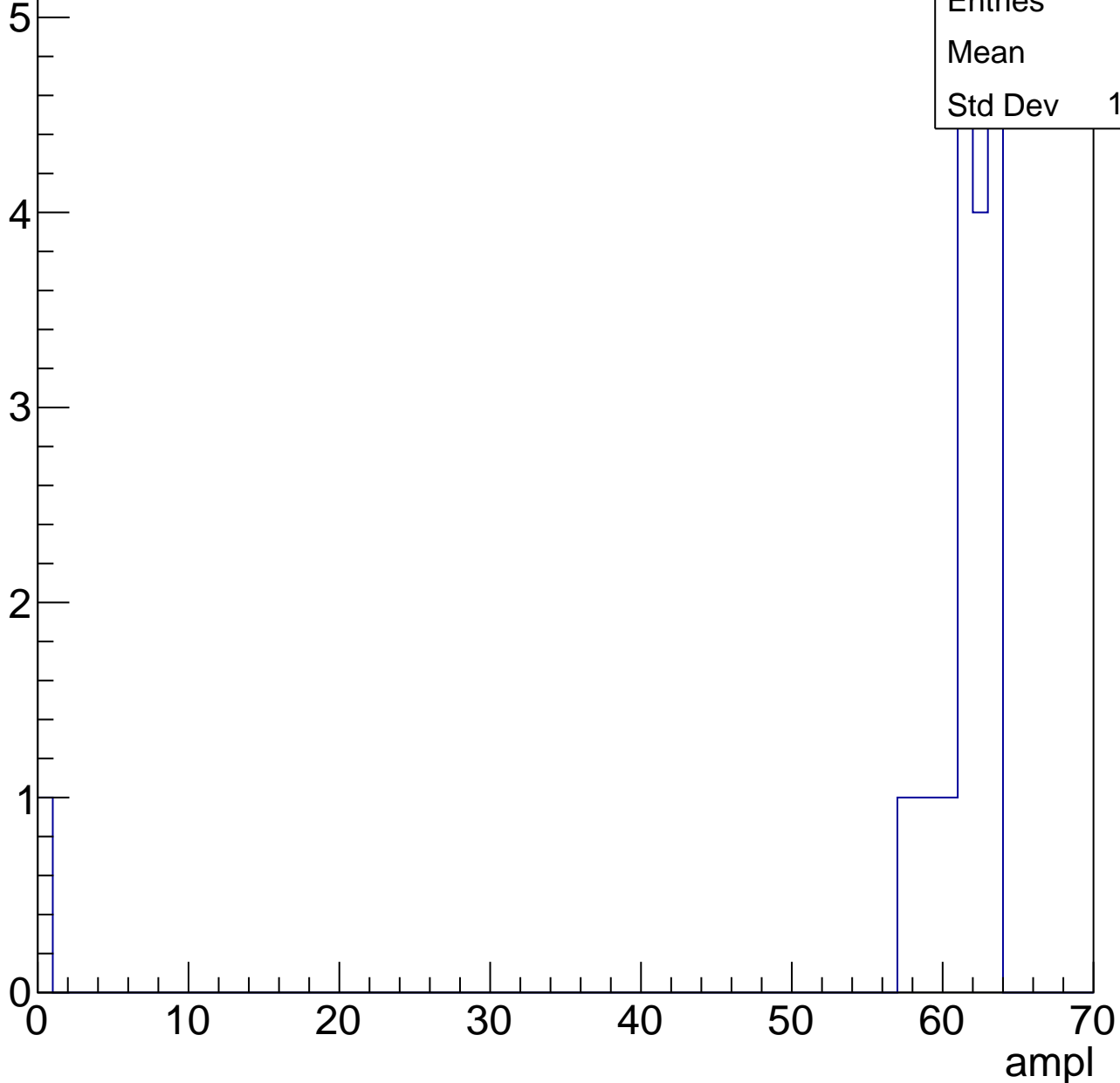


B1L103S, U17-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

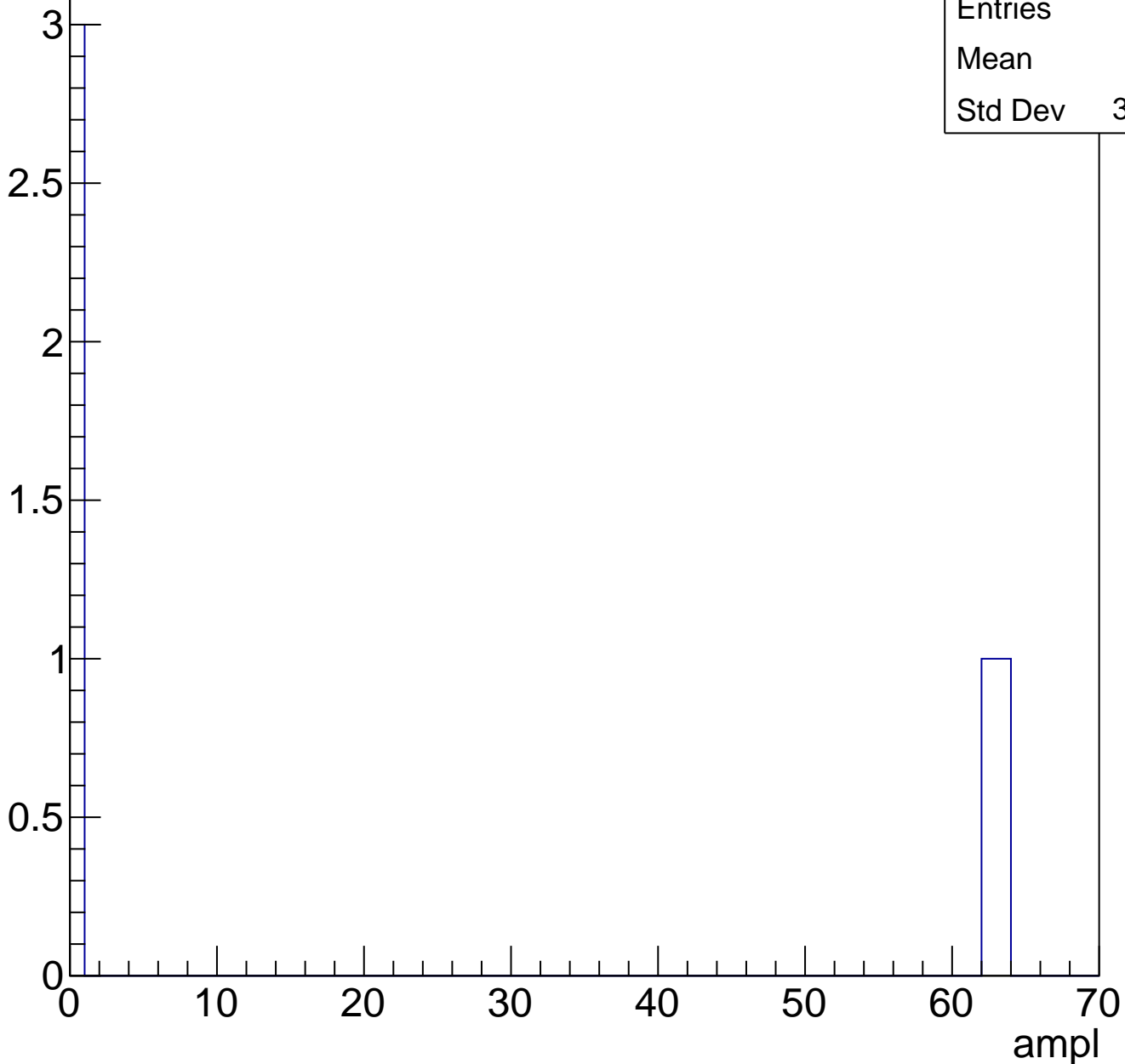
Entries	19
Mean	58
Std Dev	13.77



B1L103S, U17-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch103, adc0

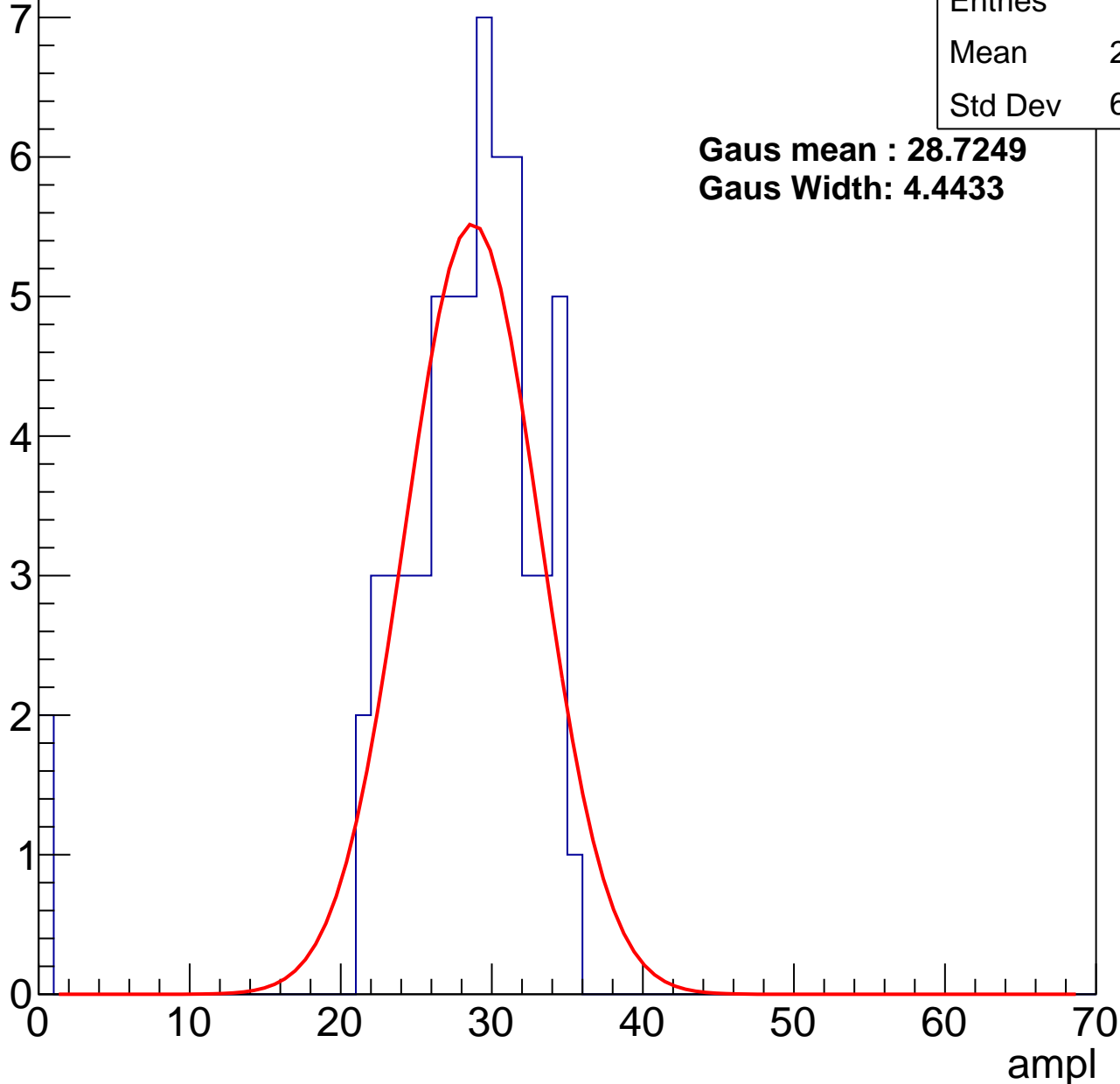
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	27.39
Std Dev	6.173

Gaus mean : 28.7249

Gaus Width: 4.4433



B1L103S, U17-ch103, adc1

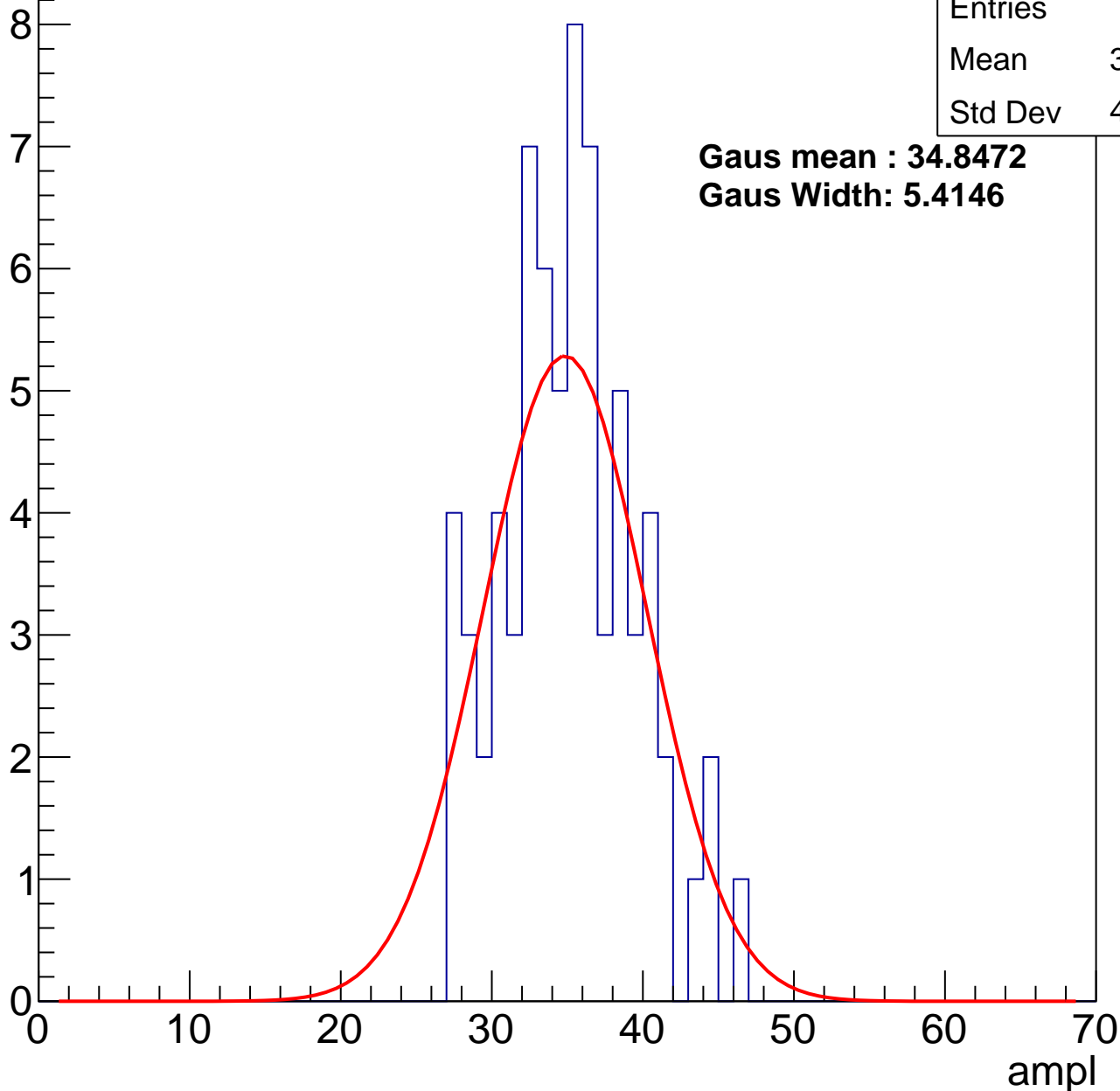
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.63
Std Dev	4.369

Gaus mean : 34.8472

Gaus Width: 5.4146



B1L103S, U17-ch103, adc2

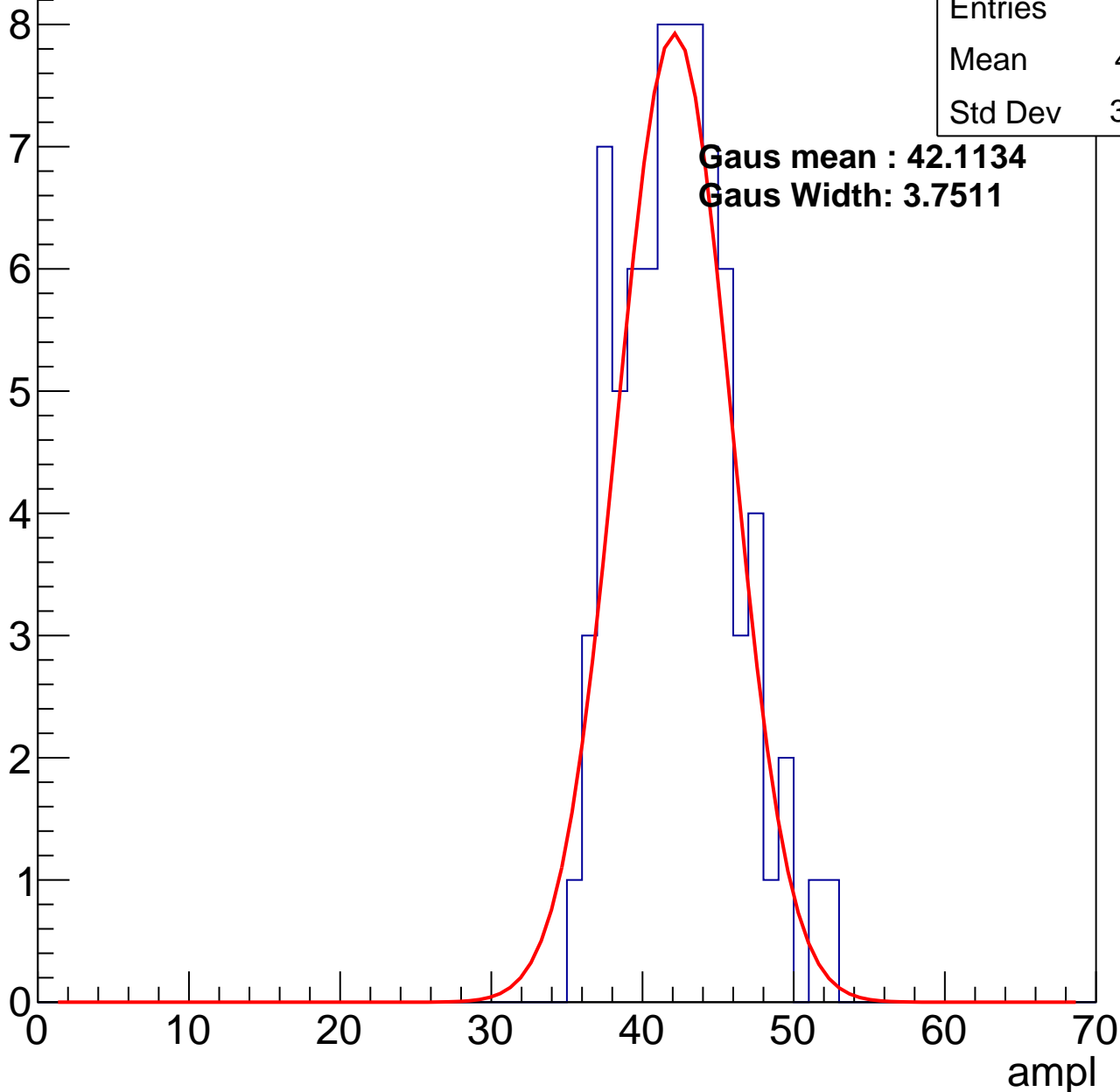
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	41.91
Std Dev	3.693

Gaus mean : 42.1134

Gaus Width: 3.7511

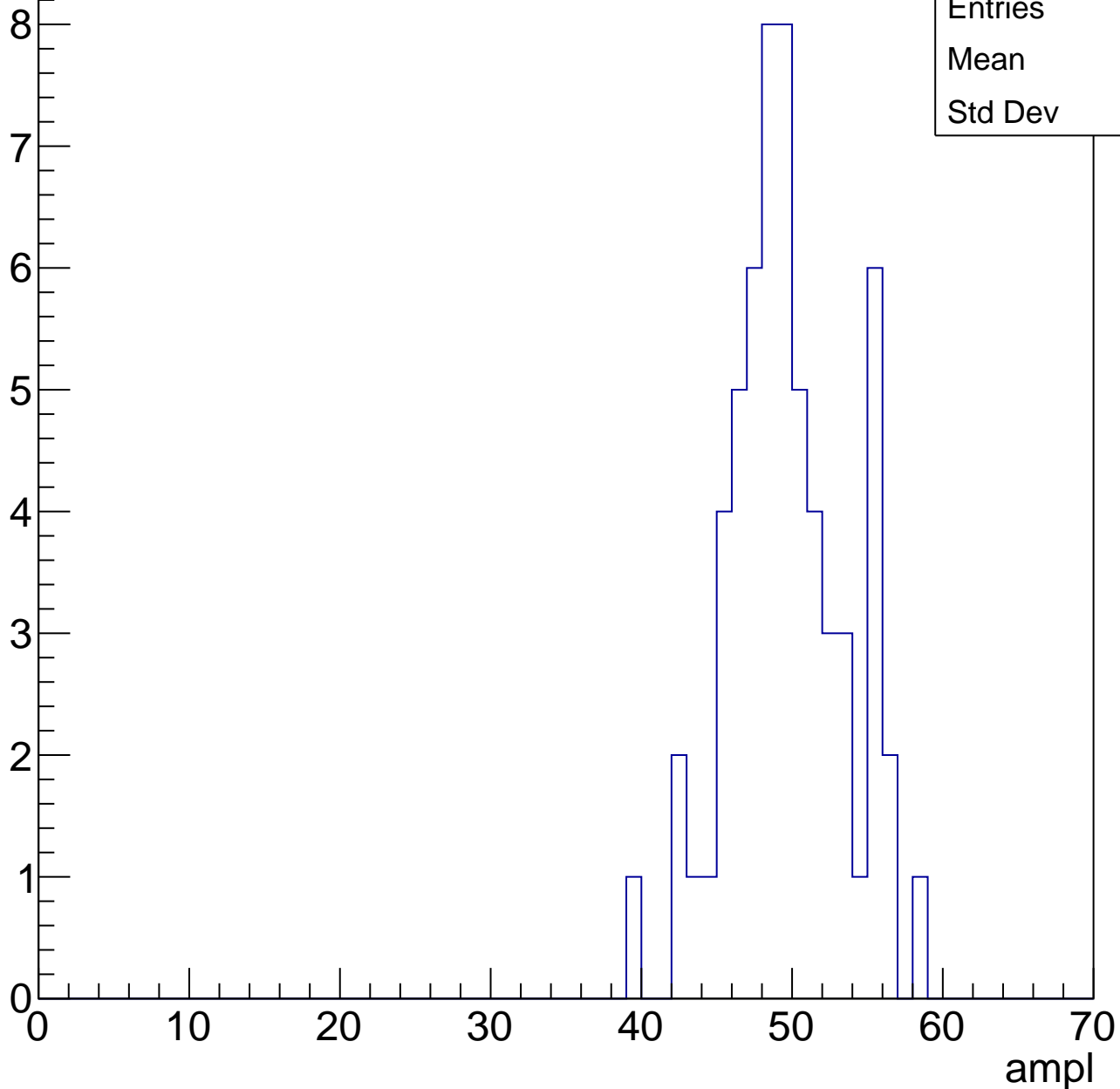


B1L103S, U17-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.2
Std Dev	3.87

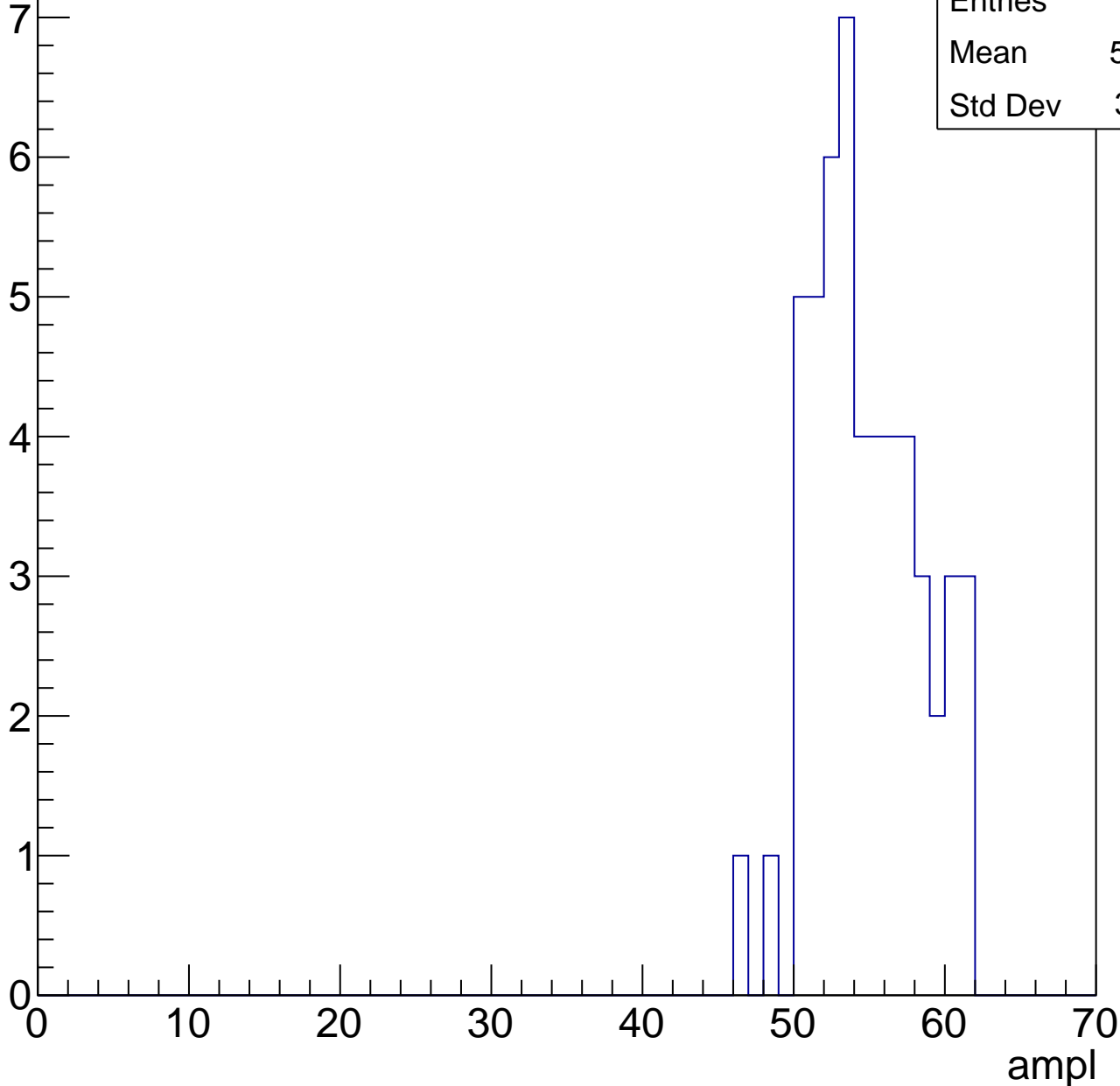


B1L103S, U17-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	54.33
Std Dev	3.561

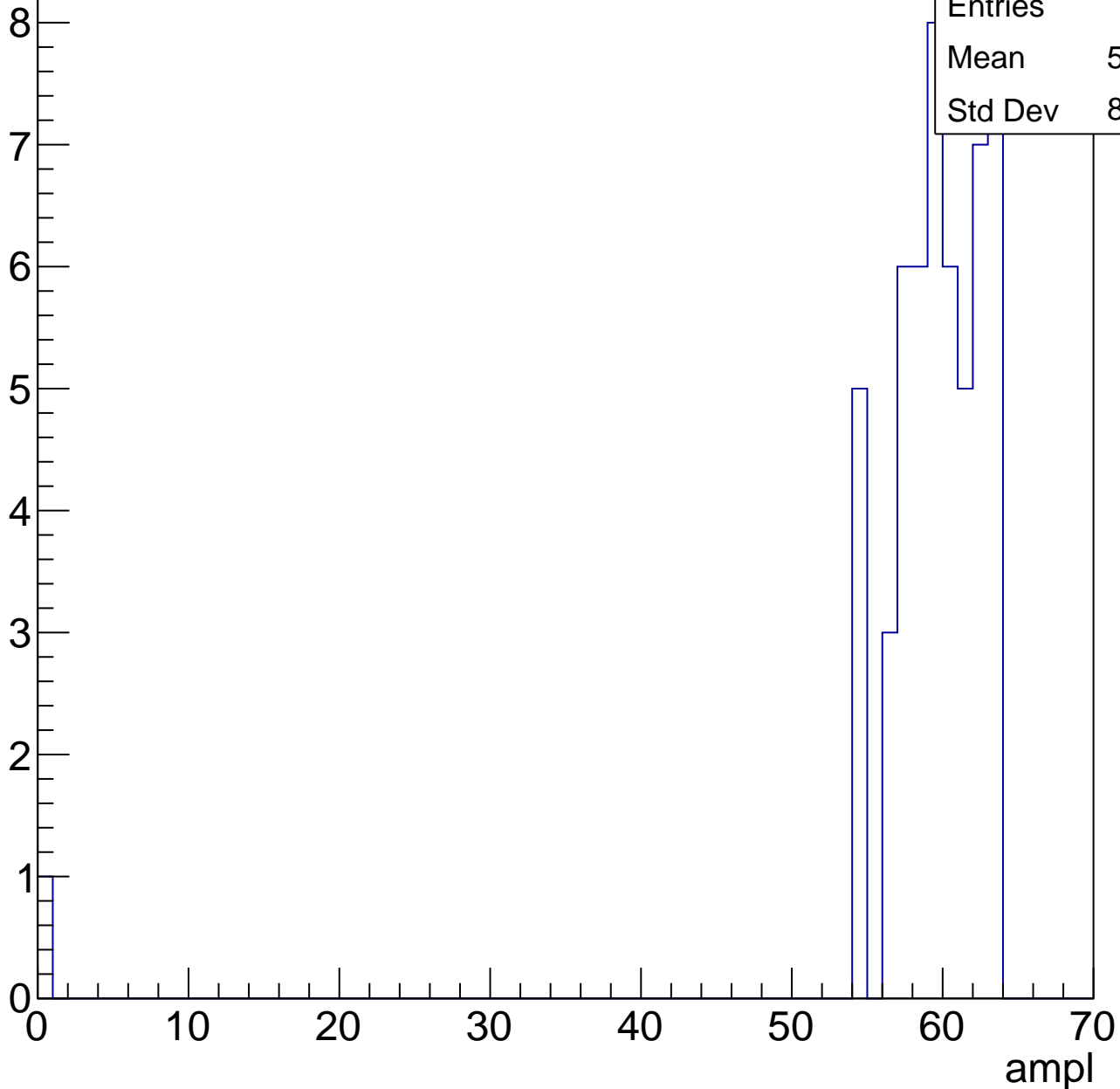


B1L103S, U17-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.24
Std Dev	8.364



B1L103S, U17-ch103, adc6

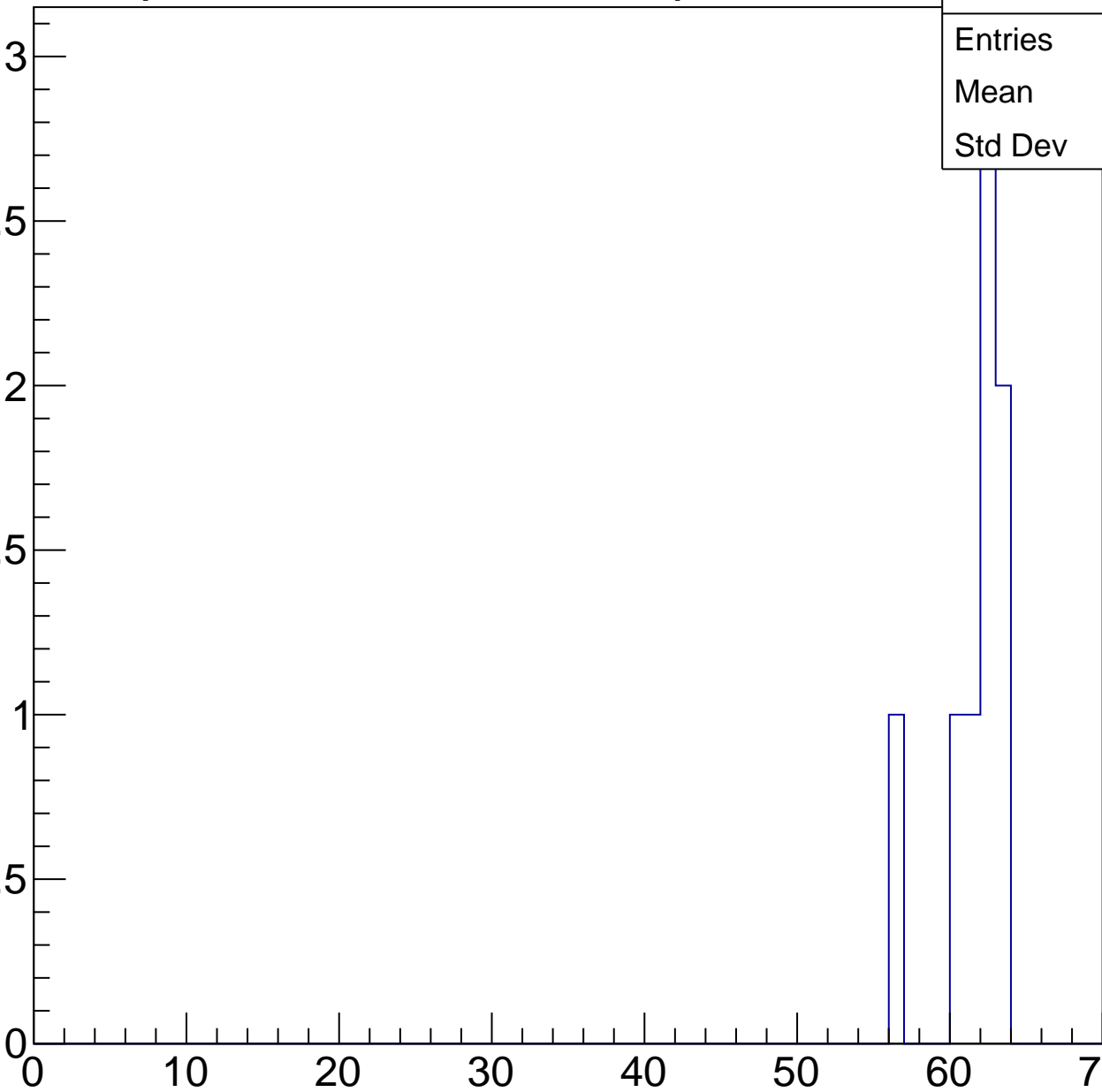
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61.12
Std Dev	2.147

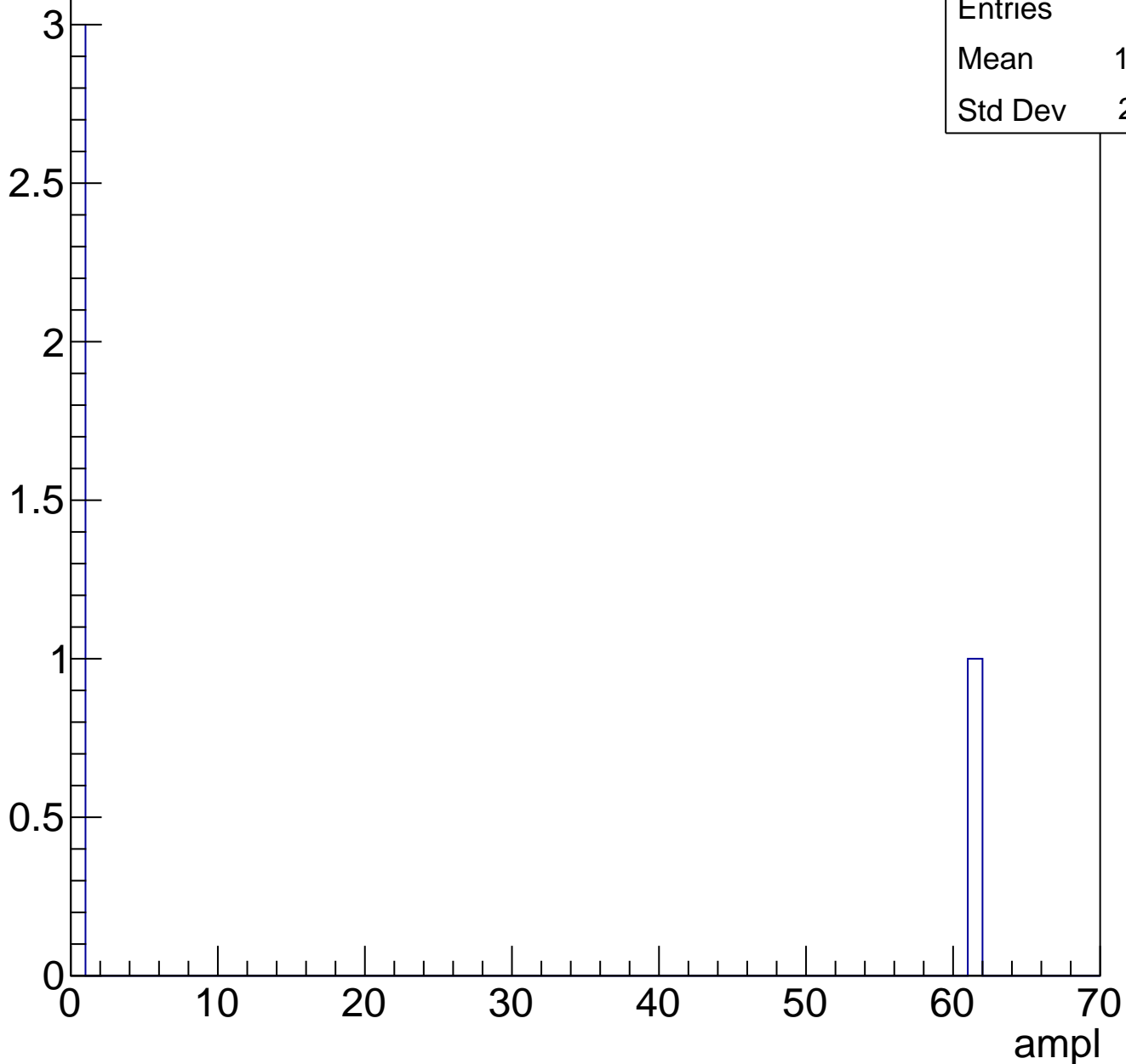
ampl



B1L103S, U17-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch104, adc0

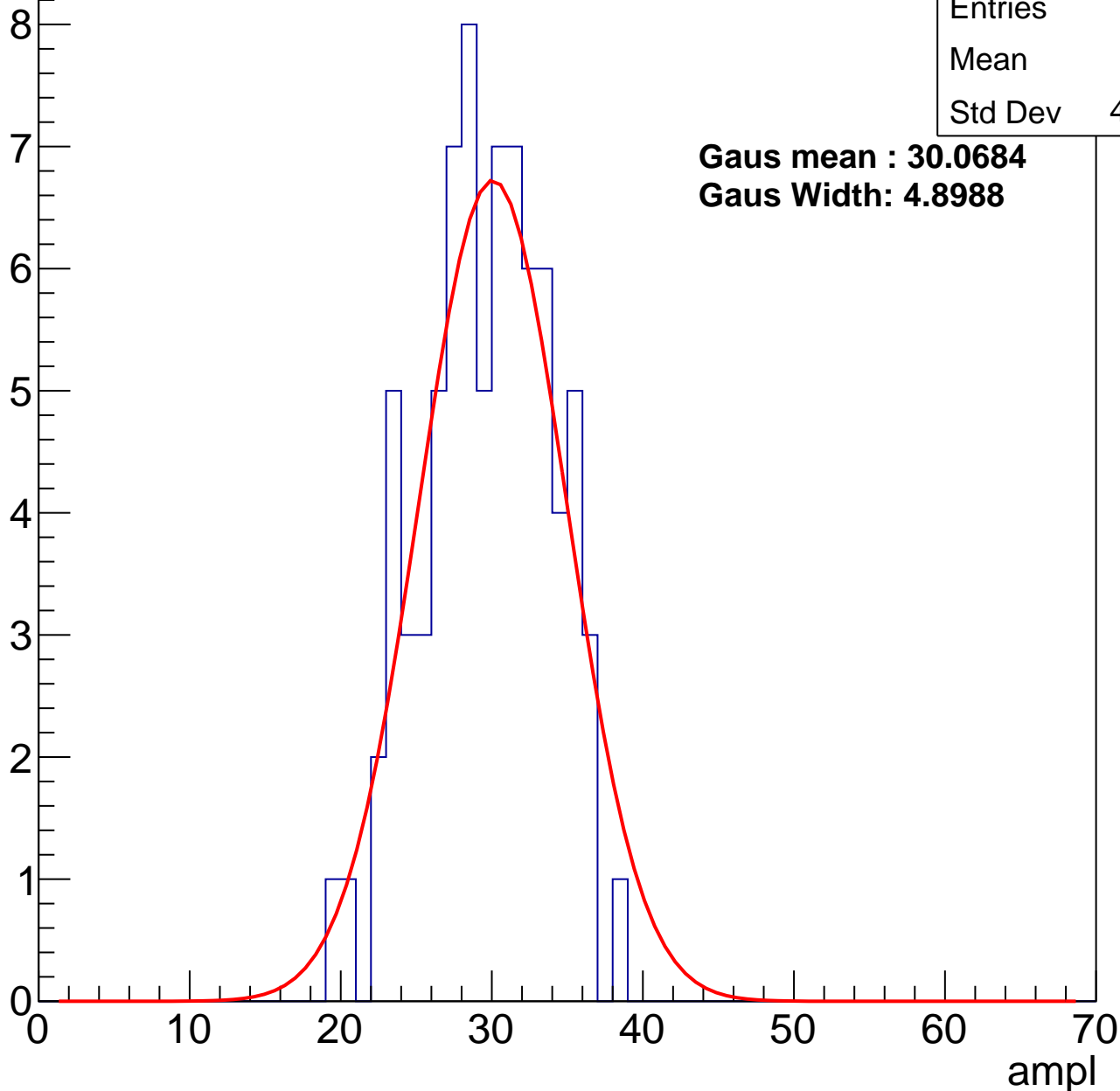
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	29.2
Std Dev	4.138

Gaus mean : 30.0684

Gaus Width: 4.8988



B1L103S, U17-ch104, adc1

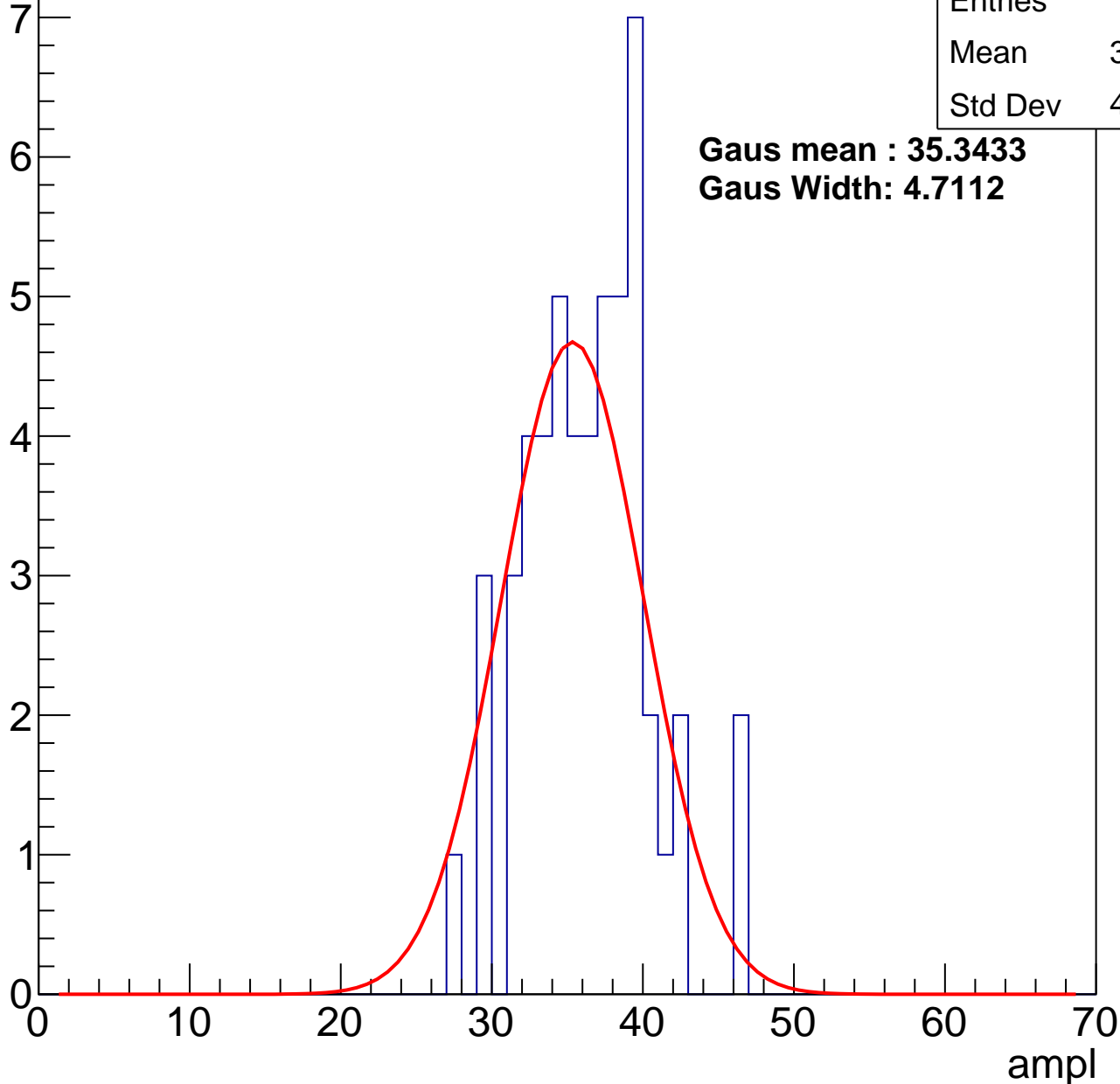
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	35.88
Std Dev	4.027

Gaus mean : 35.3433

Gaus Width: 4.7112



B1L103S, U17-ch104, adc2

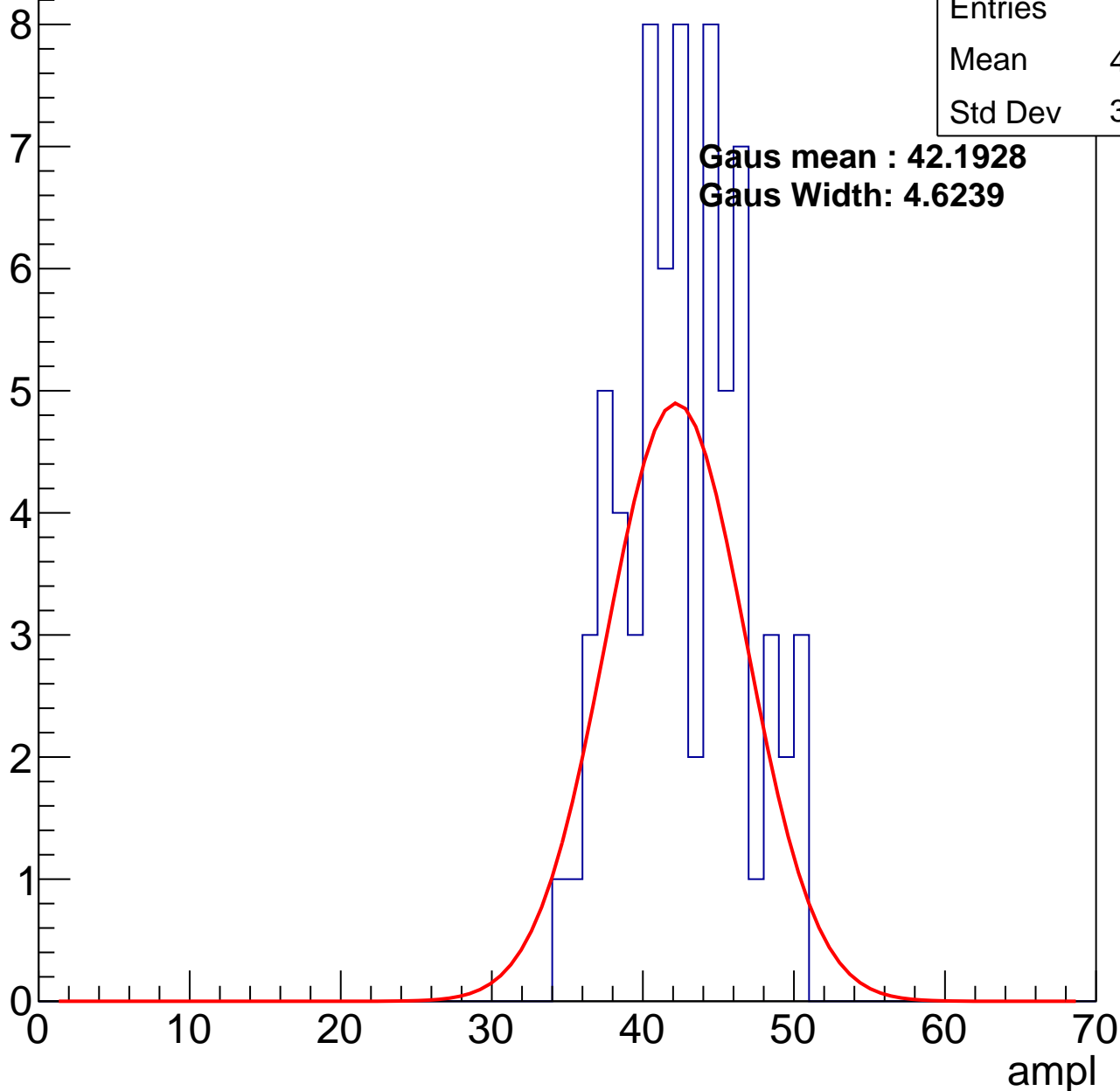
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.24
Std Dev	3.937

Gaus mean : 42.1928

Gaus Width: 4.6239

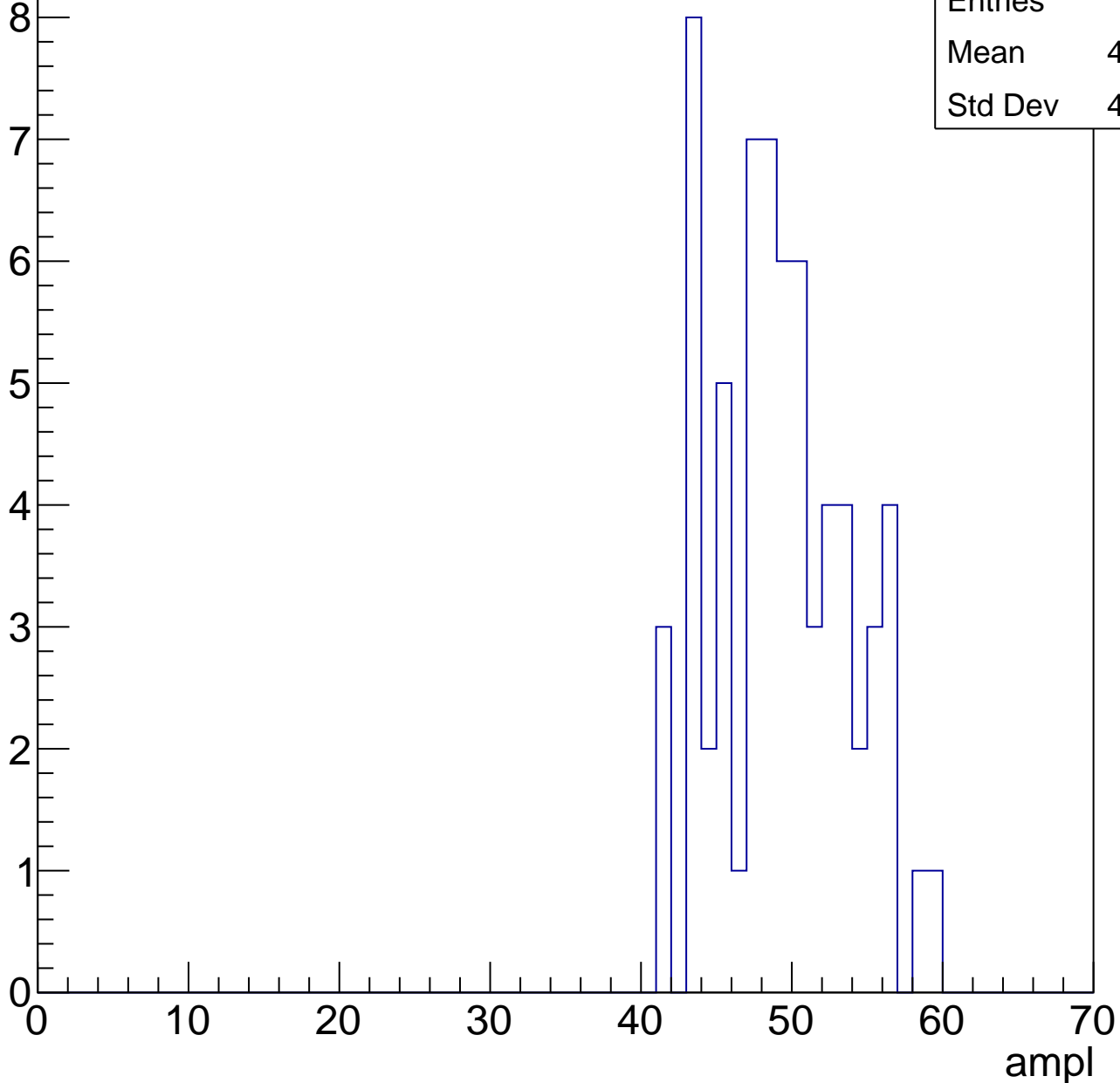


B1L103S, U17-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.84
Std Dev	4.414

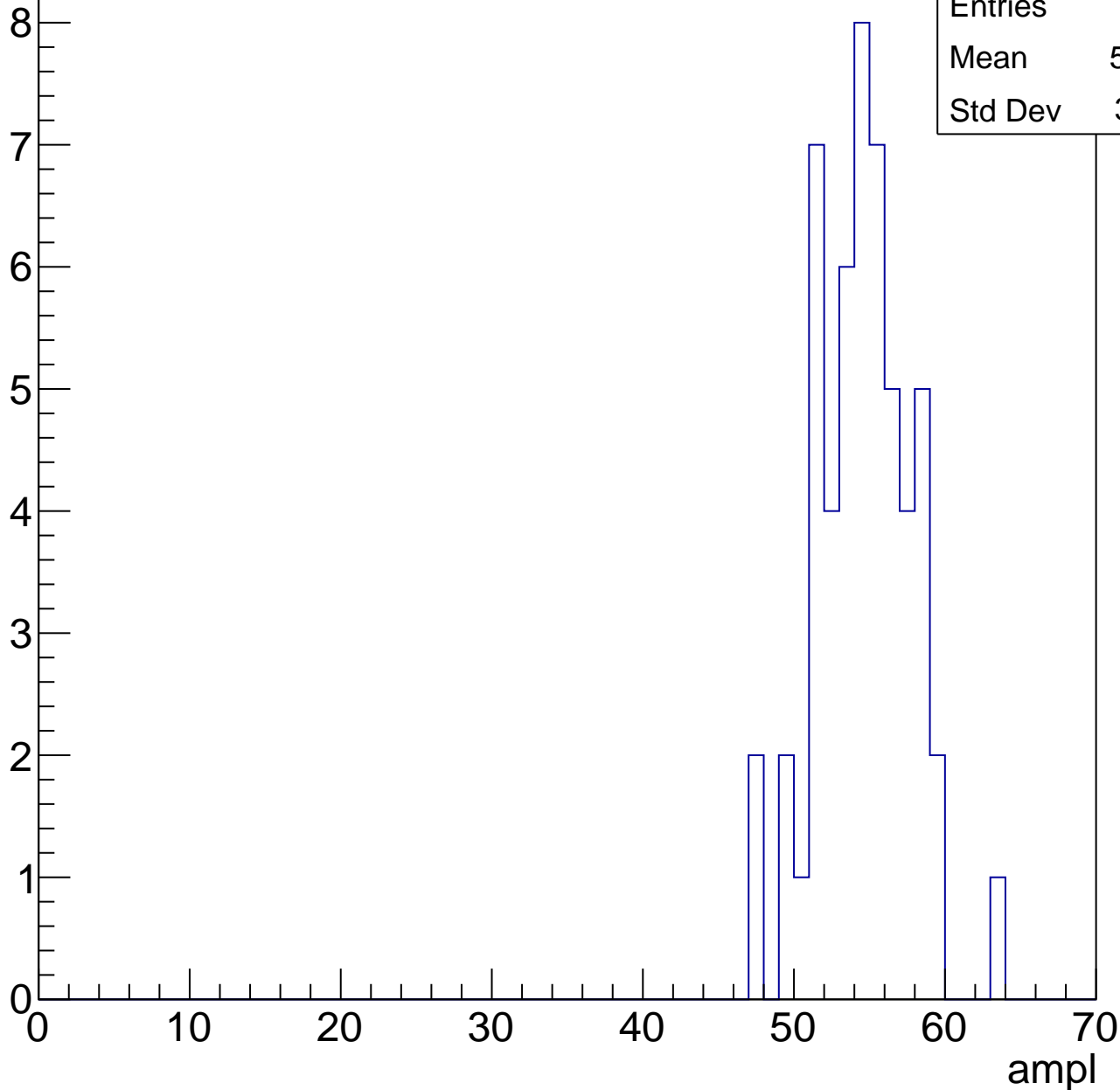


B1L103S, U17-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.09
Std Dev	3.111

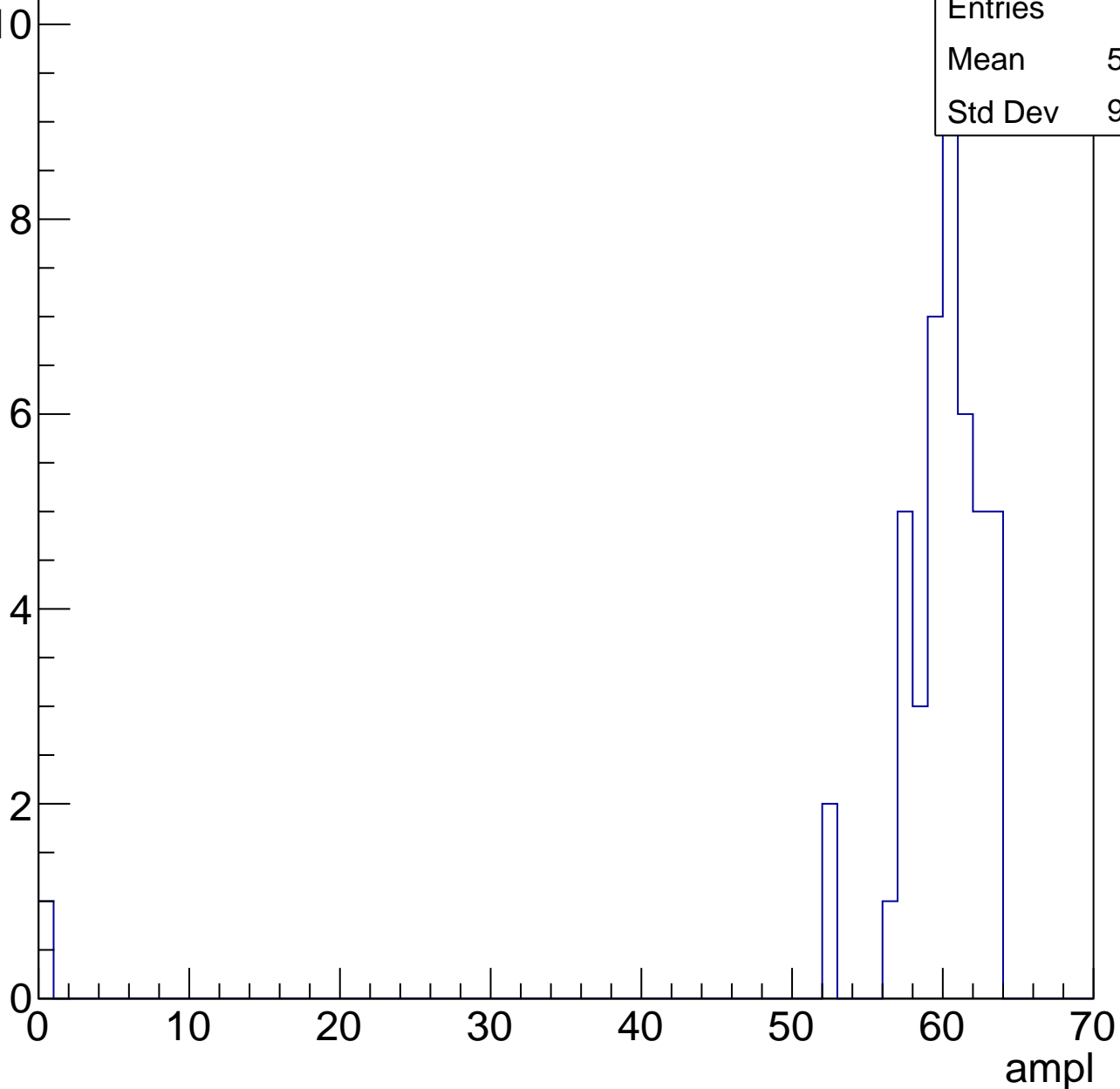


B1L103S, U17-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

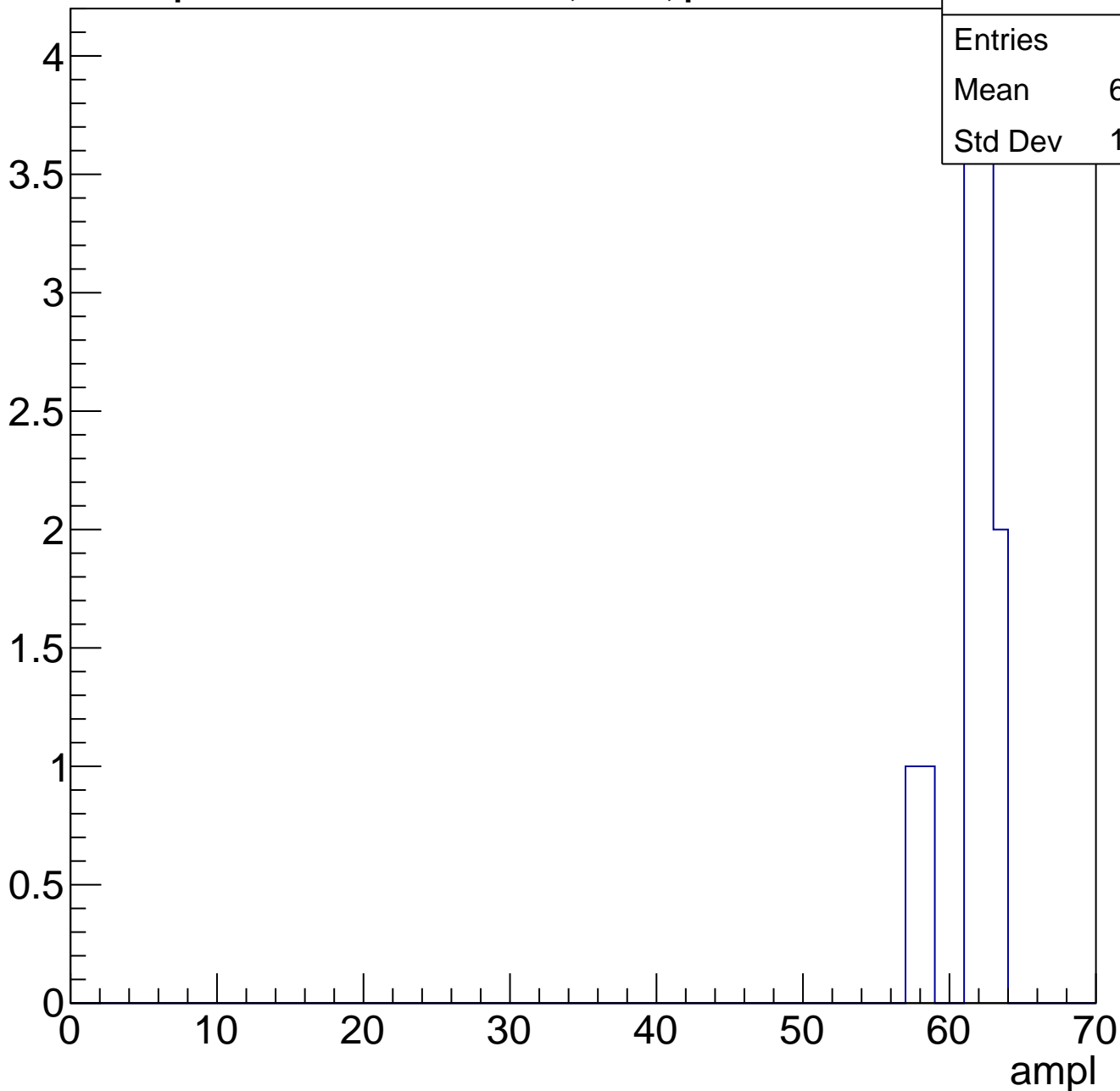
Entries	45
Mean	58.29
Std Dev	9.125



B1L103S, U17-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	61.08
Std Dev	1.754

B1L103S, U17-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch105, adc0

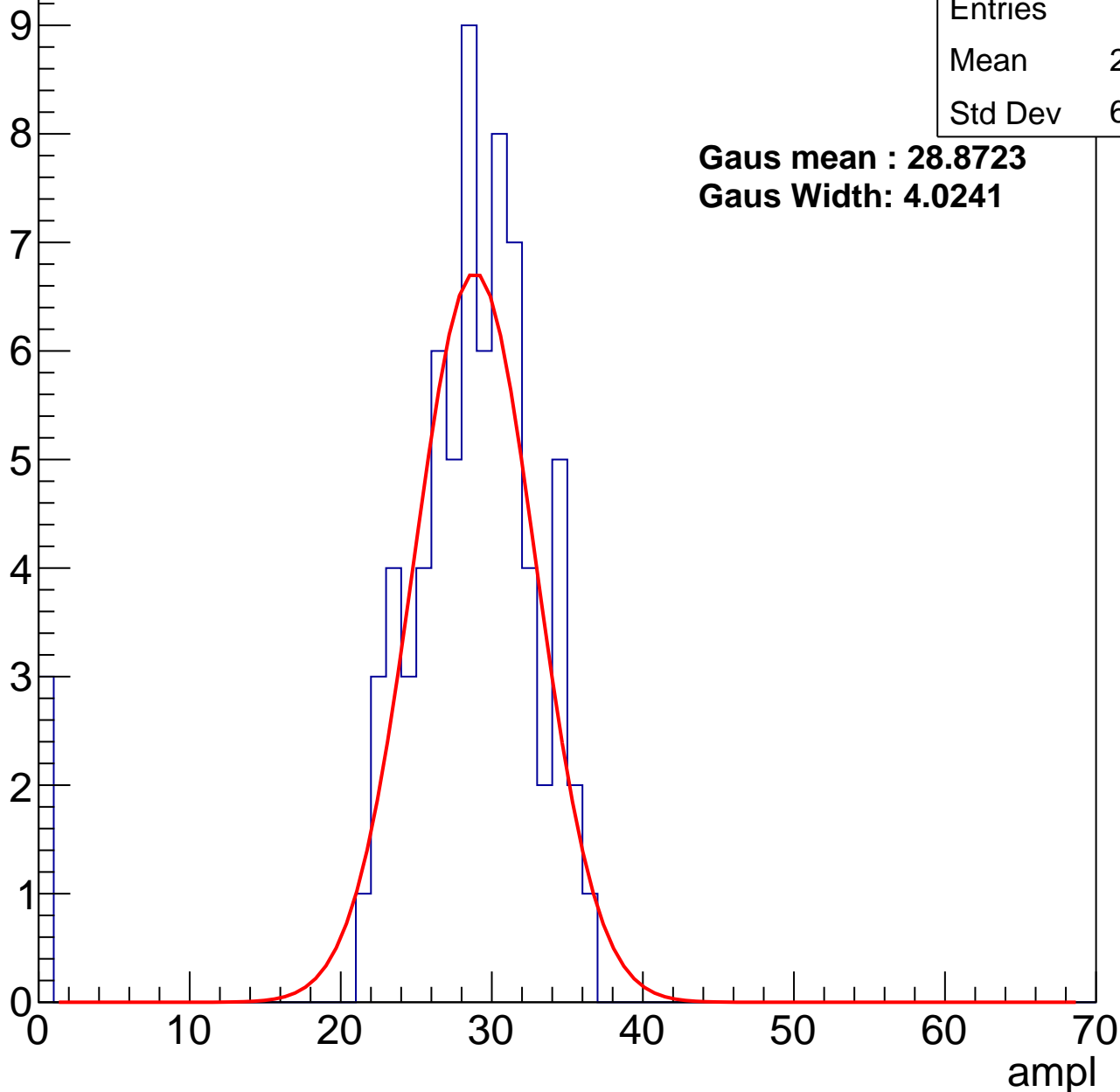
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.33
Std Dev	6.668

Gaus mean : 28.8723

Gaus Width: 4.0241



B1L103S, U17-ch105, adc1

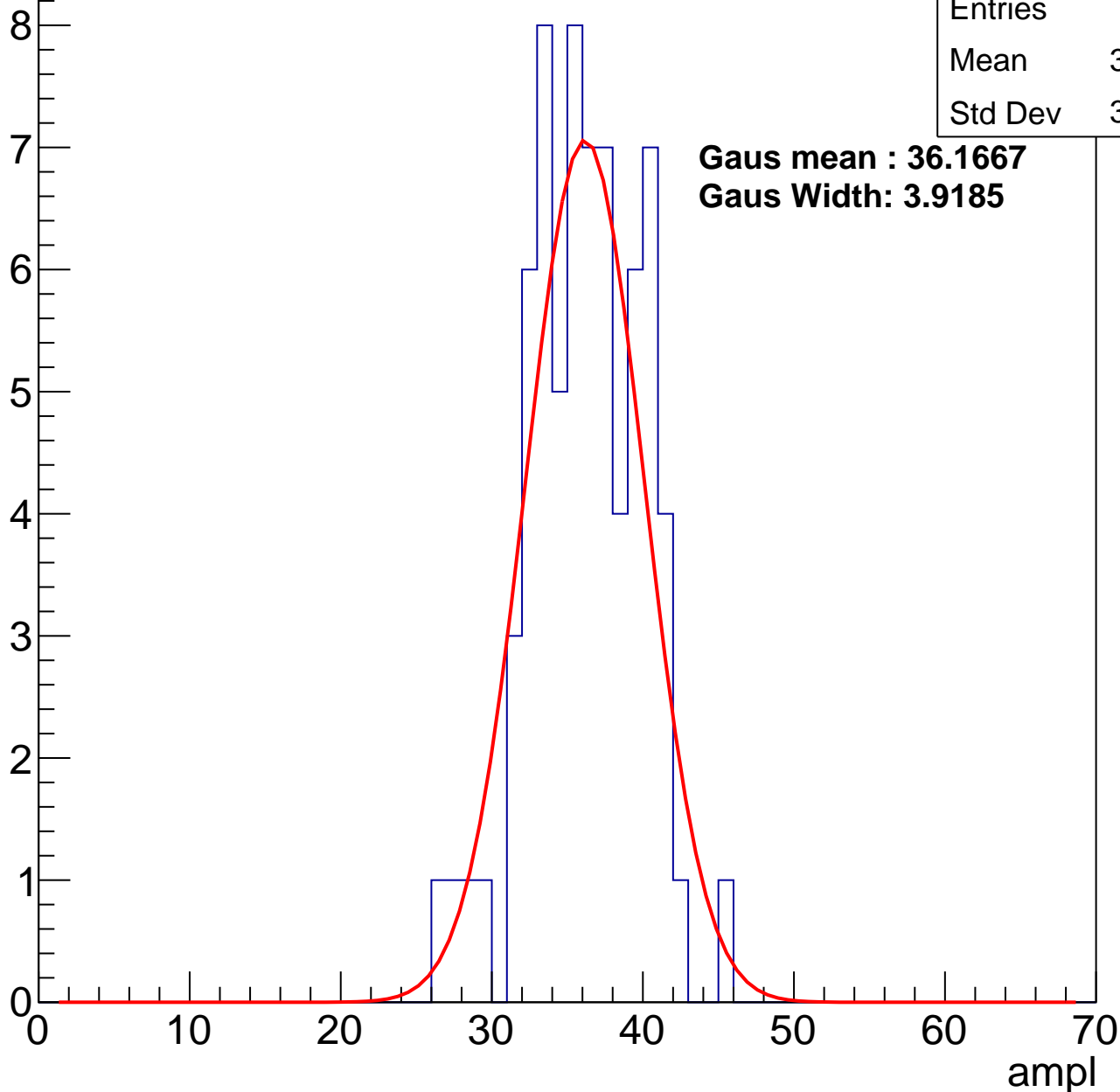
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.73
Std Dev	3.688

Gaus mean : 36.1667

Gaus Width: 3.9185



B1L103S, U17-ch105, adc2

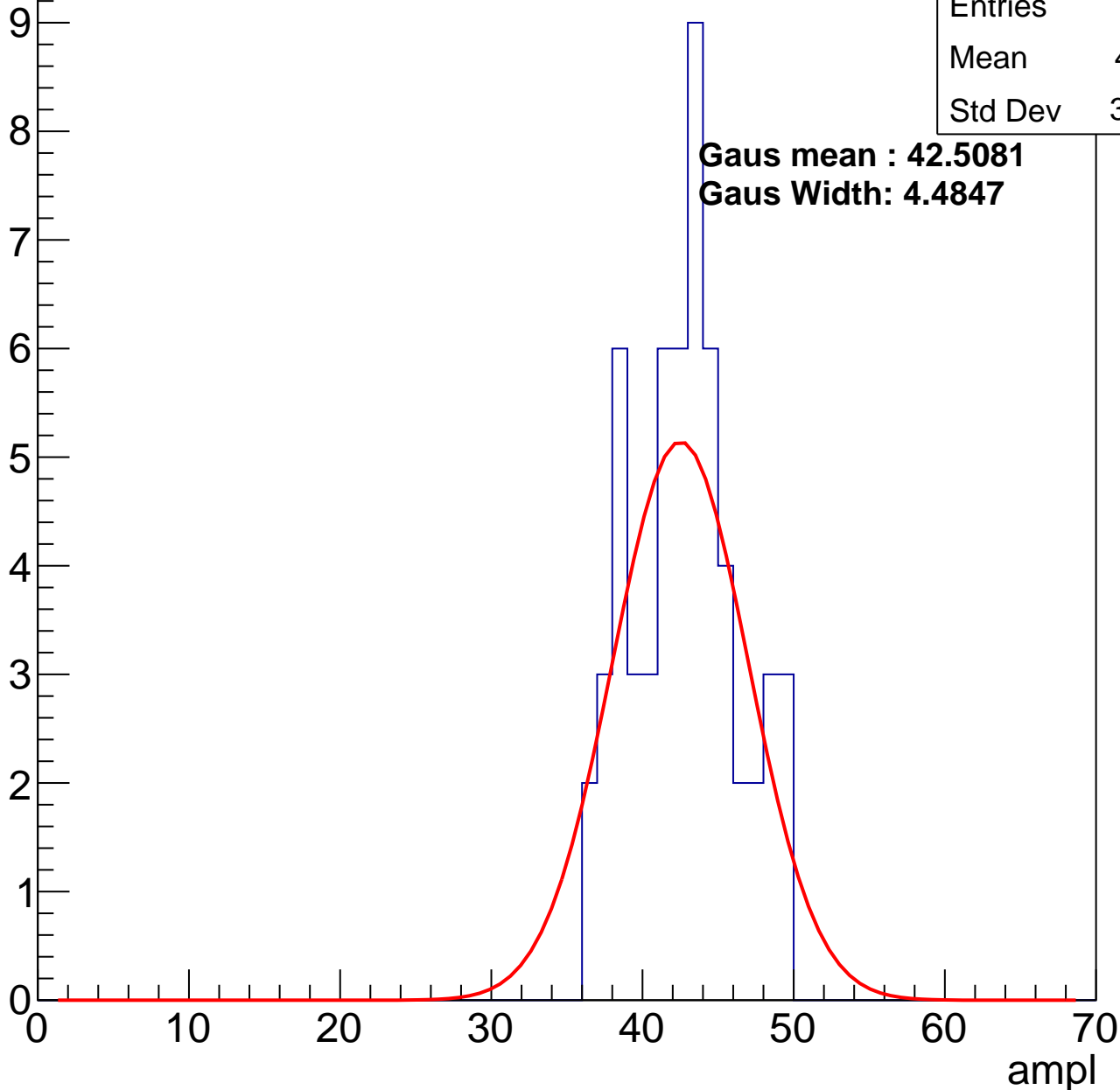
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.31
Std Dev	3.445

Gaus mean : 42.5081

Gaus Width: 4.4847



B1L103S, U17-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

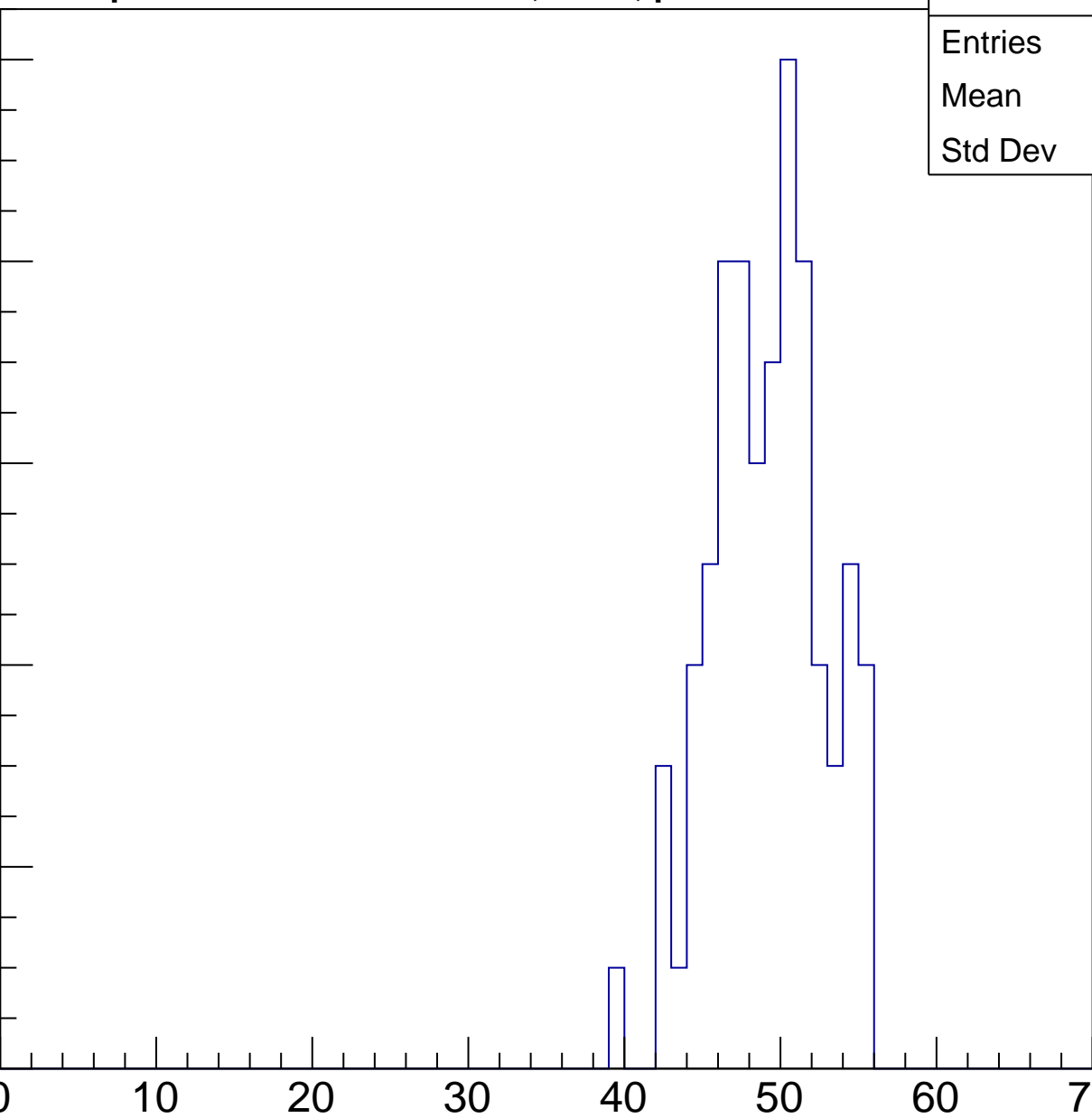
Entries	77
Mean	48.69
Std Dev	3.532

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

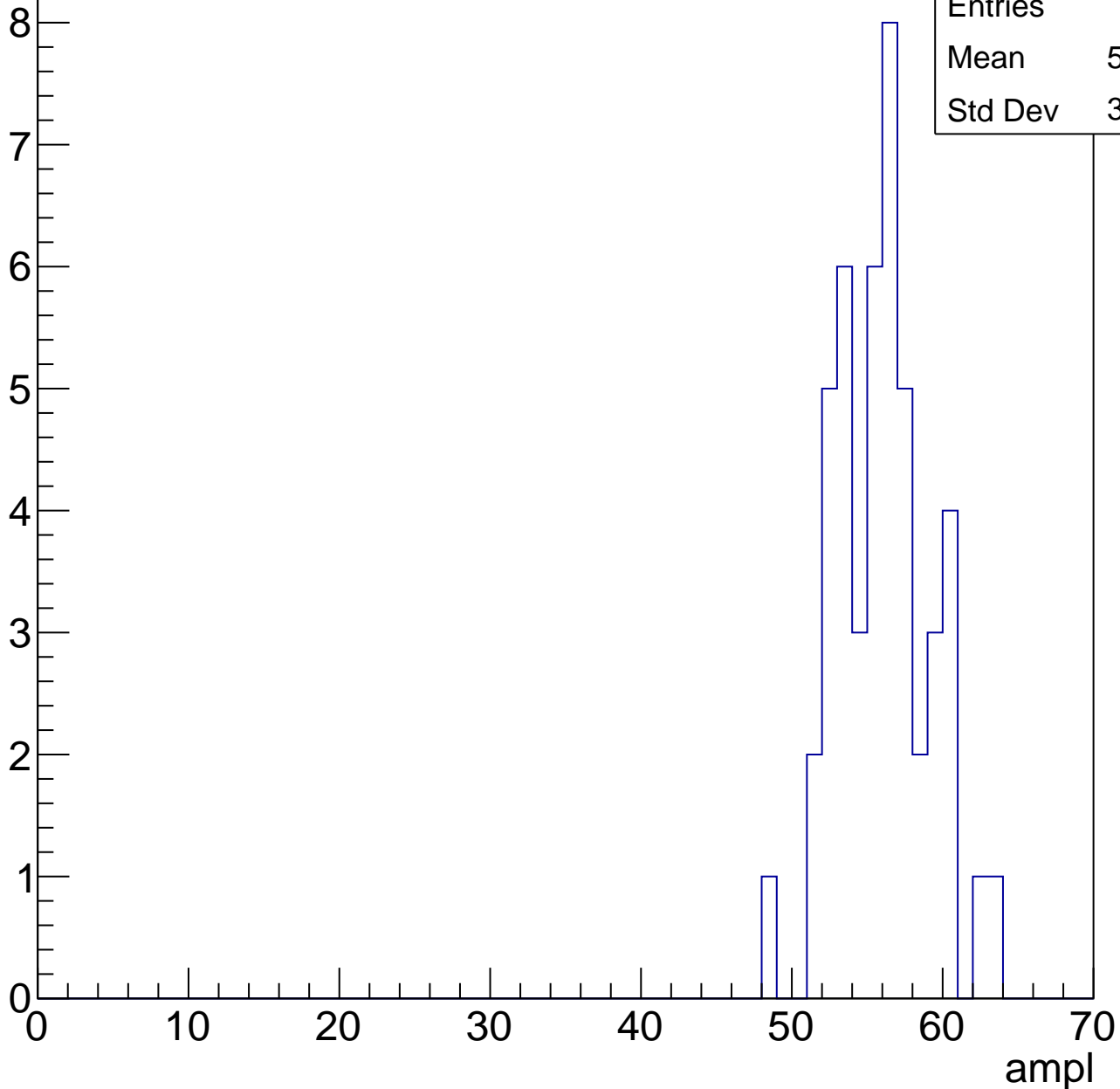


B1L103S, U17-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	55.55
Std Dev	3.079

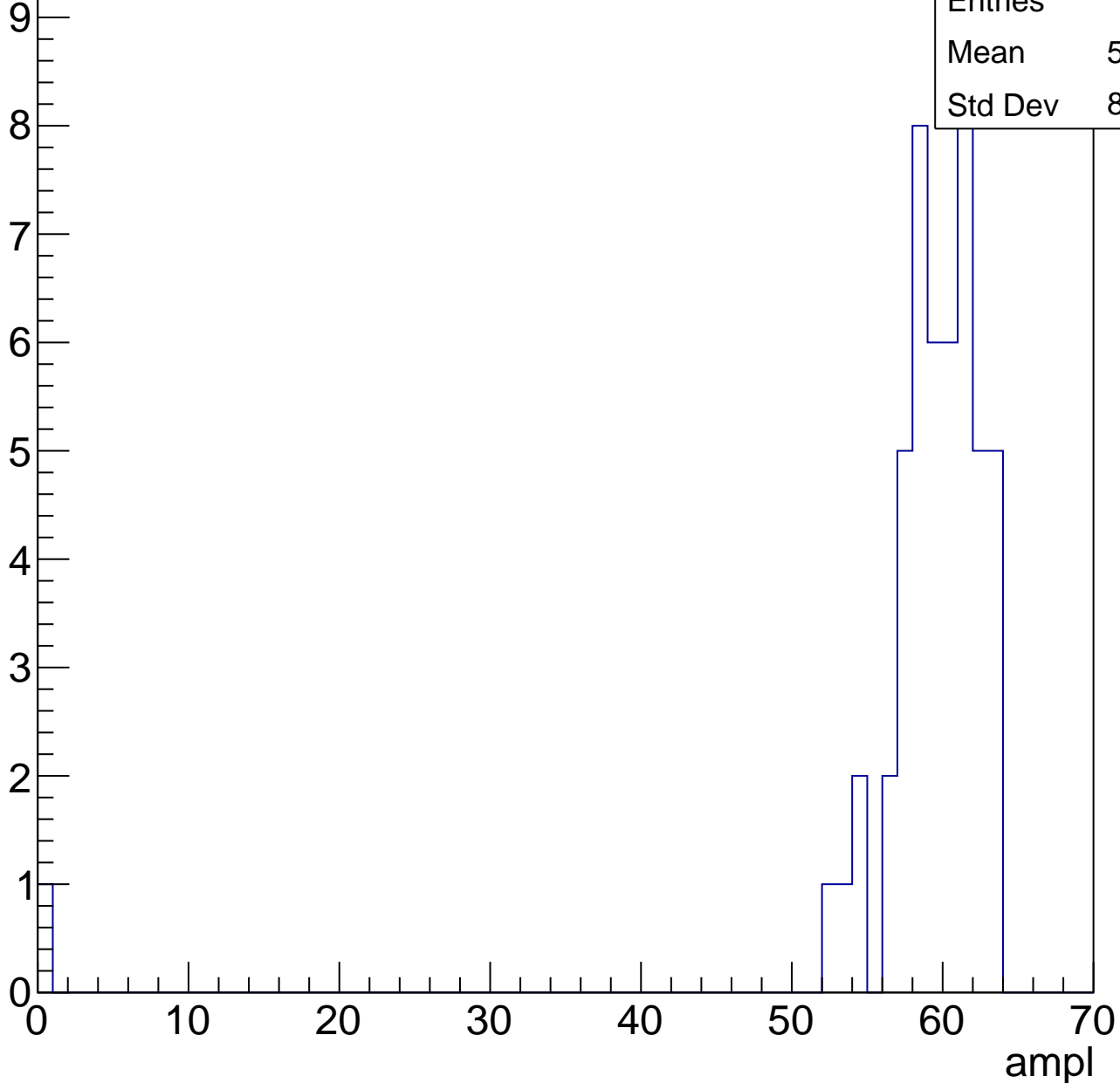


B1L103S, U17-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

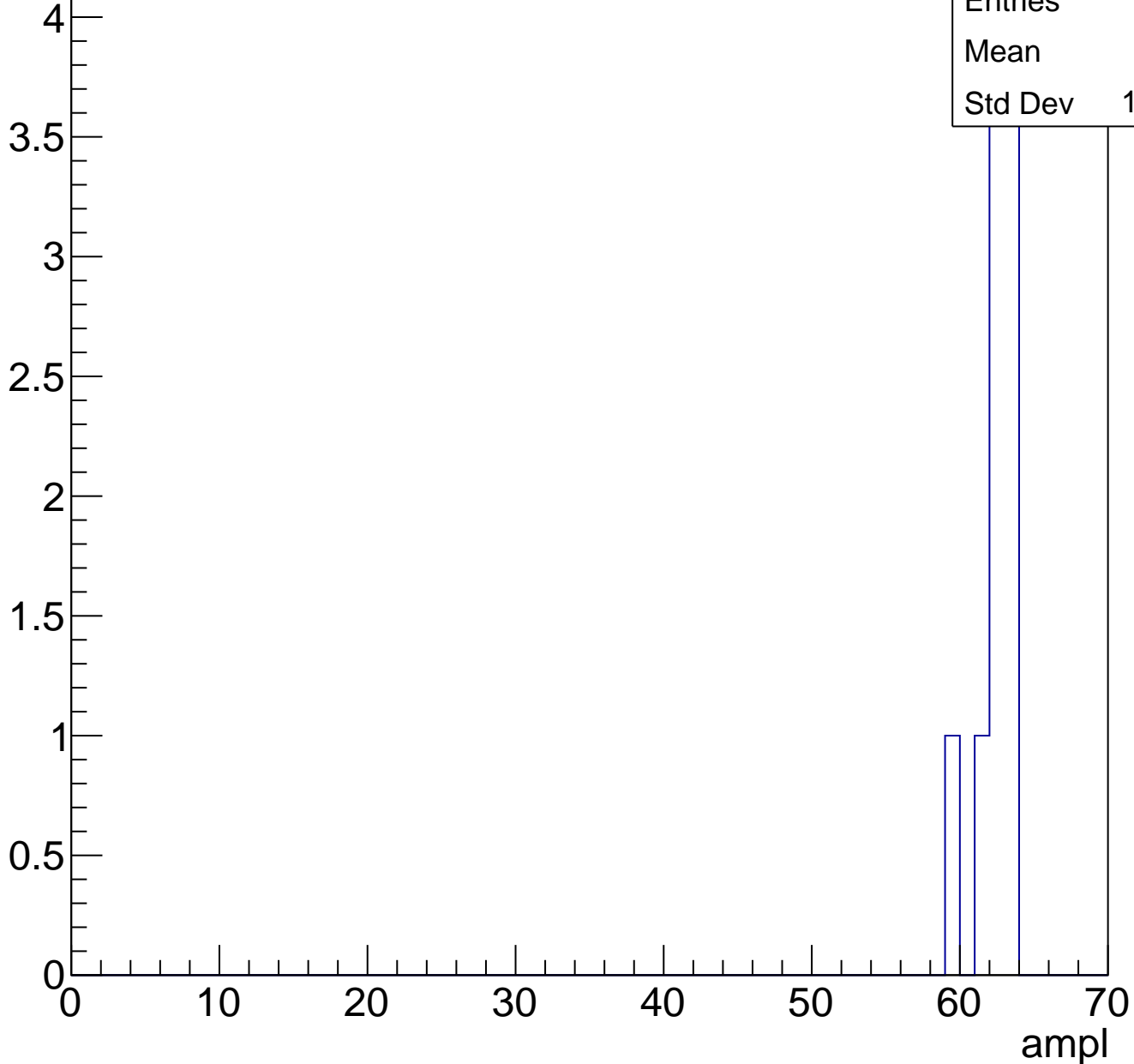
Entries	51
Mean	58.08
Std Dev	8.616



B1L103S, U17-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch106, adc0

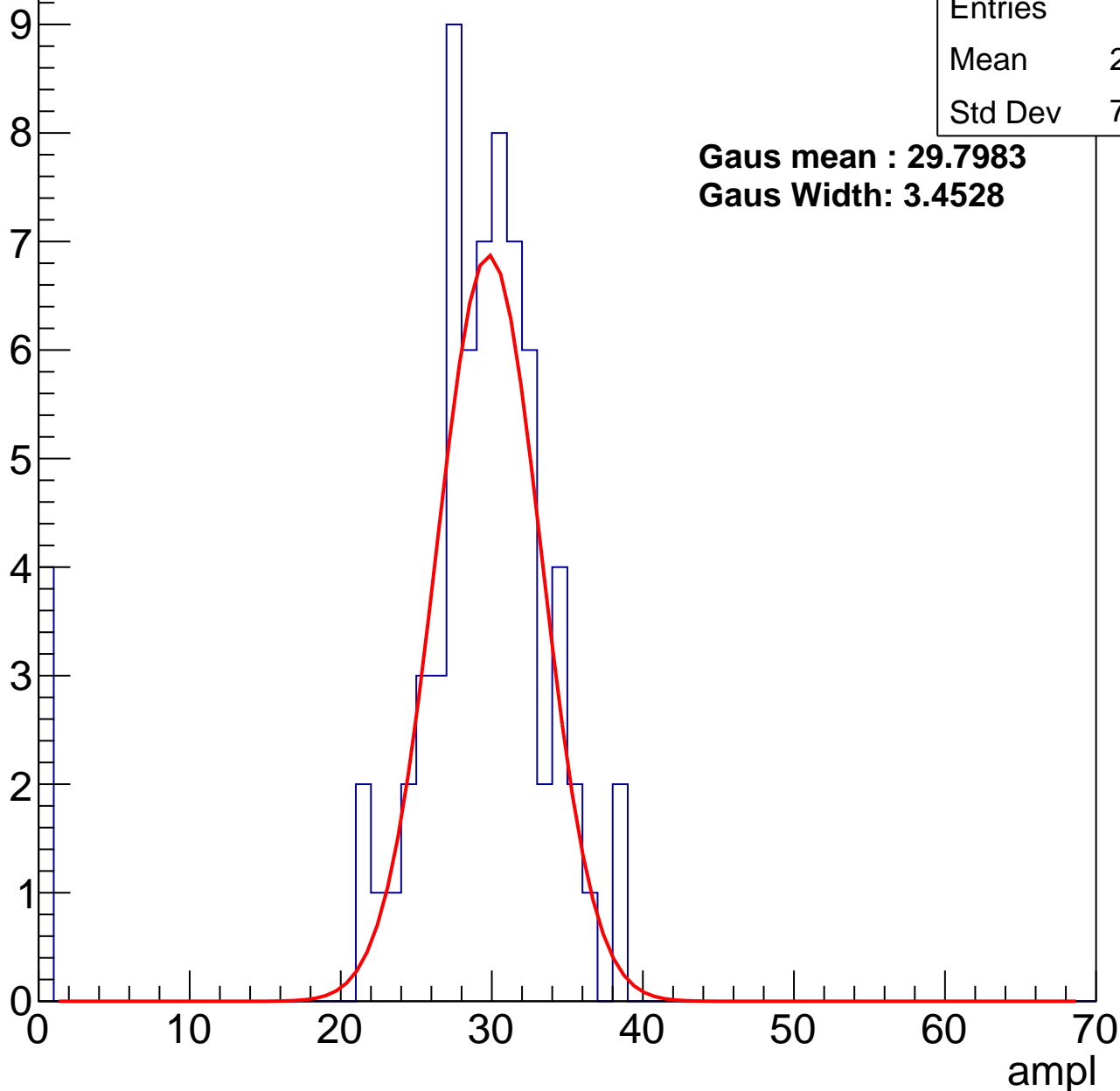
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	27.64
Std Dev	7.672

Gaus mean : 29.7983

Gaus Width: 3.4528



B1L103S, U17-ch106, adc1

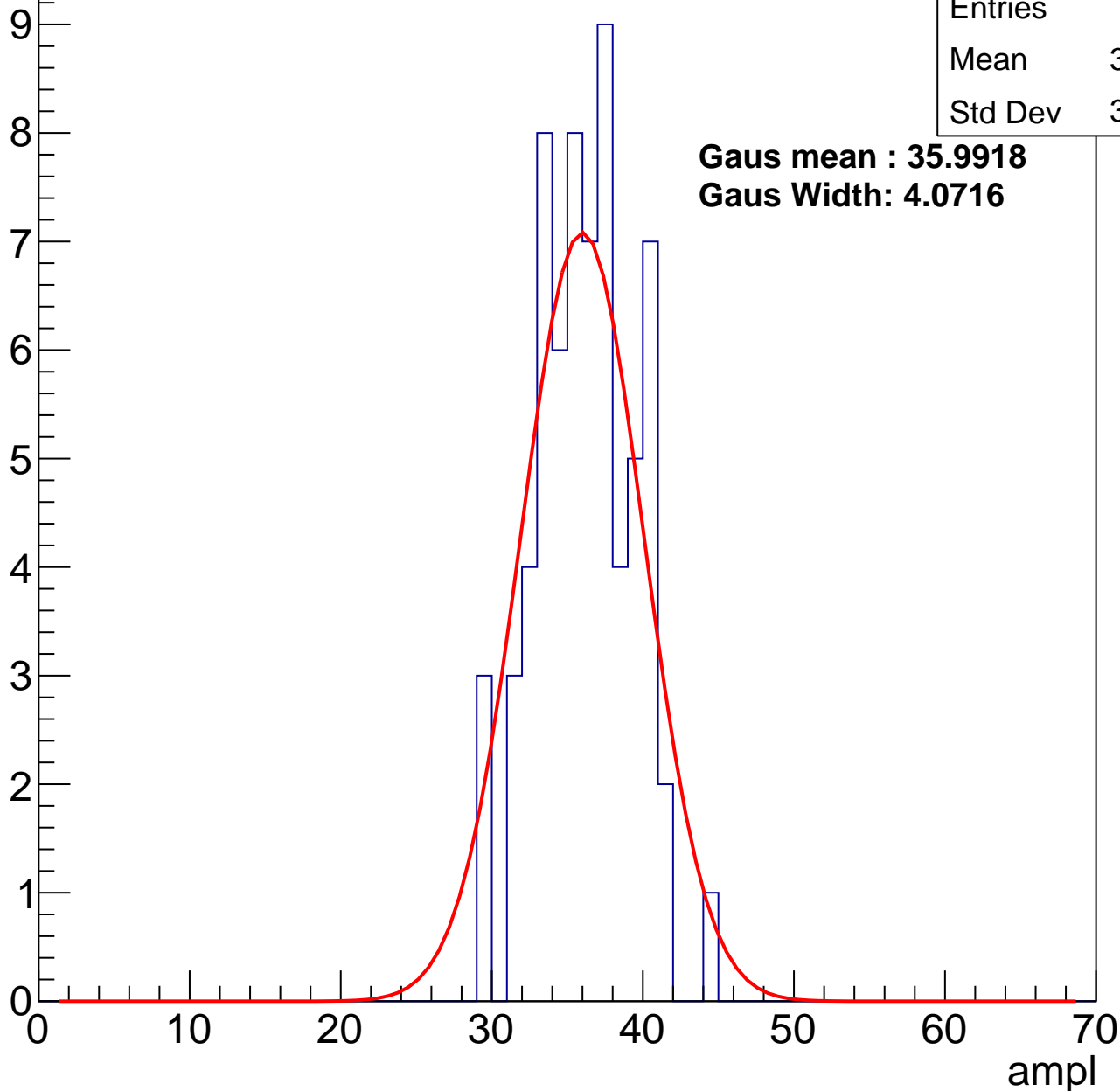
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.73
Std Dev	3.189

Gaus mean : 35.9918

Gaus Width: 4.0716



B1L103S, U17-ch106, adc2

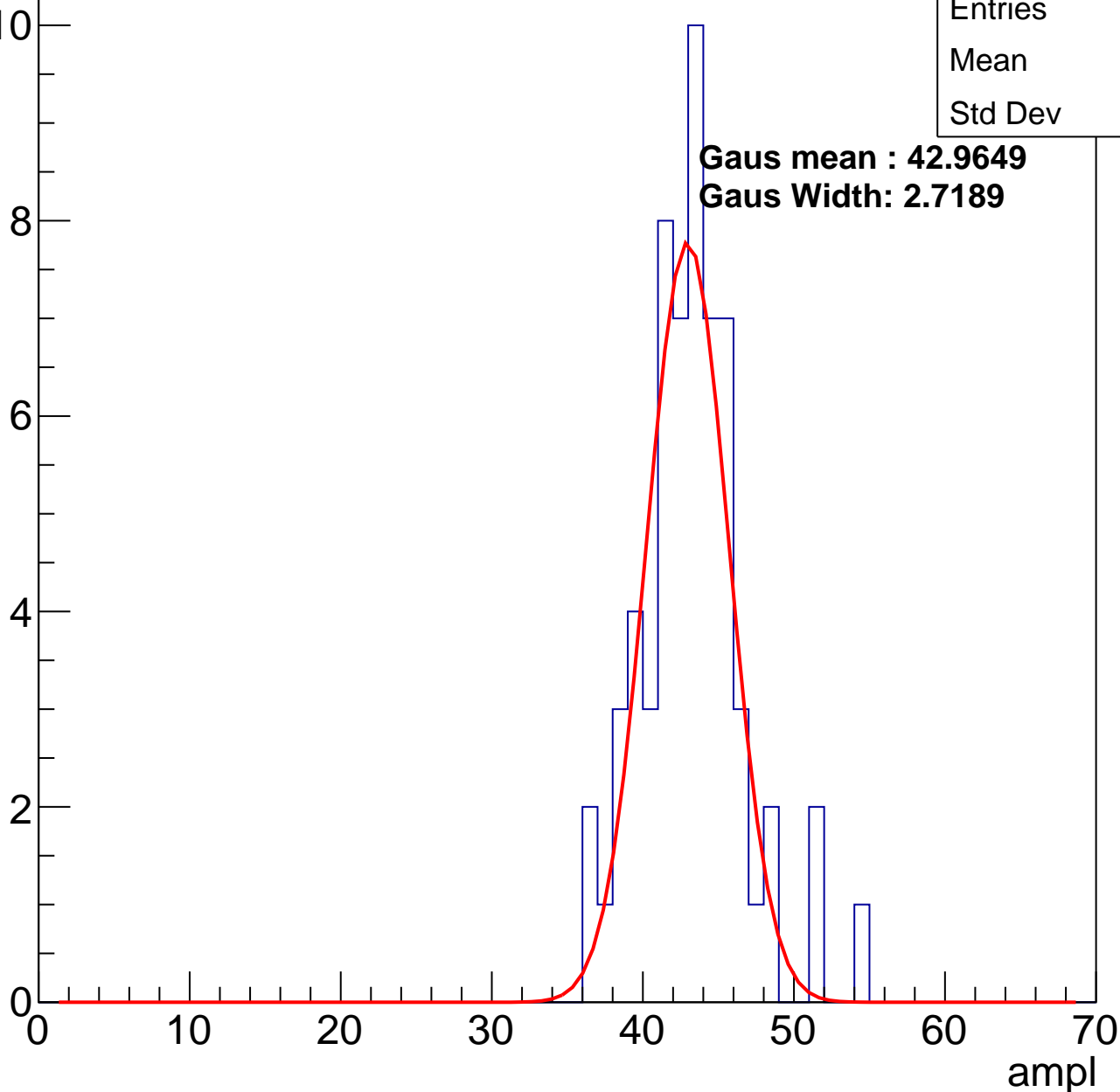
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.8
Std Dev	3.42

Gaus mean : 42.9649

Gaus Width: 2.7189

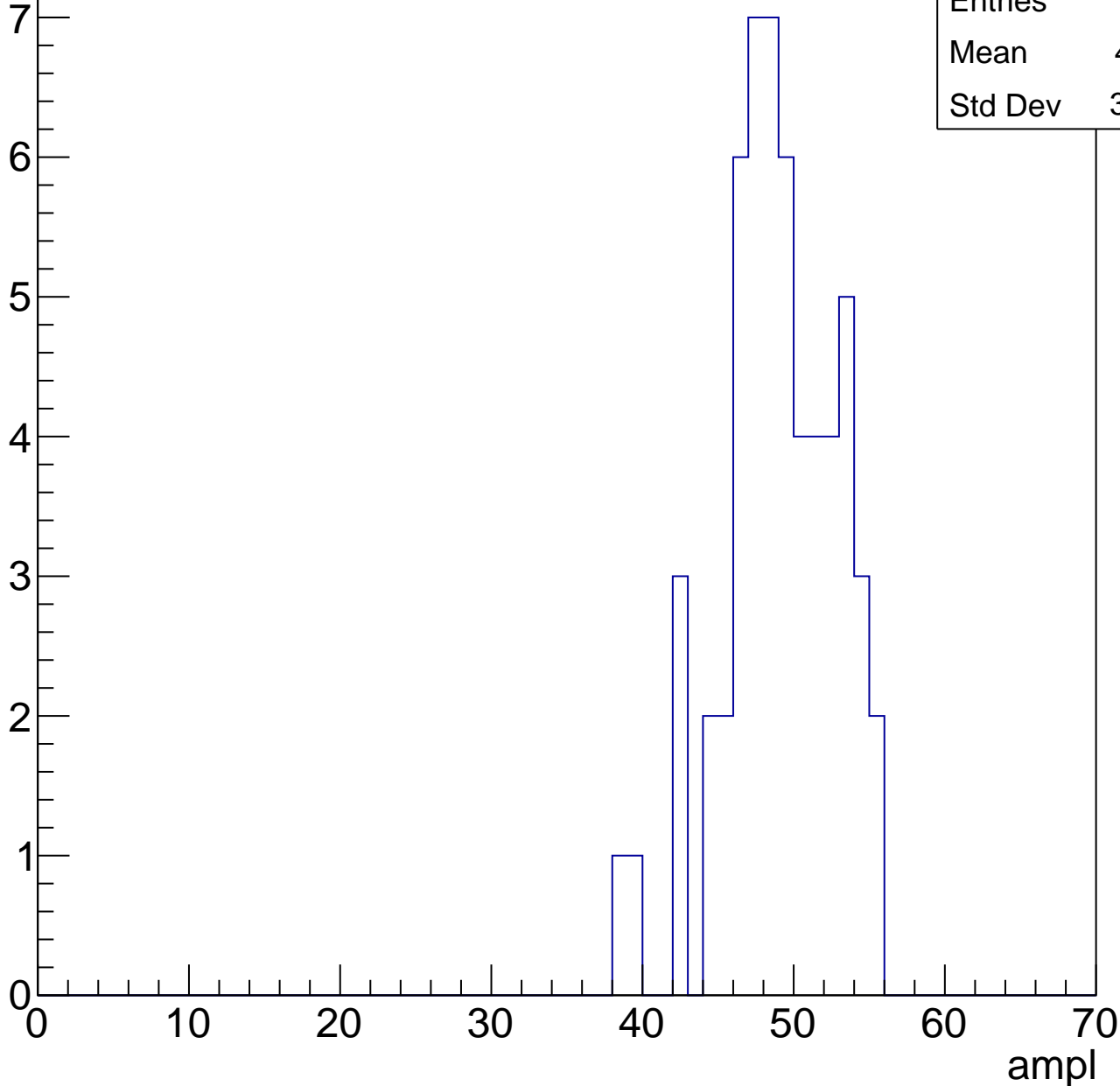


B1L103S, U17-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	48.51
Std Dev	3.766

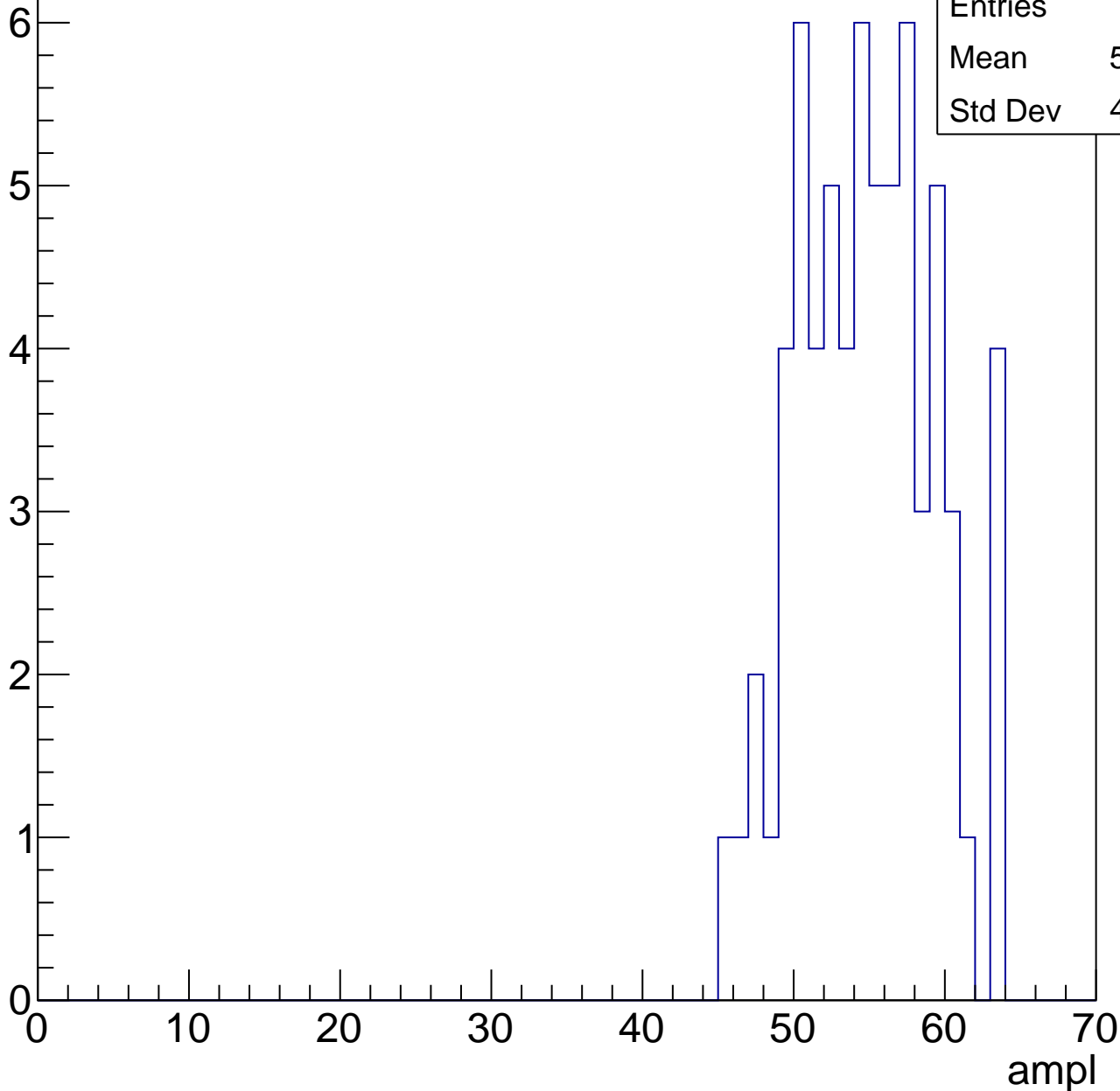


B1L103S, U17-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.36
Std Dev	4.368

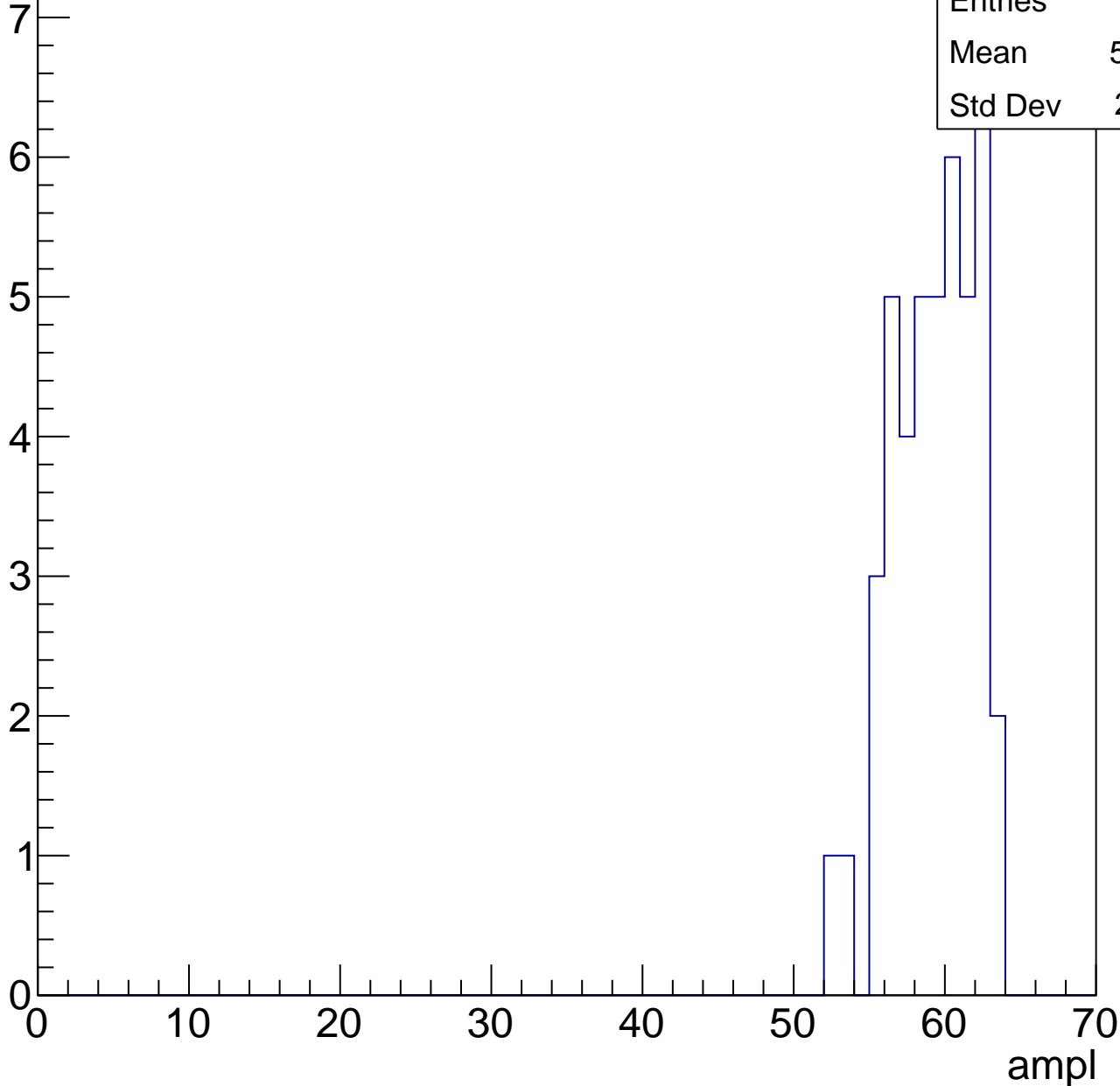


B1L103S, U17-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

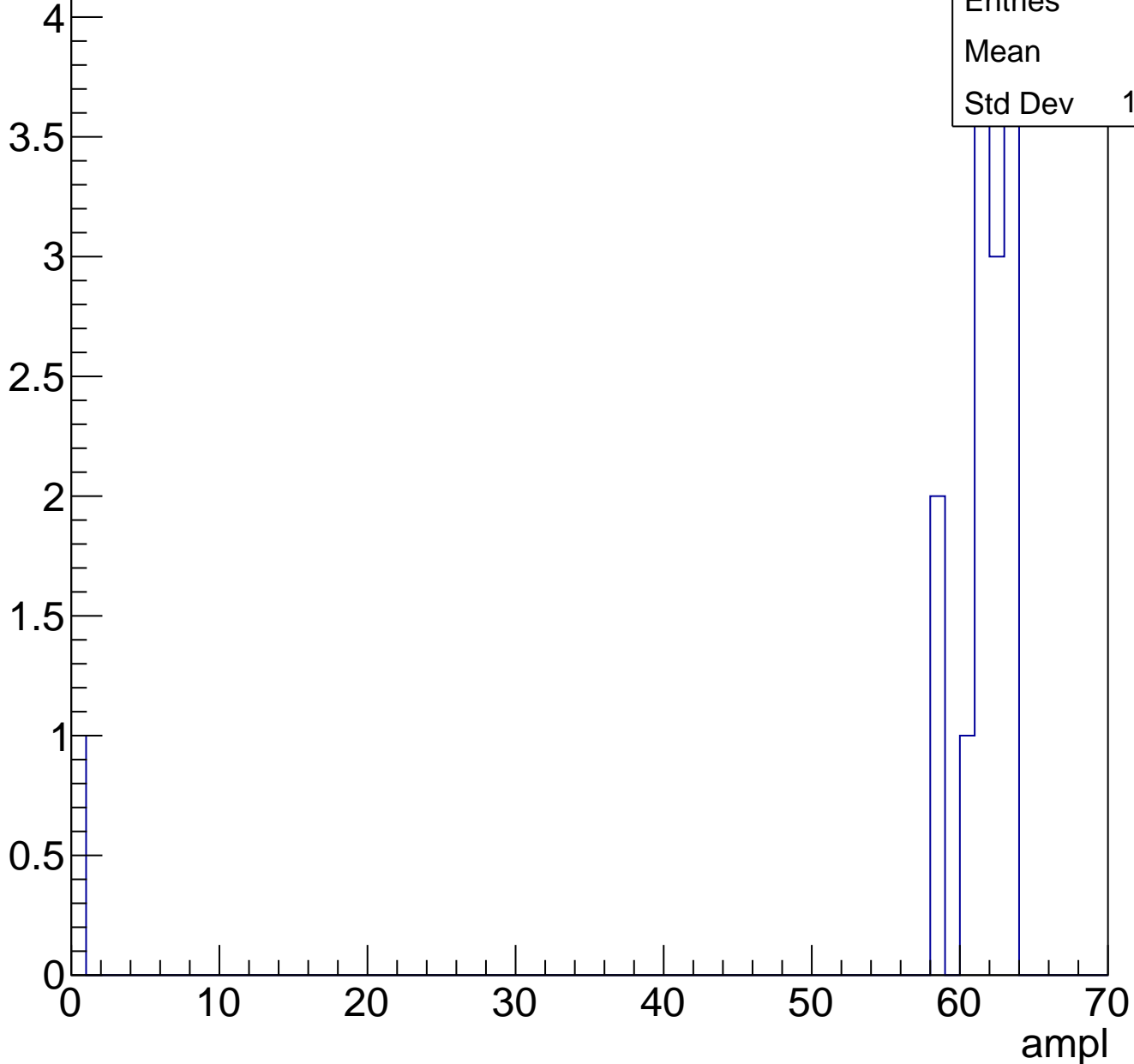
Entries	44
Mean	58.82
Std Dev	2.691



B1L103S, U17-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

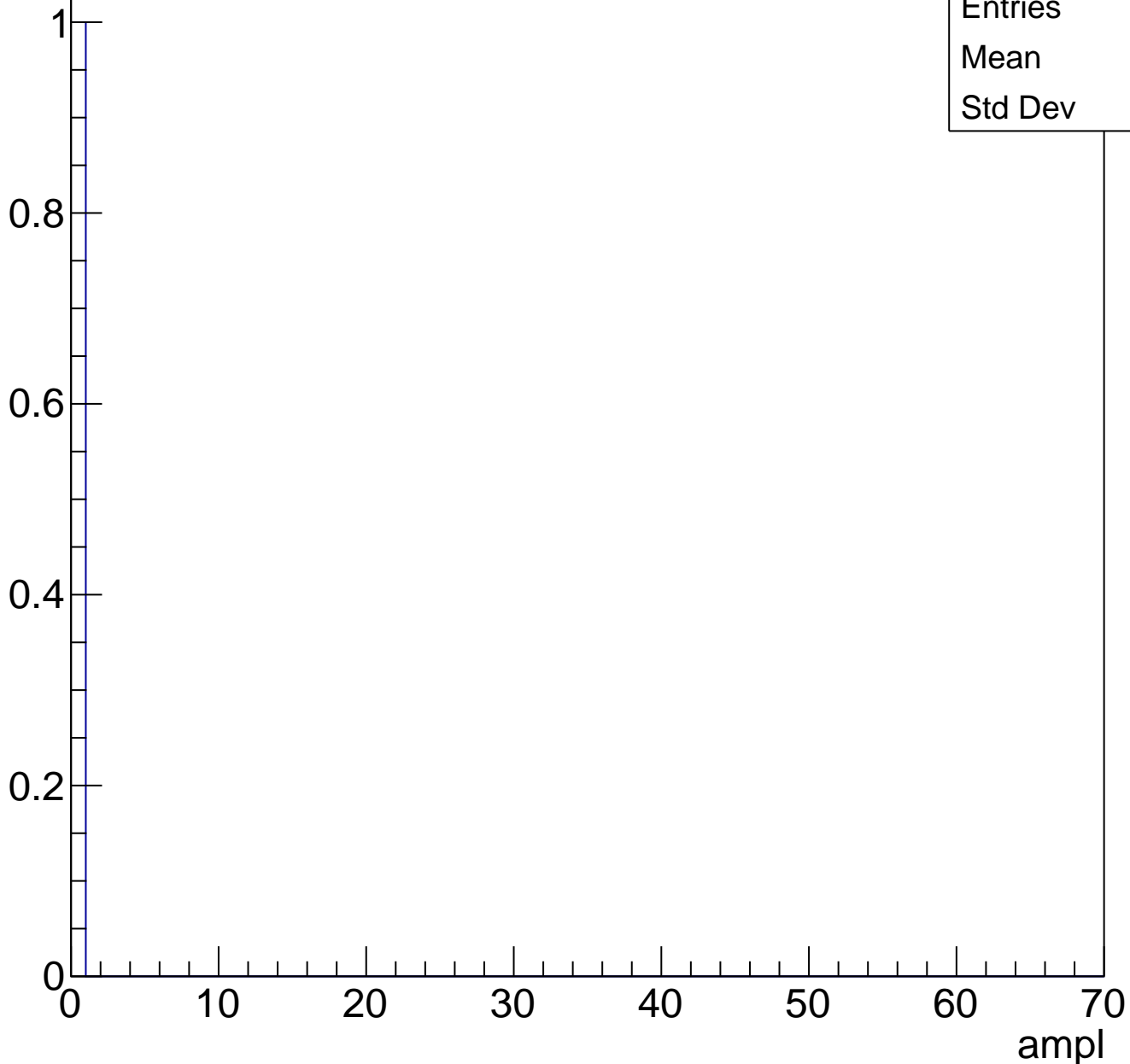
Entry



B1L103S, U17-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch107, adc0

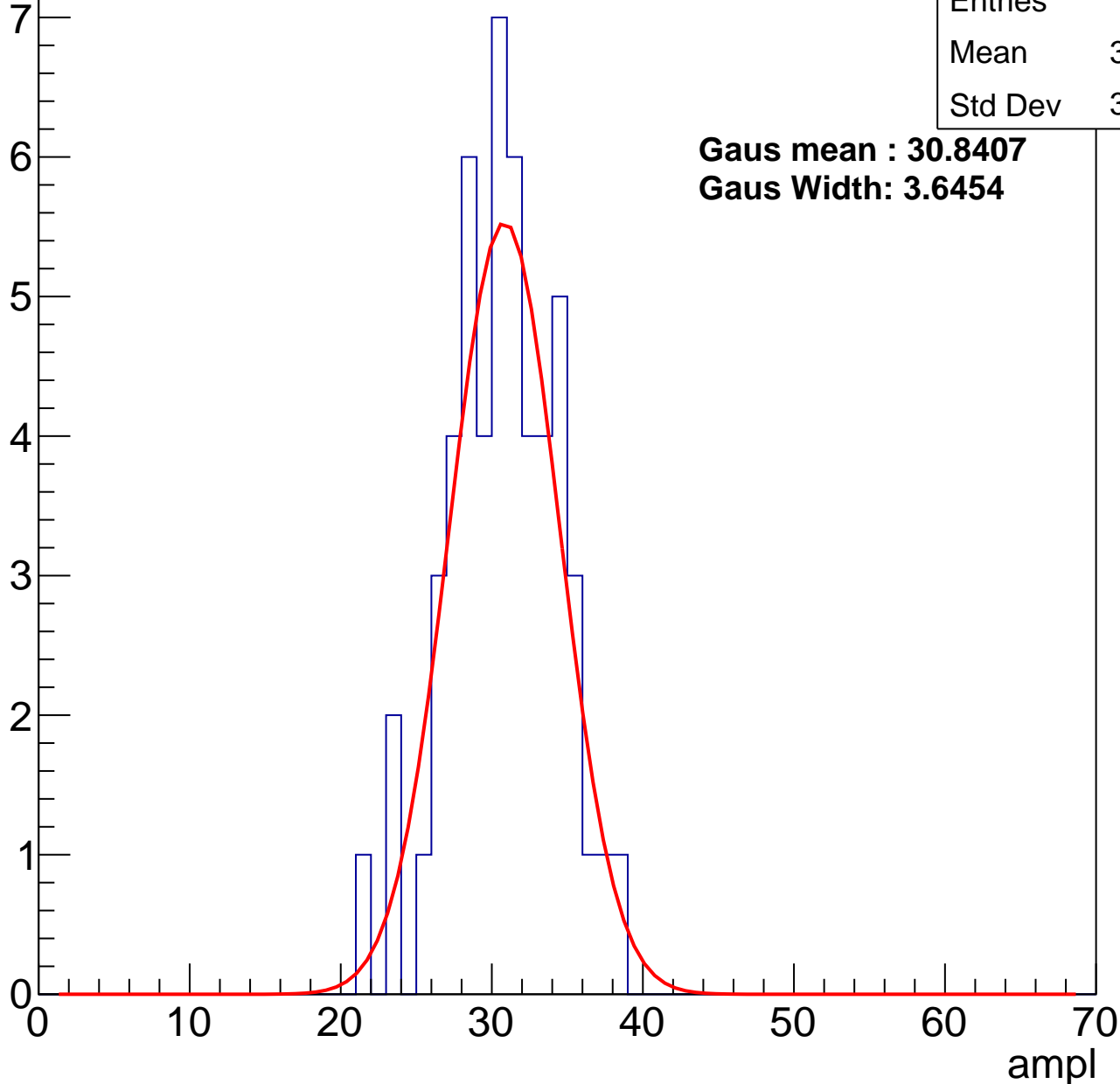
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	30.26
Std Dev	3.567

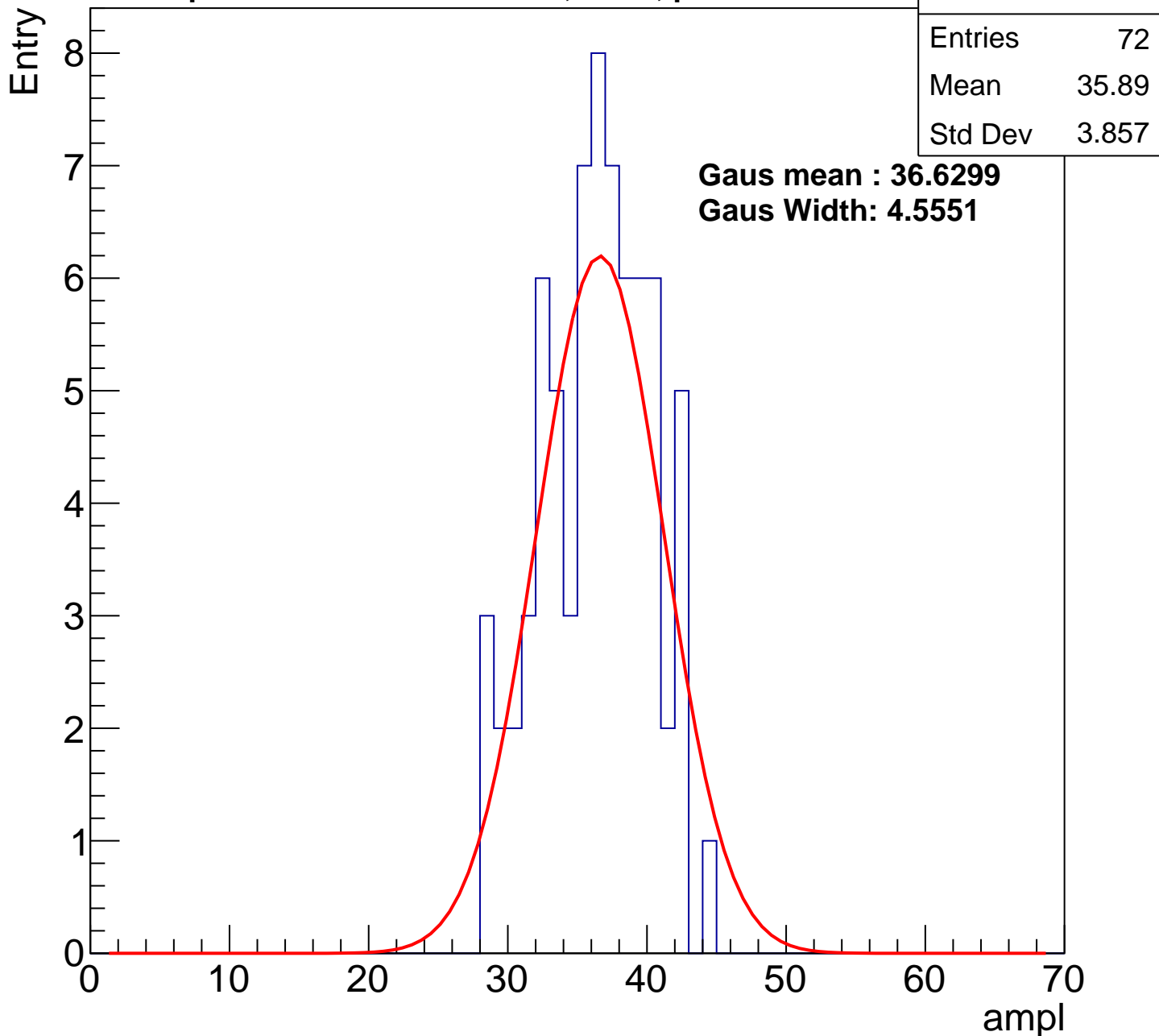
Gaus mean : 30.8407

Gaus Width: 3.6454



B1L103S, U17-ch107, adc1

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U17-ch107, adc2

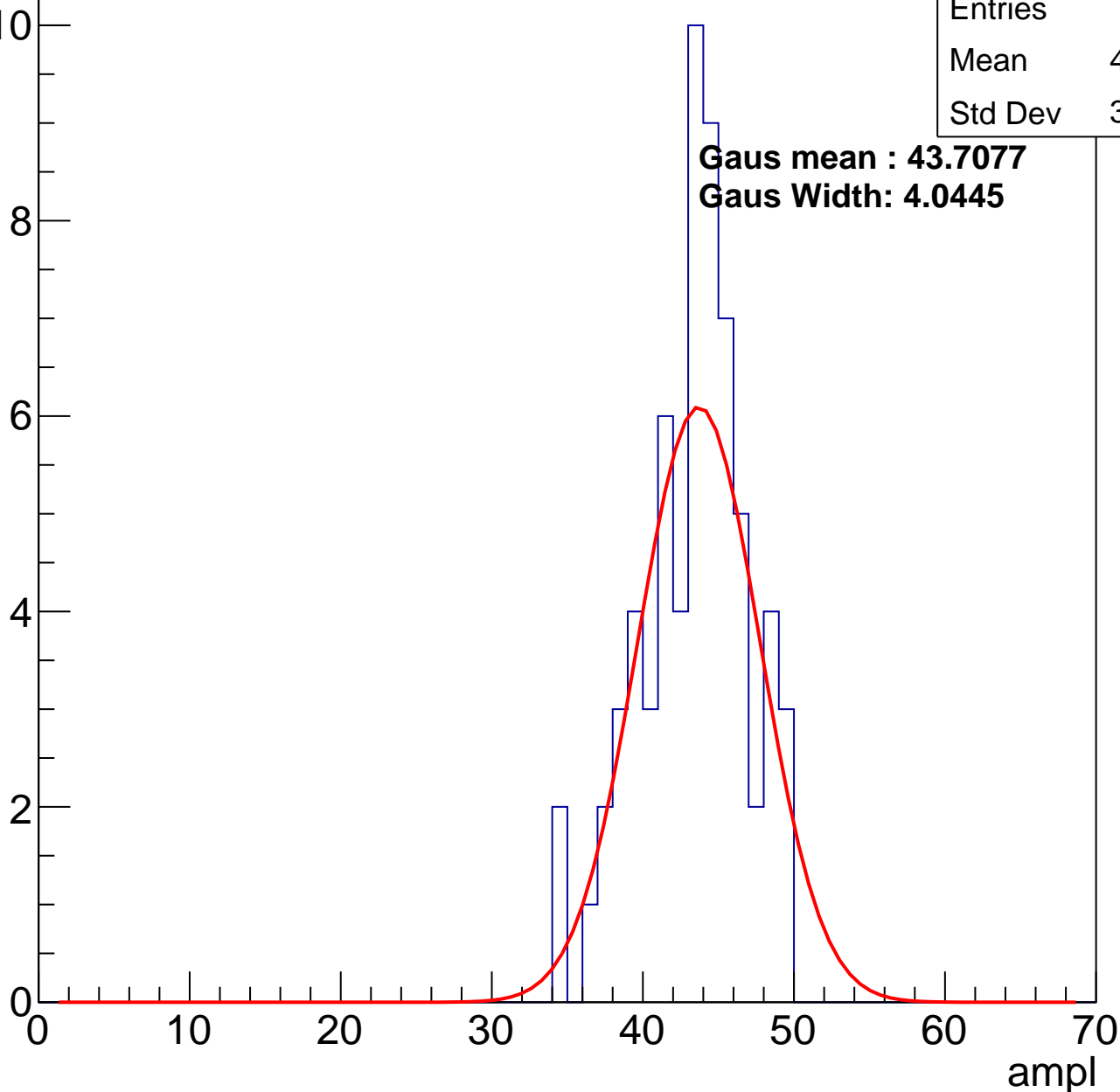
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.86
Std Dev	3.503

Gaus mean : 43.7077

Gaus Width: 4.0445

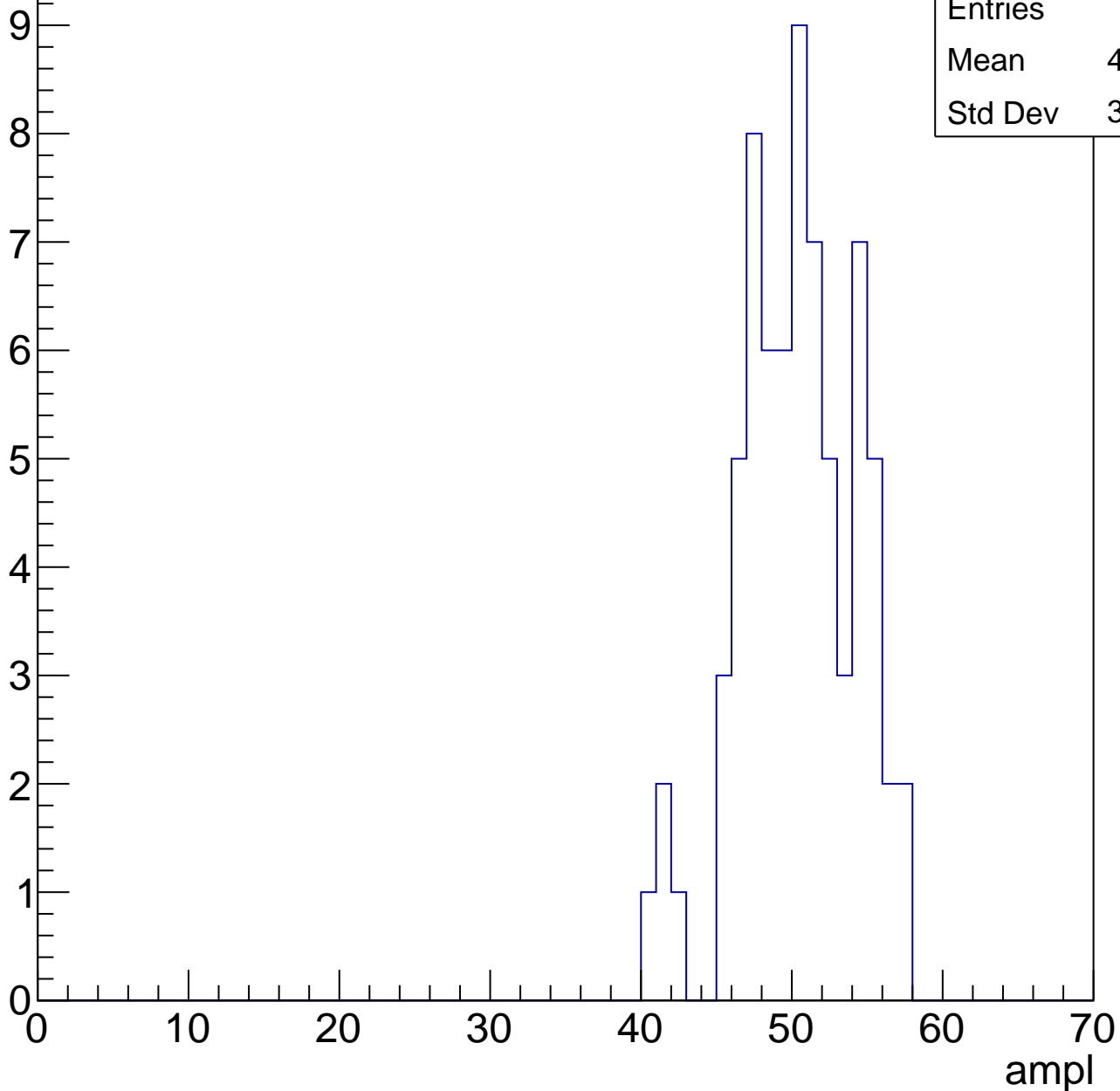


B1L103S, U17-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.89
Std Dev	3.806

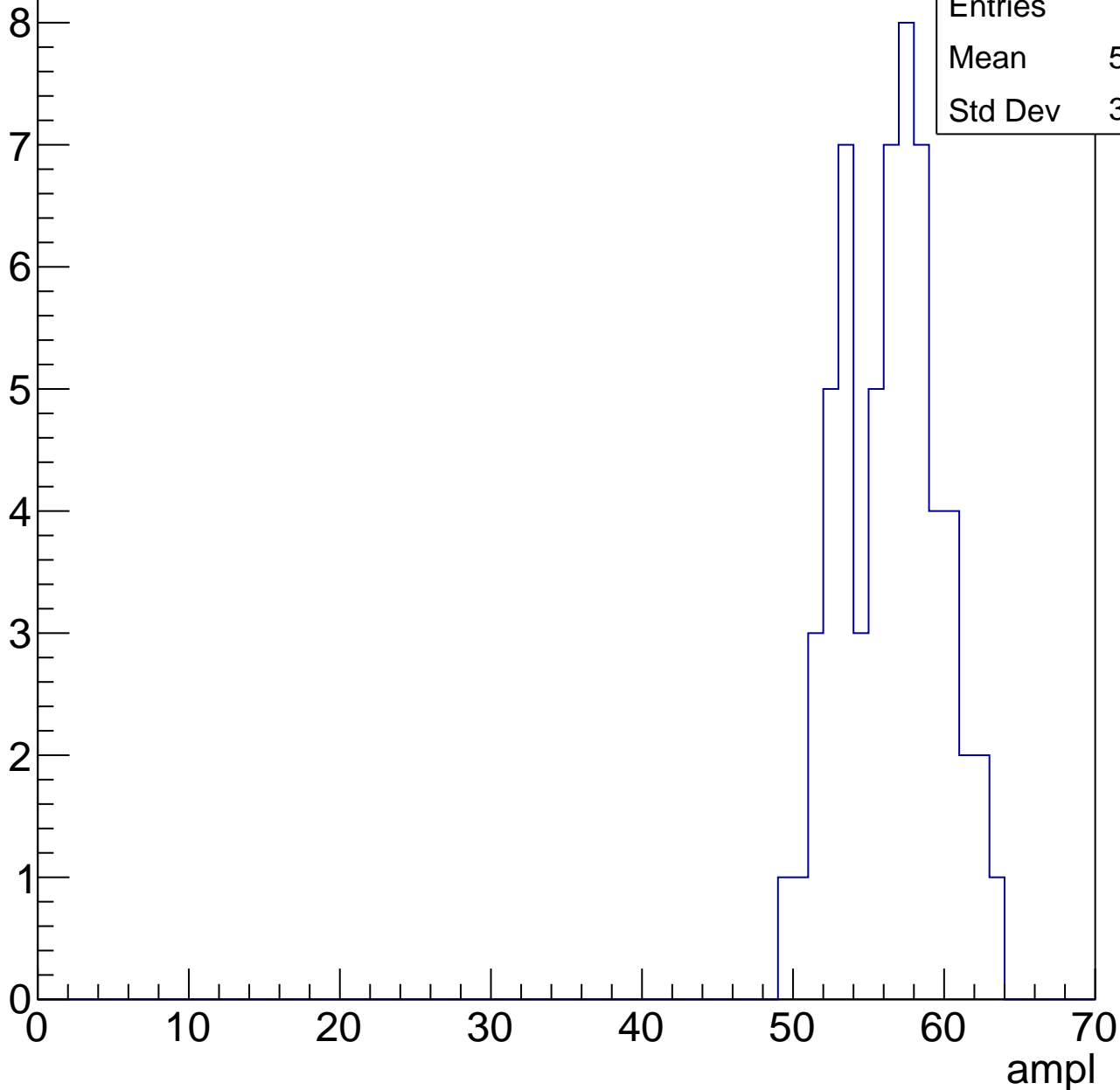


B1L103S, U17-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.98
Std Dev	3.233

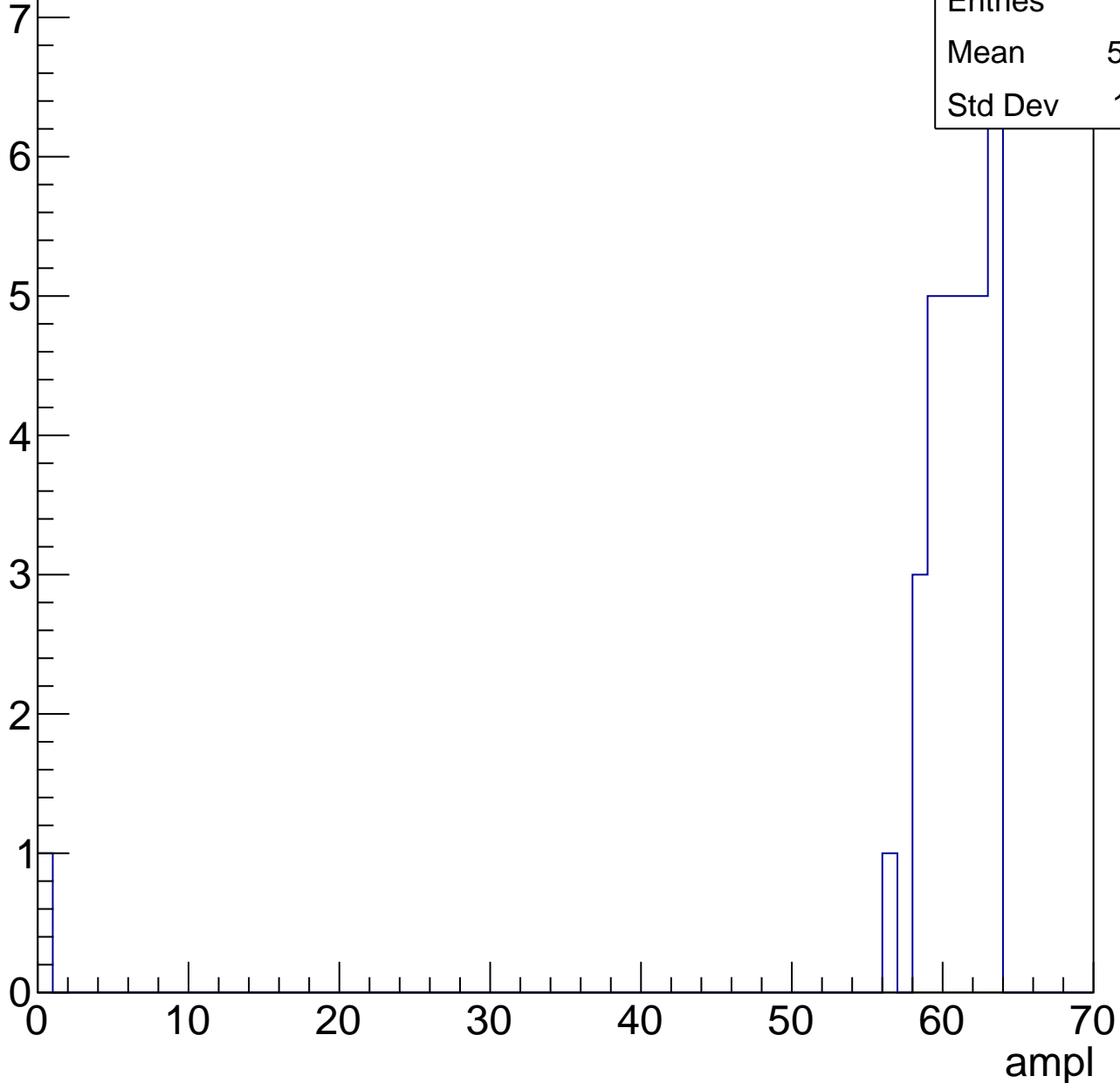


B1L103S, U17-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

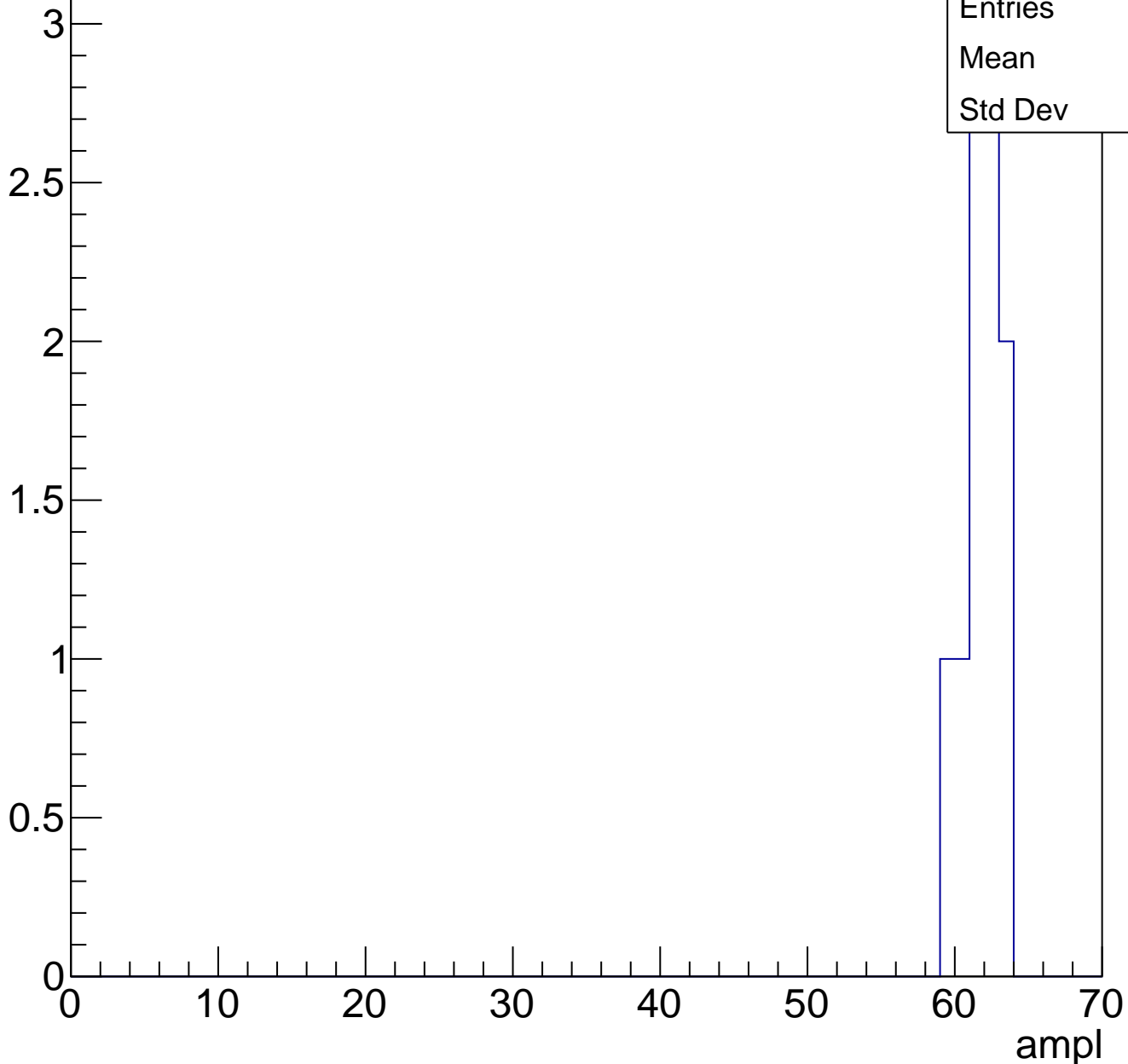
Entries	32
Mean	58.78
Std Dev	10.71



B1L103S, U17-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.4
Std Dev	1.2

B1L103S, U17-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch108, adc0

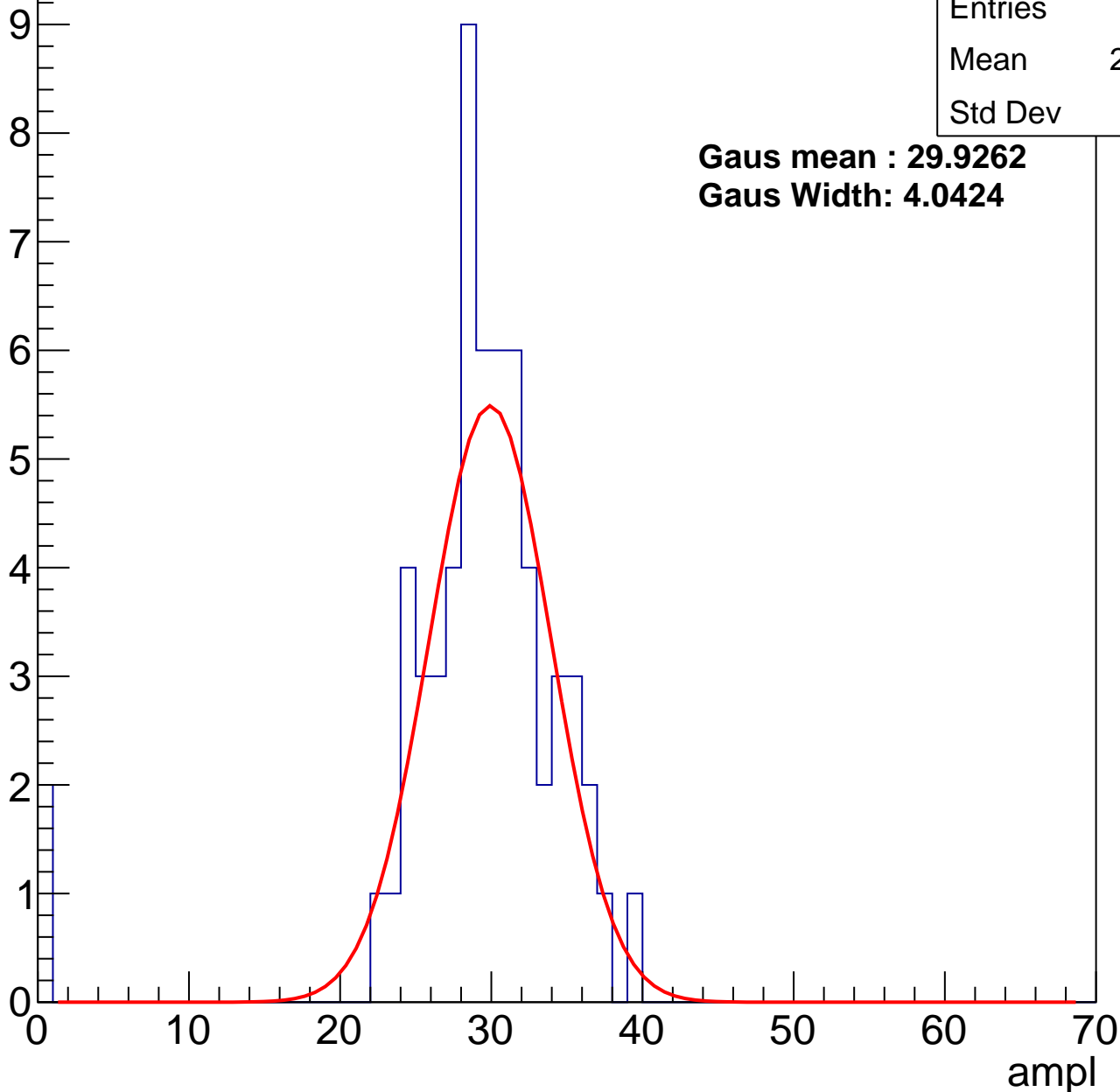
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.57
Std Dev	6.4

Gaus mean : 29.9262

Gaus Width: 4.0424



B1L103S, U17-ch108, adc1

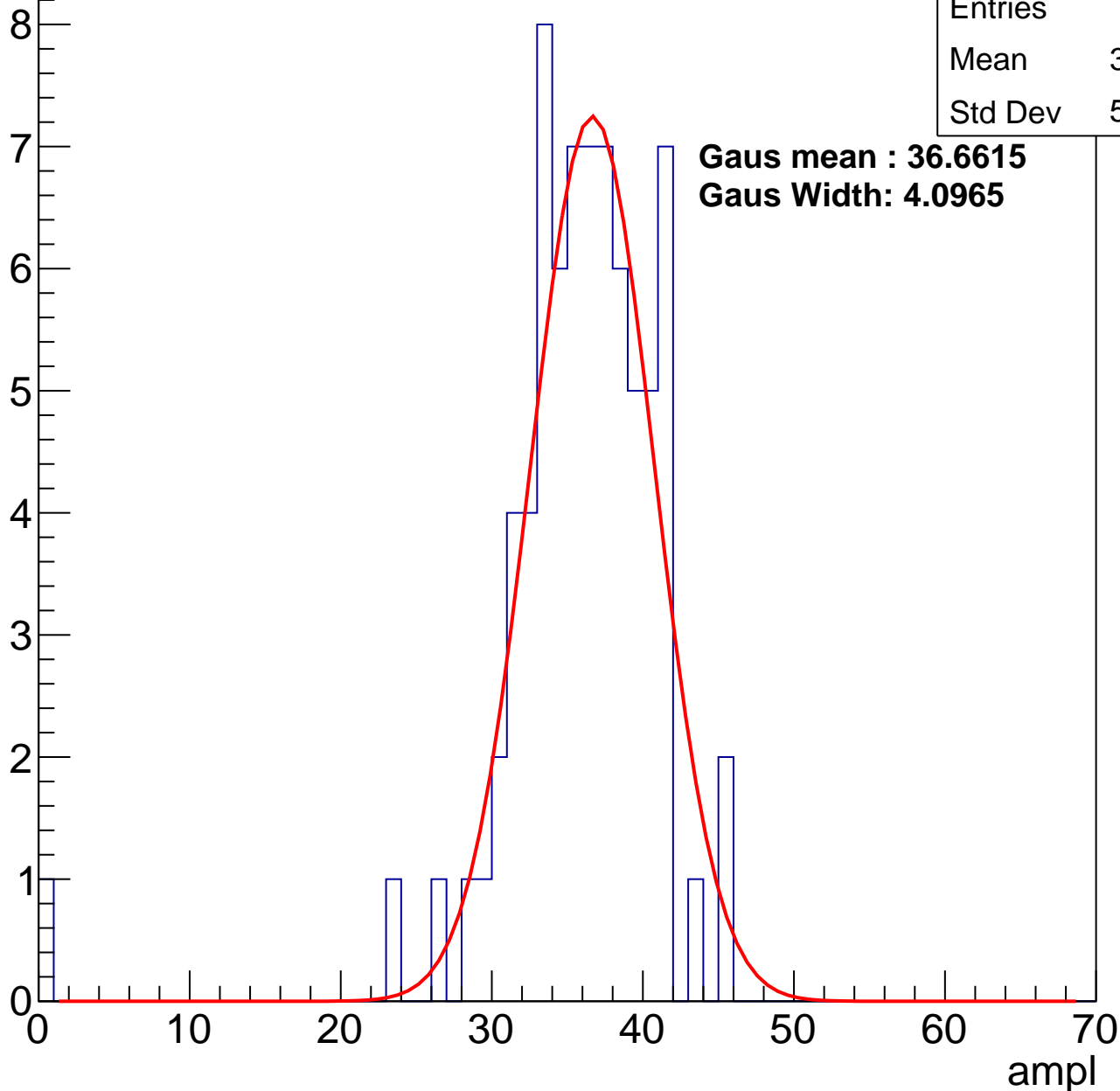
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.33
Std Dev	5.764

Gaus mean : 36.6615

Gaus Width: 4.0965



B1L103S, U17-ch108, adc2

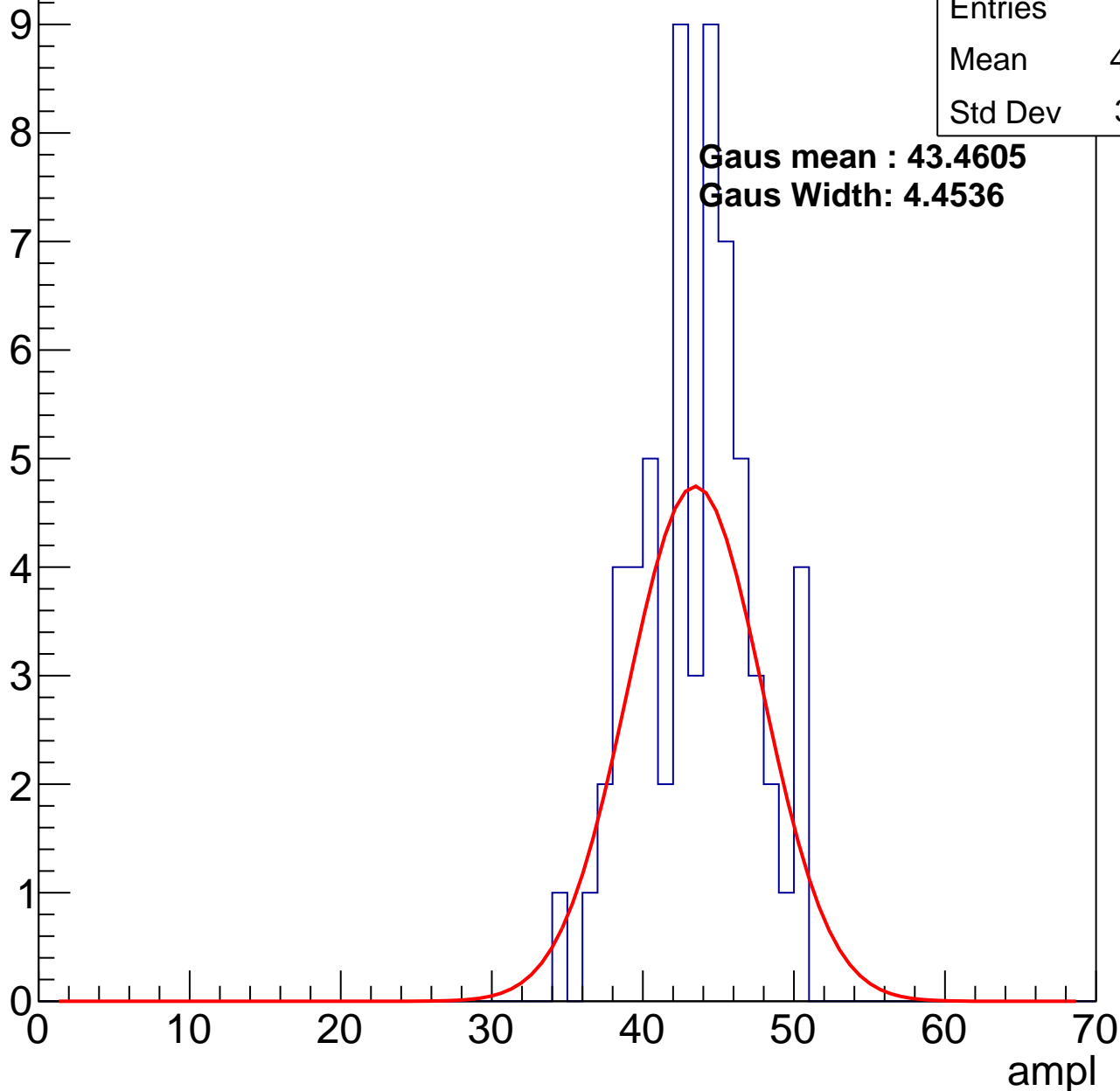
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.03
Std Dev	3.681

Gaus mean : 43.4605

Gaus Width: 4.4536

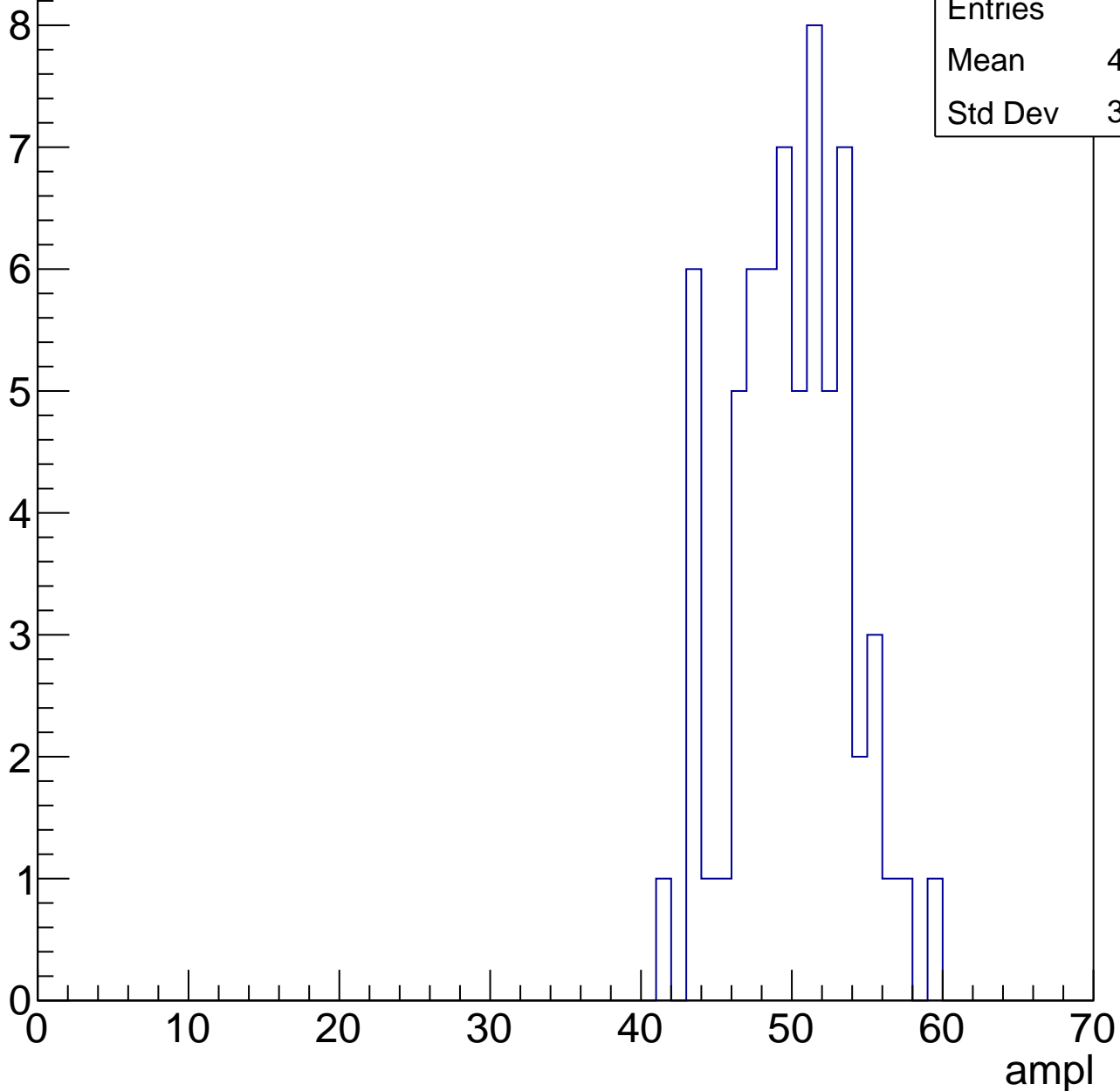


B1L103S, U17-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.47
Std Dev	3.787

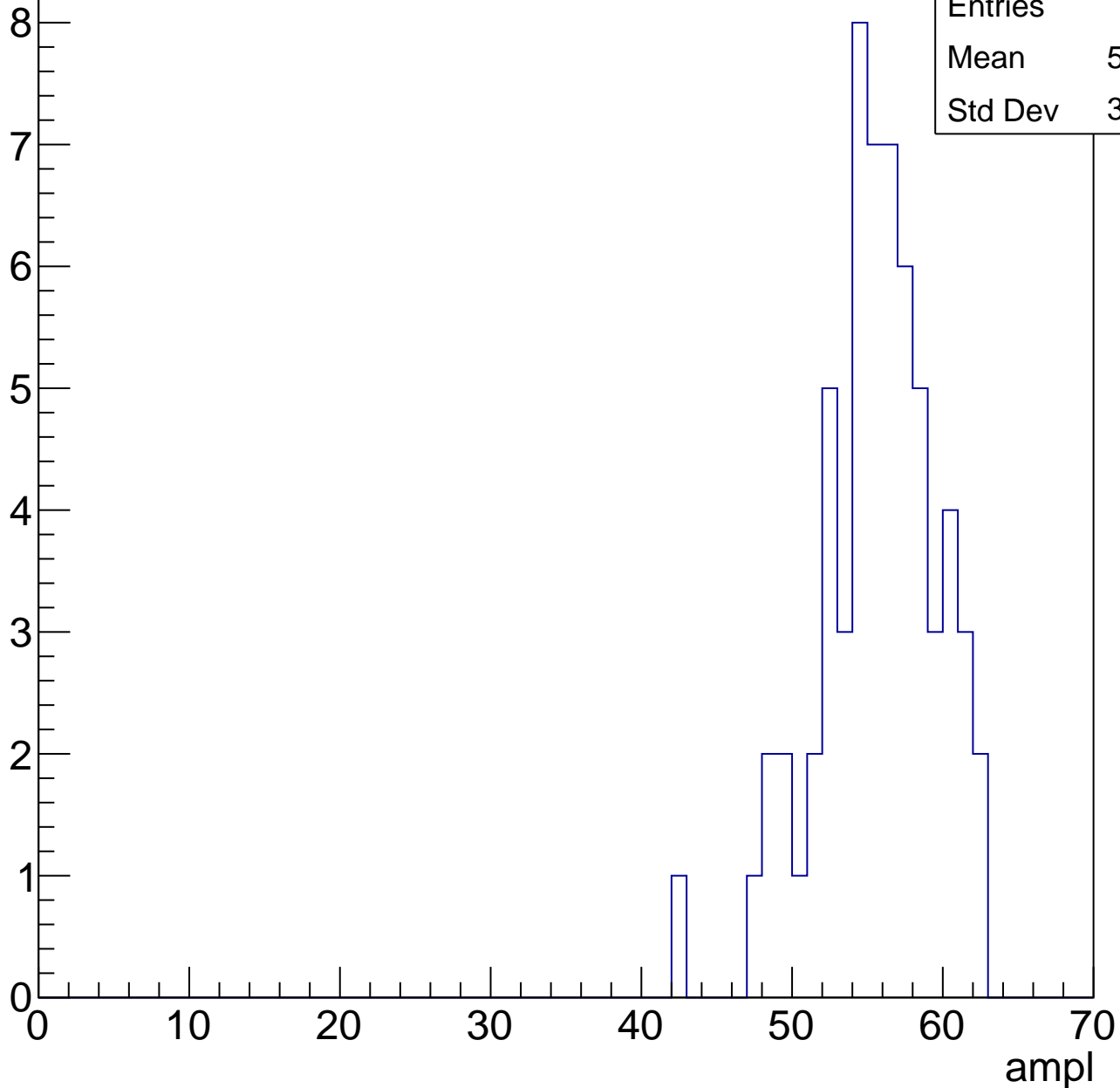


B1L103S, U17-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	55.15
Std Dev	3.922

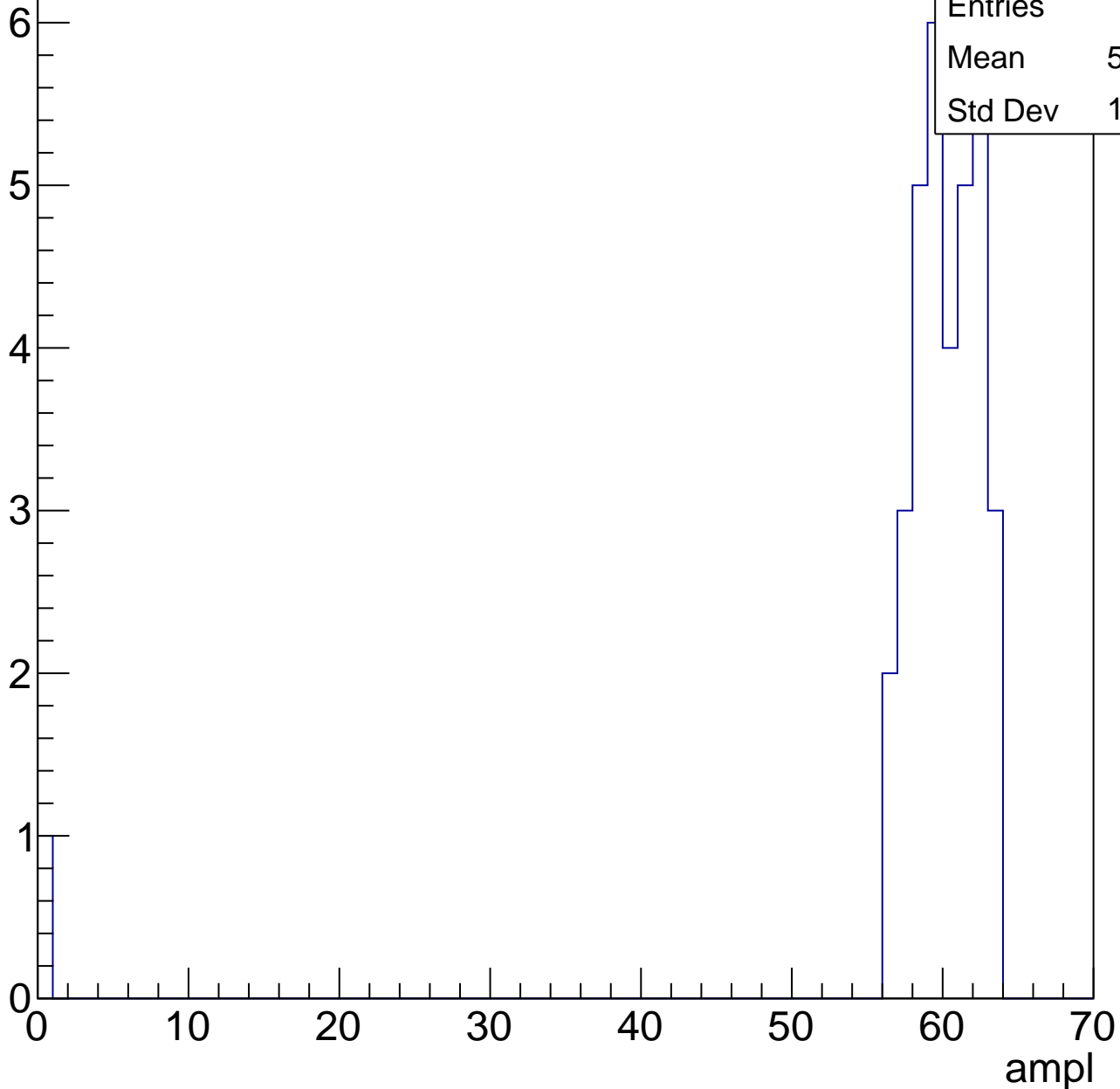


B1L103S, U17-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

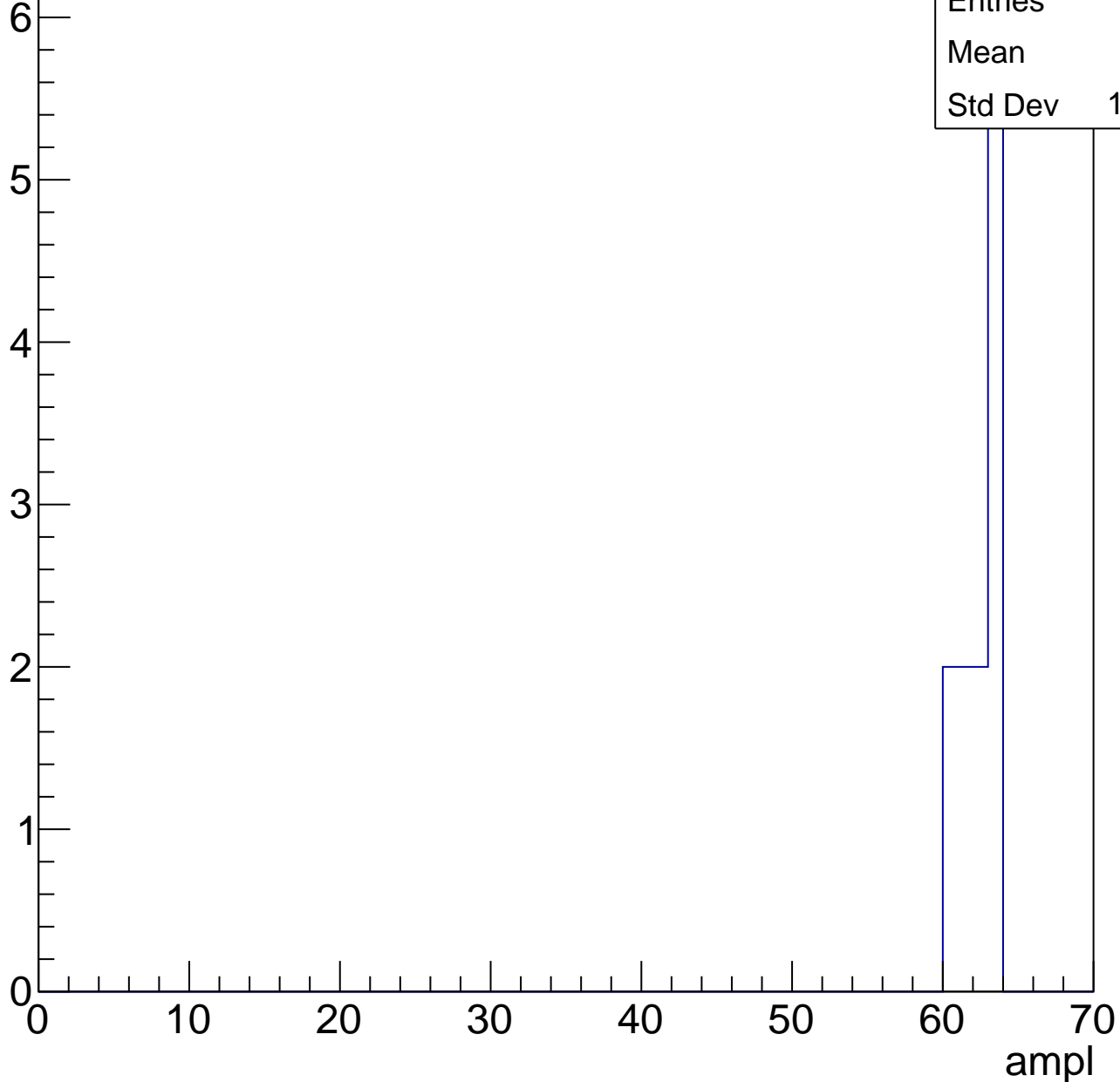
Entries	35
Mean	58.09
Std Dev	10.16



B1L103S, U17-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	62
Std Dev	1.155

B1L103S, U17-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch109, adc0

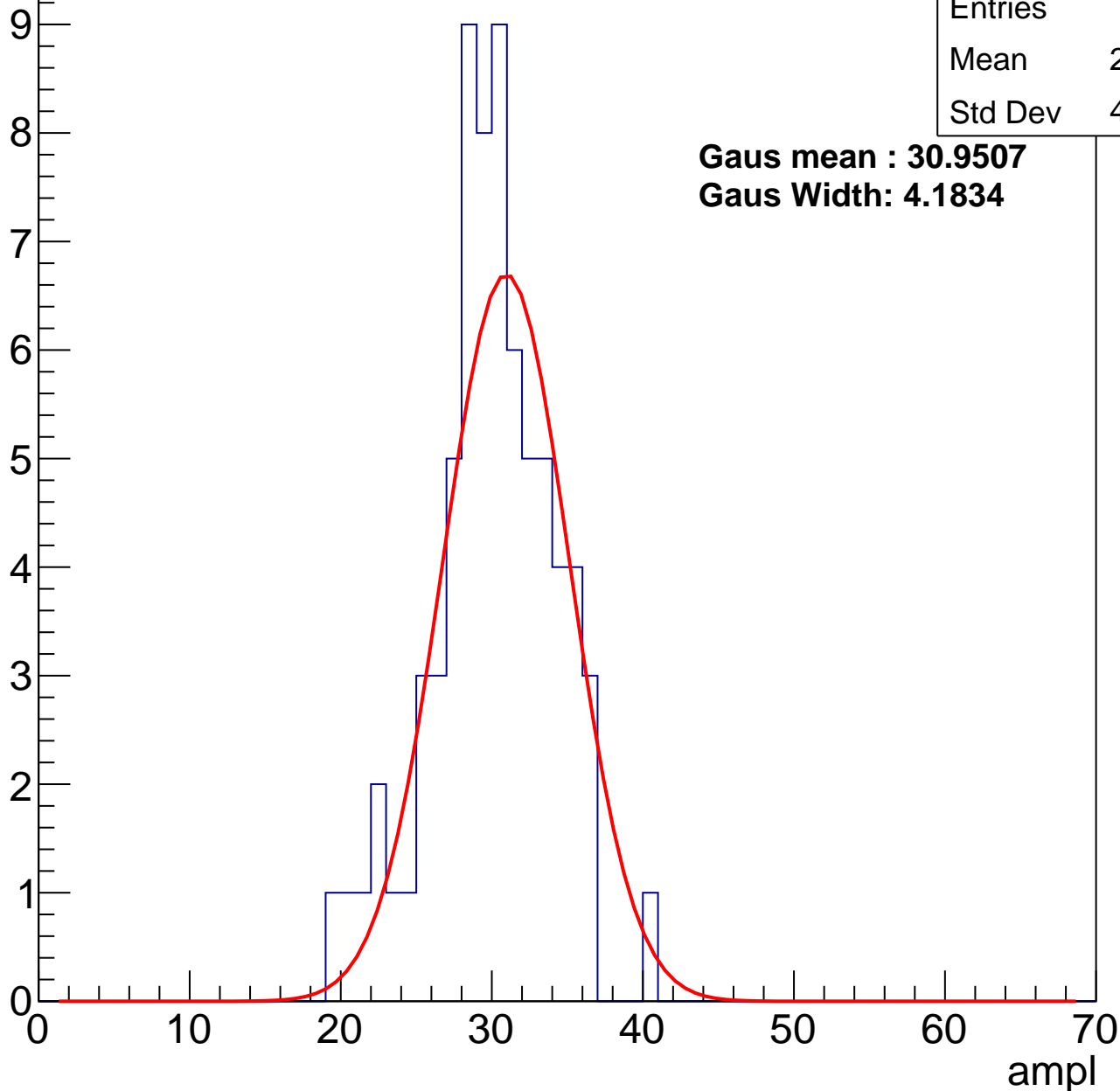
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.56
Std Dev	4.007

Gaus mean : 30.9507

Gaus Width: 4.1834



B1L103S, U17-ch109, adc1

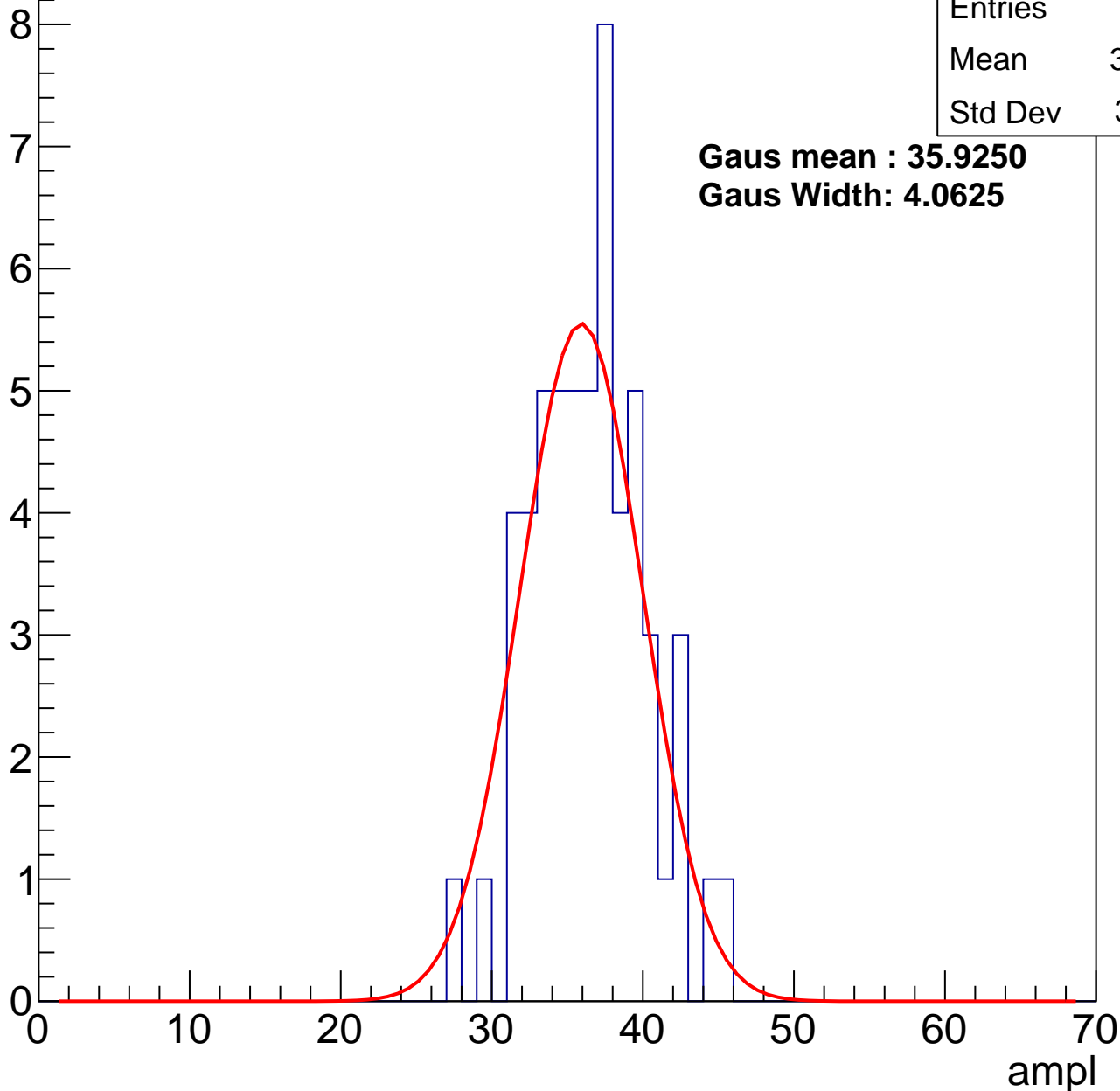
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.02
Std Dev	3.681

Gaus mean : 35.9250

Gaus Width: 4.0625



B1L103S, U17-ch109, adc2

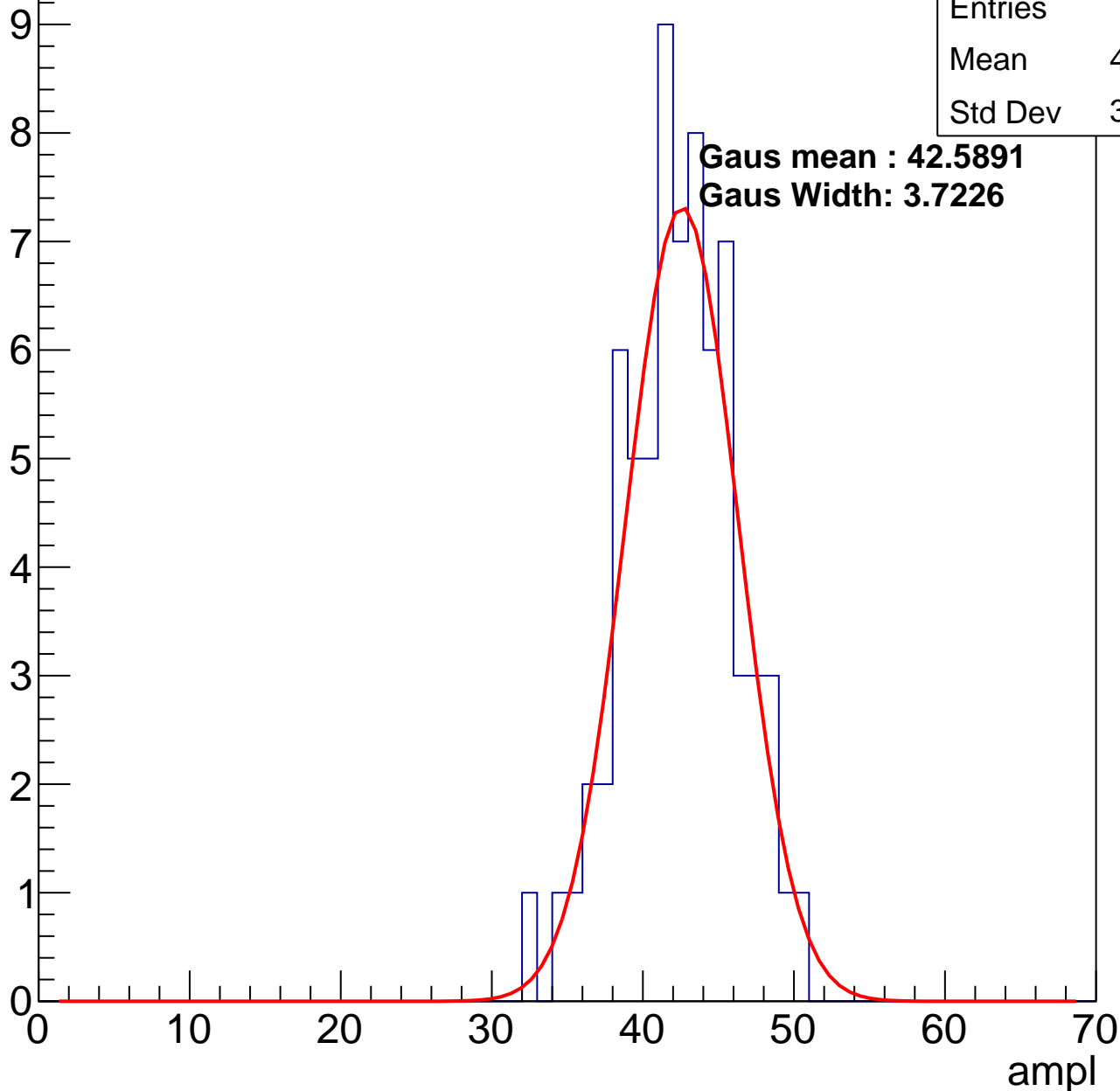
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	41.94
Std Dev	3.646

Gaus mean : 42.5891

Gaus Width: 3.7226

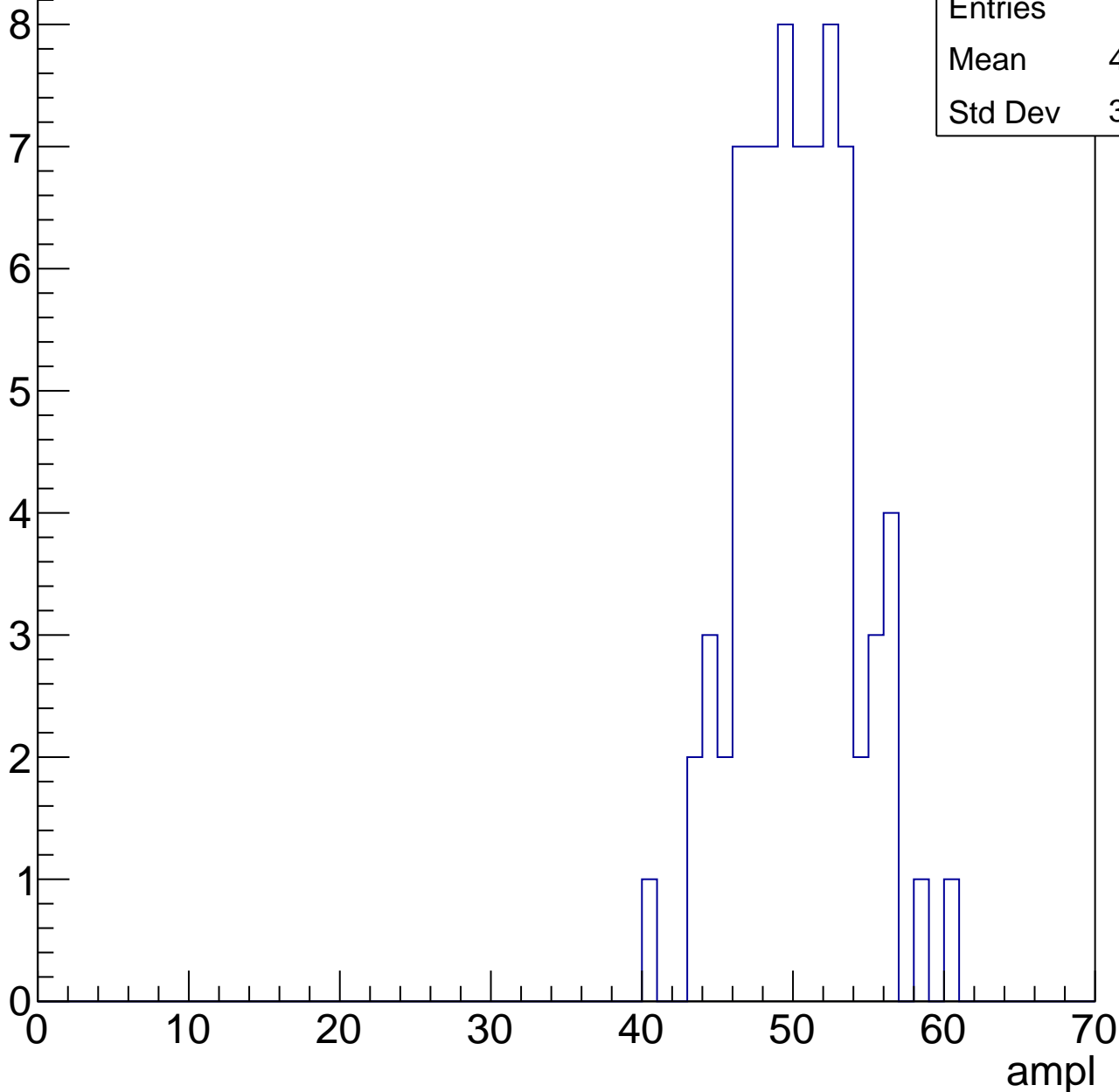


B1L103S, U17-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	49.82
Std Dev	3.748

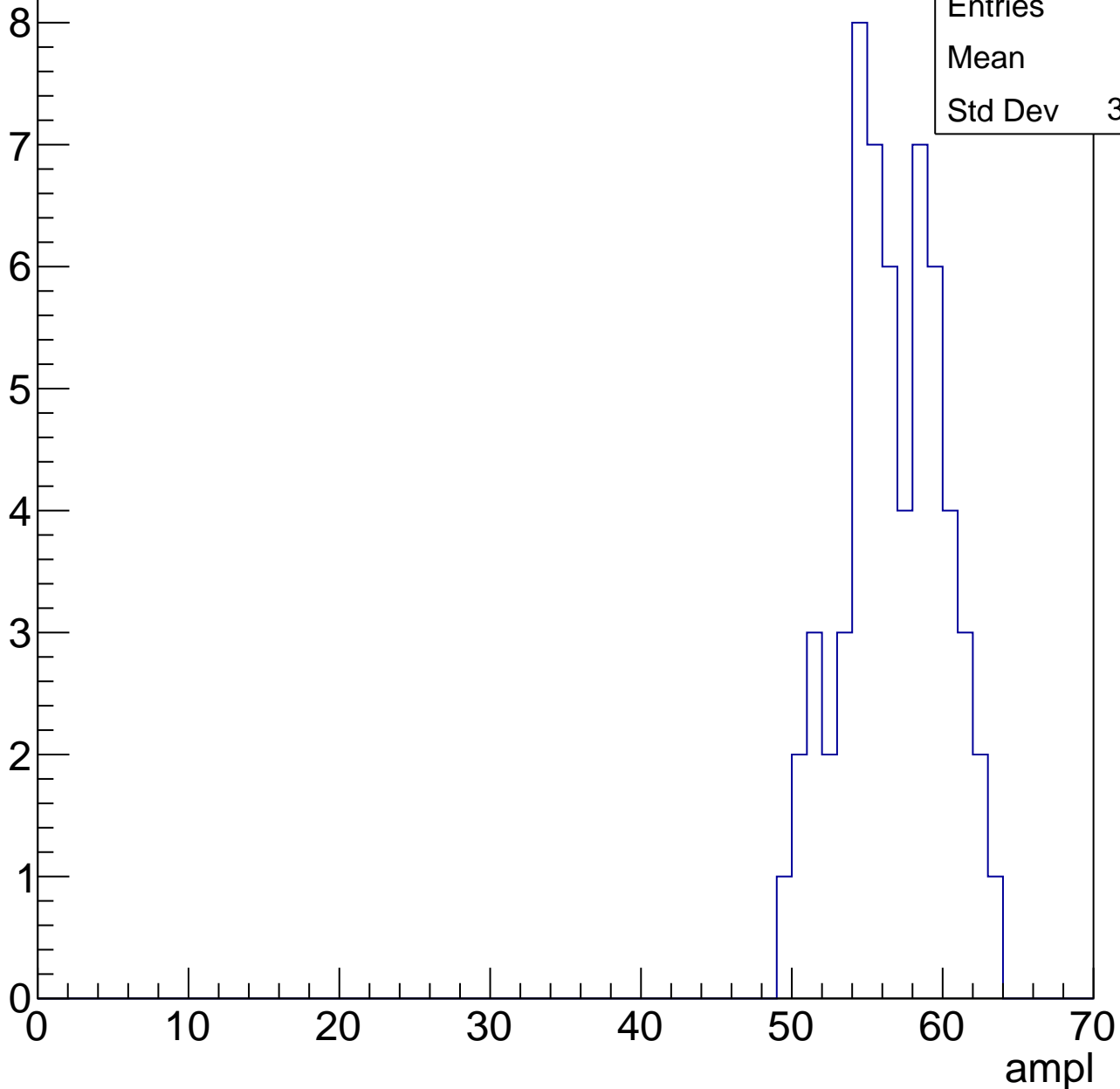


B1L103S, U17-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	56.2
Std Dev	3.287

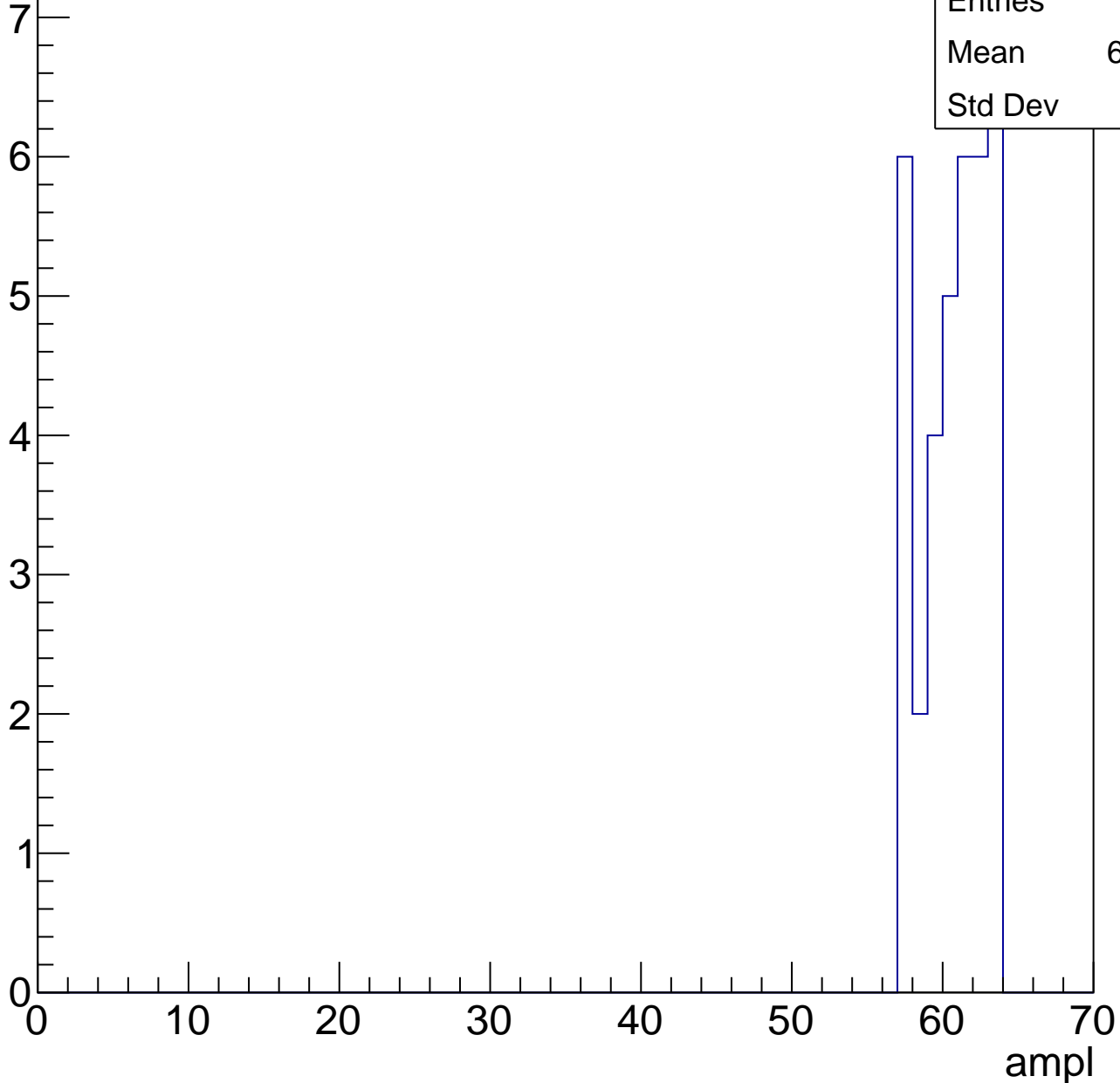


B1L103S, U17-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	60.36
Std Dev	2.07



B1L103S, U17-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

B1L103S, U17-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch110, adc0

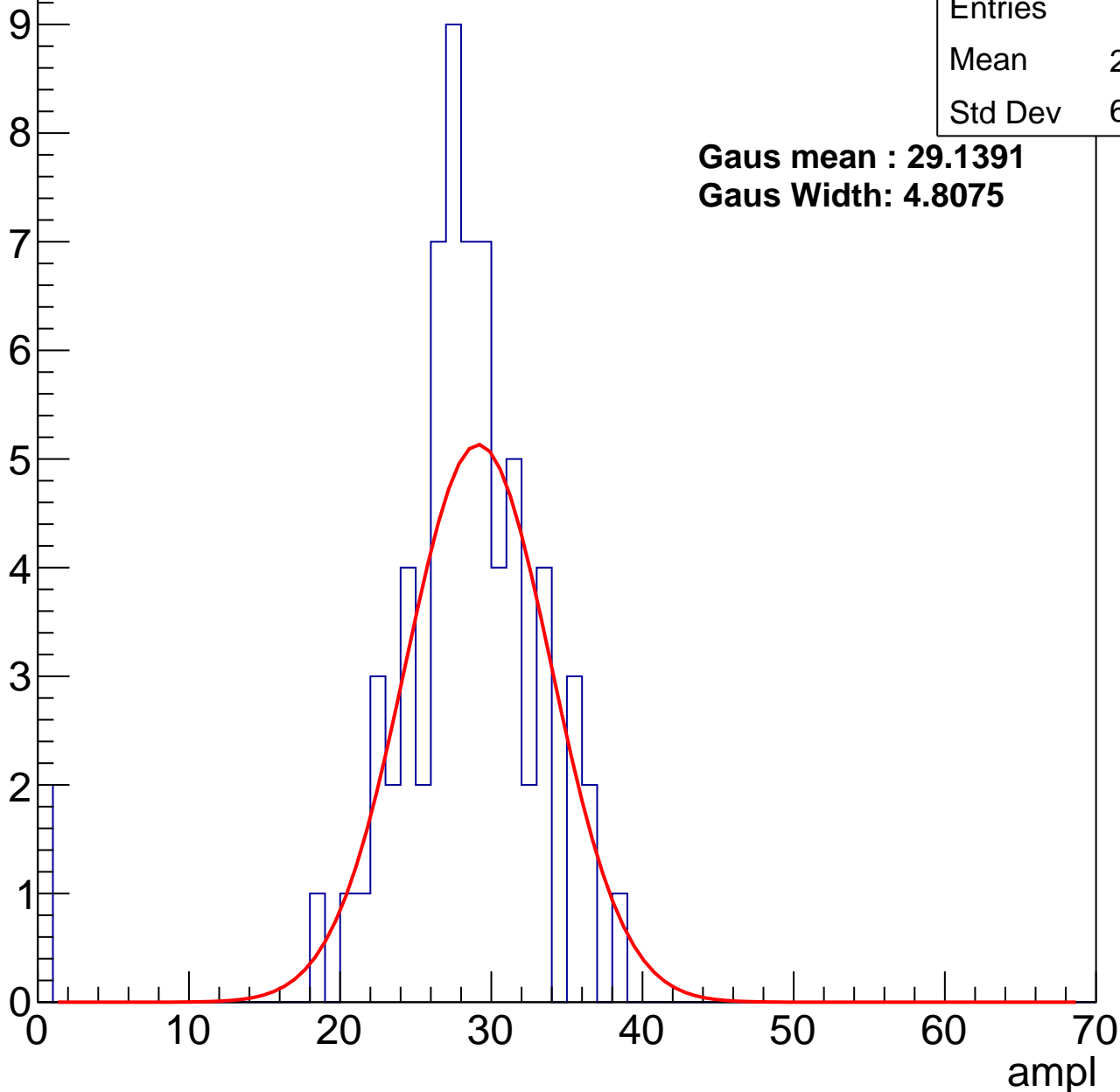
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.27
Std Dev	6.226

Gaus mean : 29.1391

Gaus Width: 4.8075



B1L103S, U17-ch110, adc1

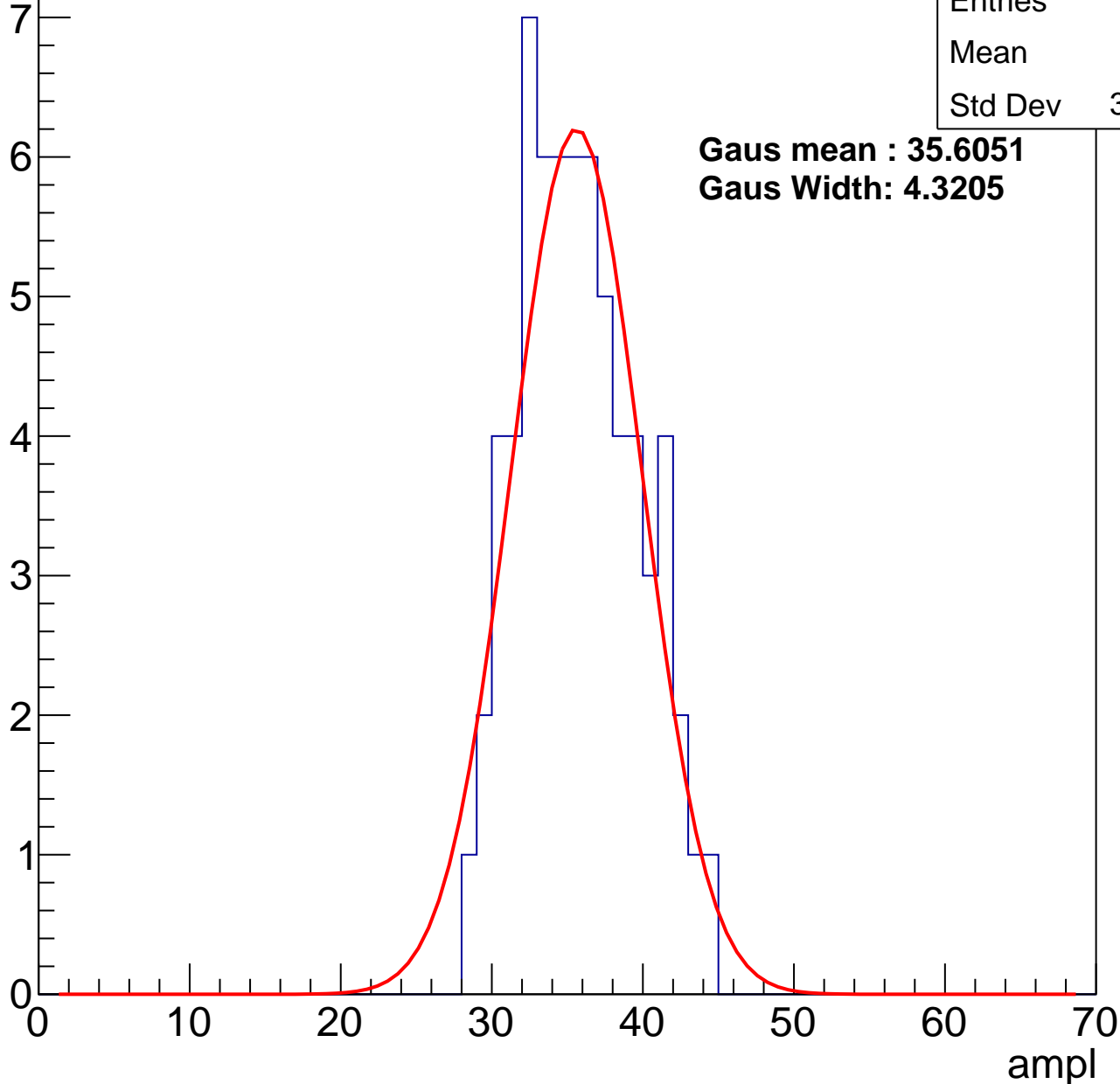
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.3
Std Dev	3.814

Gaus mean : 35.6051

Gaus Width: 4.3205



B1L103S, U17-ch110, adc2

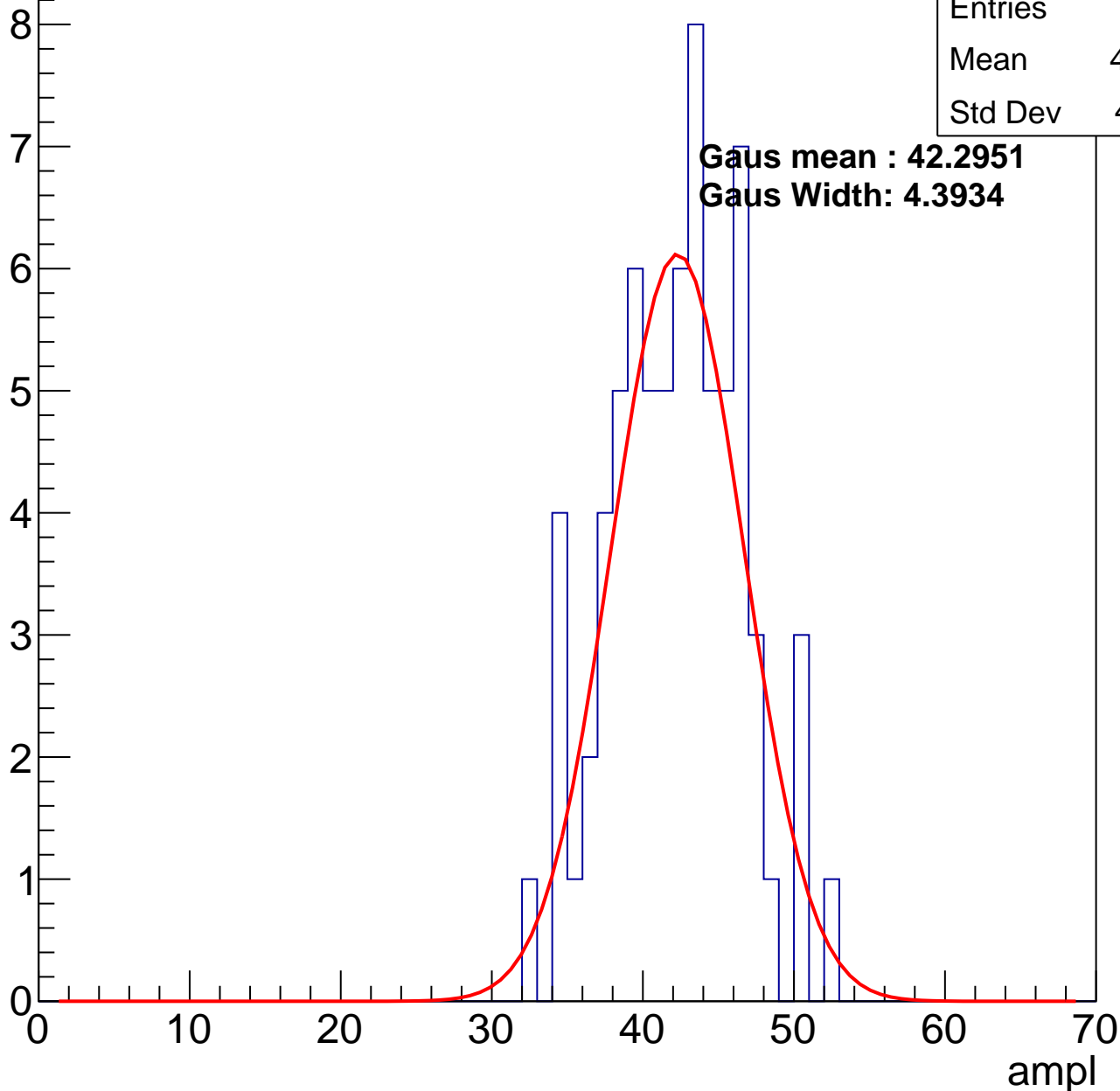
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.75
Std Dev	4.281

Gaus mean : 42.2951

Gaus Width: 4.3934

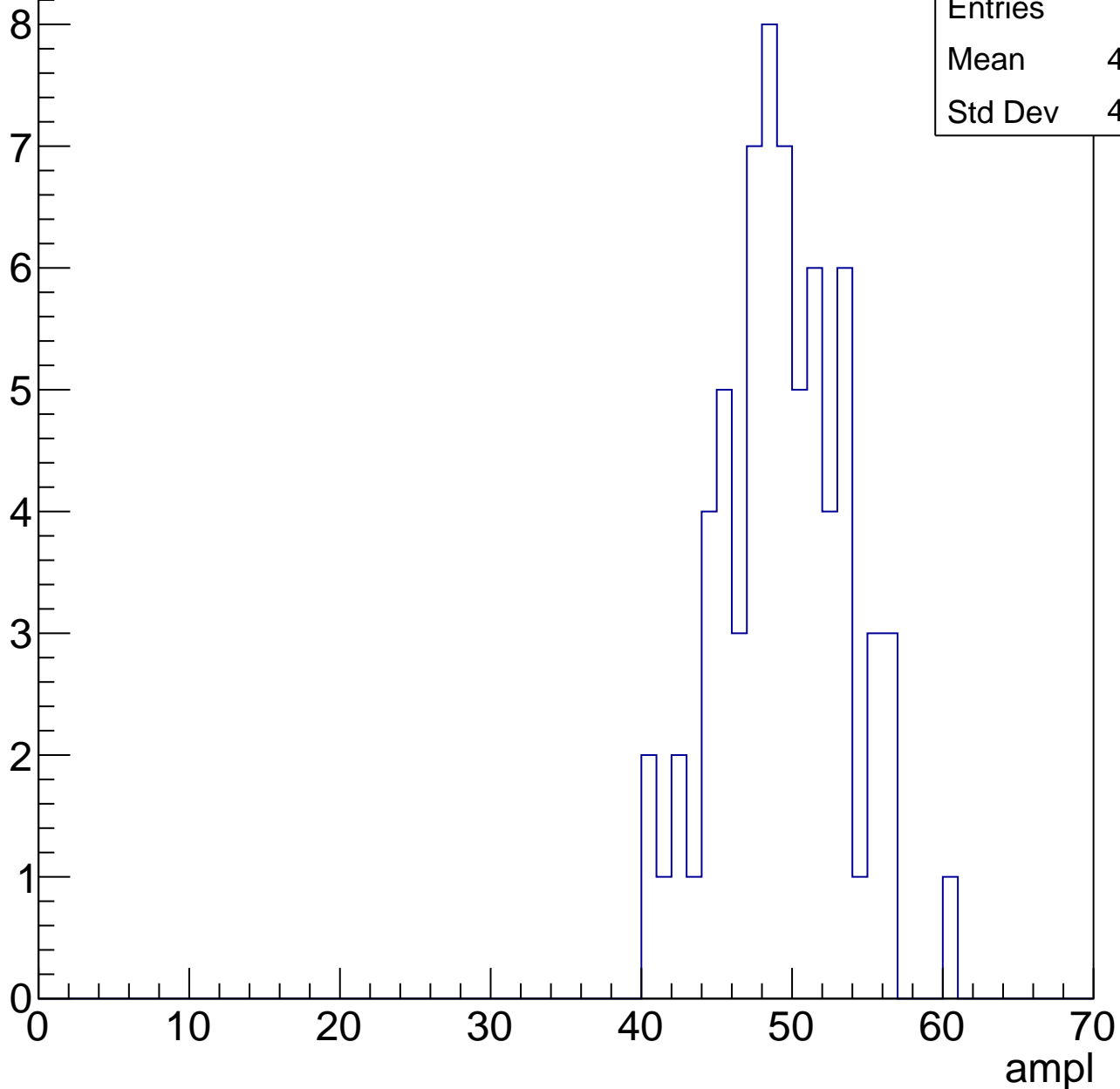


B1L103S, U17-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.87
Std Dev	4.107

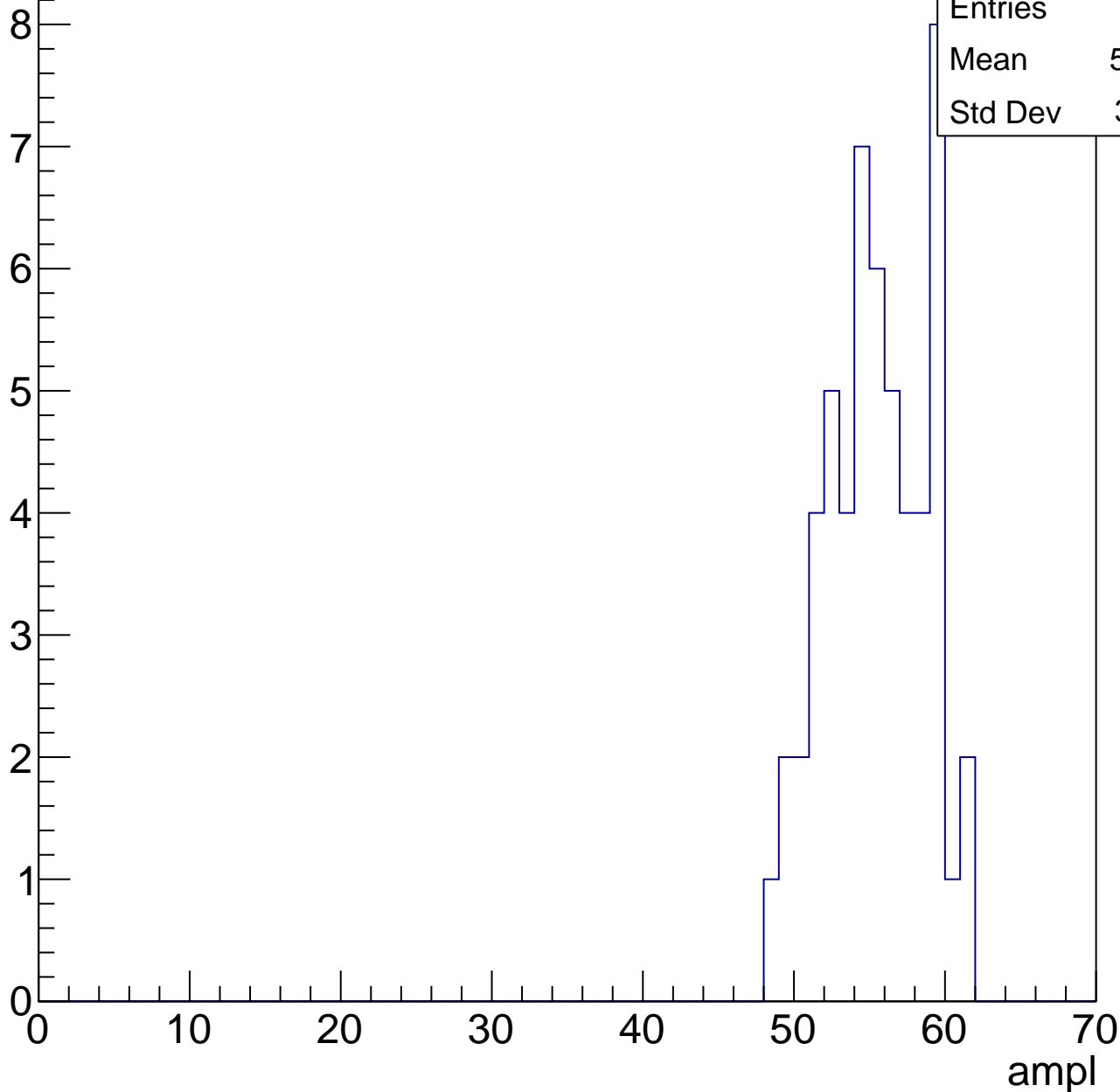


B1L103S, U17-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.98
Std Dev	3.261

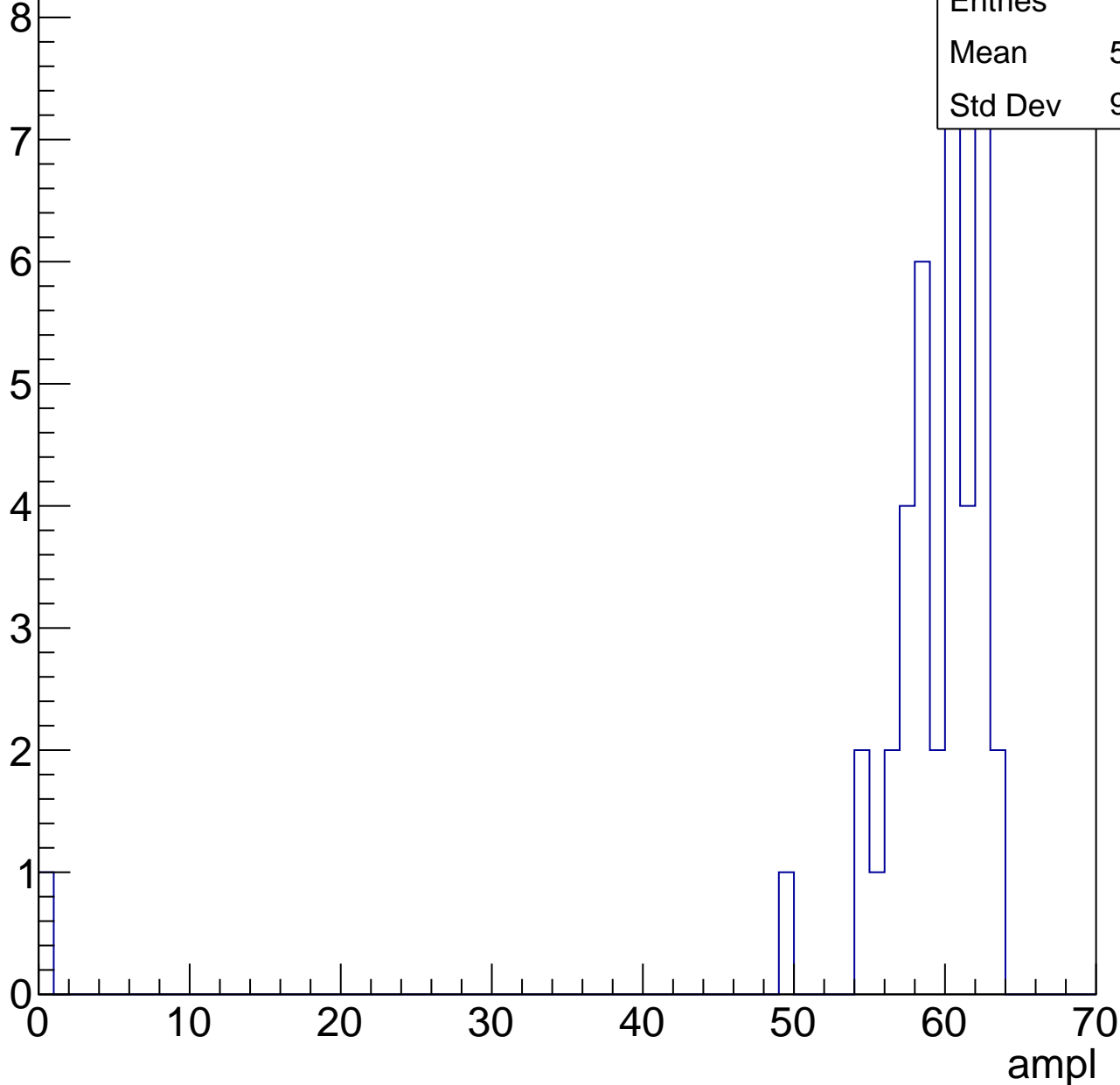


B1L103S, U17-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	57.66
Std Dev	9.552

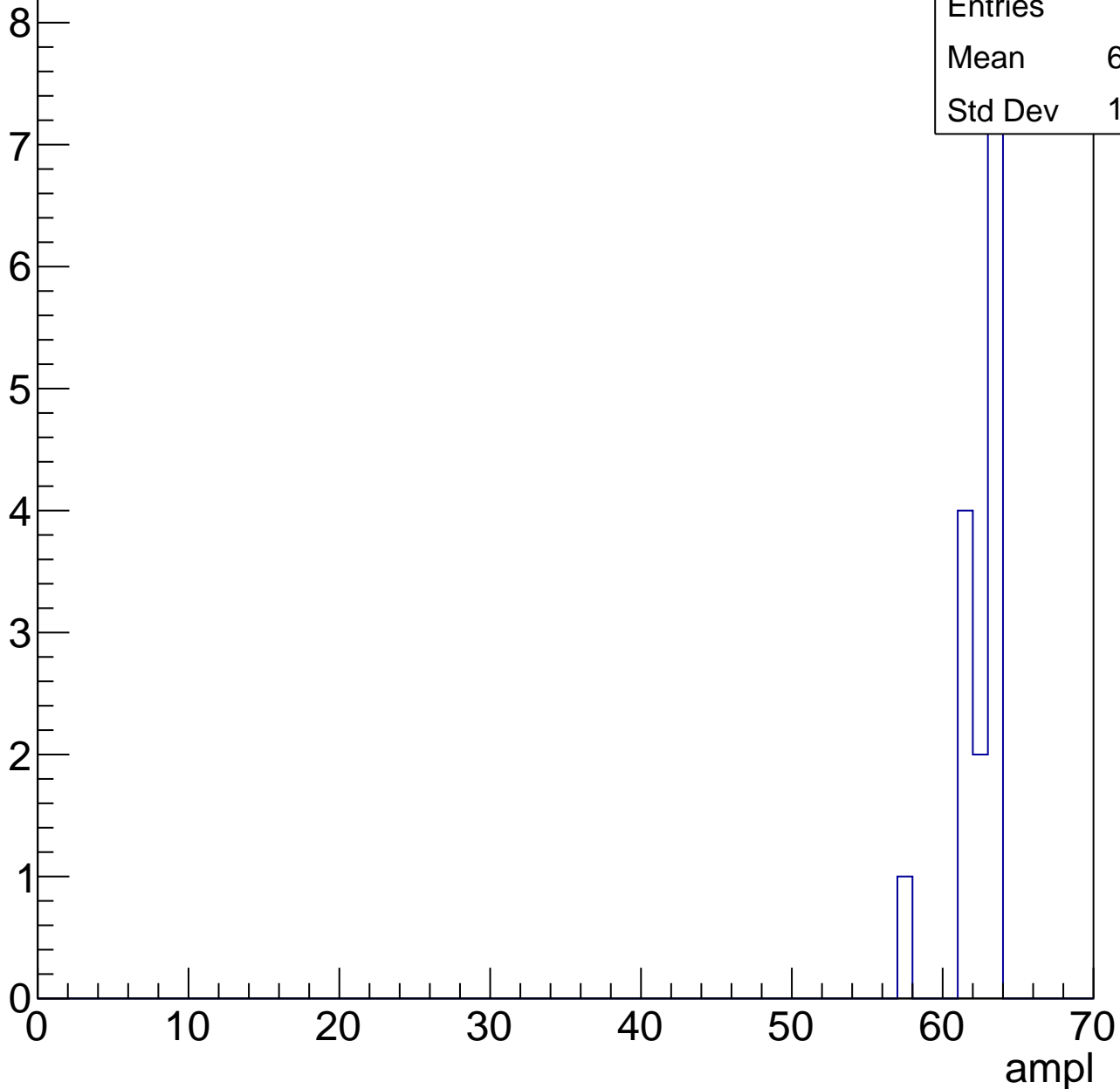


B1L103S, U17-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.93
Std Dev	1.569



B1L103S, U17-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	1
Mean	57
Std Dev	0

B1L103S, U17-ch111, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	29.22
Std Dev	3.961

Gaus mean : 29.5135

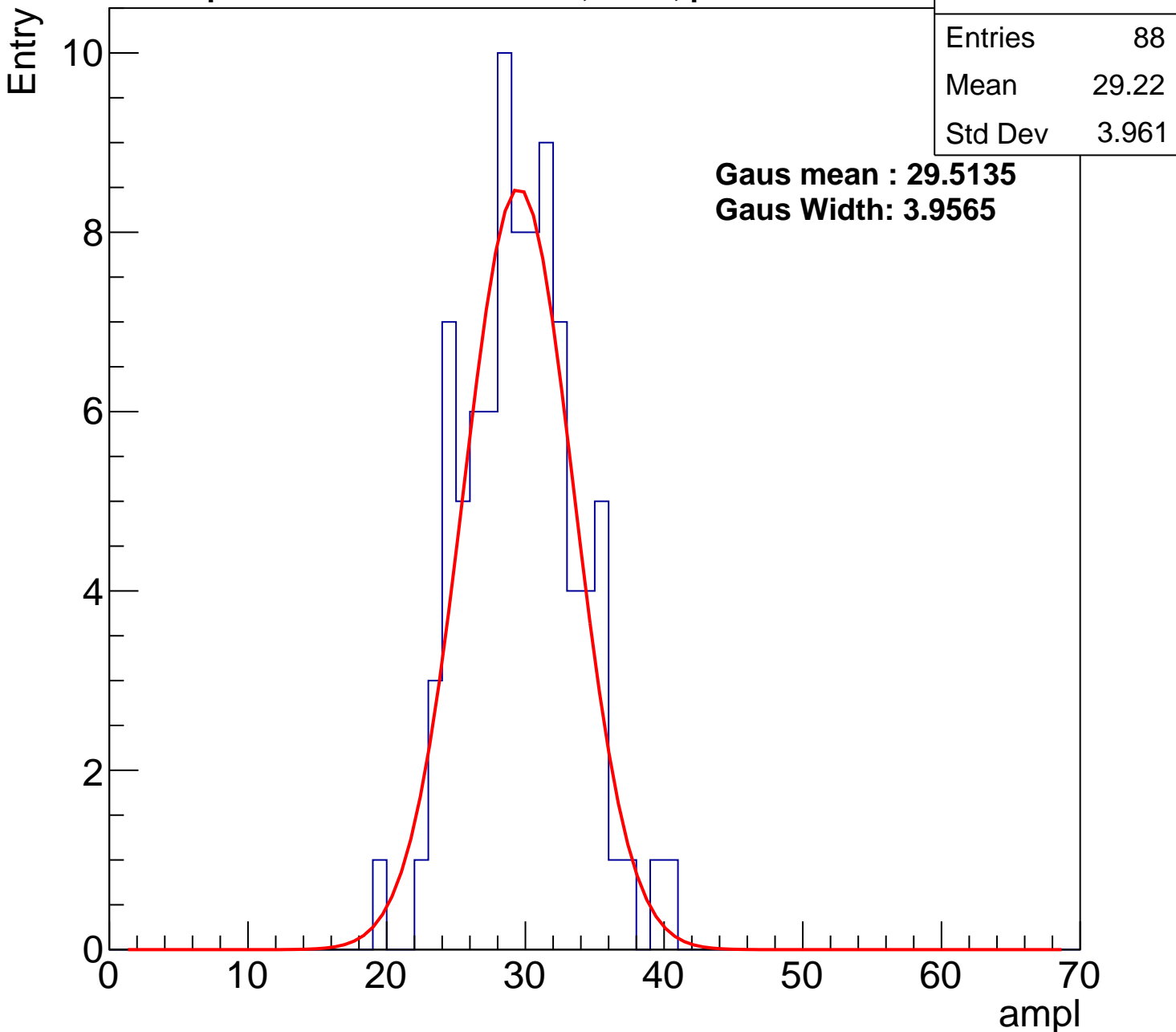
Gaus Width: 3.9565

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch111, adc1

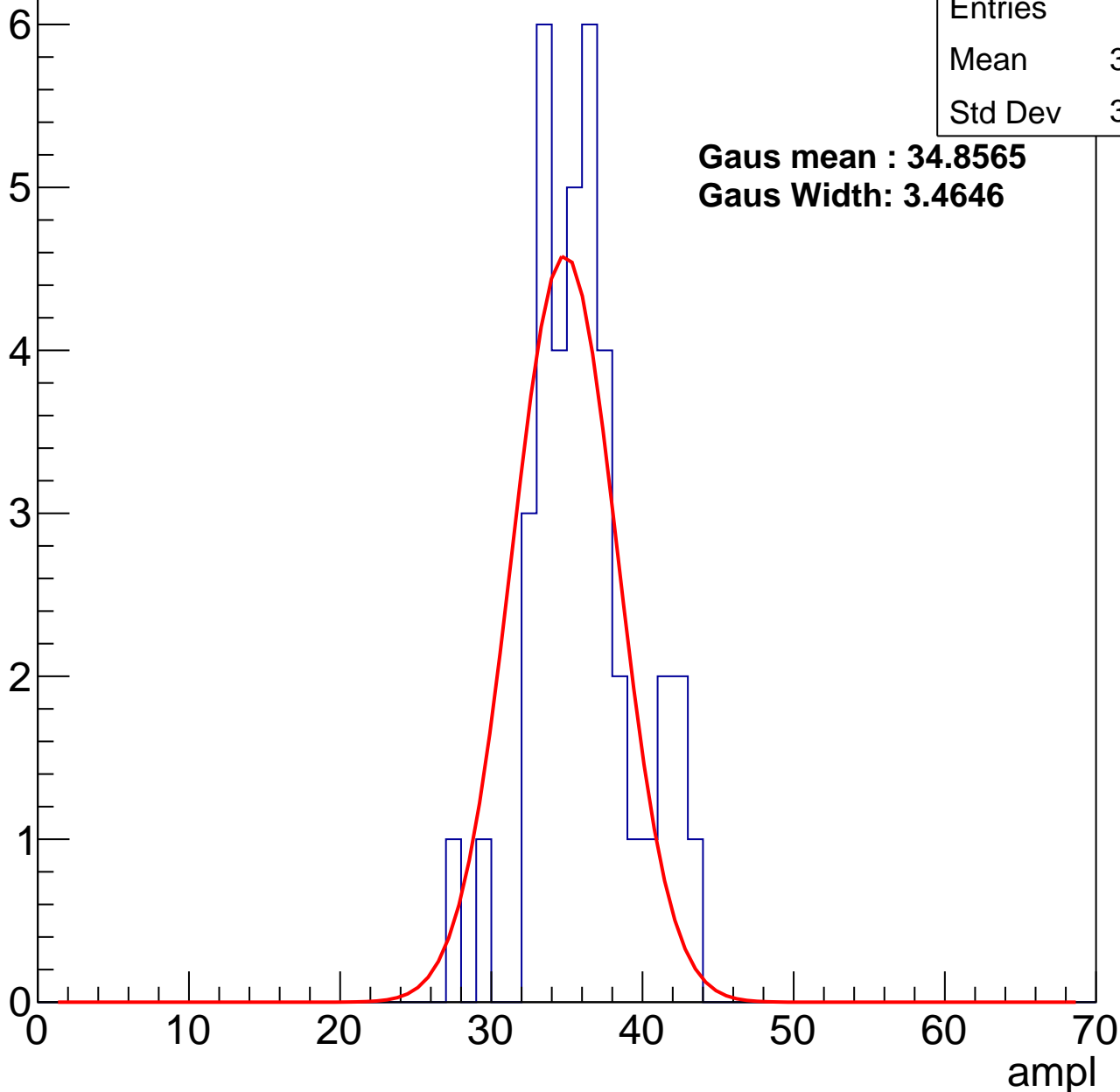
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	35.62
Std Dev	3.417

Gaus mean : 34.8565

Gaus Width: 3.4646



B1L103S, U17-ch111, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	94
Mean	42.33
Std Dev	4.028

Gaus mean : 42.0029

Gaus Width: 4.2079

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

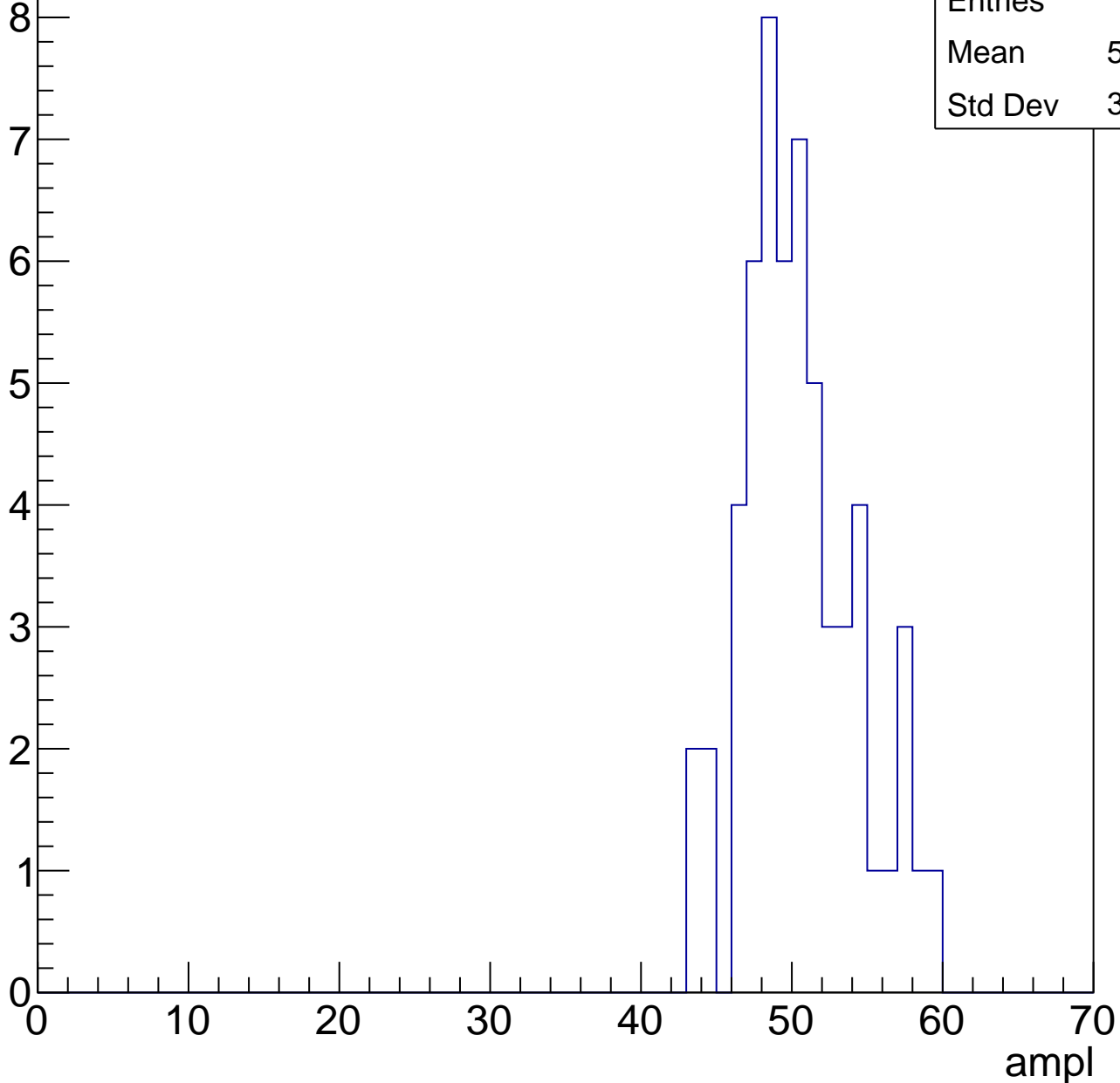
70

B1L103S, U17-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	50.05
Std Dev	3.715

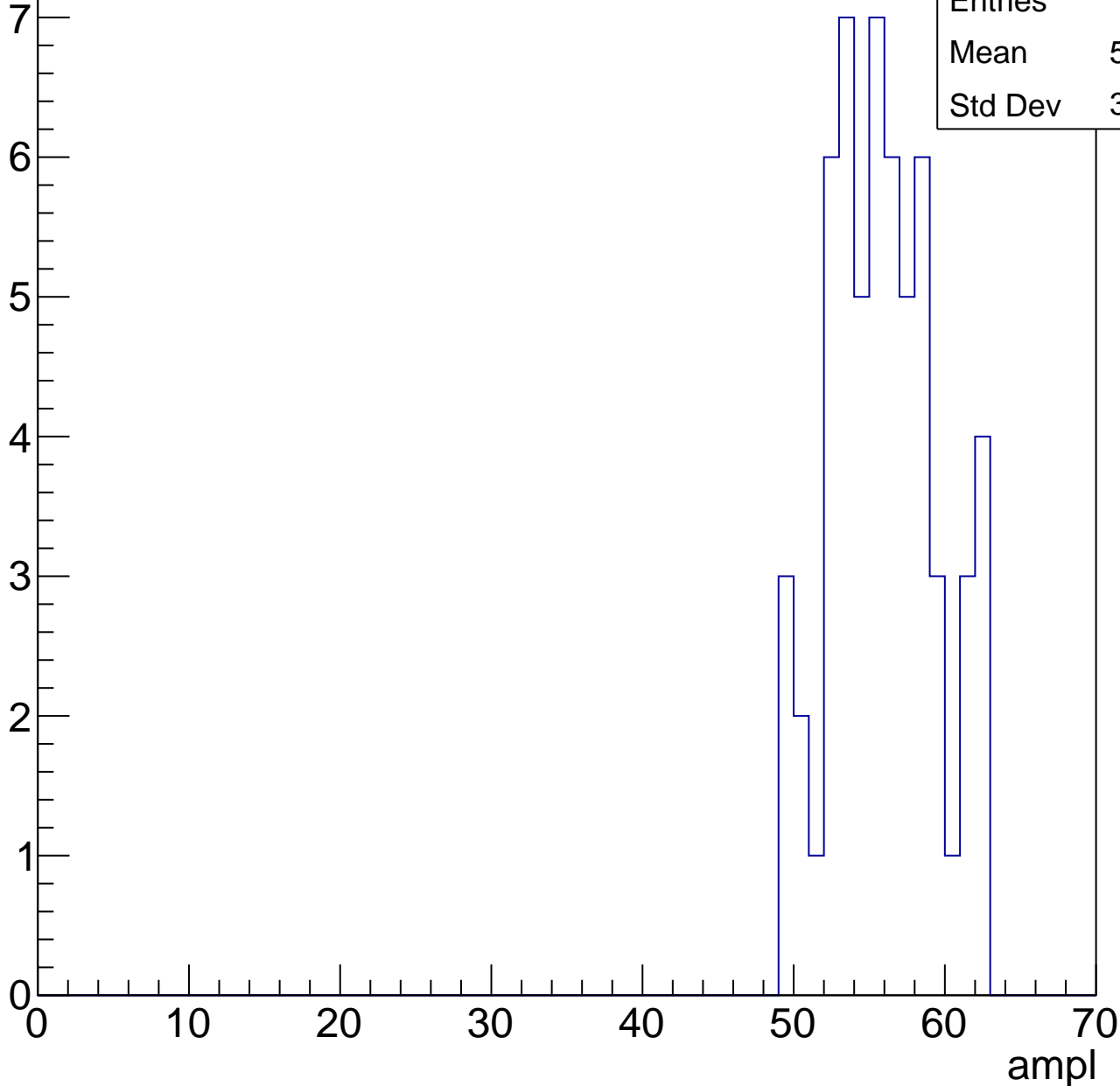


B1L103S, U17-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.47
Std Dev	3.456

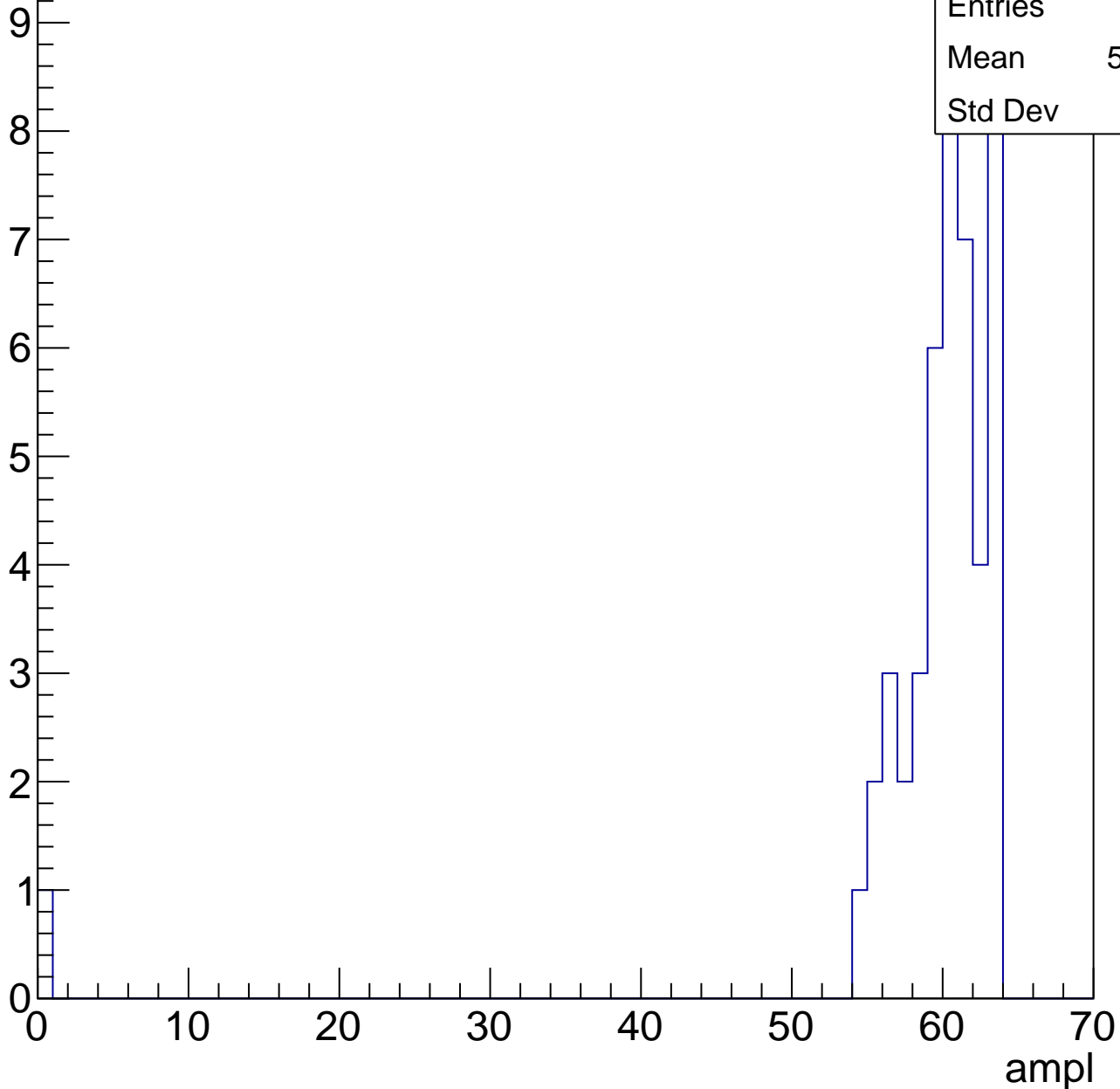


B1L103S, U17-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.54
Std Dev	9.05



B1L103S, U17-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L103S, U17-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U17-ch112, adc0

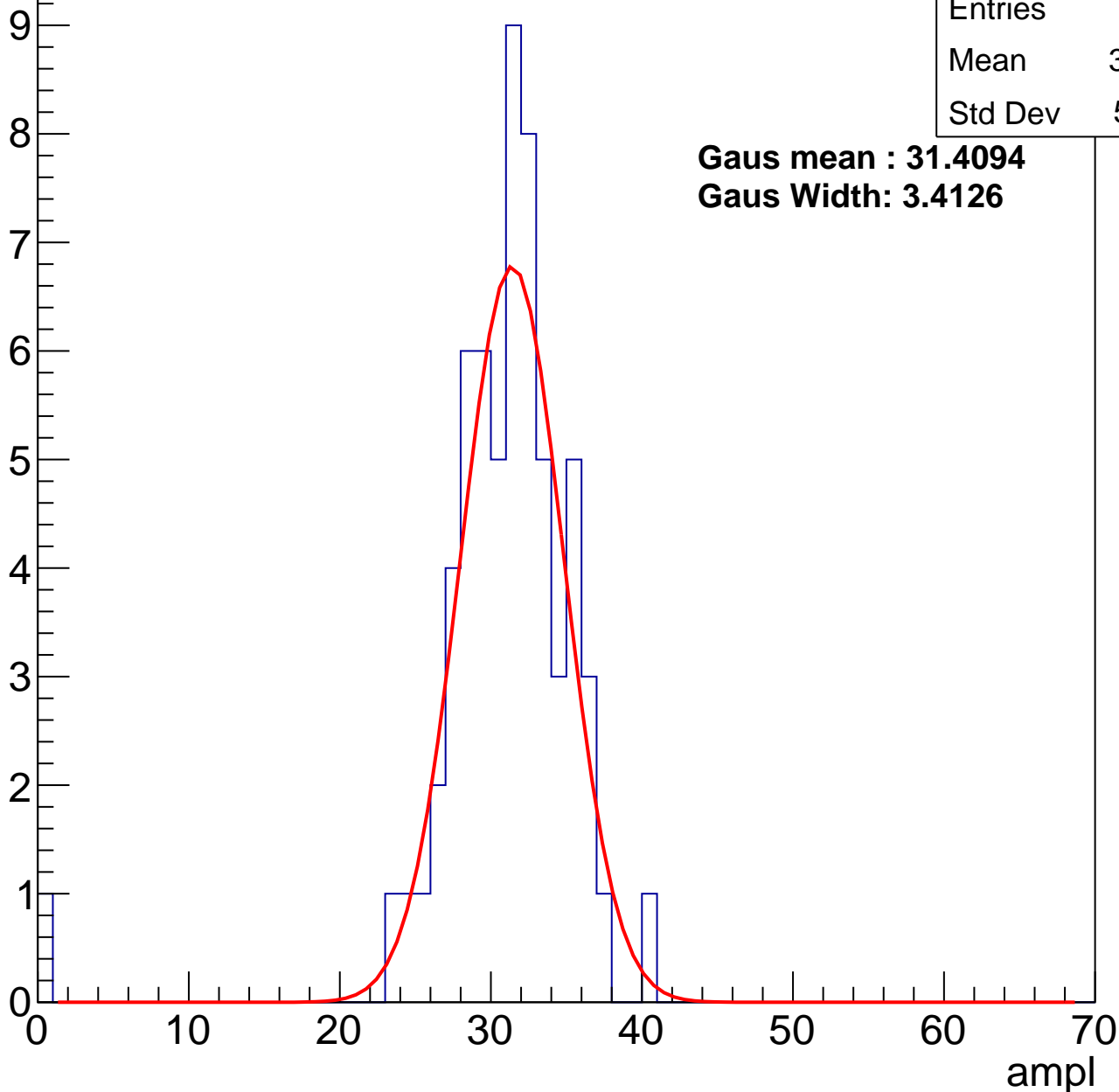
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	30.42
Std Dev	5.101

Gaus mean : 31.4094

Gaus Width: 3.4126



B1L103S, U17-ch112, adc1

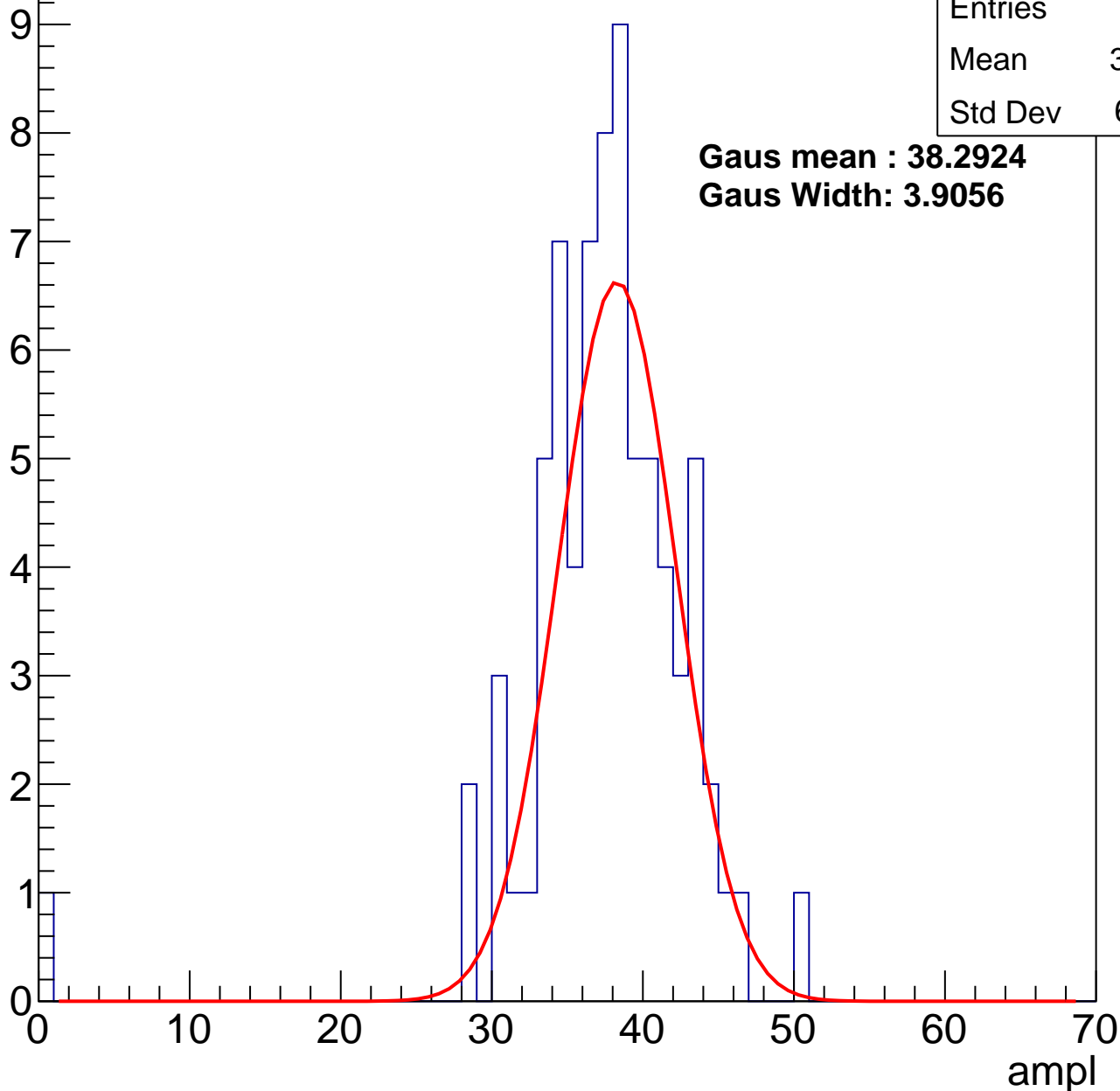
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.95
Std Dev	6.011

Gaus mean : 38.2924

Gaus Width: 3.9056



B1L103S, U17-ch112, adc2

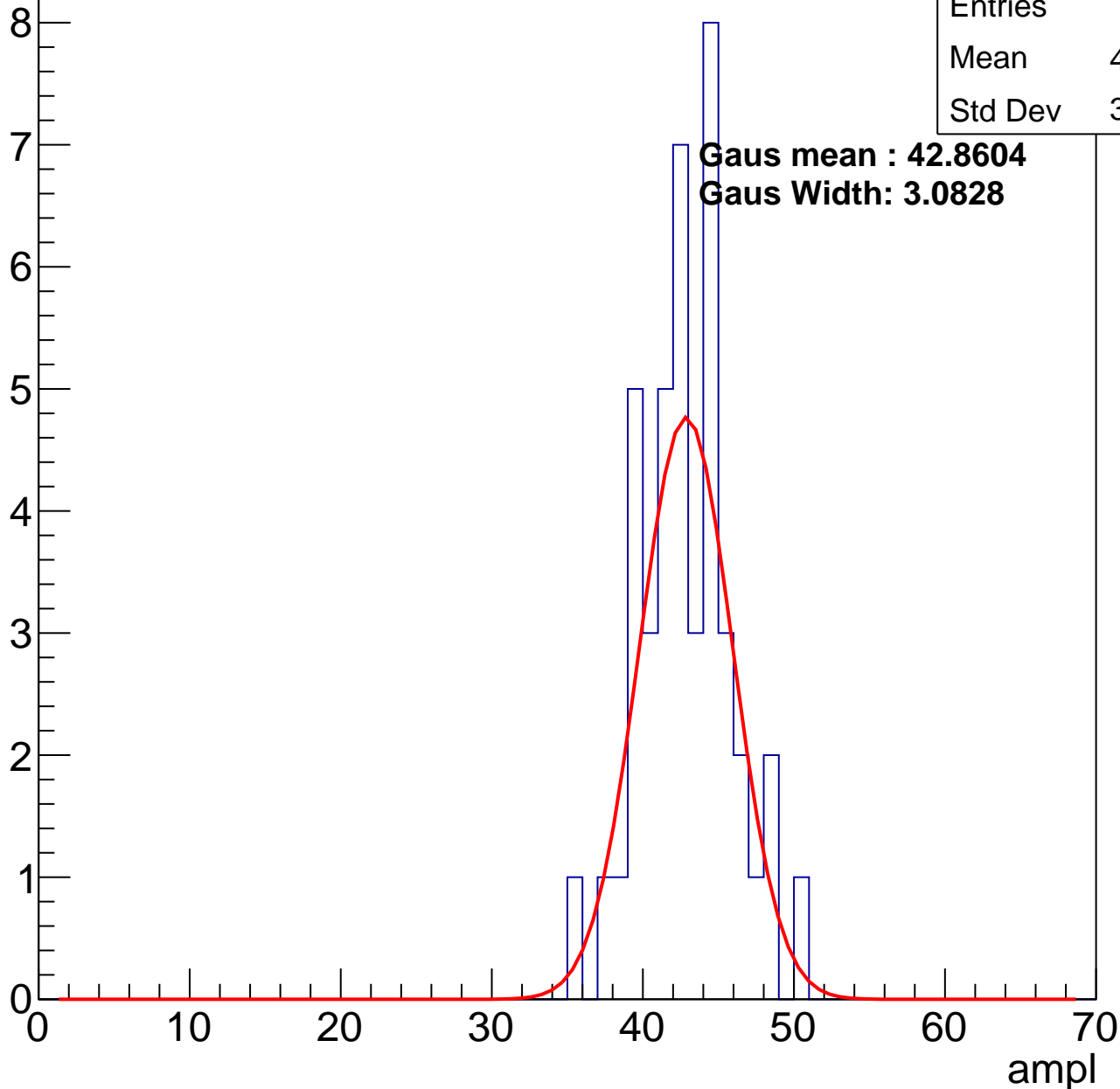
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	42.44
Std Dev	3.045

Gaus mean : 42.8604

Gaus Width: 3.0828



B1L103S, U17-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

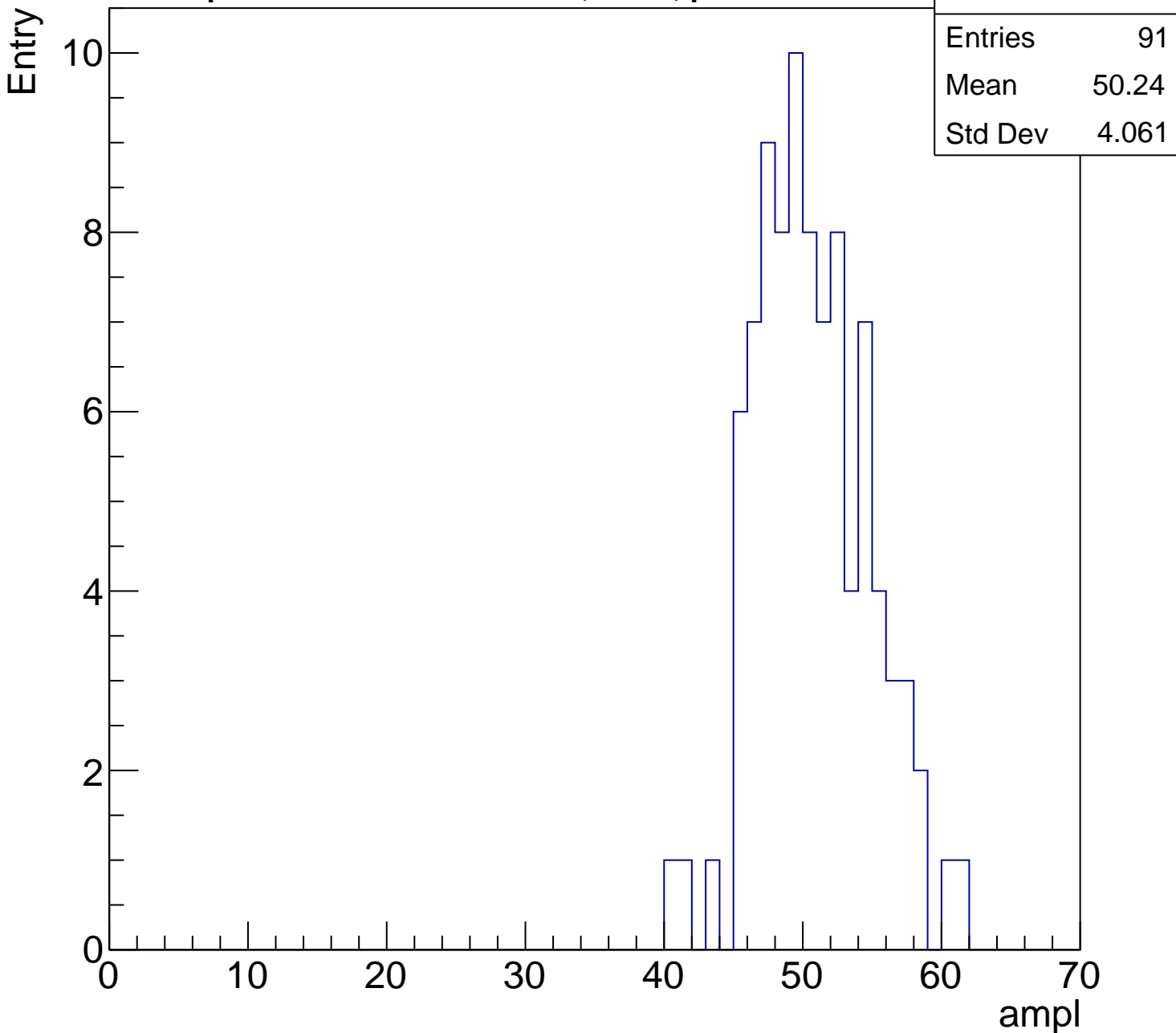
Entries	91
Mean	50.24
Std Dev	4.061

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

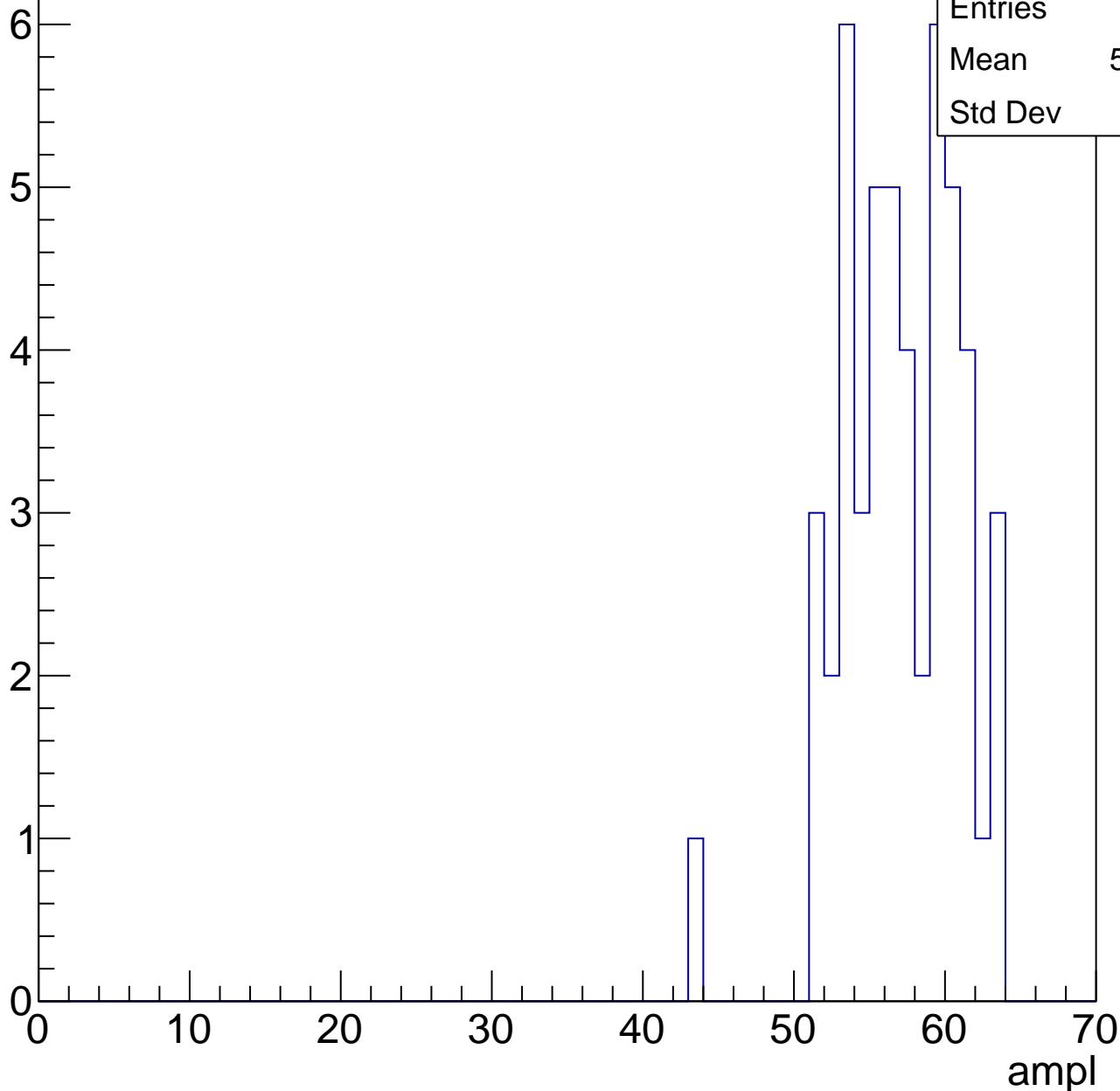


B1L103S, U17-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	56.56
Std Dev	3.9

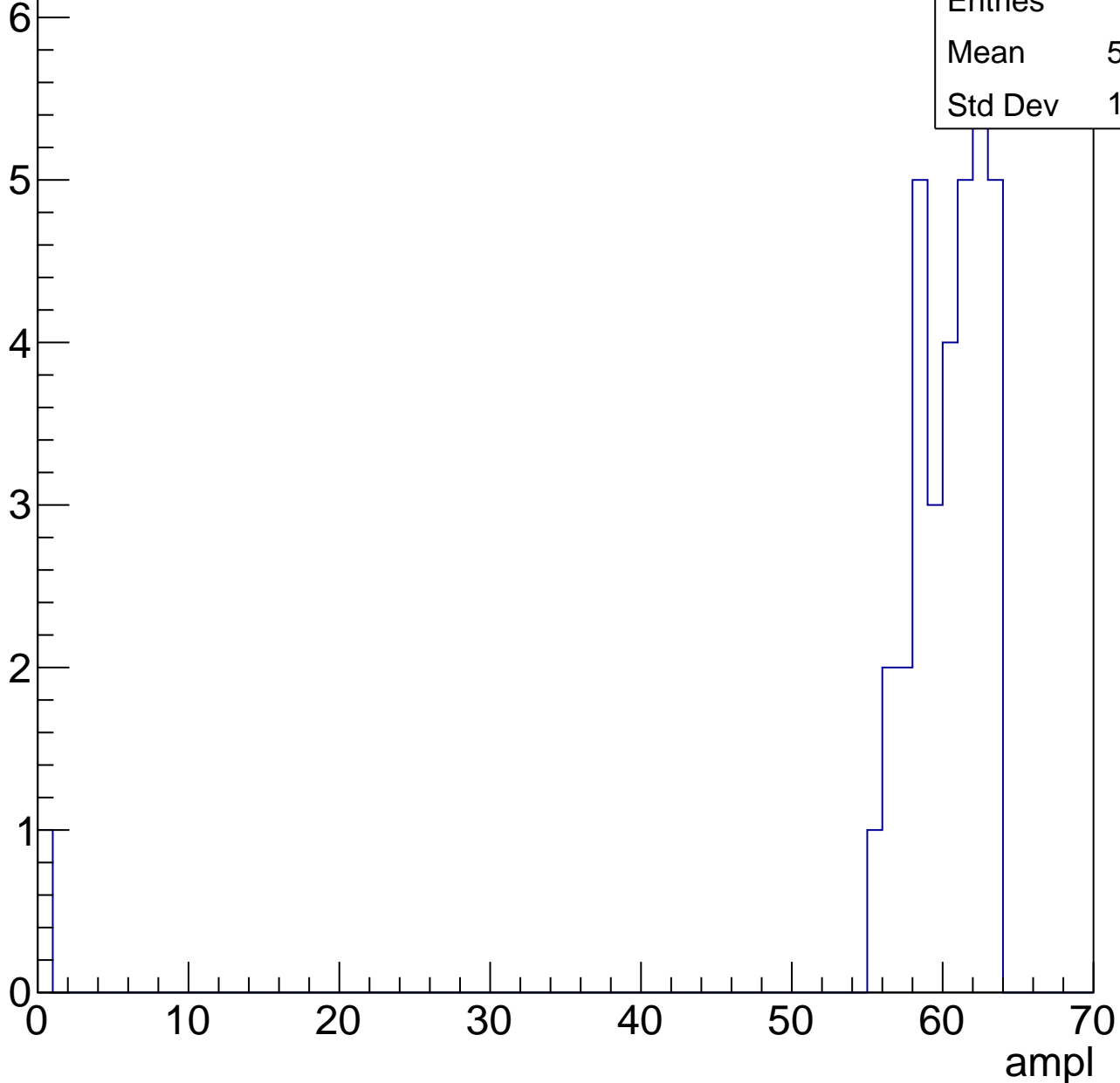


B1L103S, U17-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

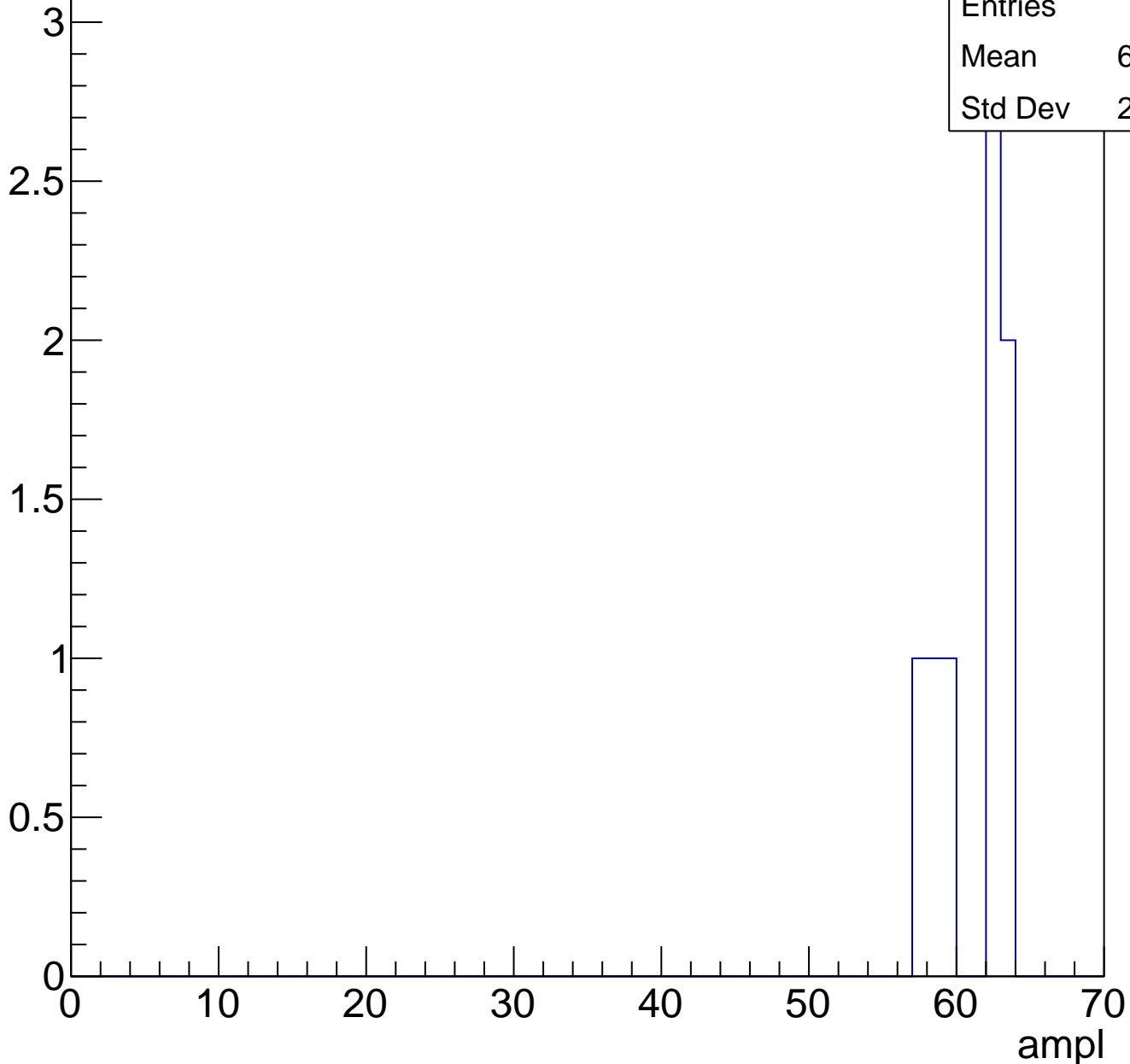
Entries	34
Mean	58.24
Std Dev	10.38



B1L103S, U17-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L103S, U17-ch113, adc0

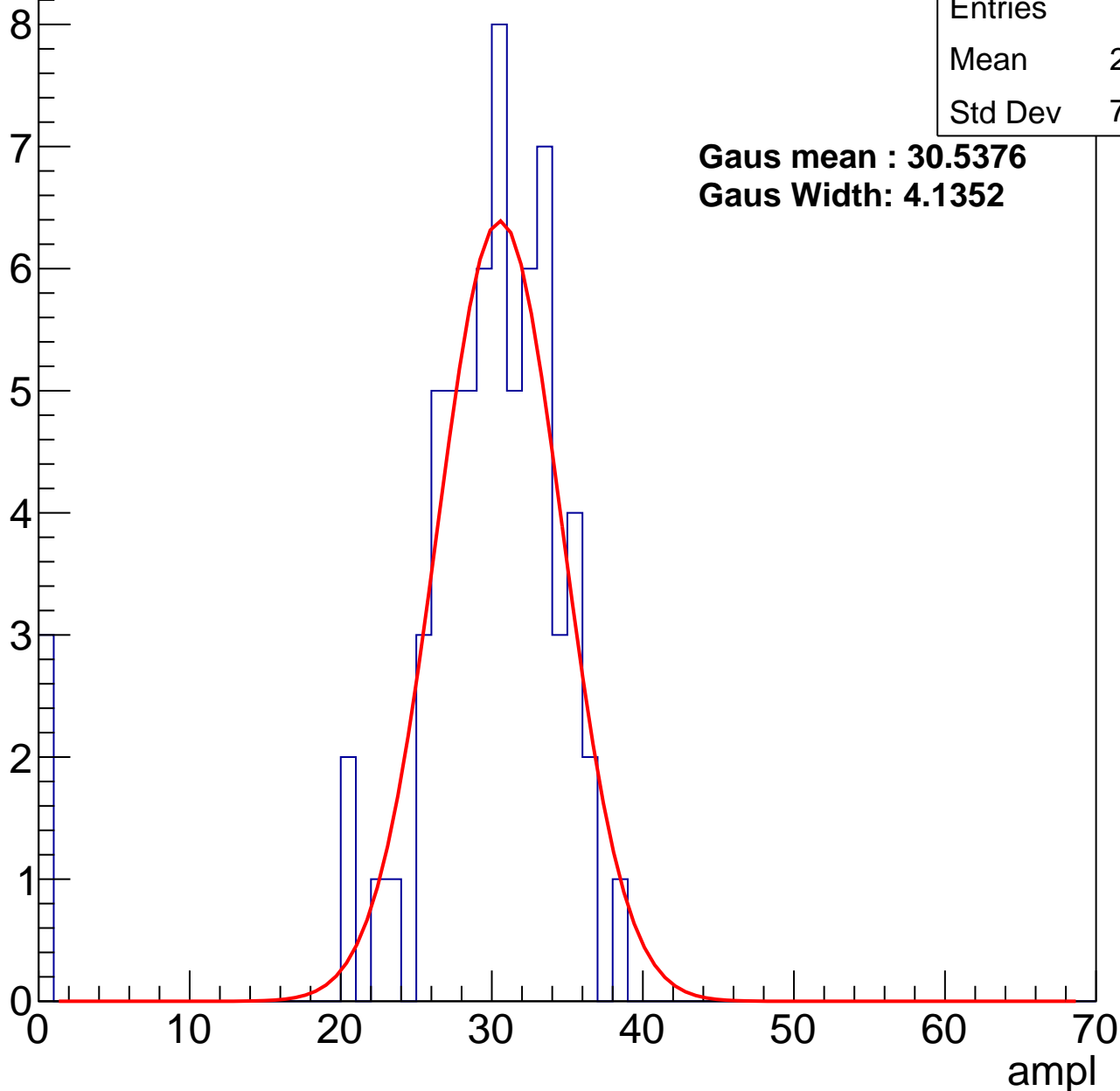
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.49
Std Dev	7.189

Gaus mean : 30.5376

Gaus Width: 4.1352



B1L103S, U17-ch113, adc1

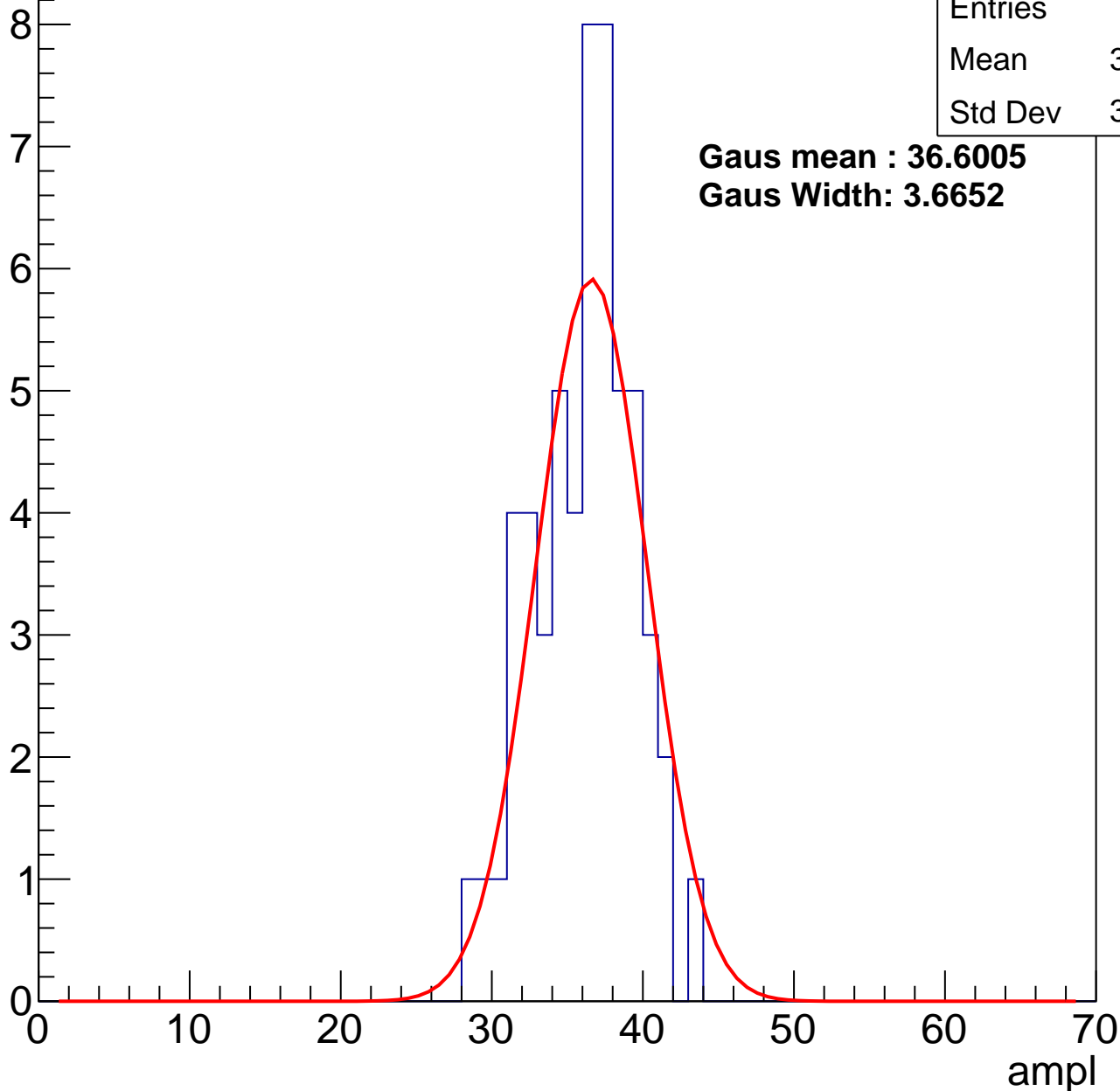
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	35.67
Std Dev	3.242

Gaus mean : 36.6005

Gaus Width: 3.6652



B1L103S, U17-ch113, adc2

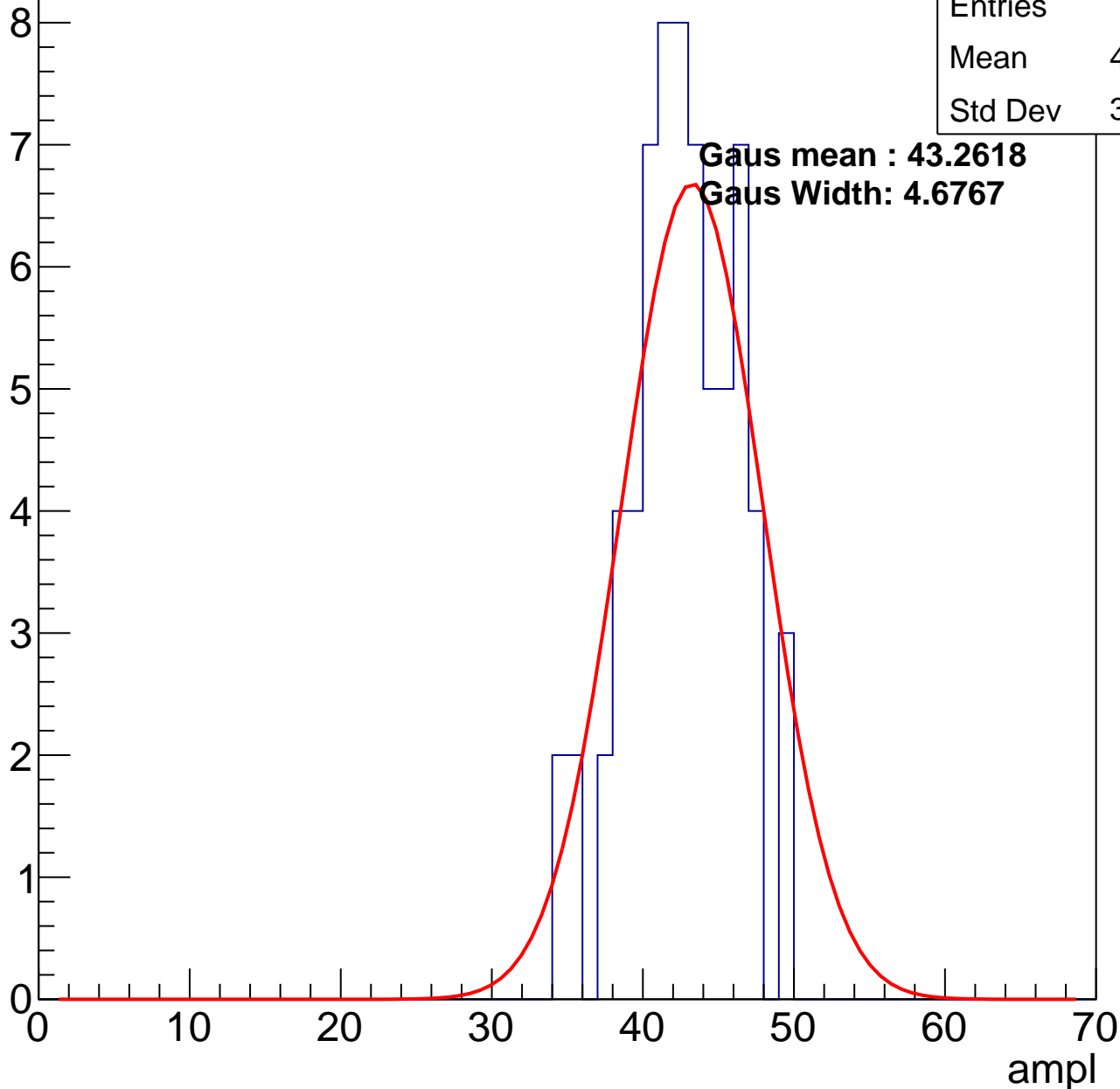
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.16
Std Dev	3.517

Gaus mean : 43.2618

Gaus Width: 4.6767

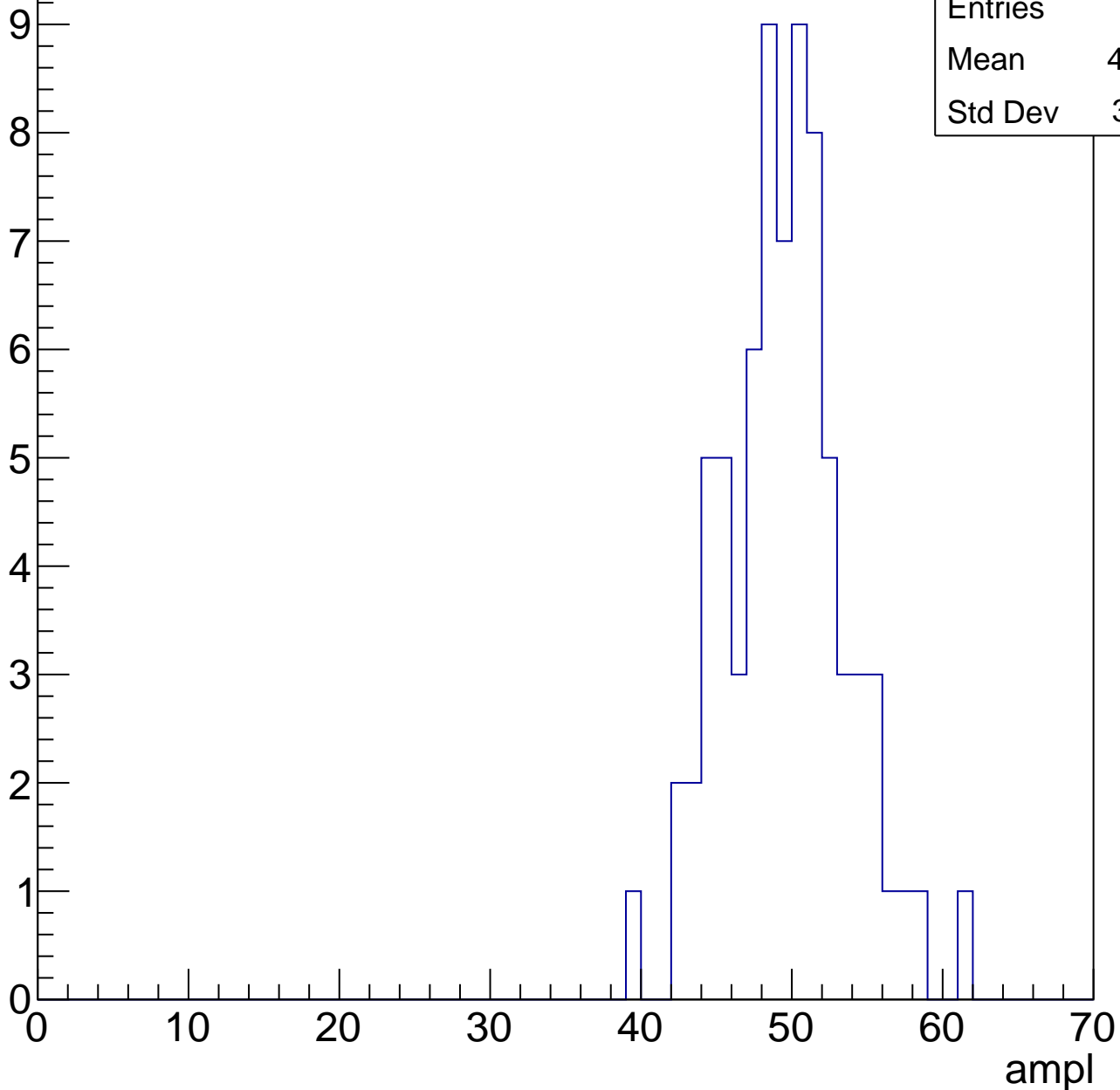


B1L103S, U17-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	49.13
Std Dev	3.981

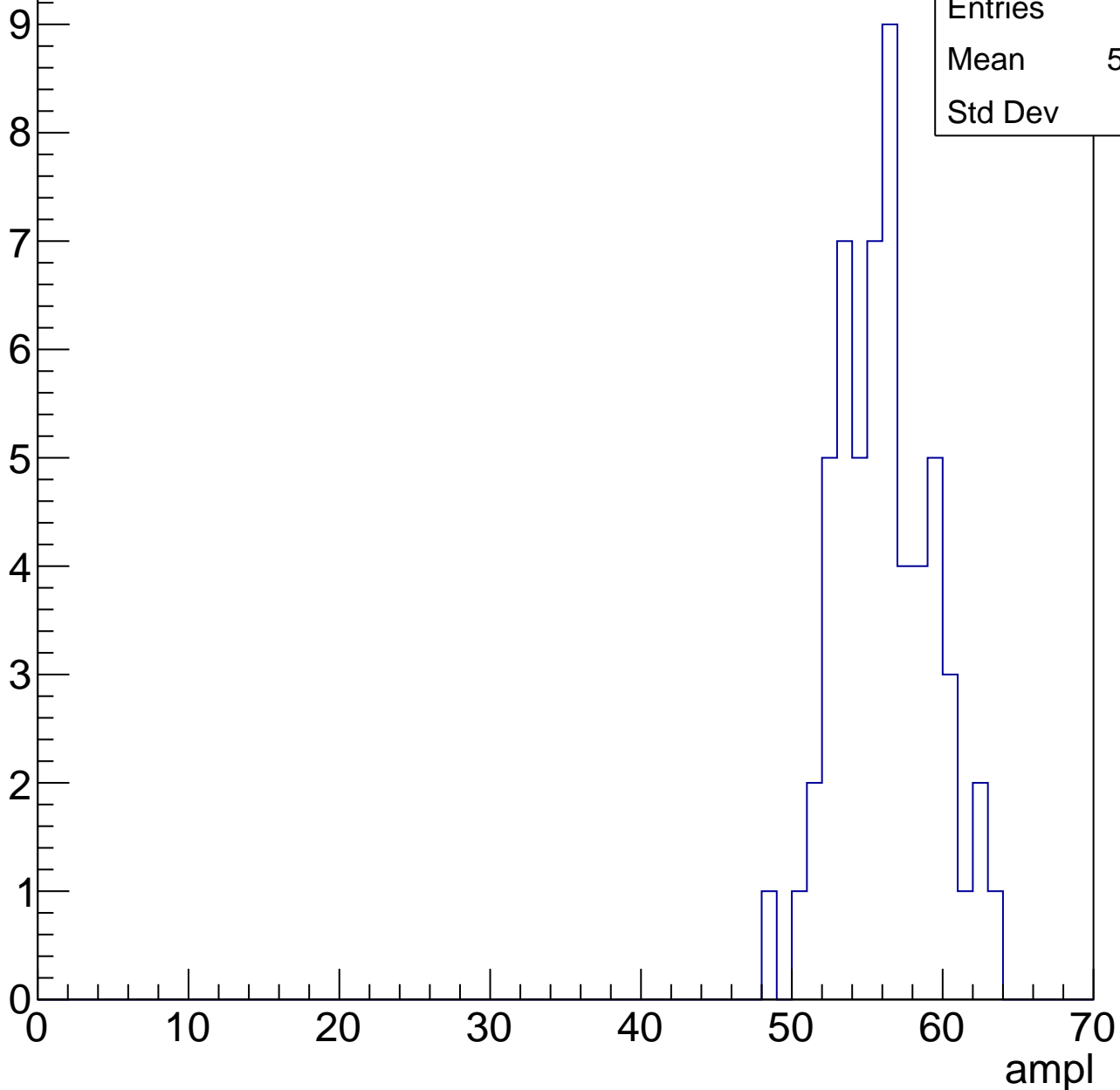


B1L103S, U17-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.67
Std Dev	3.17

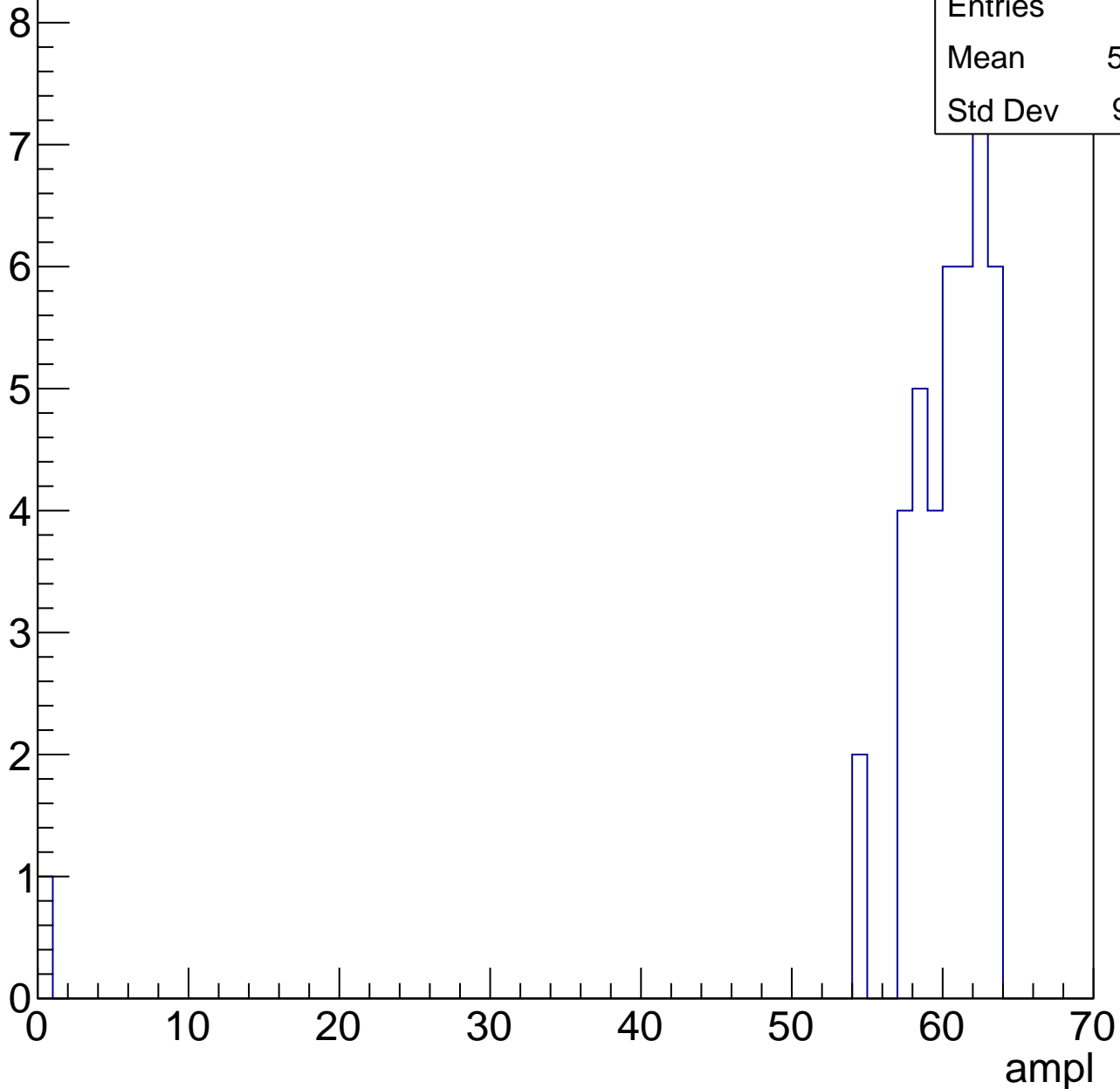


B1L103S, U17-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

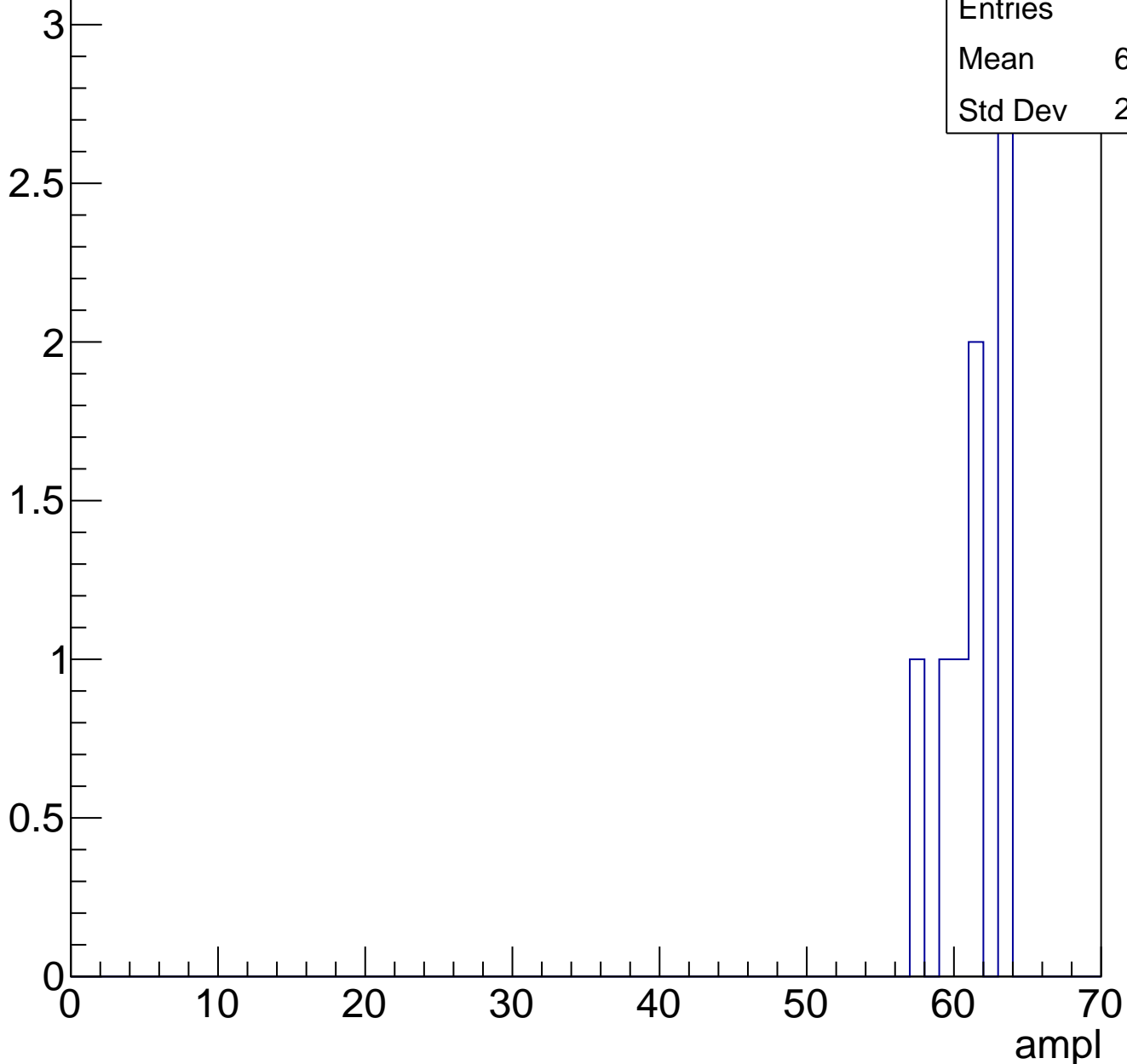
Entries	42
Mean	58.62
Std Dev	9.441



B1L103S, U17-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U17-ch114, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	27.77
Std Dev	3.967

Gaus mean : 28.2018

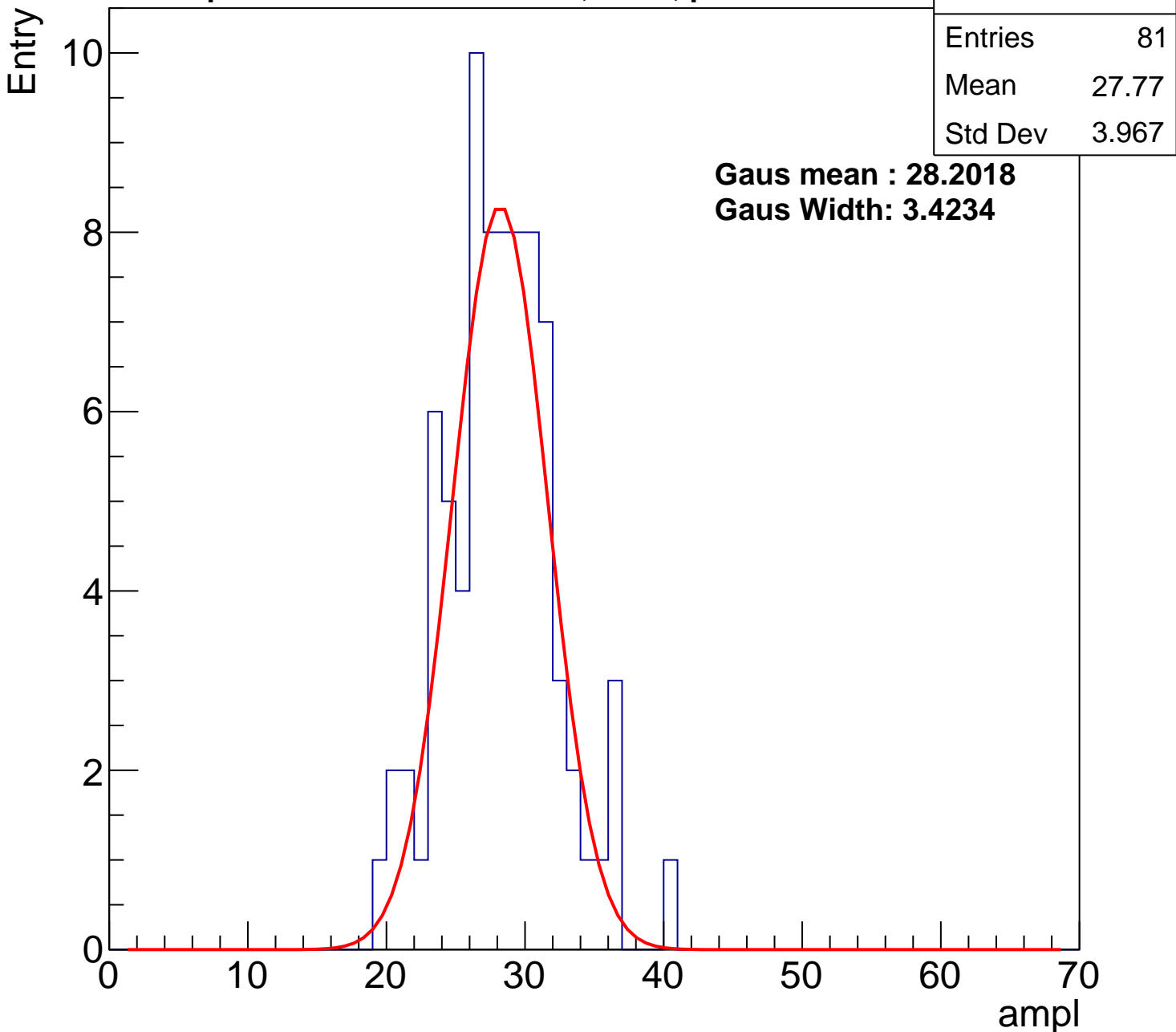
Gaus Width: 3.4234

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch114, adc1

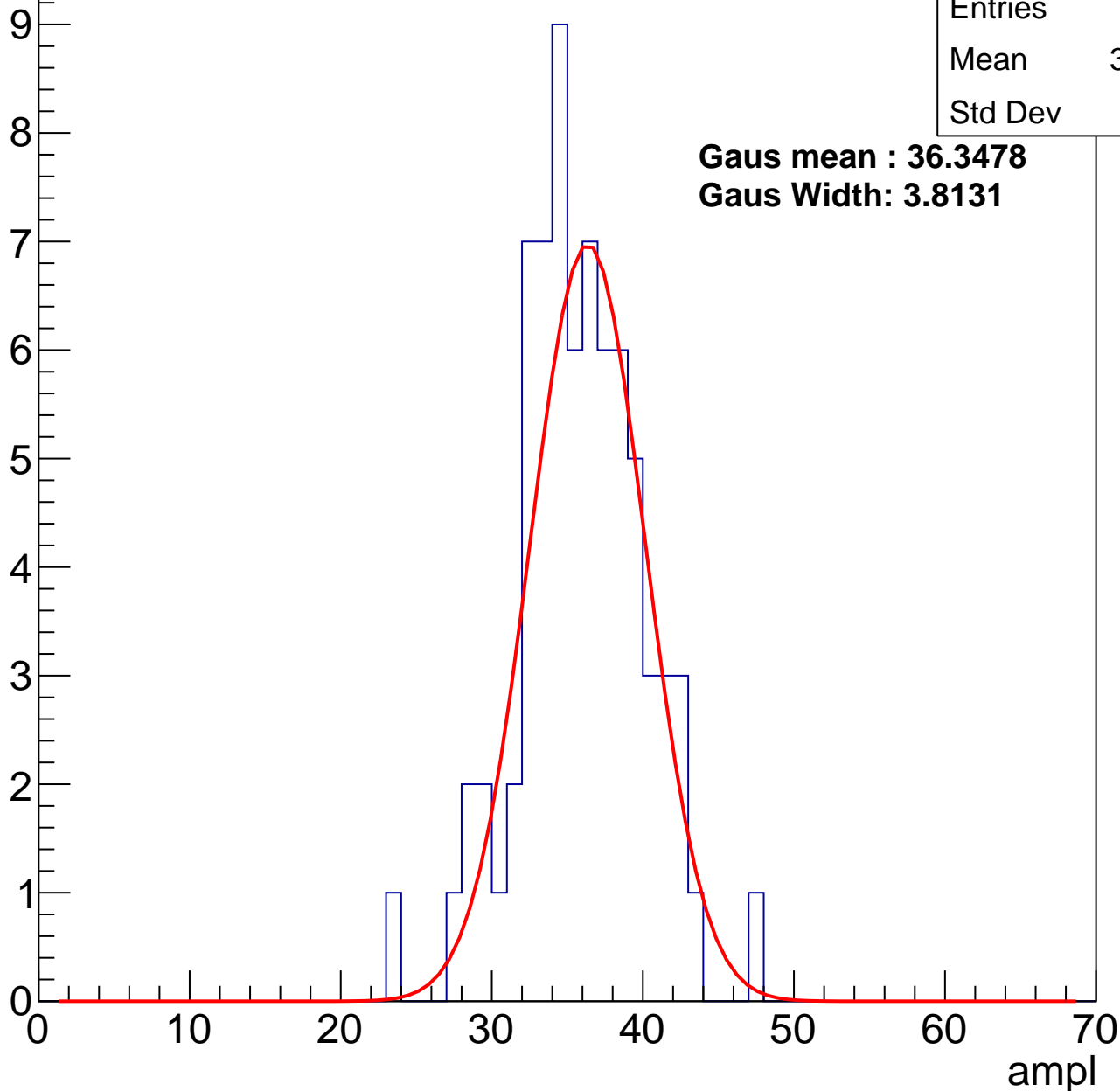
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.38
Std Dev	4.1

Gaus mean : 36.3478

Gaus Width: 3.8131



B1L103S, U17-ch114, adc2

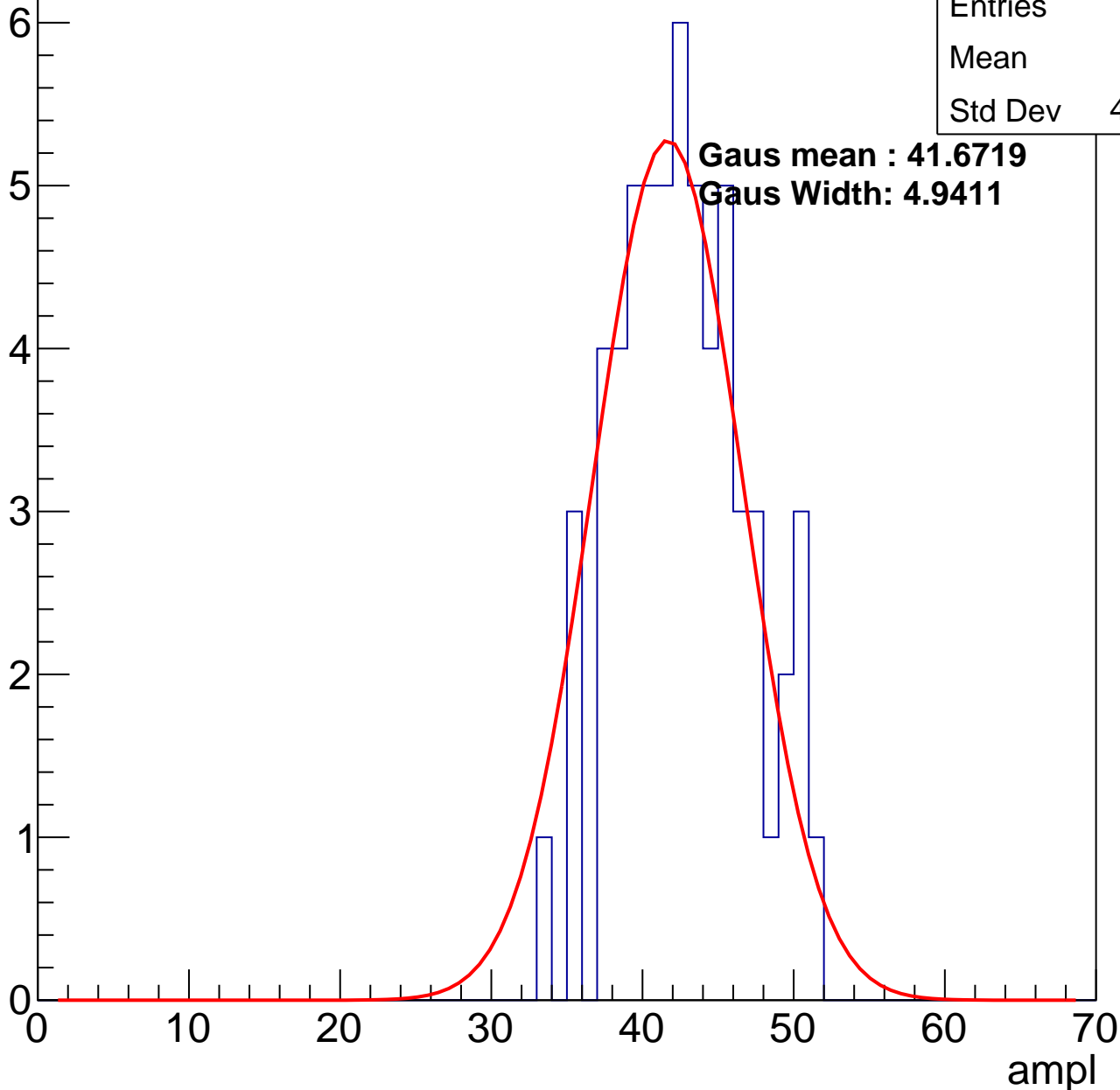
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.2
Std Dev	4.194

Gaus mean : 41.6719

Gaus Width: 4.9411

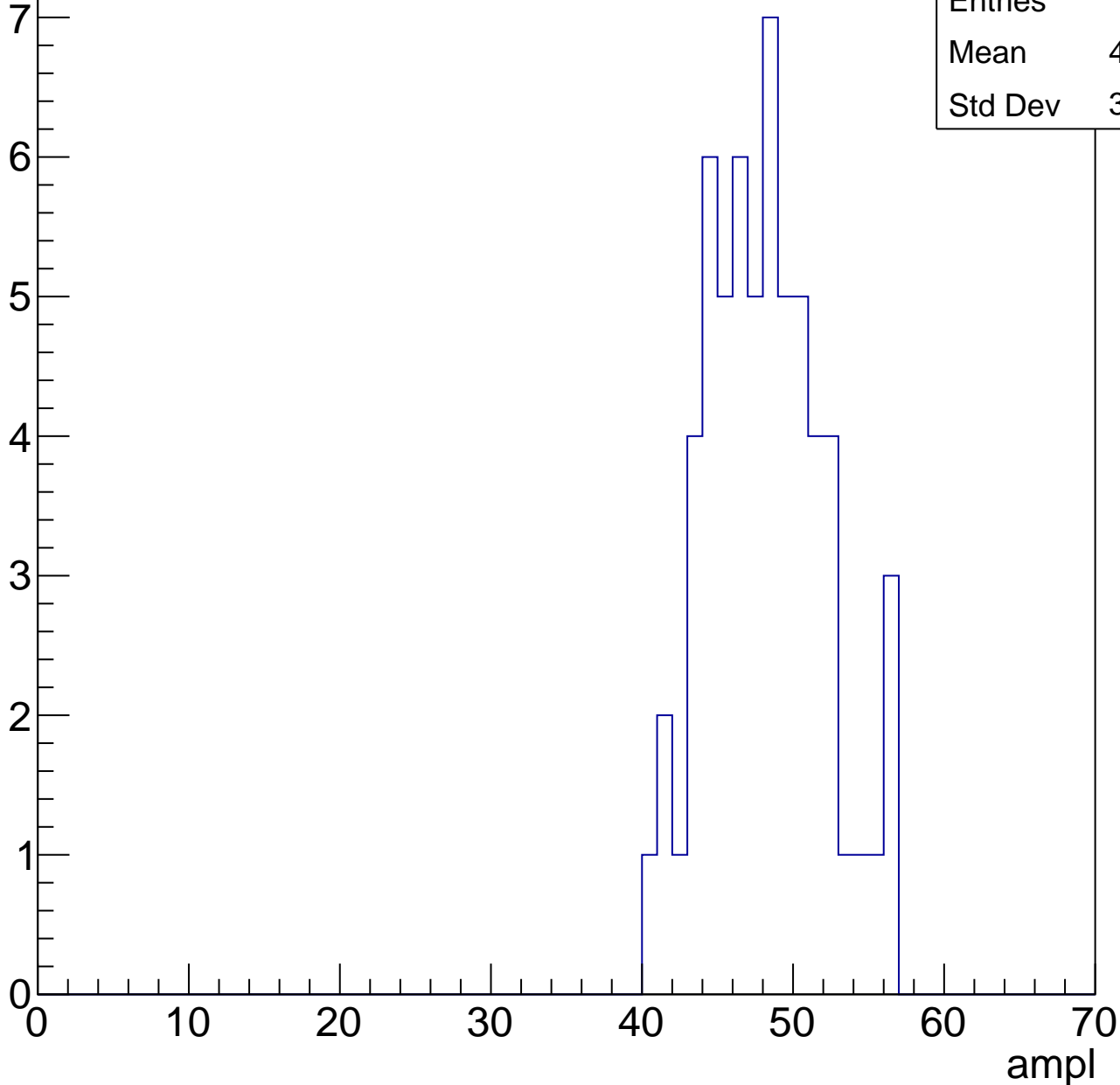


B1L103S, U17-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	47.69
Std Dev	3.818

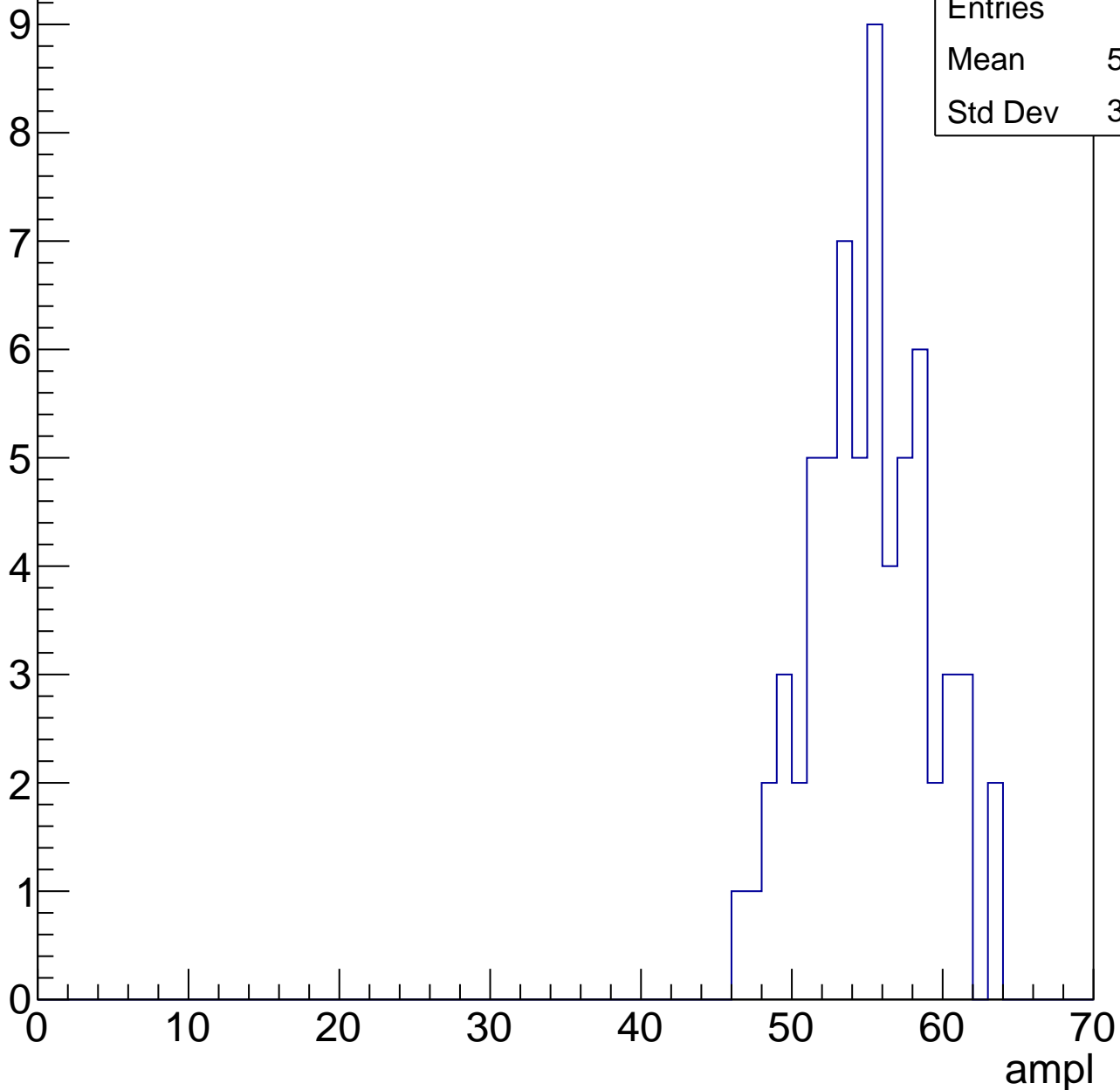


B1L103S, U17-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

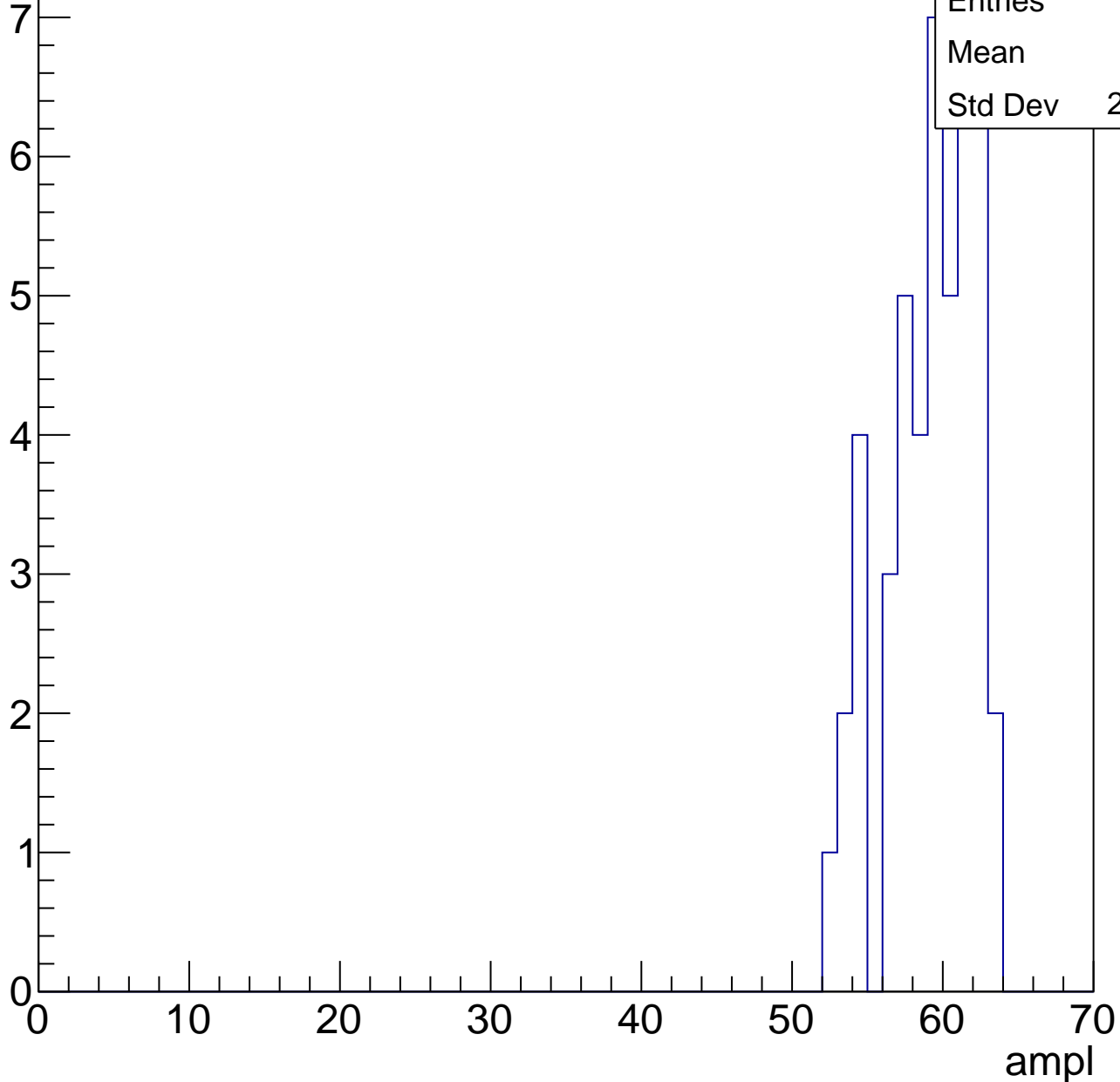
Entries	65
Mean	54.63
Std Dev	3.865



B1L103S, U17-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

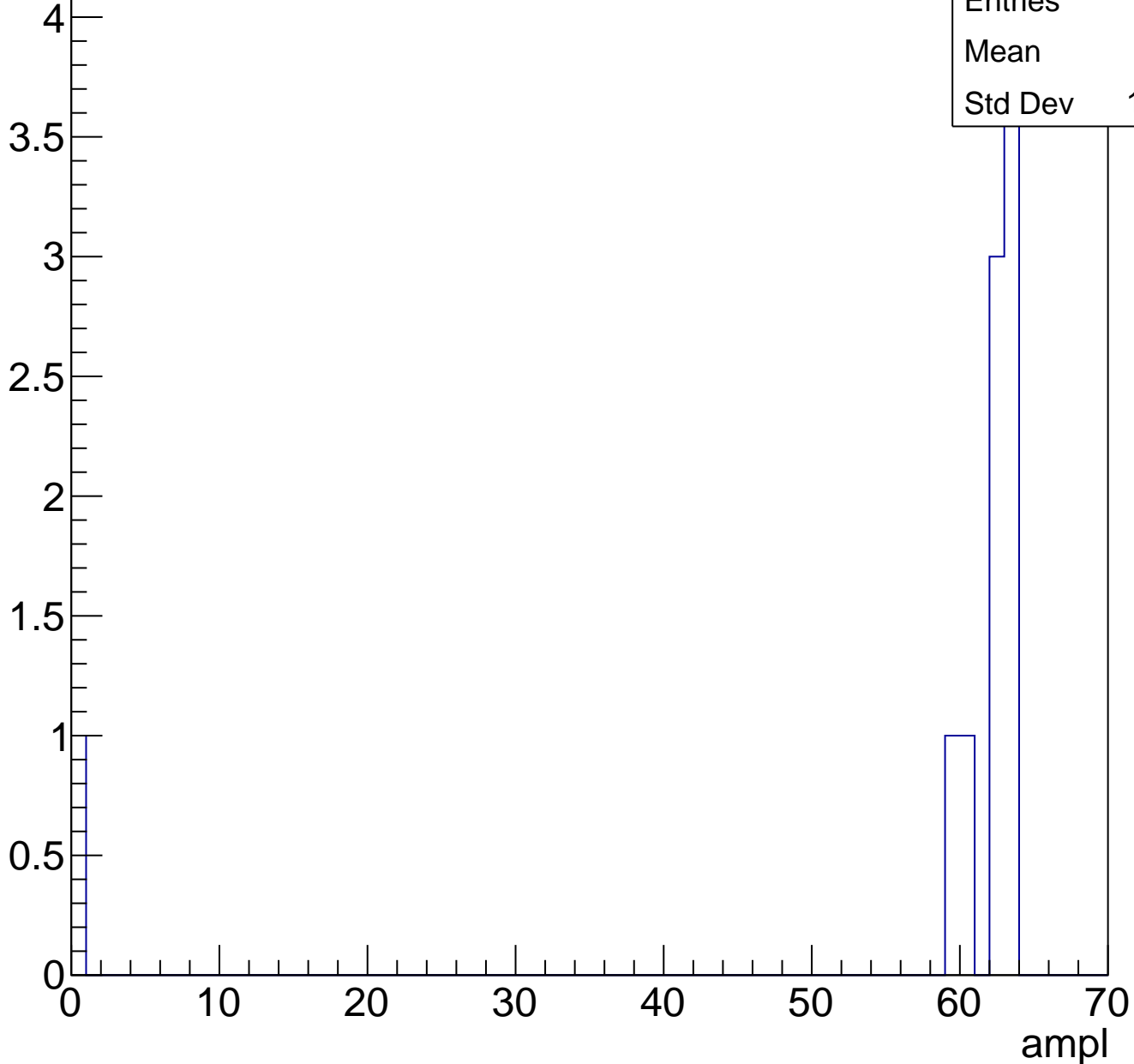


Entries	47
Mean	58.7
Std Dev	2.902

B1L103S, U17-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

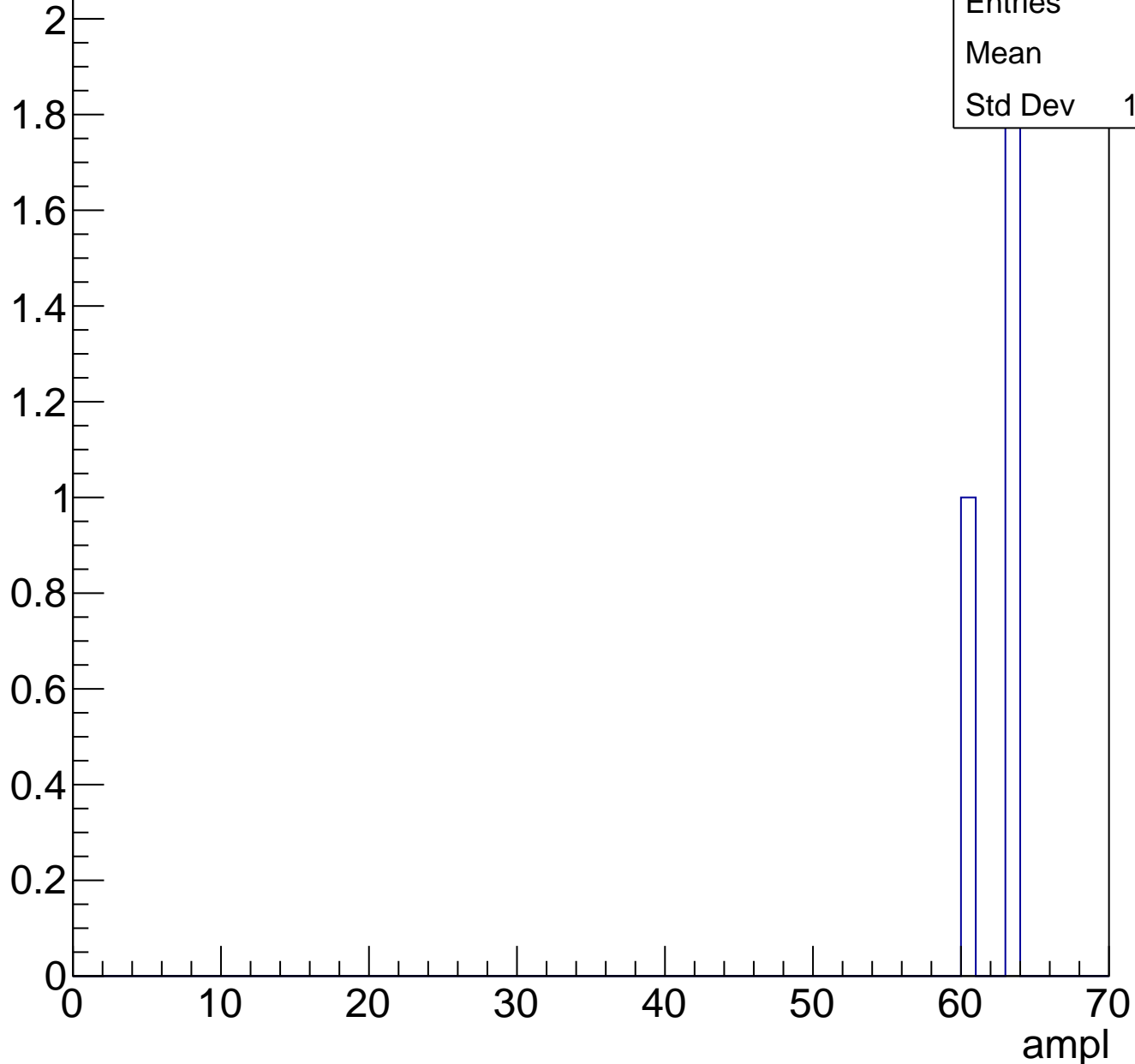
Entry



B1L103S, U17-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch115, adc0

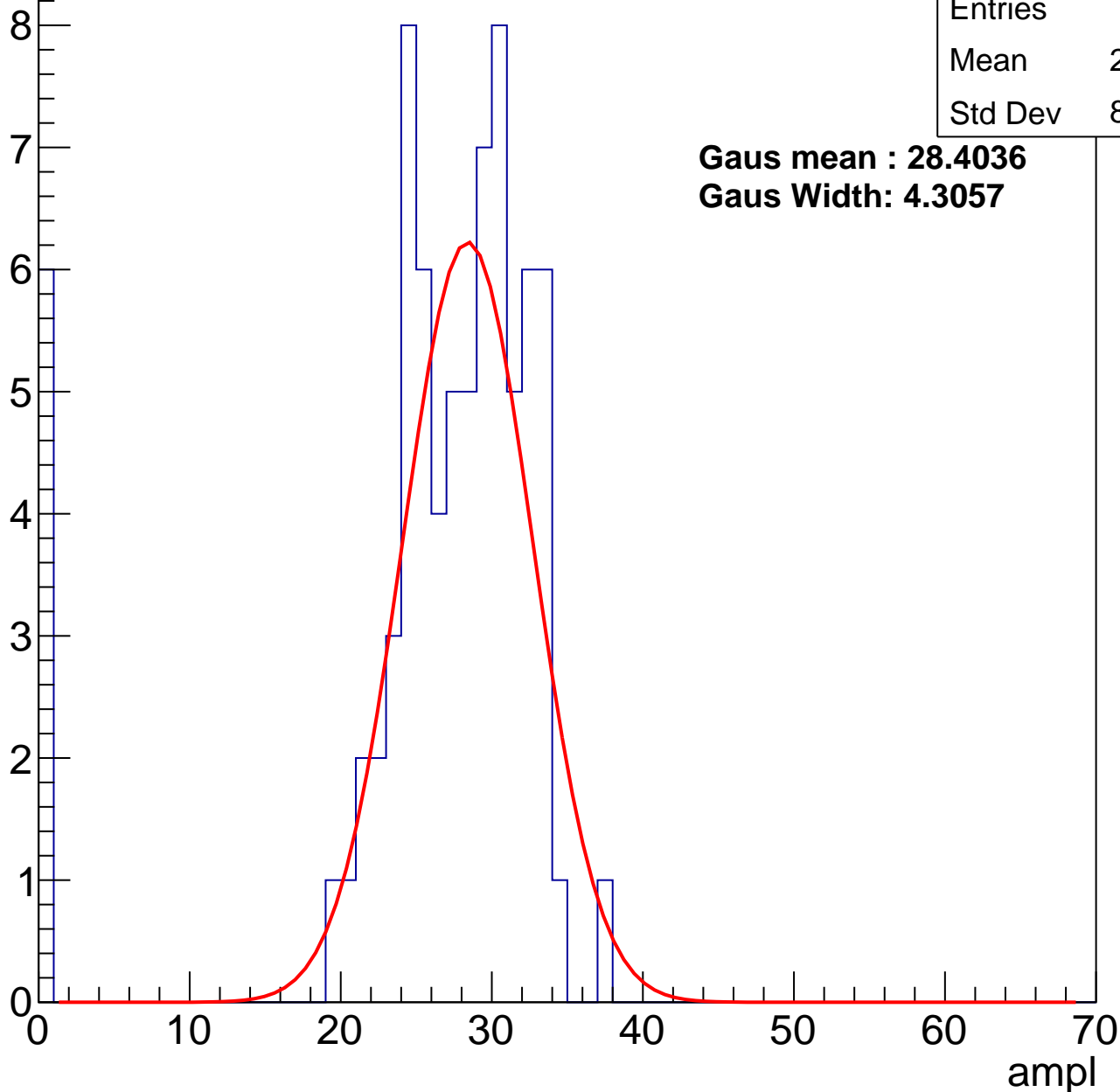
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	25.64
Std Dev	8.307

Gaus mean : 28.4036

Gaus Width: 4.3057



B1L103S, U17-ch115, adc1

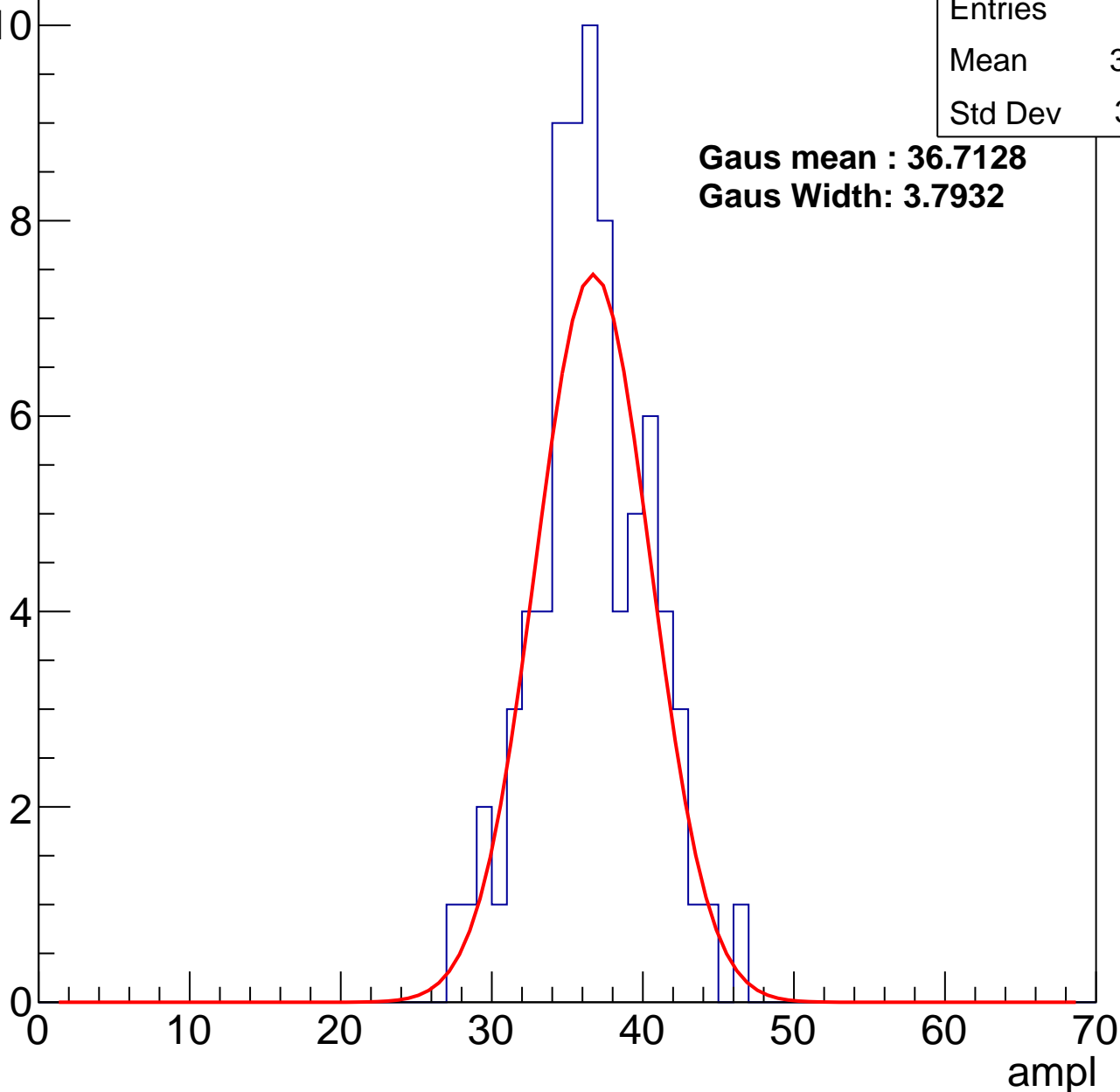
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.14
Std Dev	3.751

Gaus mean : 36.7128

Gaus Width: 3.7932

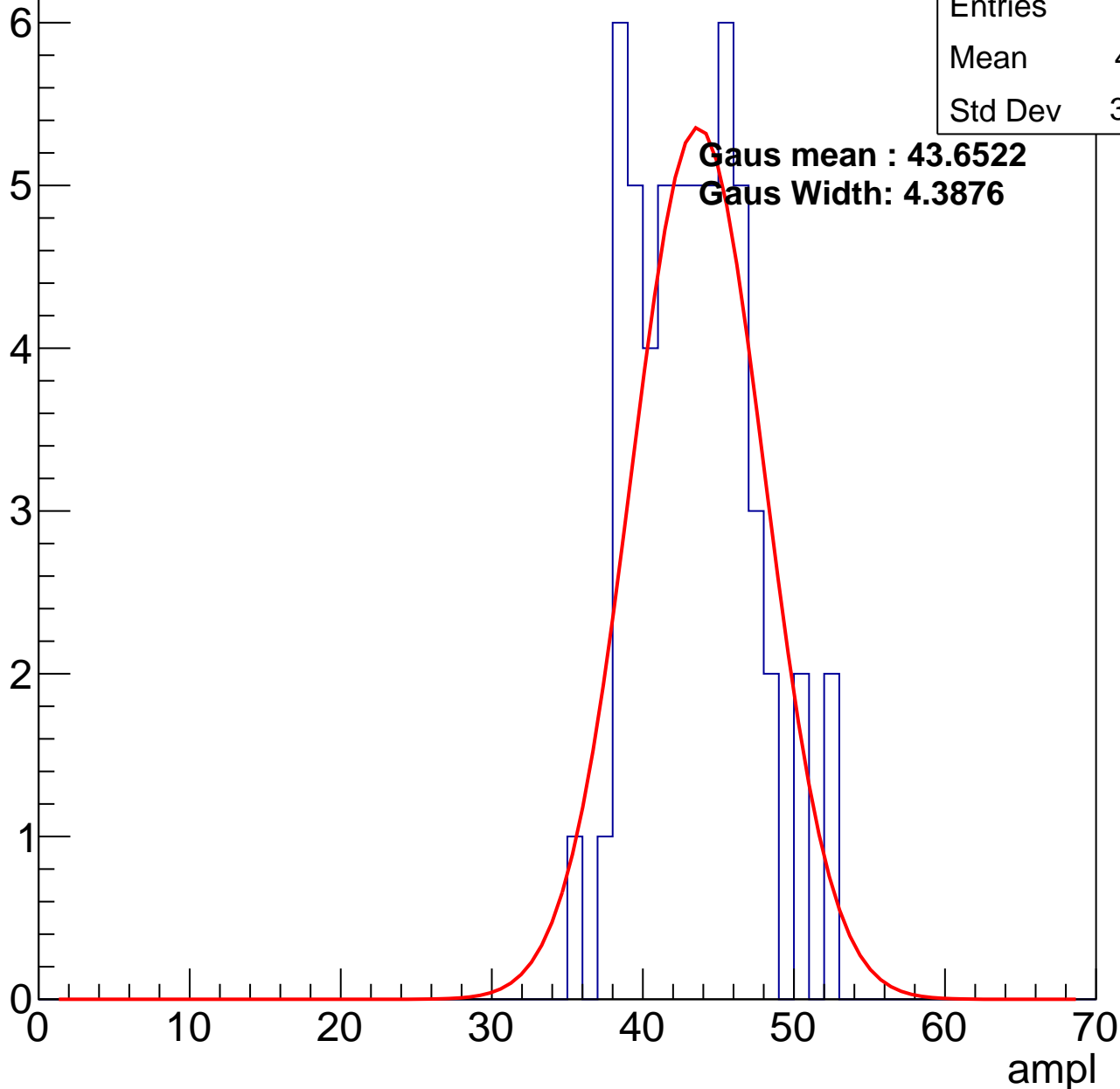


B1L103S, U17-ch115, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.91
Std Dev	3.799

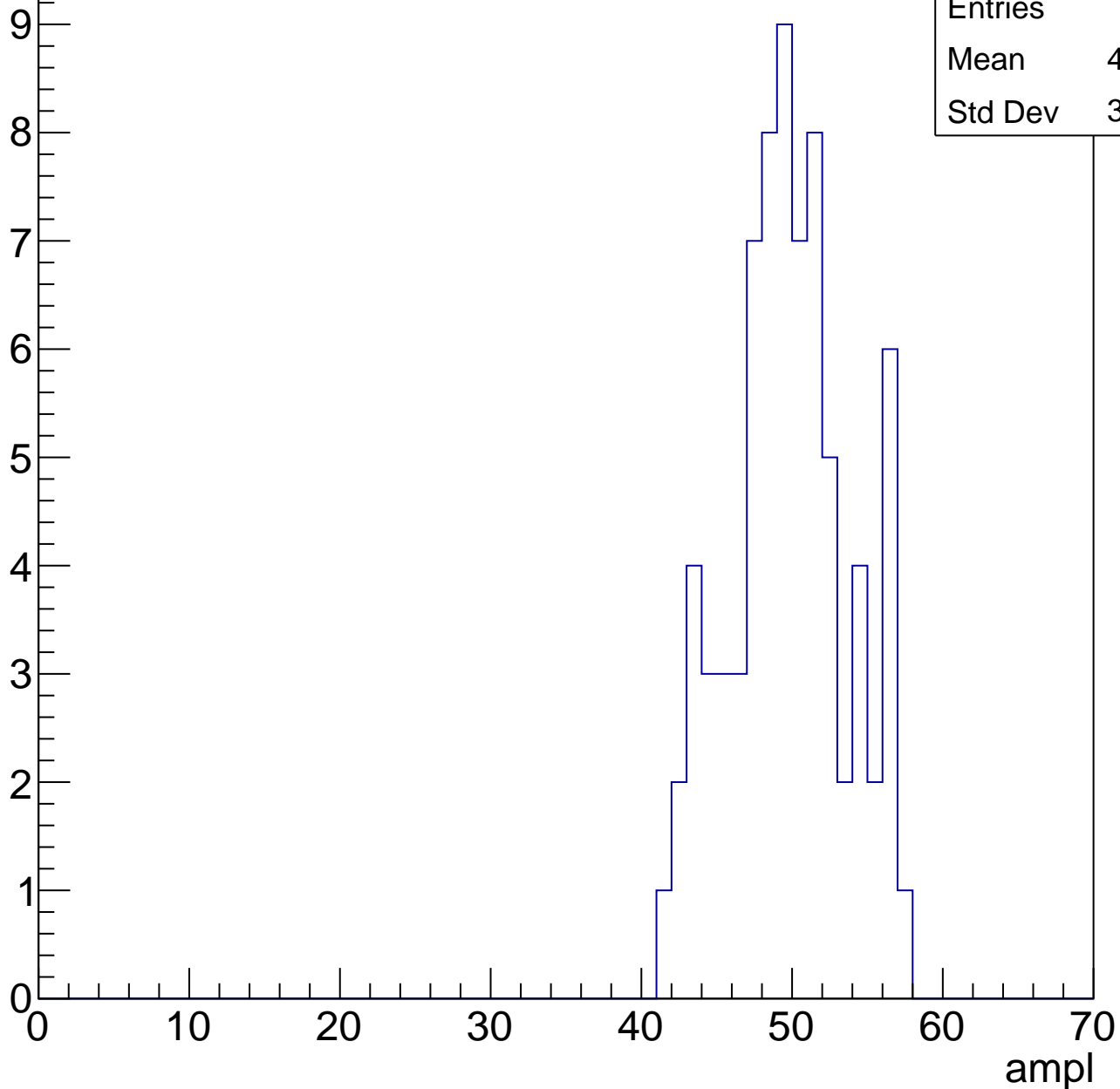


B1L103S, U17-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	49.32
Std Dev	3.882

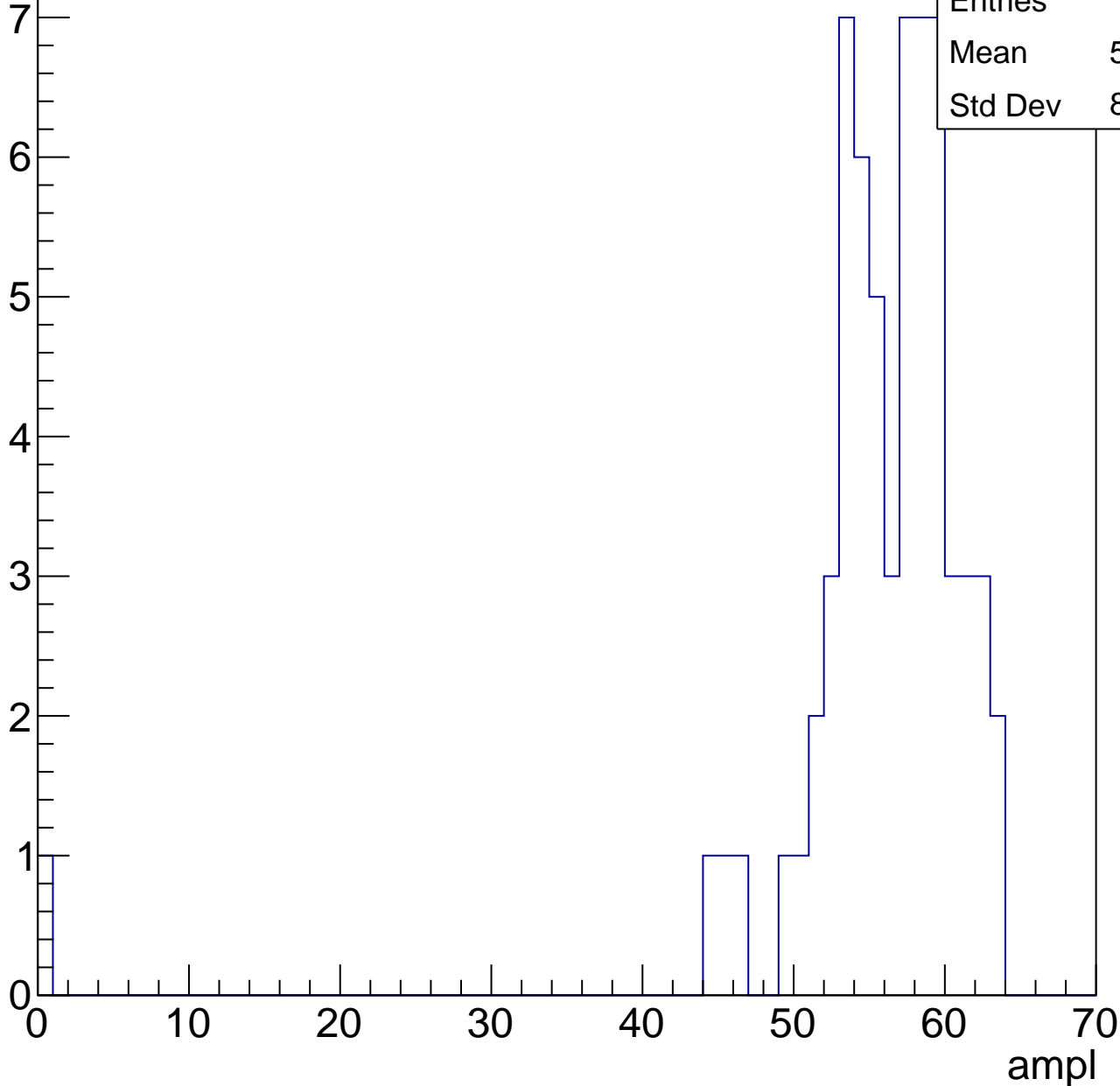


B1L103S, U17-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.05
Std Dev	8.048

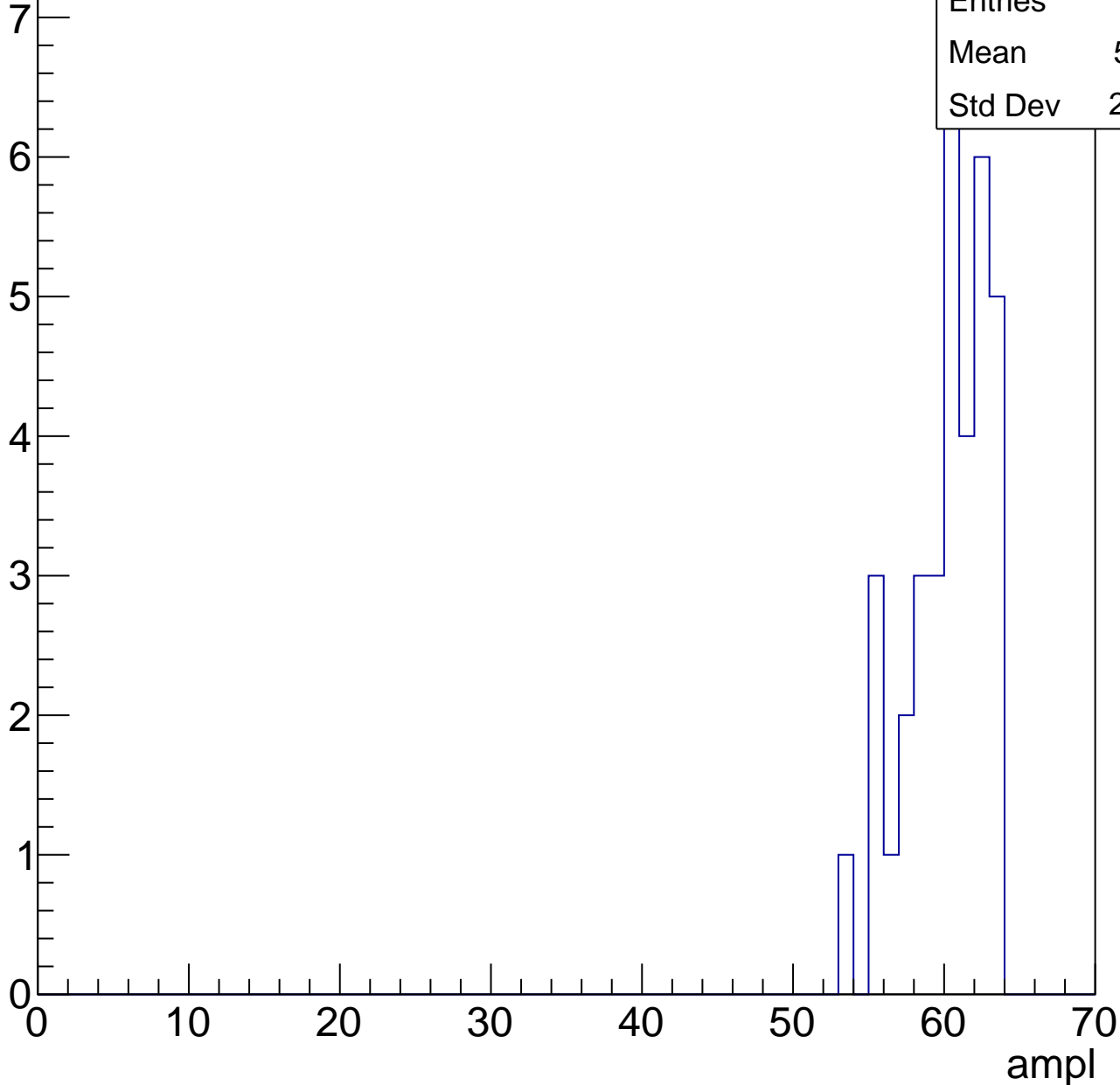


B1L103S, U17-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

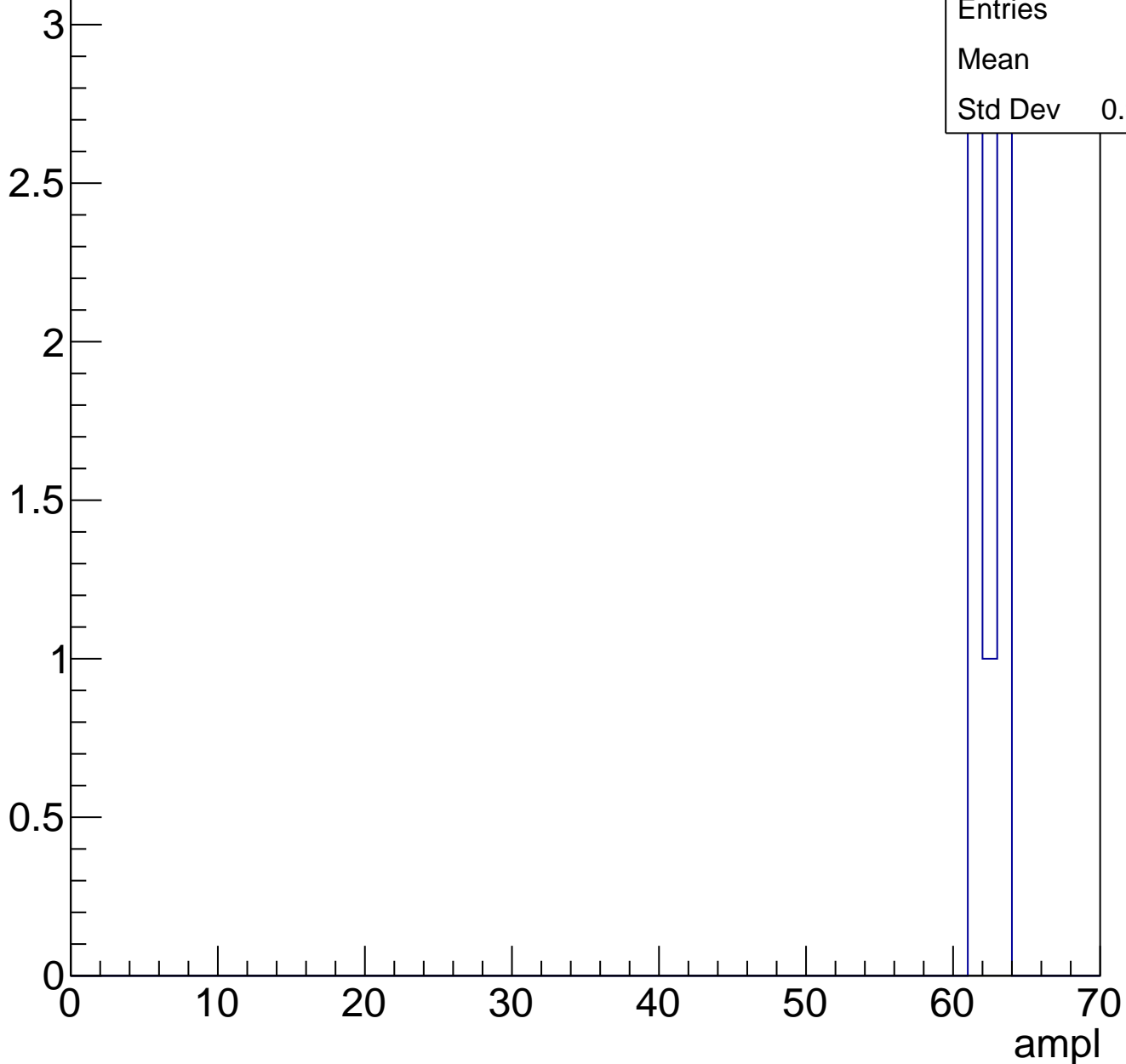
Entries	35
Mean	59.71
Std Dev	2.636



B1L103S, U17-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch116, adc0

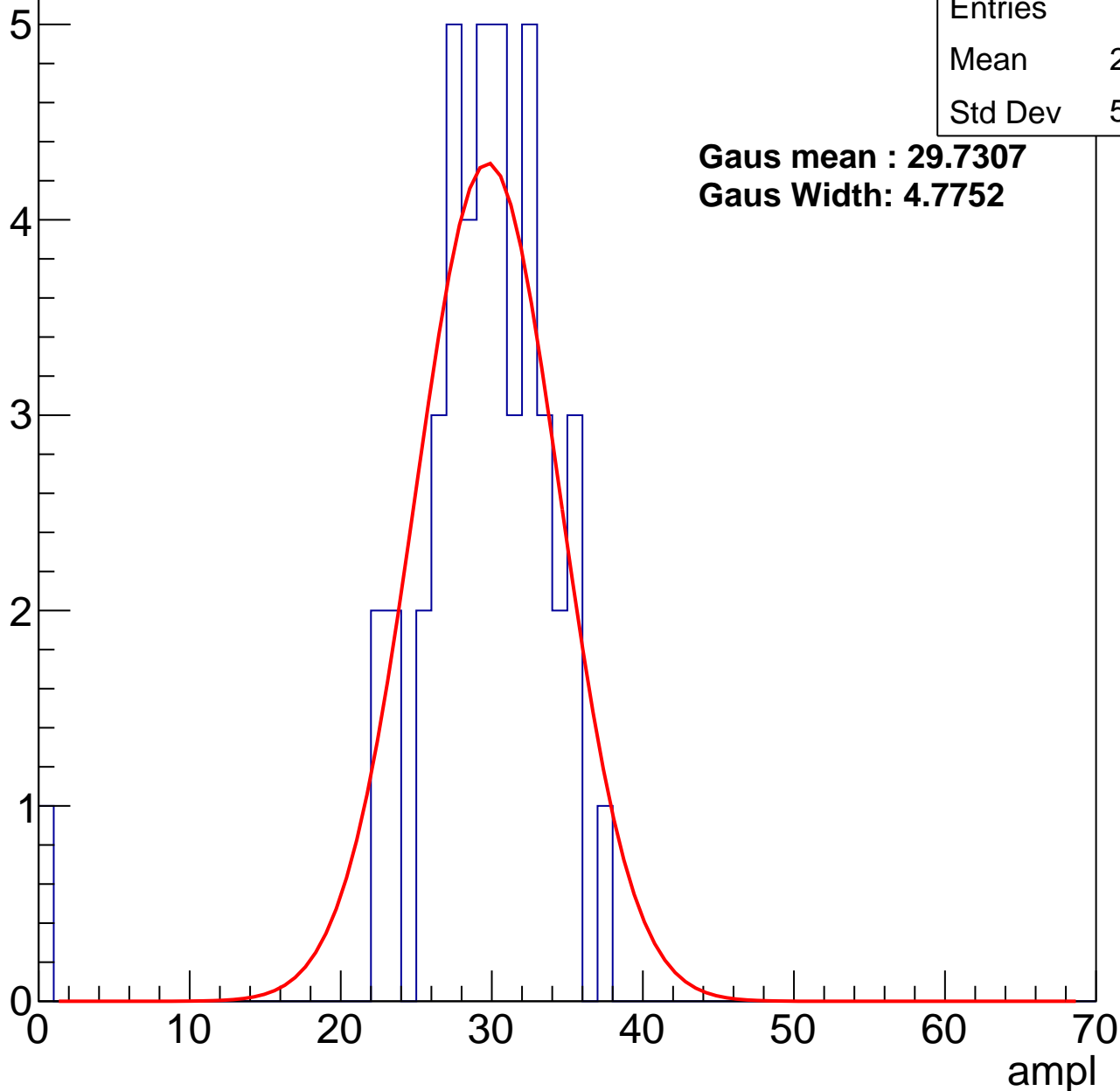
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	28.74
Std Dev	5.562

Gaus mean : 29.7307

Gaus Width: 4.7752



B1L103S, U17-ch116, adc1

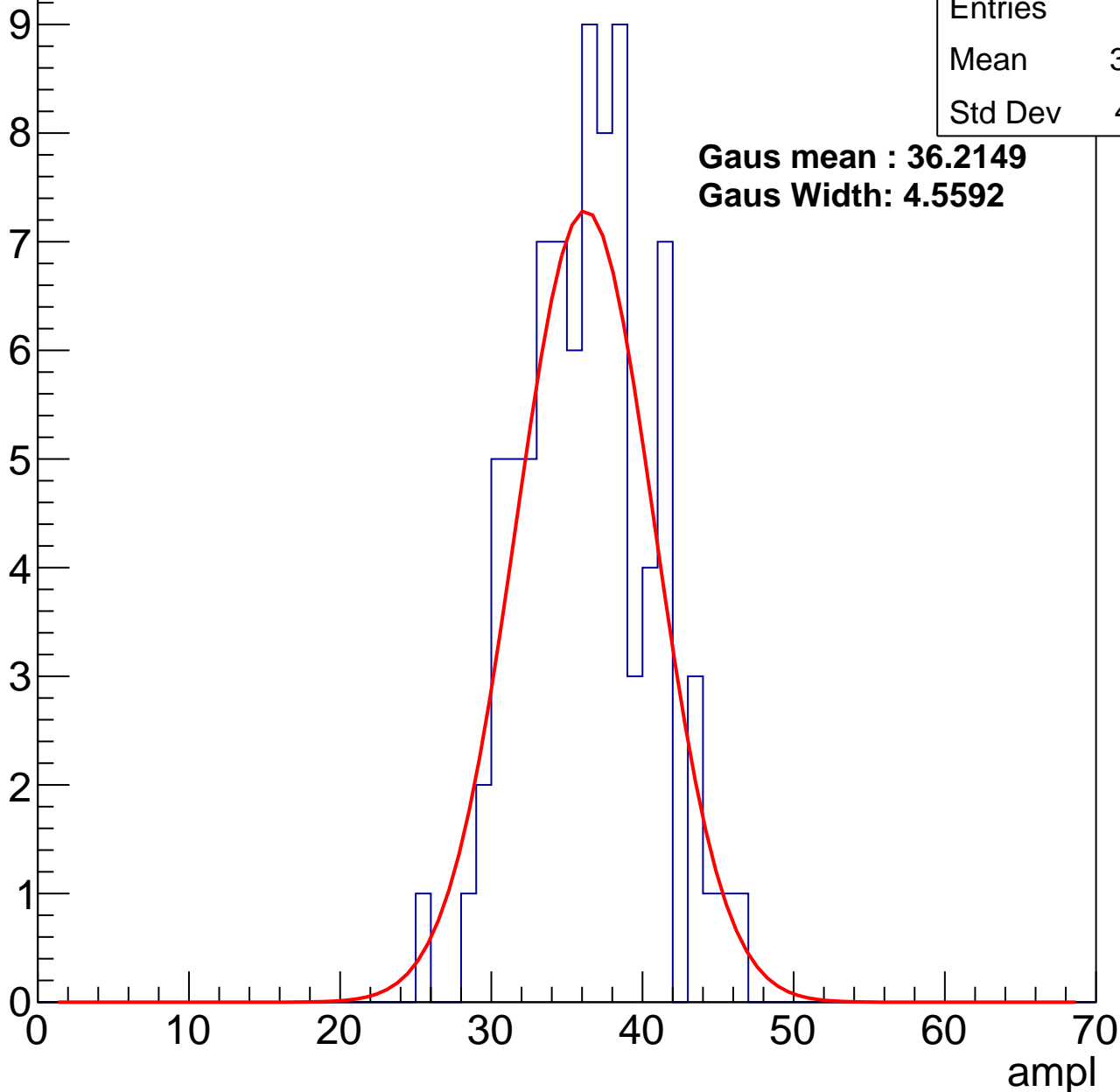
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	35.82
Std Dev	4.151

Gaus mean : 36.2149

Gaus Width: 4.5592



B1L103S, U17-ch116, adc2

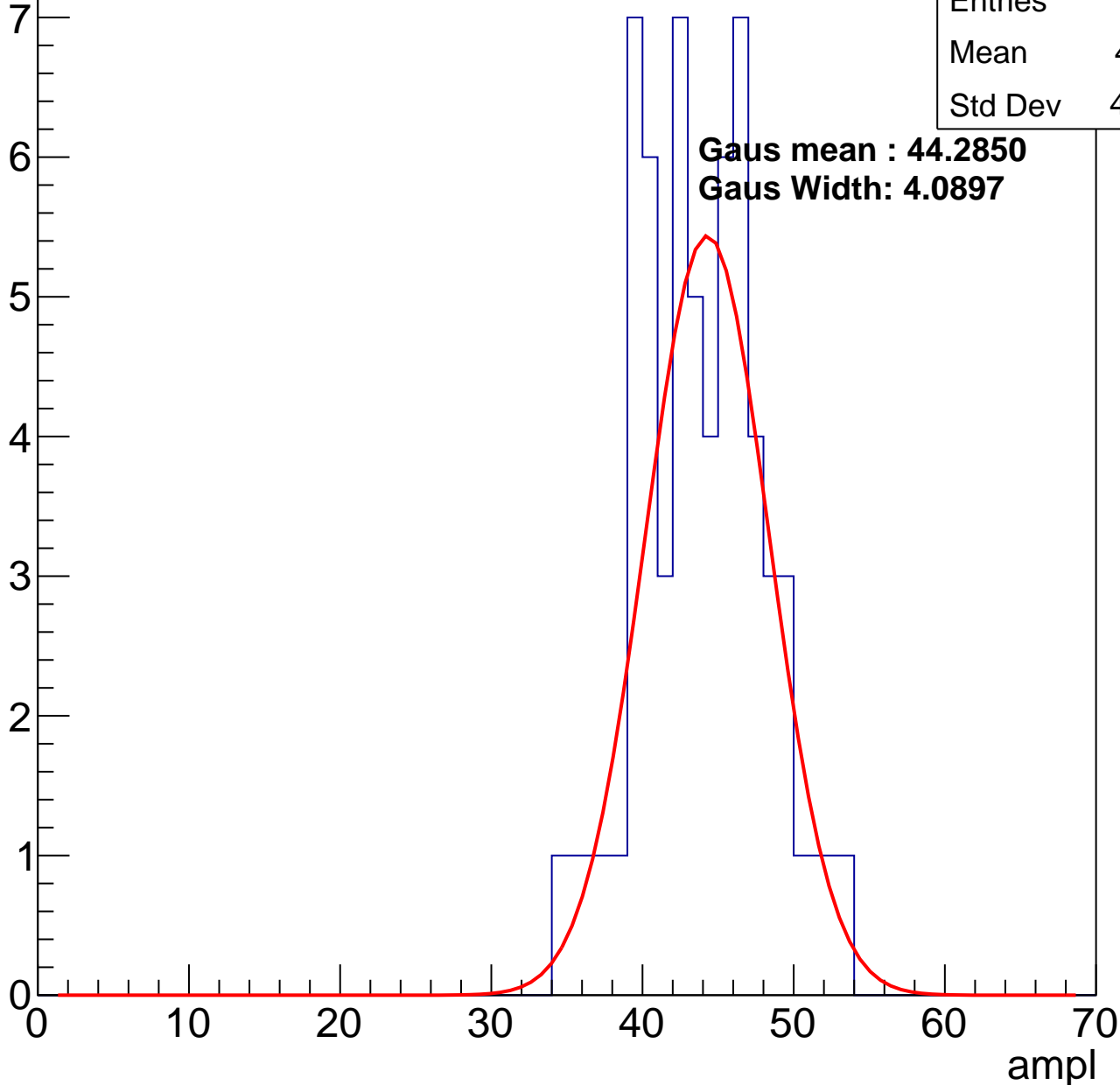
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.41
Std Dev	4.072

Gaus mean : 44.2850

Gaus Width: 4.0897

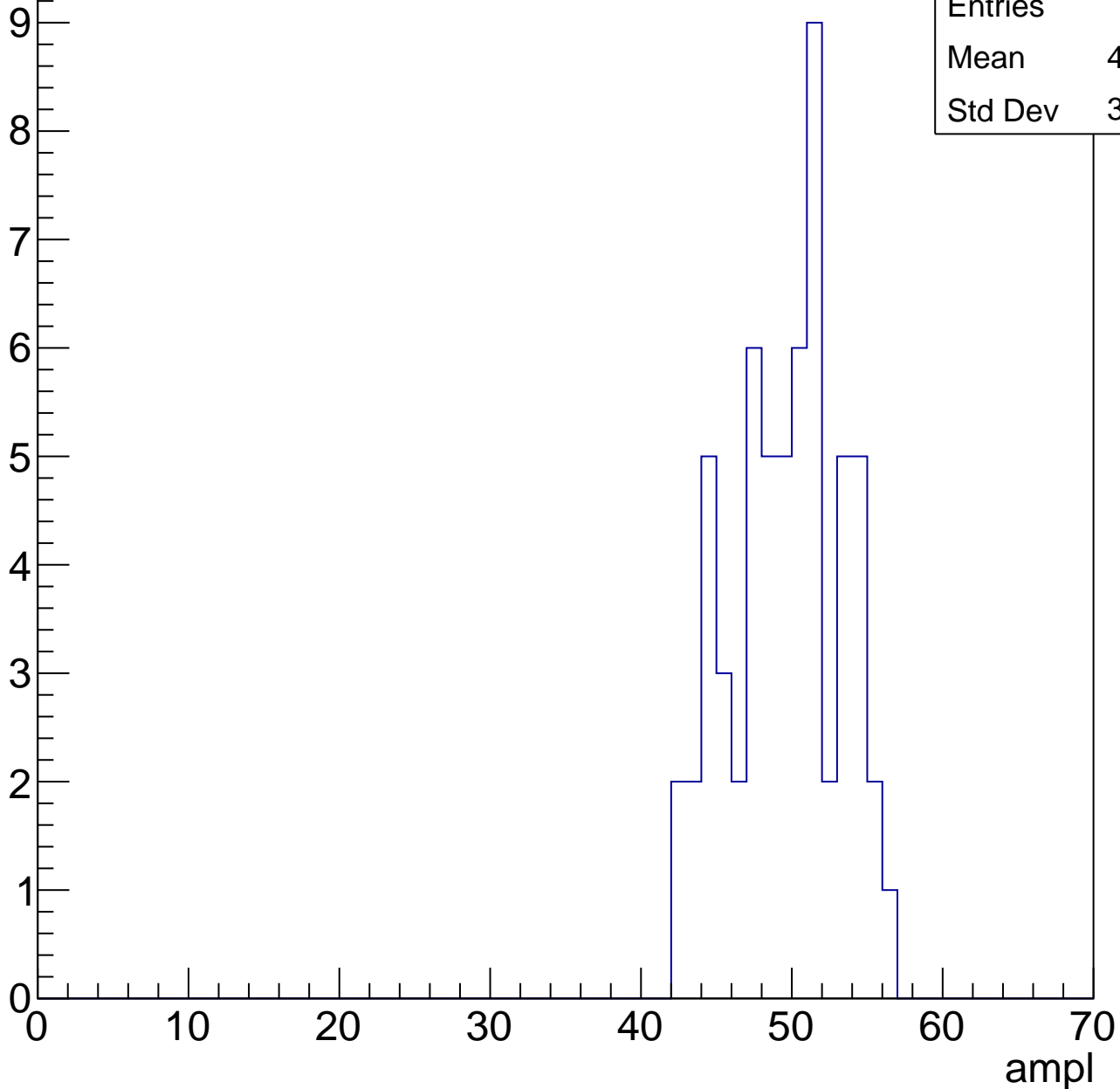


B1L103S, U17-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.13
Std Dev	3.594

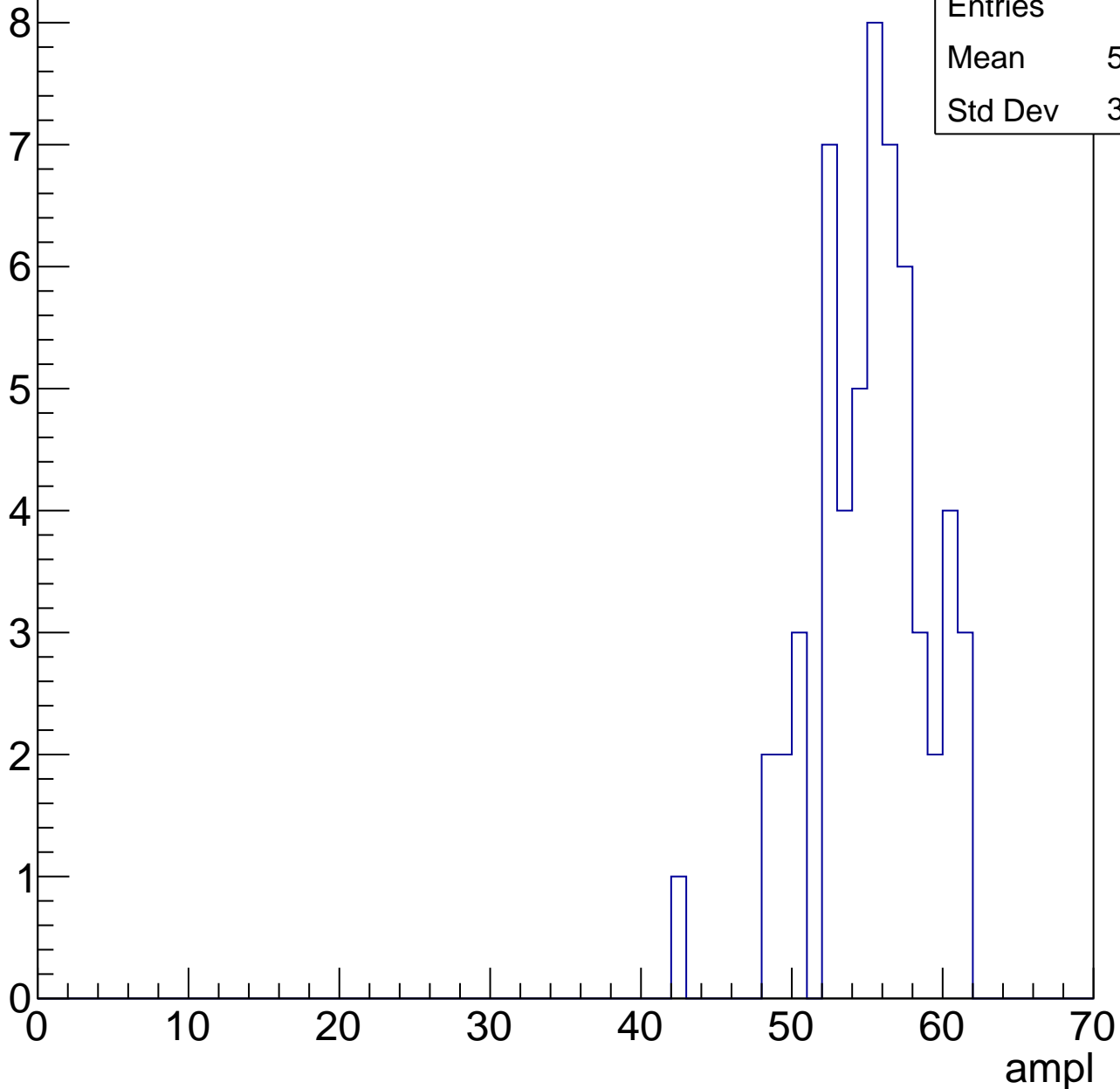


B1L103S, U17-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.75
Std Dev	3.729



B1L103S, U17-ch116, adc5

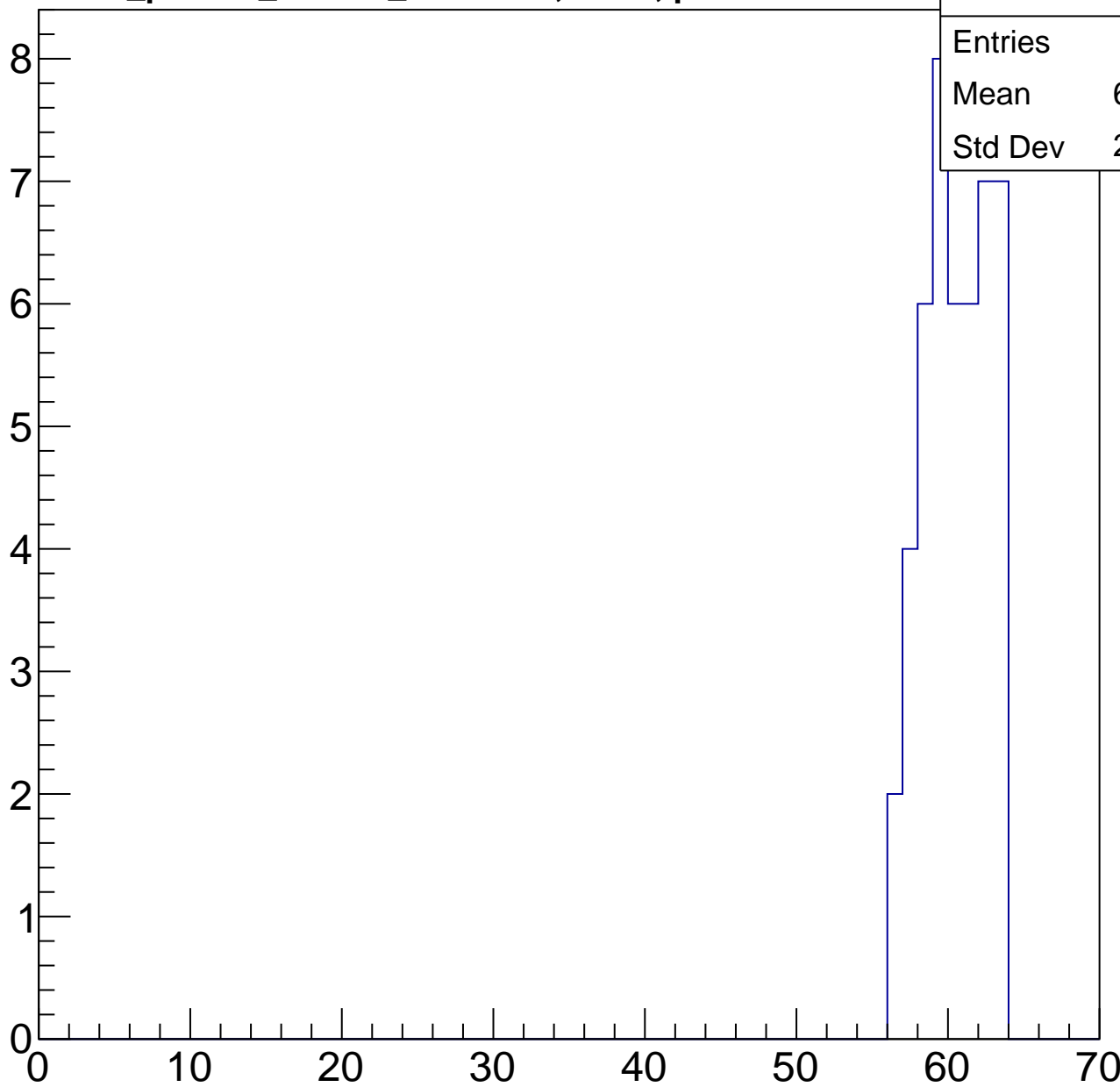
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.02
Std Dev	2.069

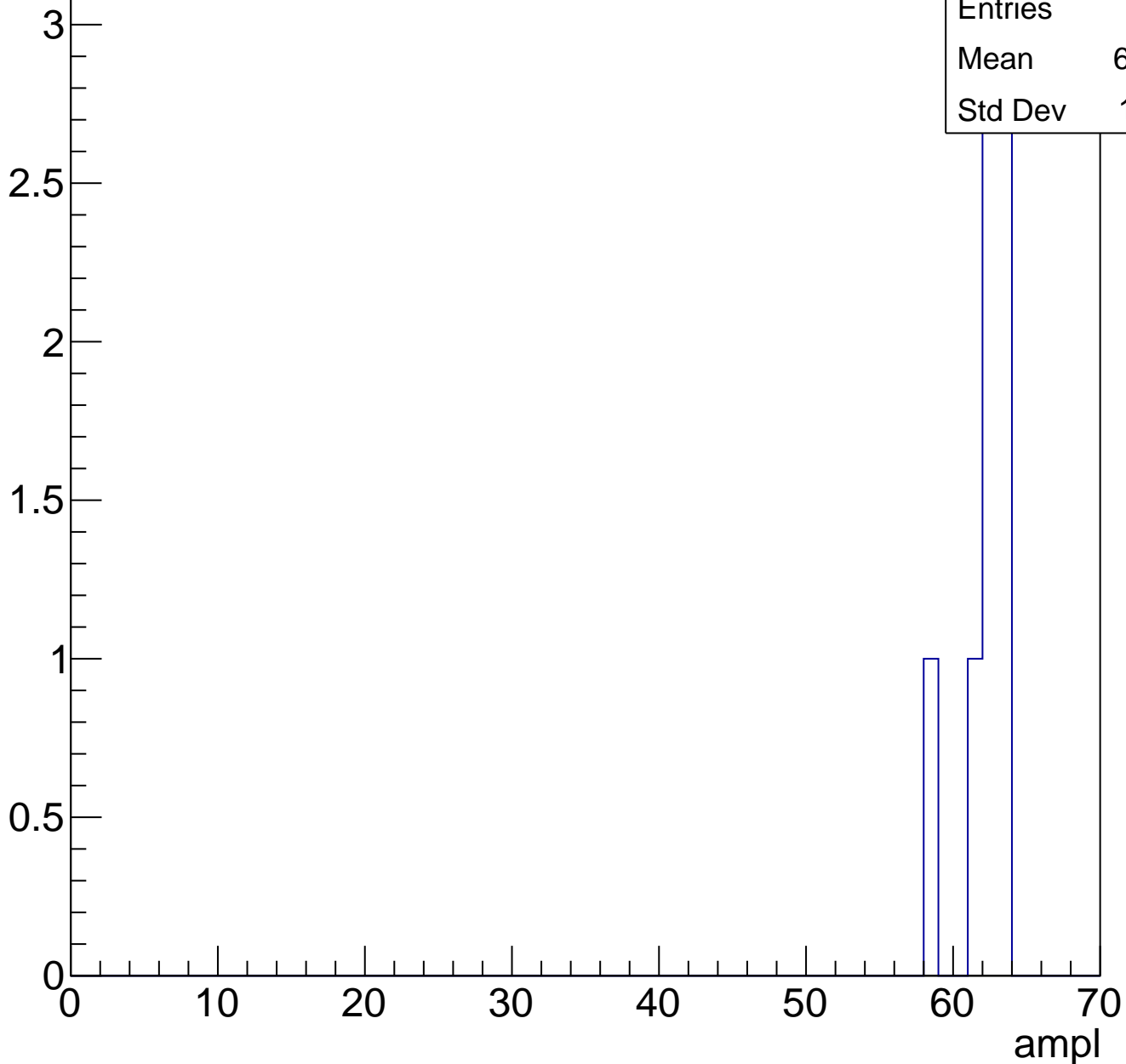
ampl



B1L103S, U17-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch117, adc0

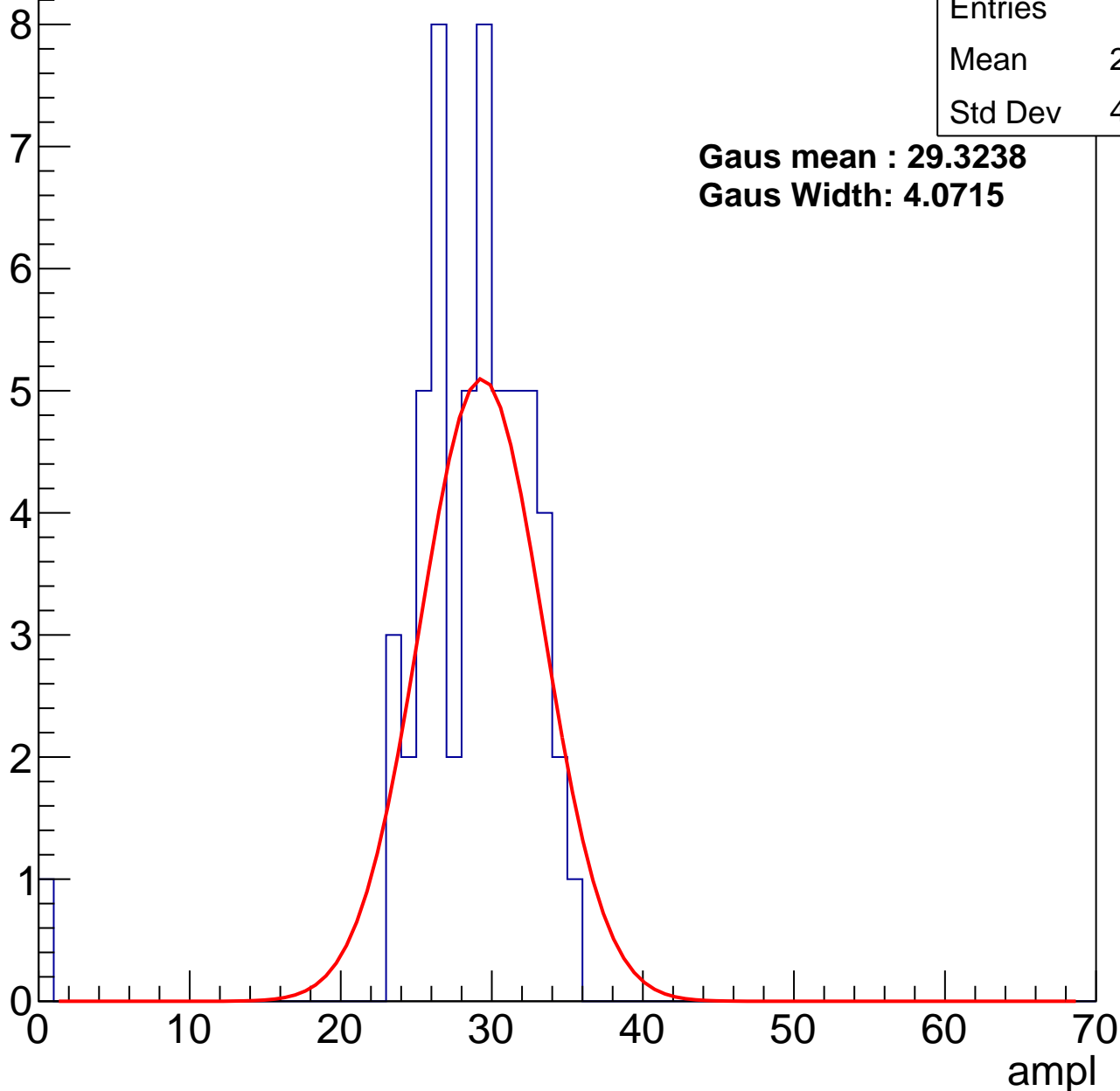
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	28.14
Std Dev	4.897

Gaus mean : 29.3238

Gaus Width: 4.0715



B1L103S, U17-ch117, adc1

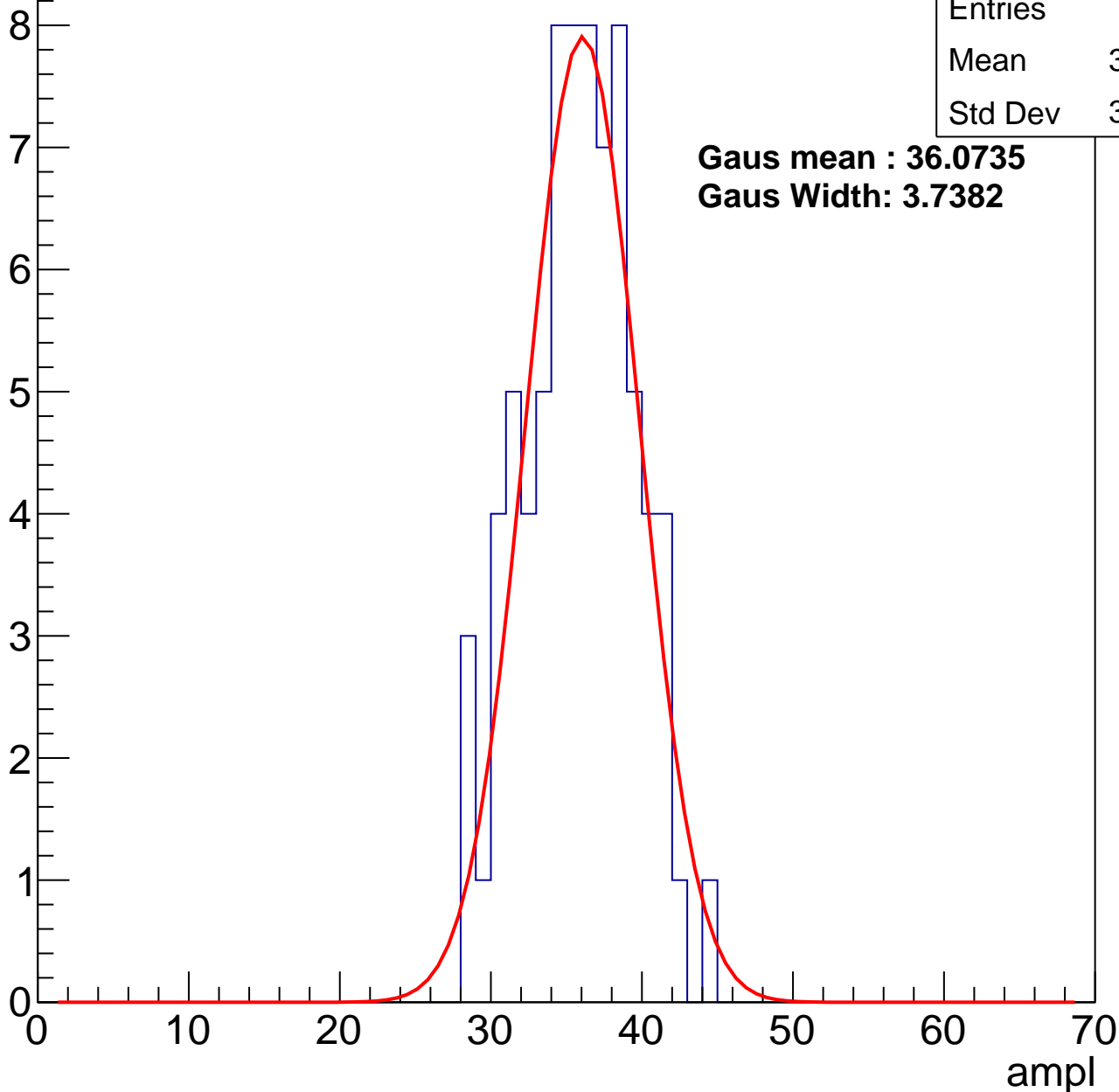
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.38
Std Dev	3.587

Gaus mean : 36.0735

Gaus Width: 3.7382



B1L103S, U17-ch117, adc2

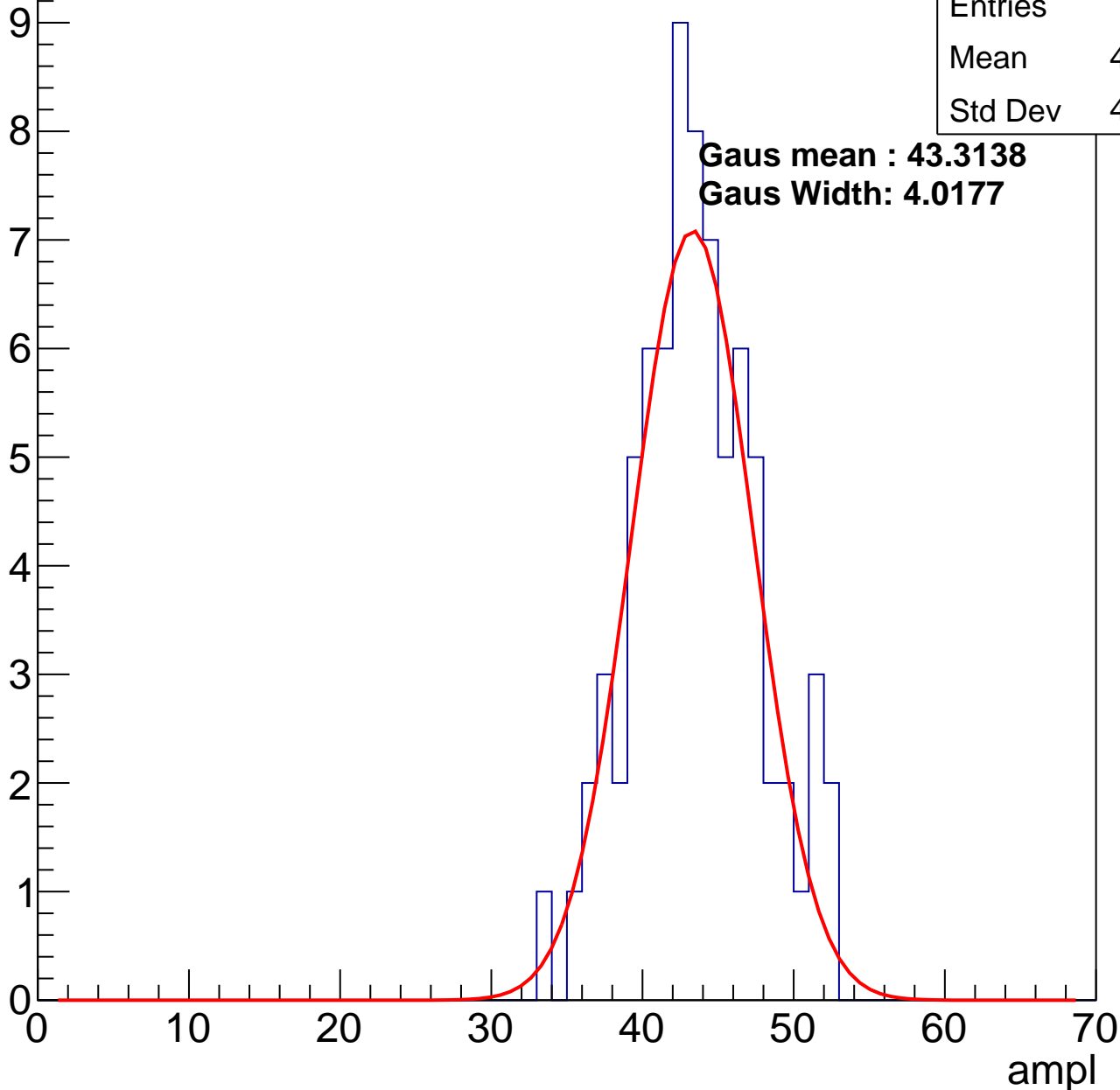
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.09
Std Dev	4.092

Gaus mean : 43.3138

Gaus Width: 4.0177

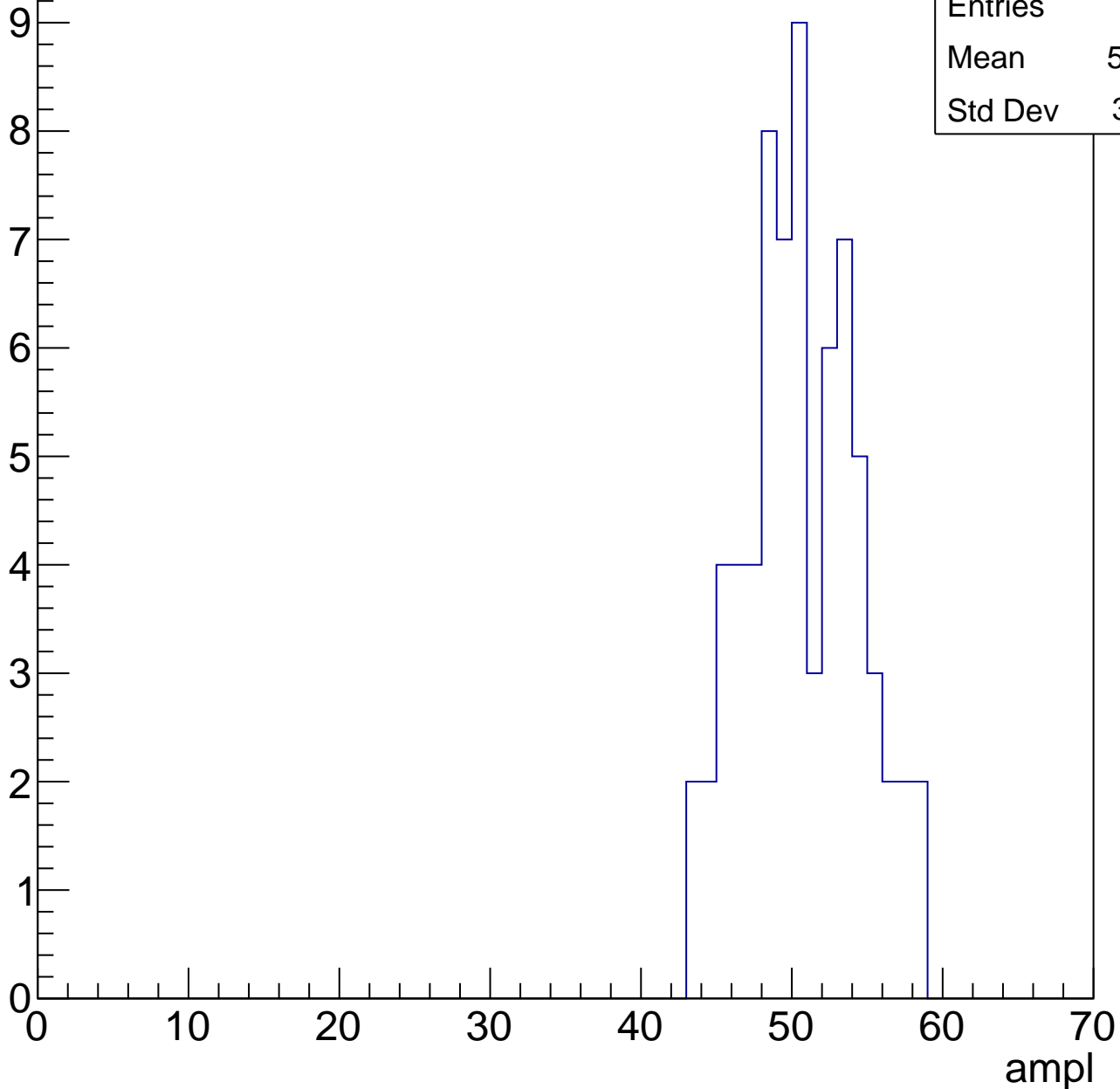


B1L103S, U17-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	50.23
Std Dev	3.681

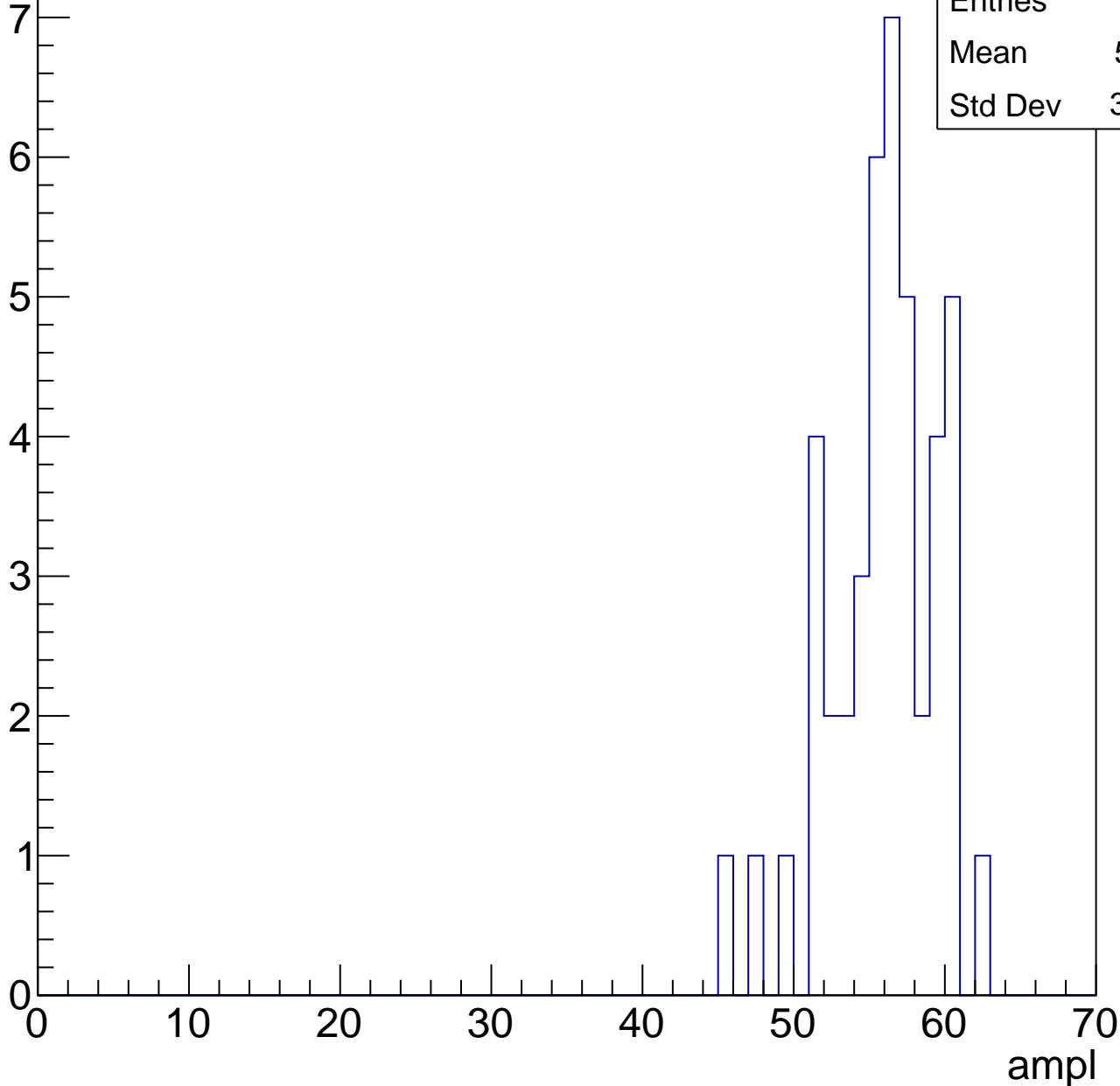


B1L103S, U17-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.41
Std Dev	3.595

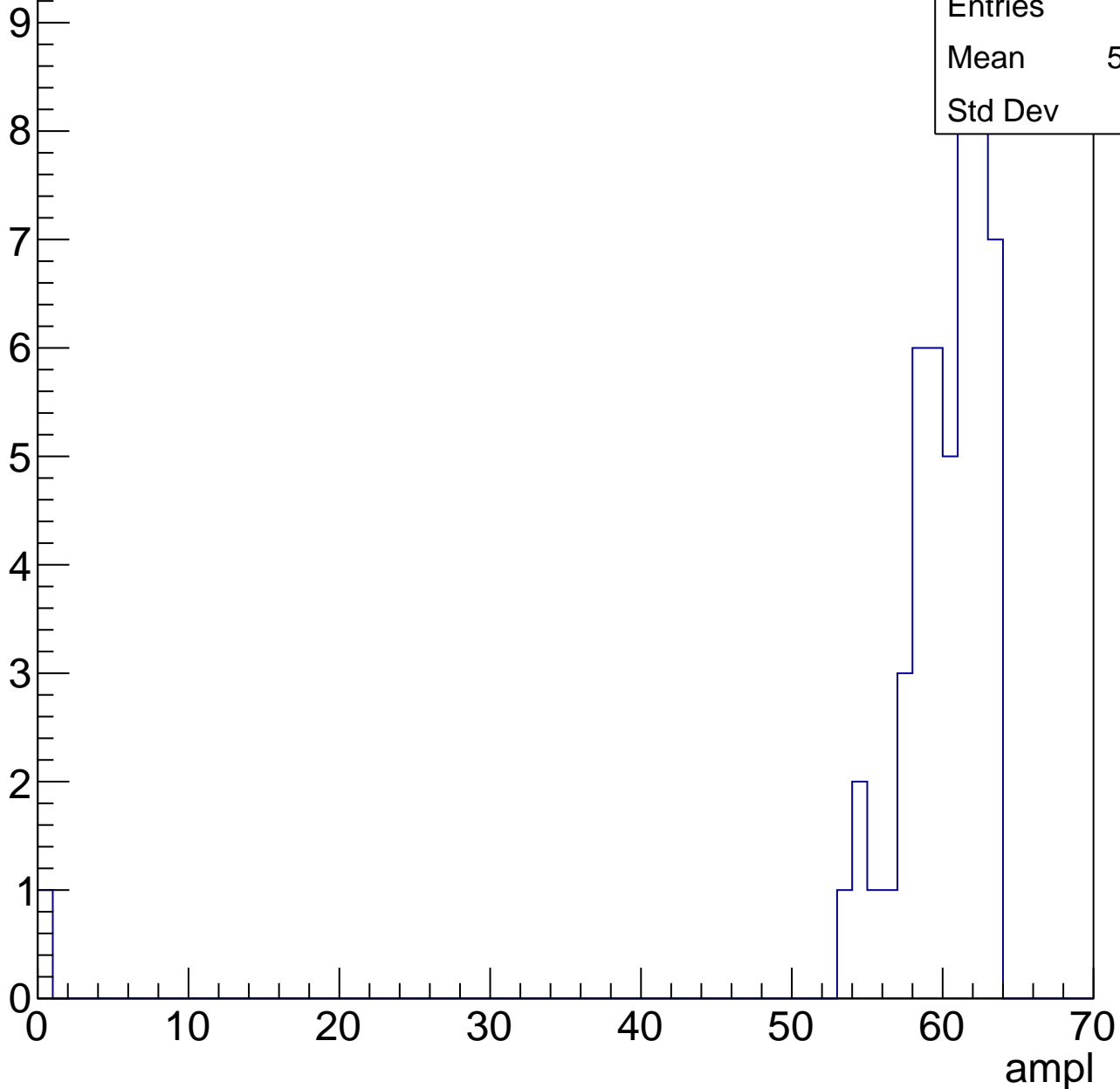


B1L103S, U17-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.62
Std Dev	8.75



B1L103S, U17-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch118, adc0

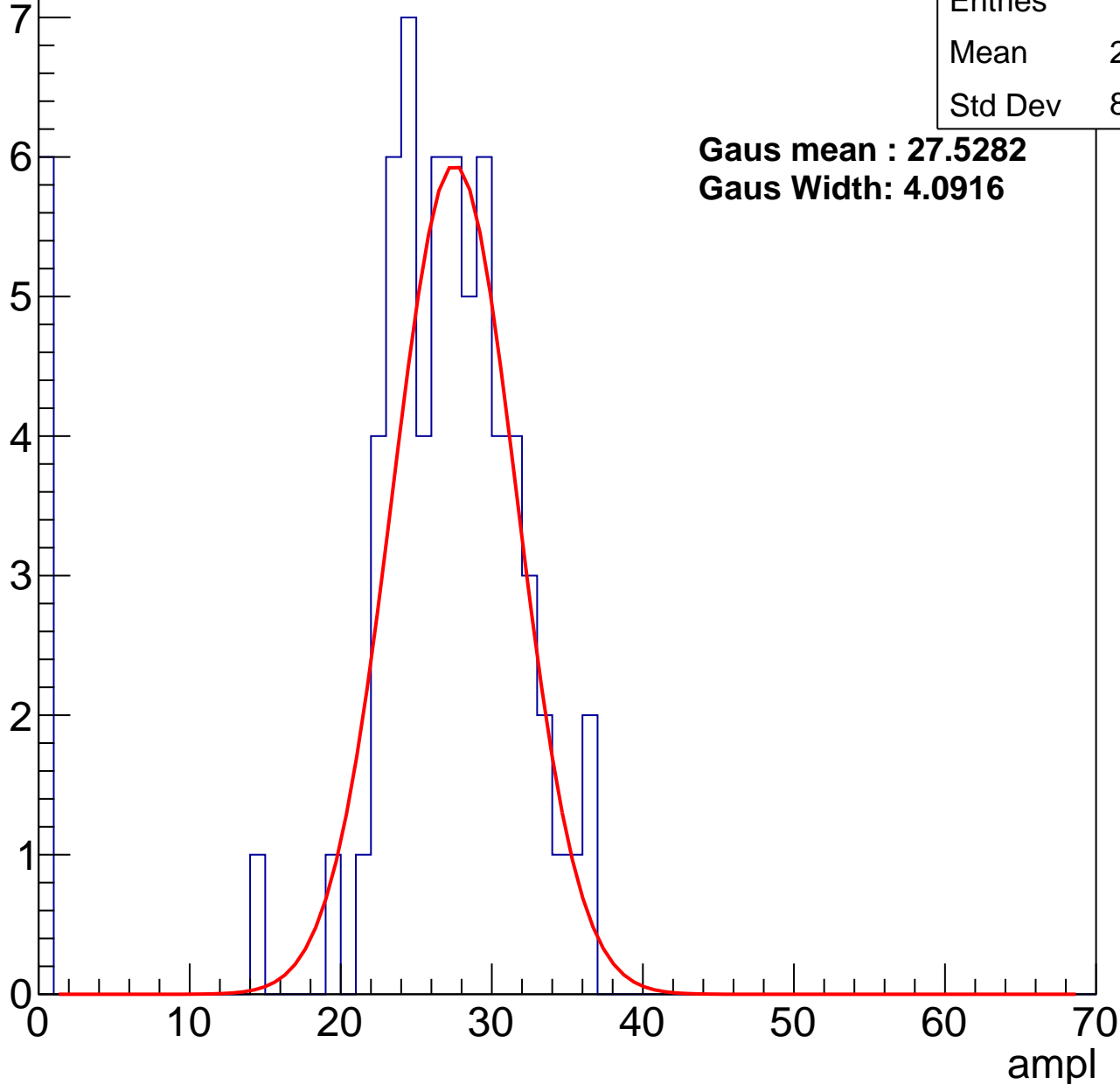
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	24.67
Std Dev	8.532

Gaus mean : 27.5282

Gaus Width: 4.0916



B1L103S, U17-ch118, adc1

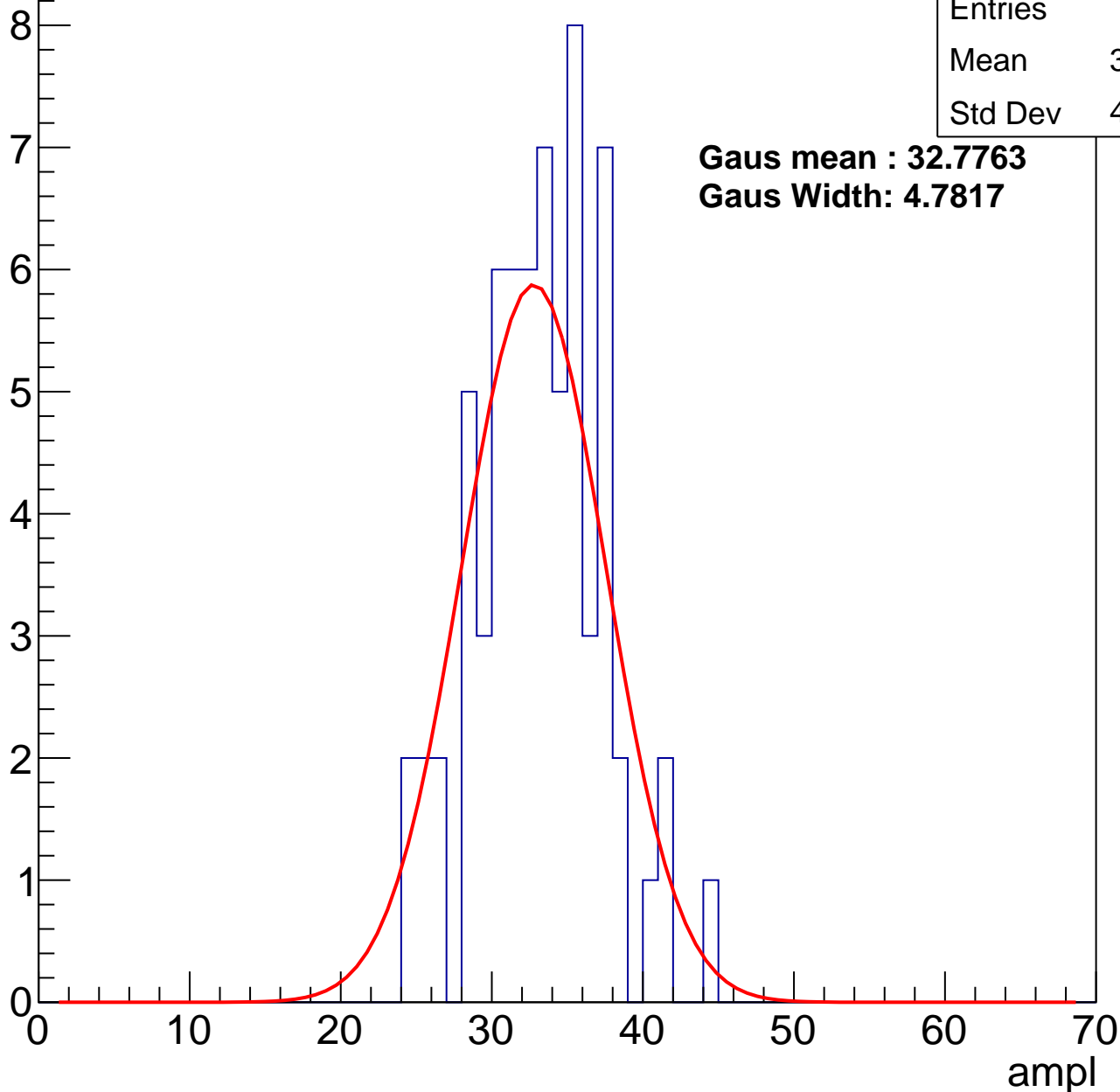
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	32.72
Std Dev	4.137

Gaus mean : 32.7763

Gaus Width: 4.7817



B1L103S, U17-ch118, adc2

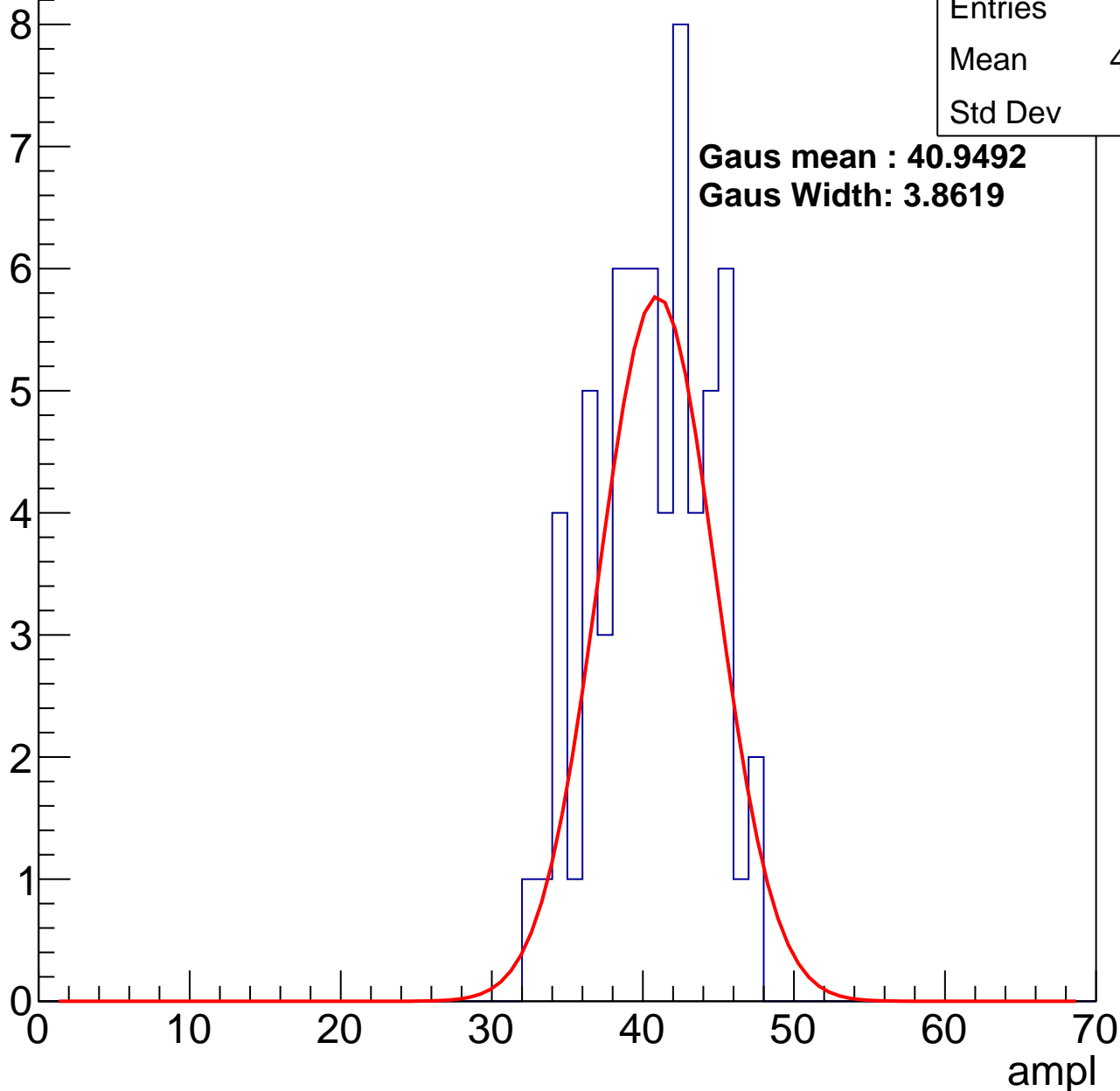
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	40.17
Std Dev	3.68

Gaus mean : 40.9492

Gaus Width: 3.8619

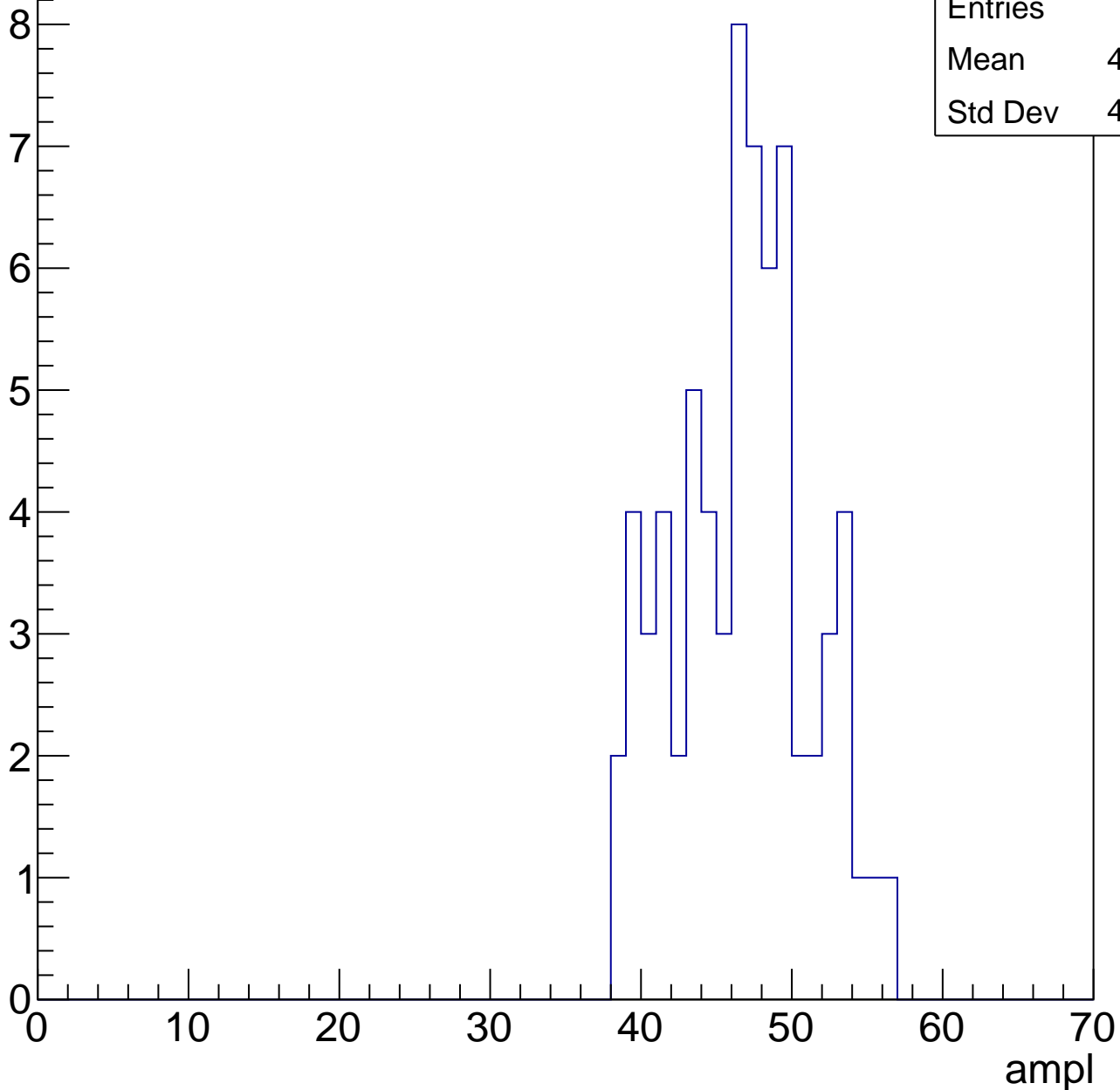


B1L103S, U17-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	46.22
Std Dev	4.423

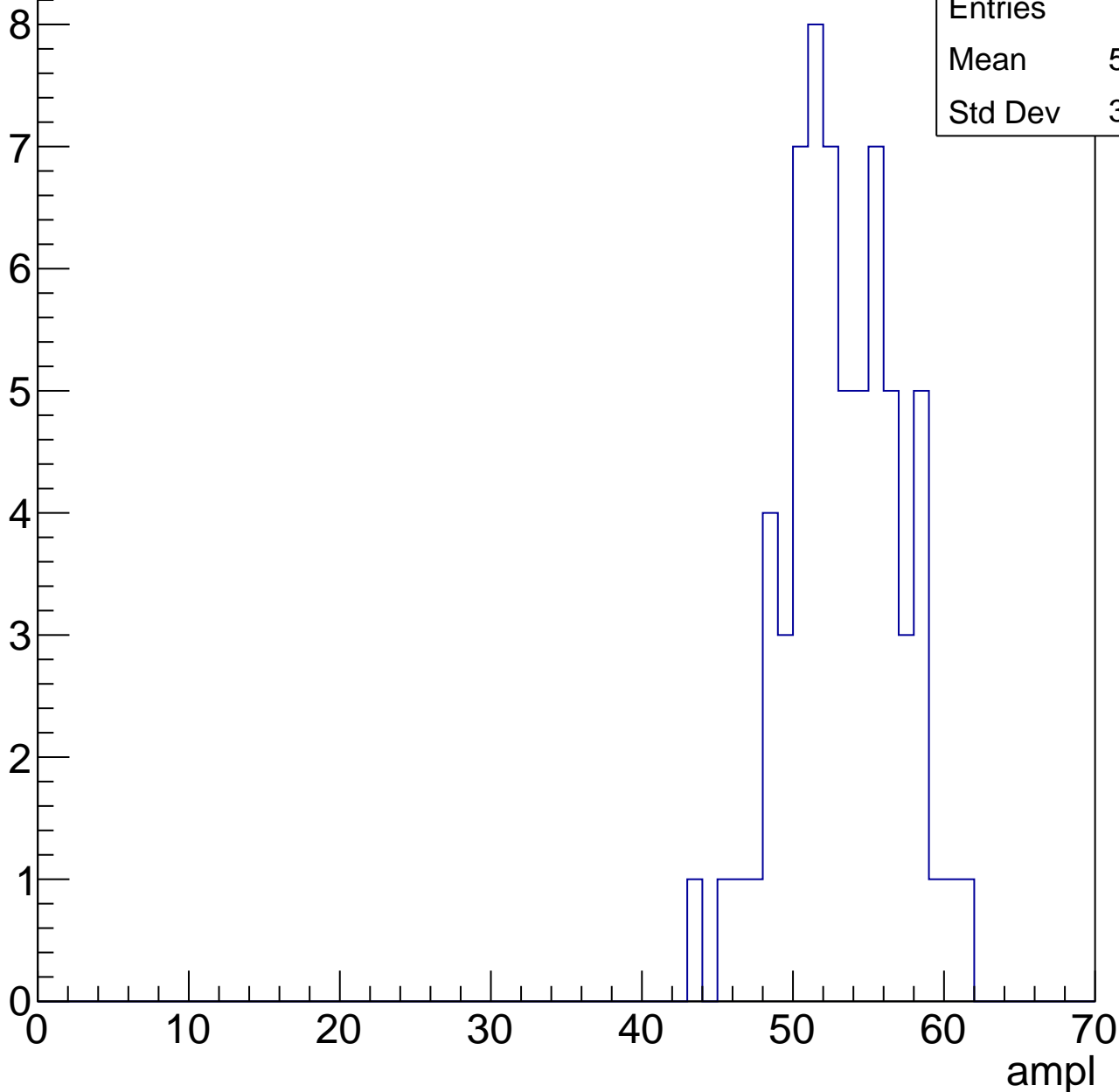


B1L103S, U17-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

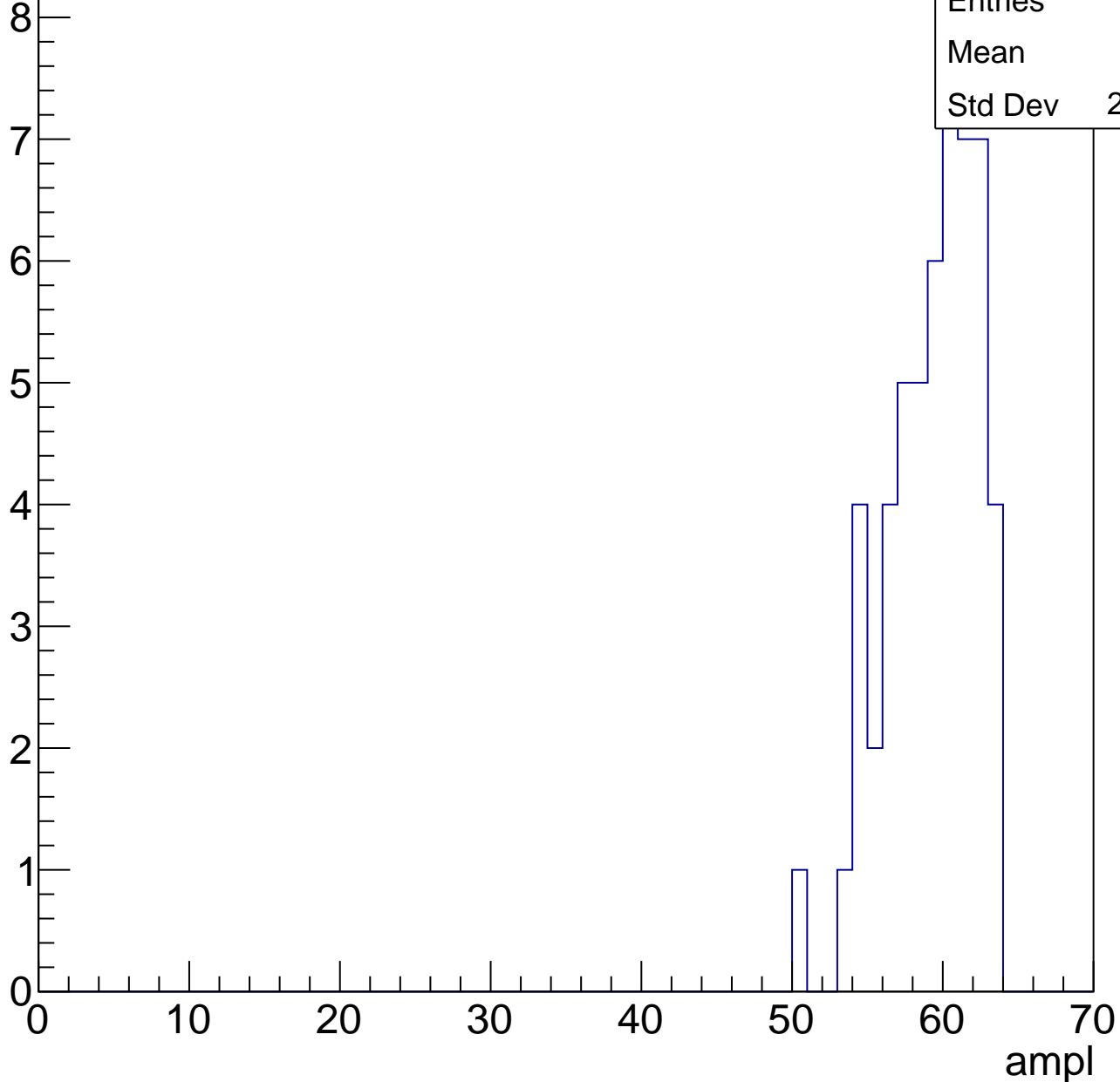
Entries	66
Mean	52.77
Std Dev	3.684



B1L103S, U17-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

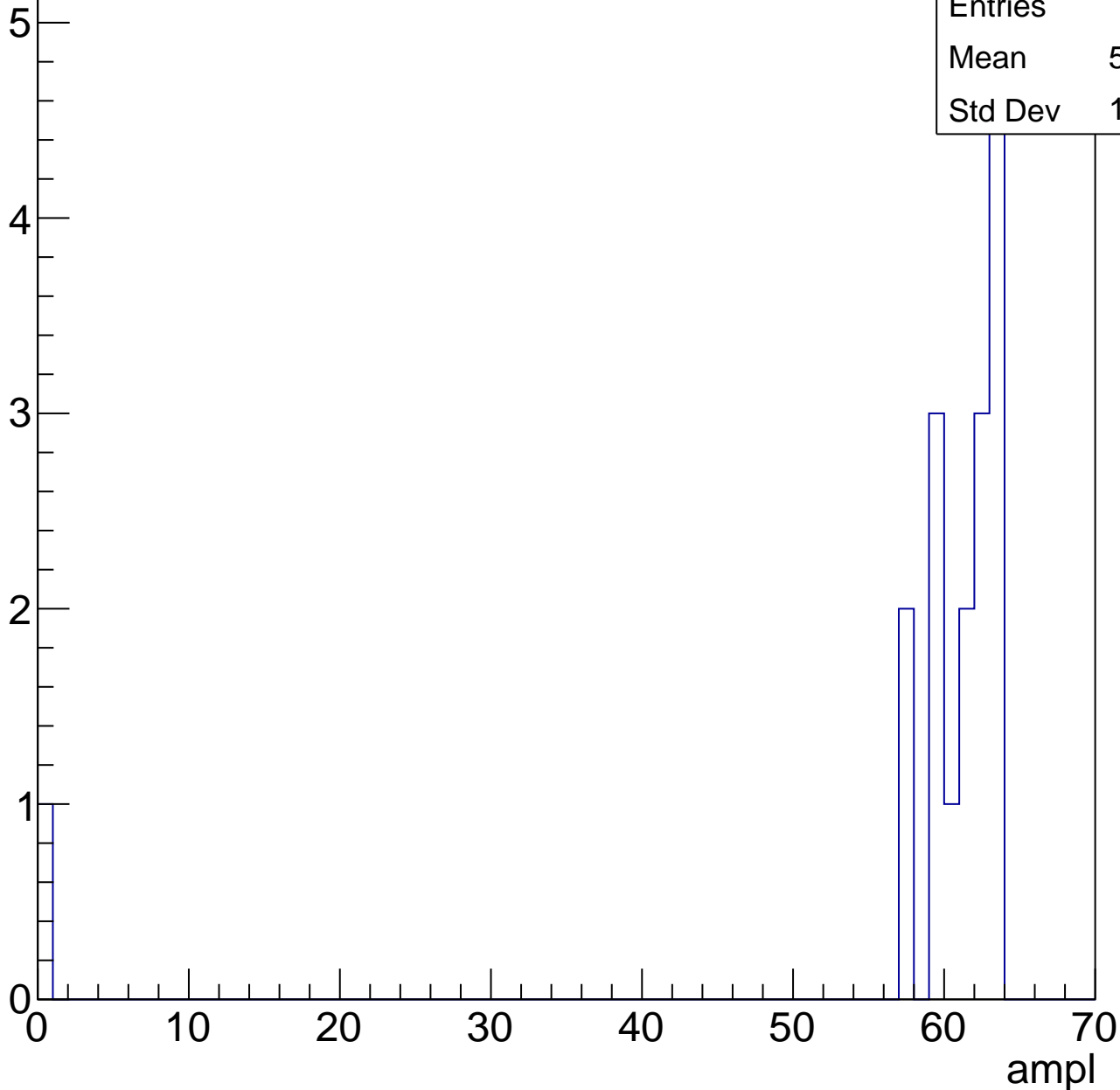


B1L103S, U17-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

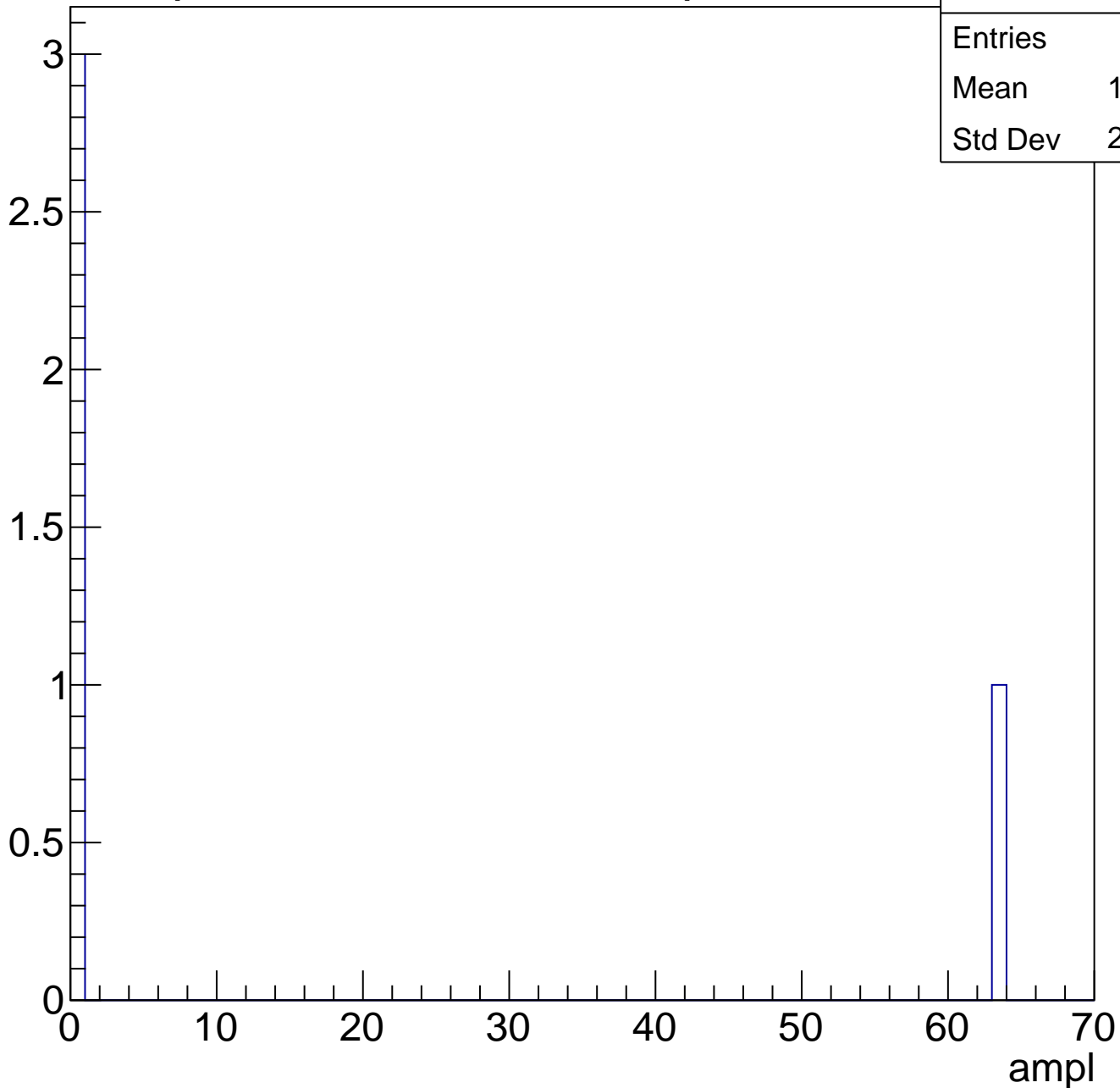
Entries	17
Mean	57.29
Std Dev	14.46



B1L103S, U17-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L103S, U17-ch119, adc0

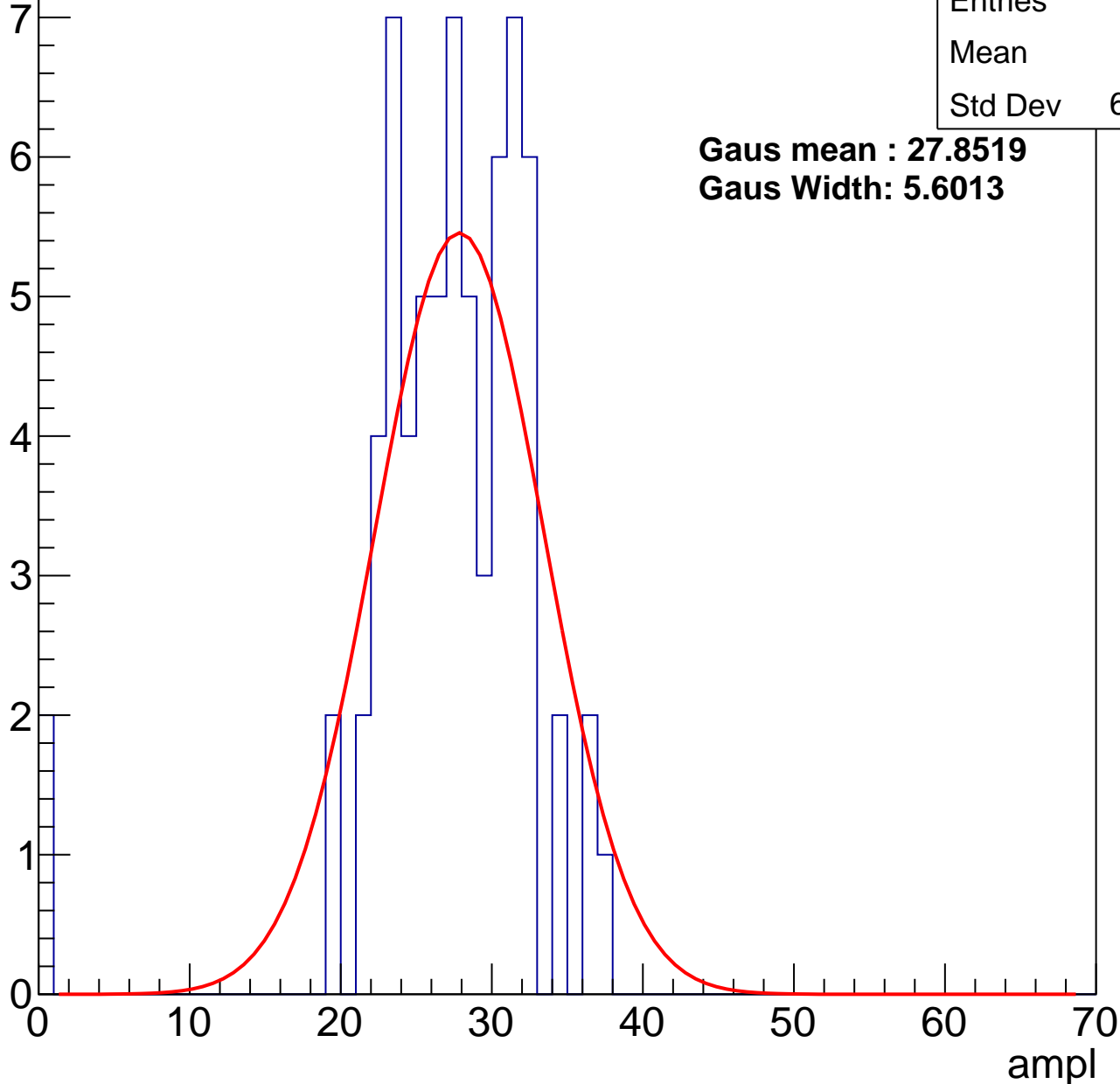
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	26.6
Std Dev	6.109

Gaus mean : 27.8519

Gaus Width: 5.6013



B1L103S, U17-ch119, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	34.05
Std Dev	3.787

Gaus mean : 35.0736

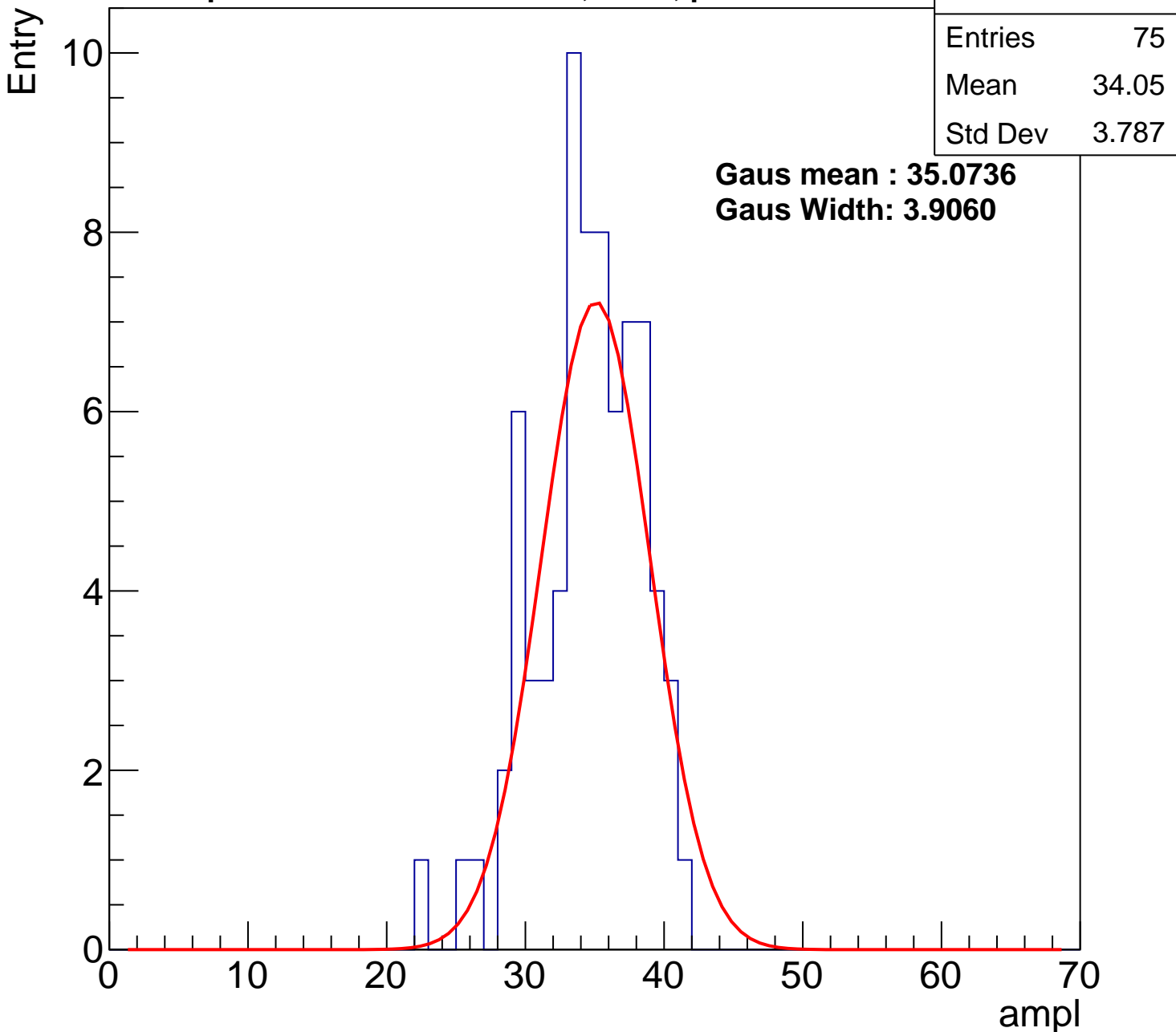
Gaus Width: 3.9060

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U17-ch119, adc2

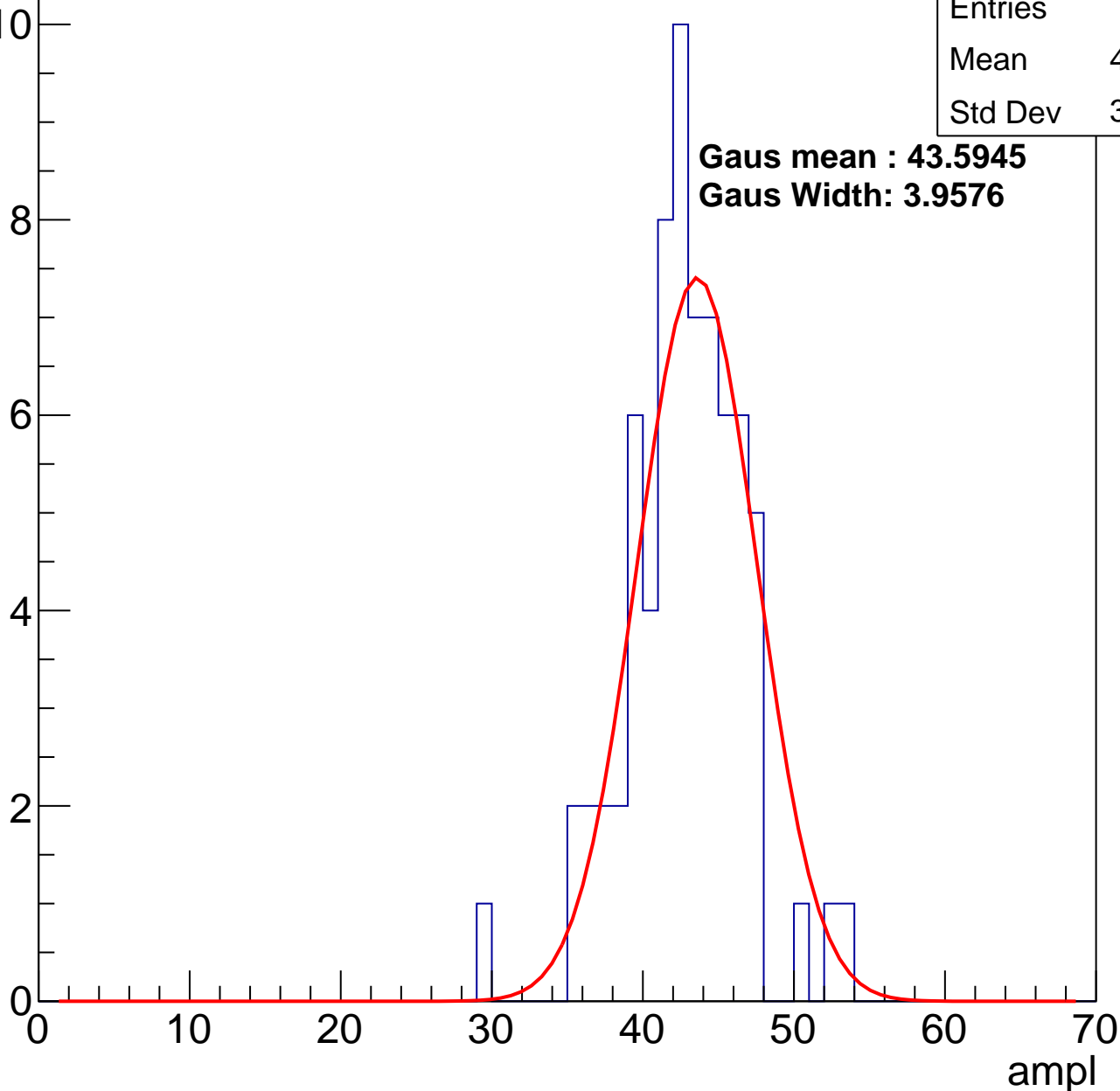
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.37
Std Dev	3.908

Gaus mean : 43.5945

Gaus Width: 3.9576

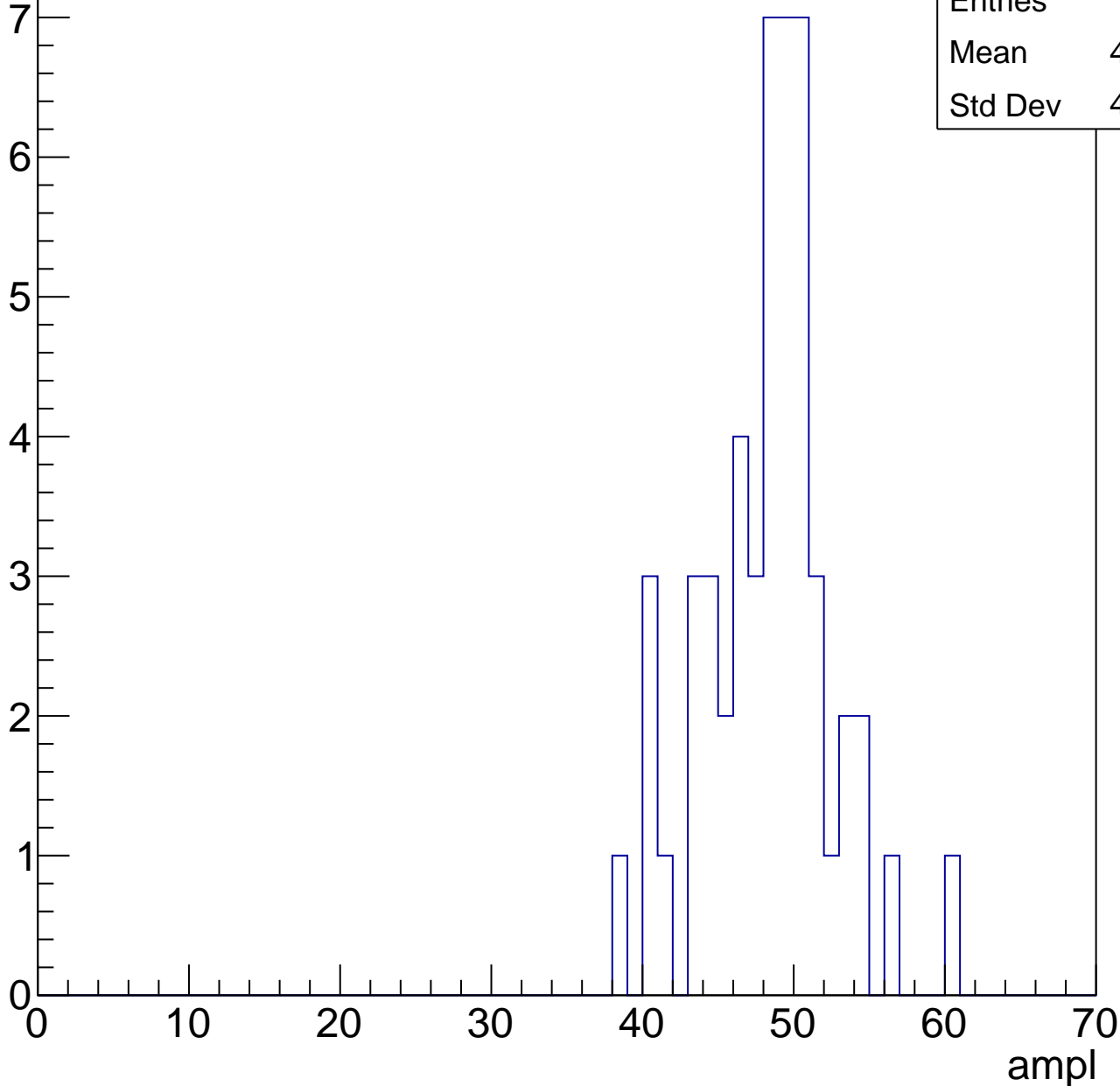


B1L103S, U17-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	47.82
Std Dev	4.218

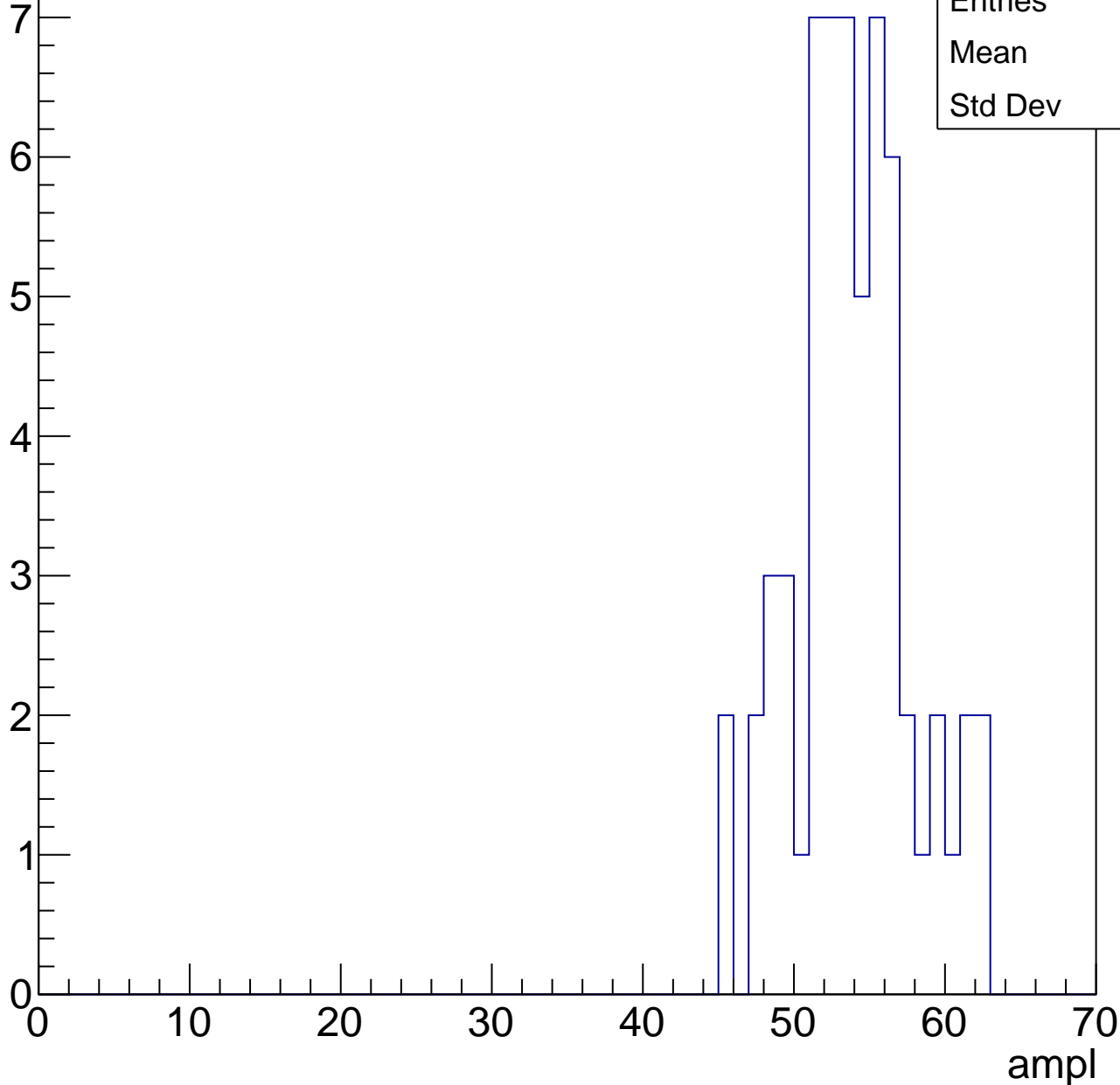


B1L103S, U17-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	53.4
Std Dev	3.9

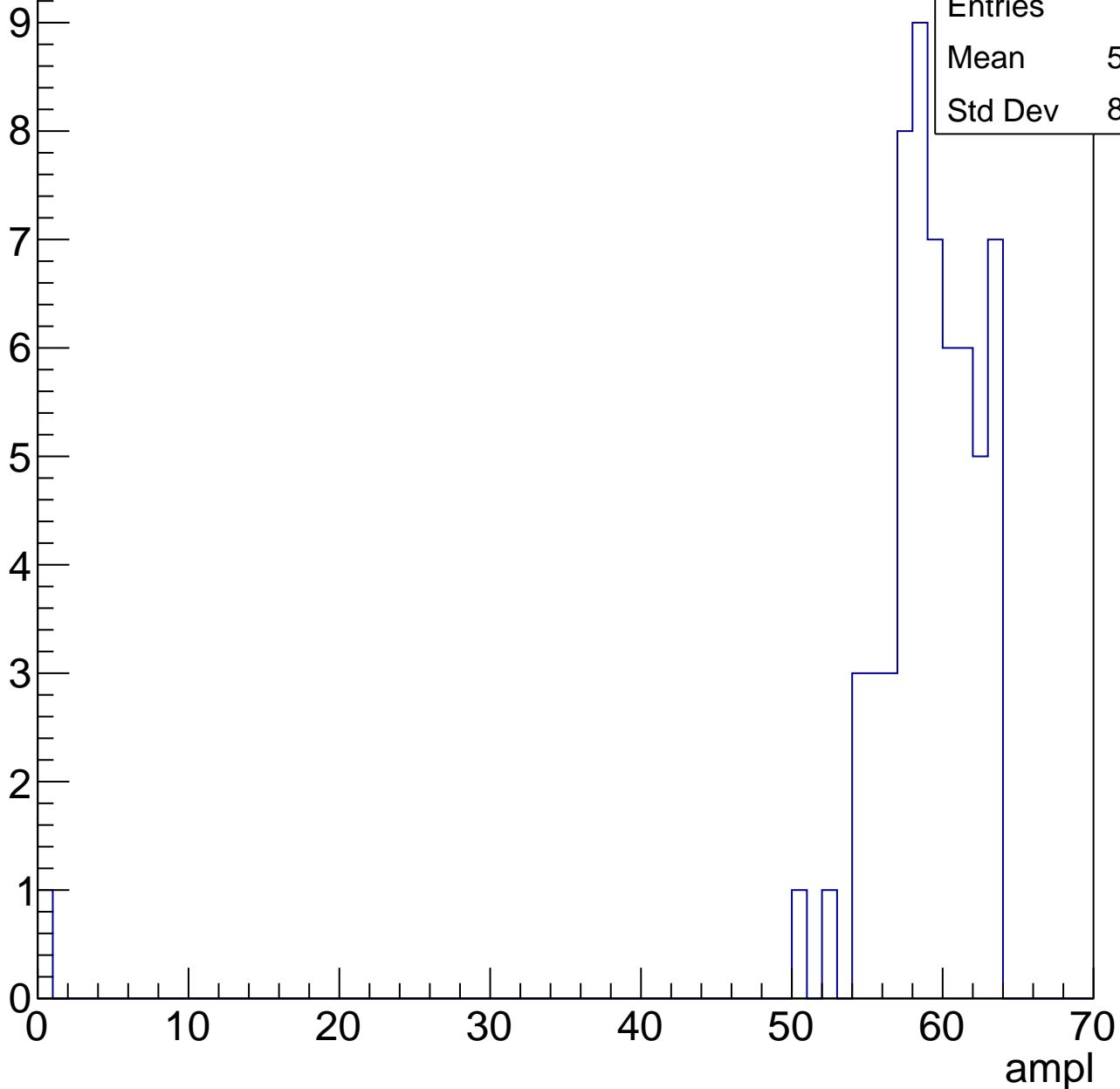


B1L103S, U17-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

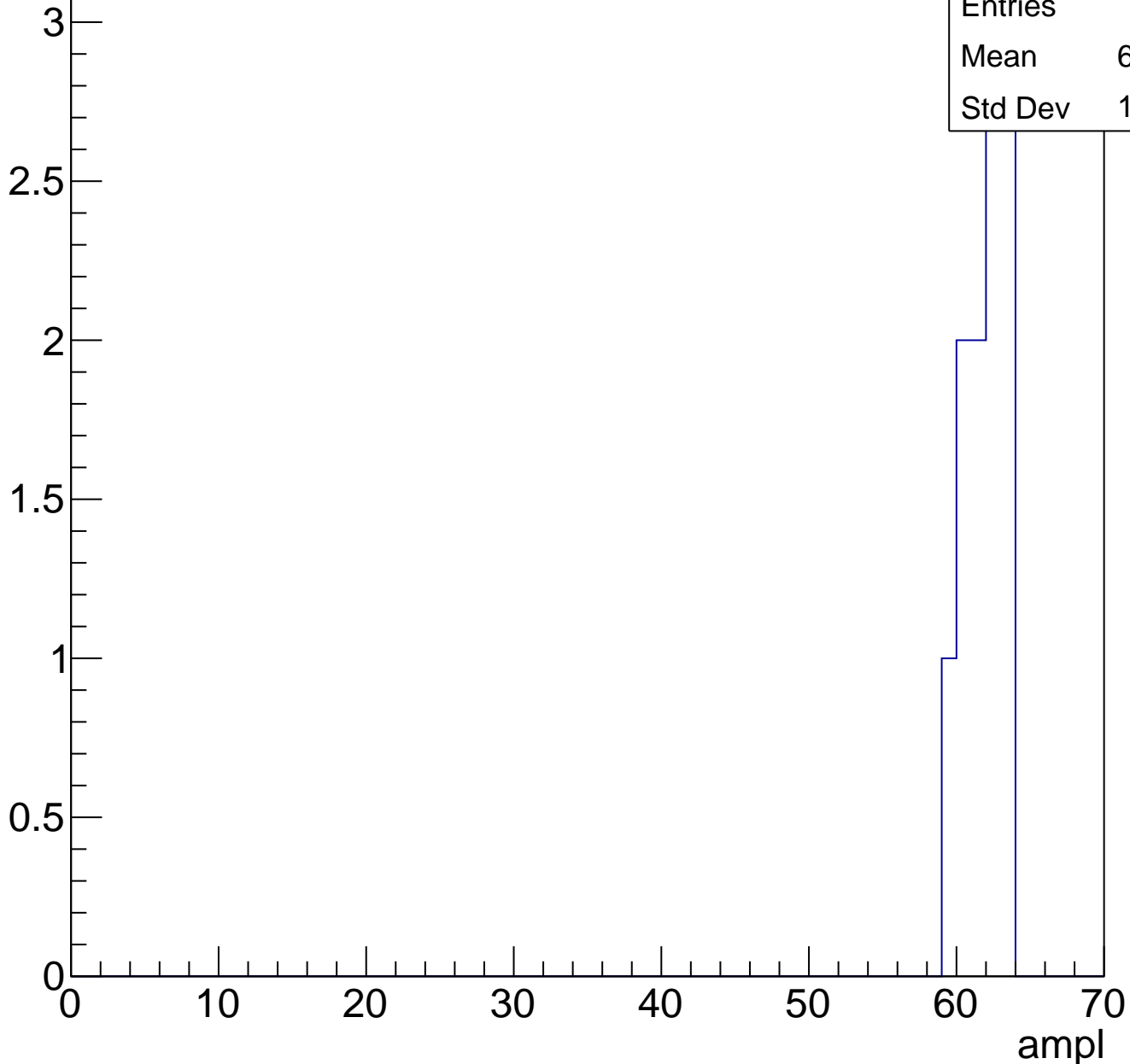
Entries	60
Mean	57.75
Std Dev	8.057



B1L103S, U17-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	11
Mean	61.45
Std Dev	1.305

B1L103S, U17-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch120, adc0

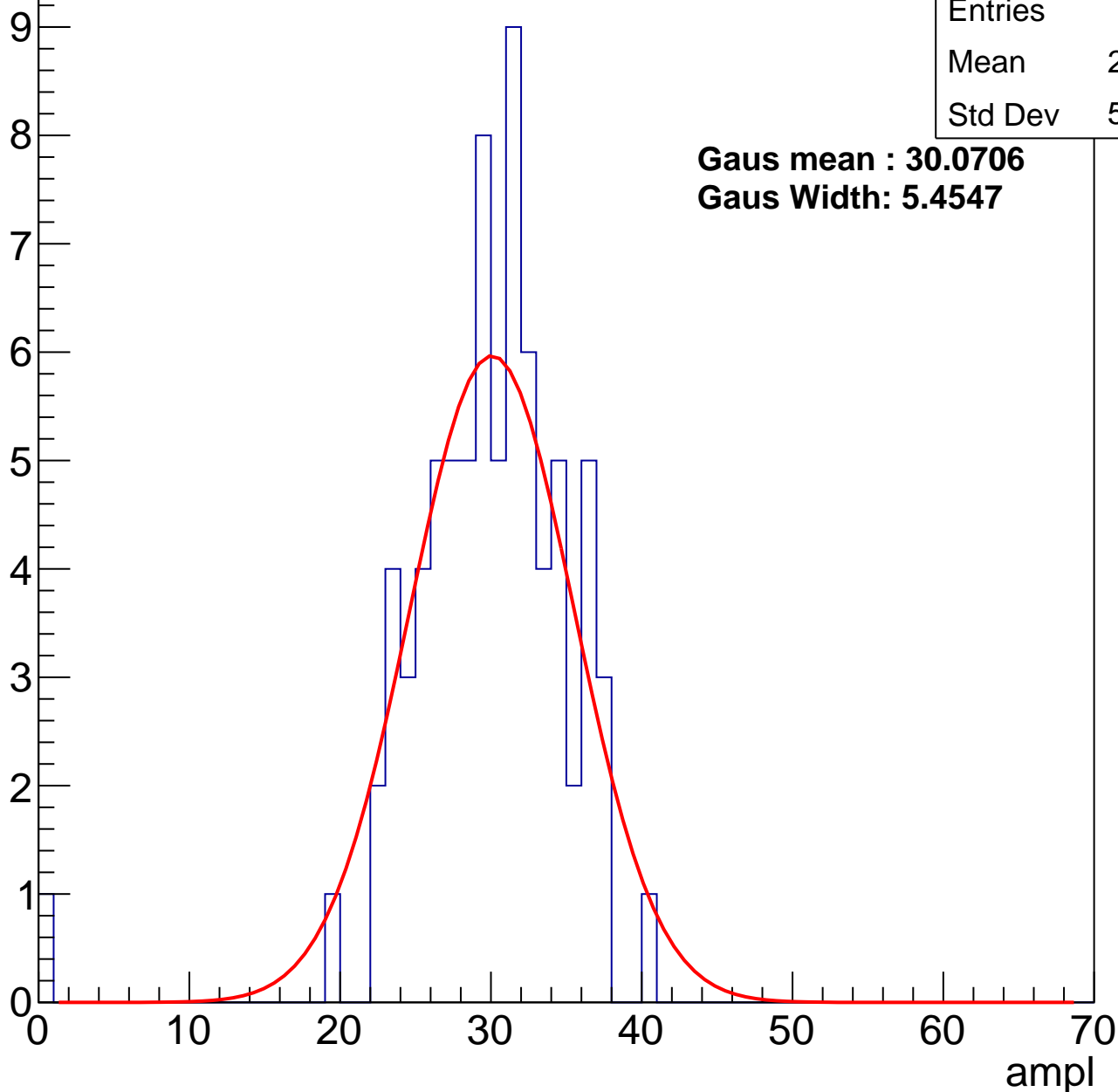
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.33
Std Dev	5.427

Gaus mean : 30.0706

Gaus Width: 5.4547



B1L103S, U17-ch120, adc1

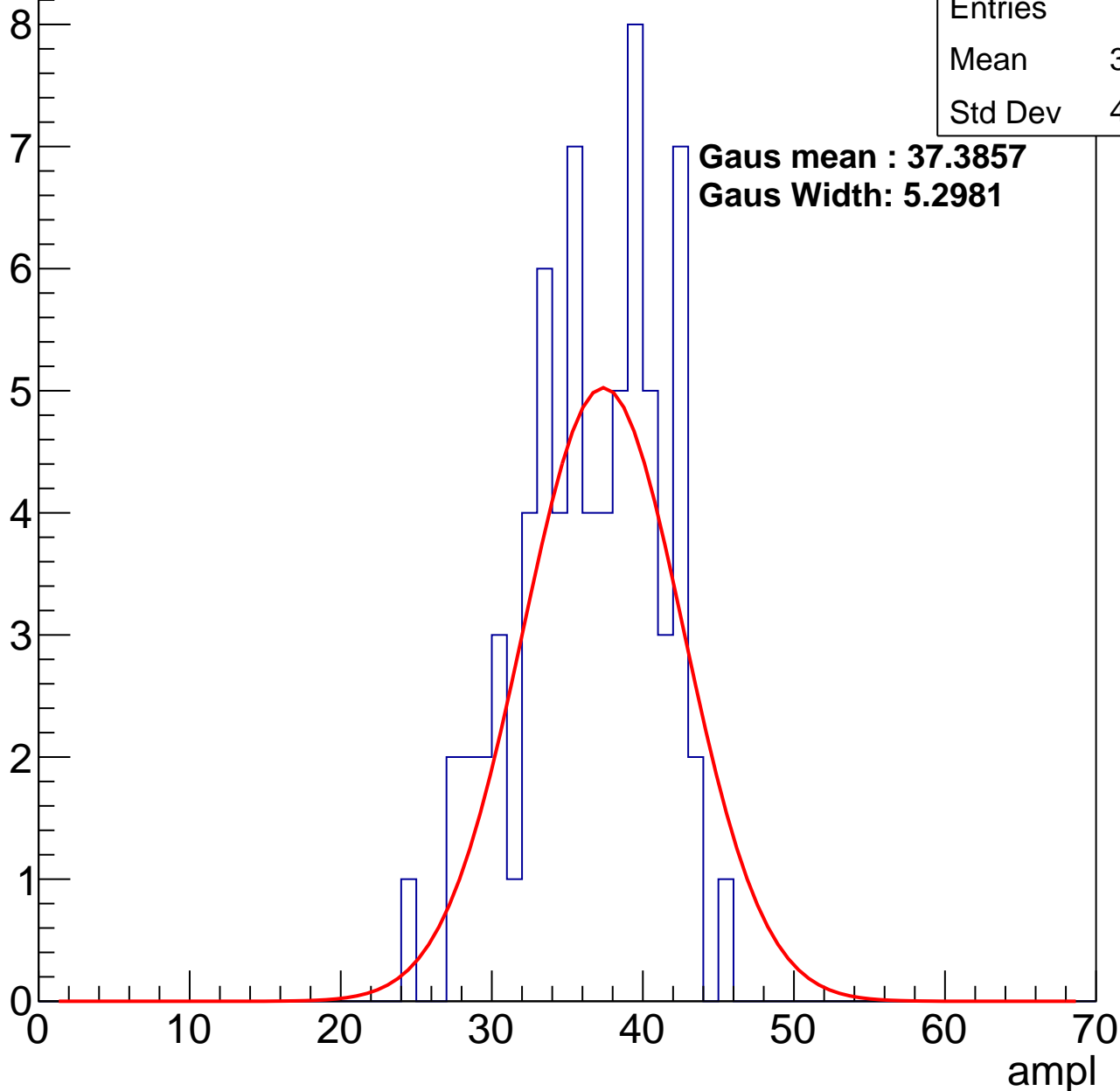
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.08
Std Dev	4.577

Gaus mean : 37.3857

Gaus Width: 5.2981



B1L103S, U17-ch120, adc2

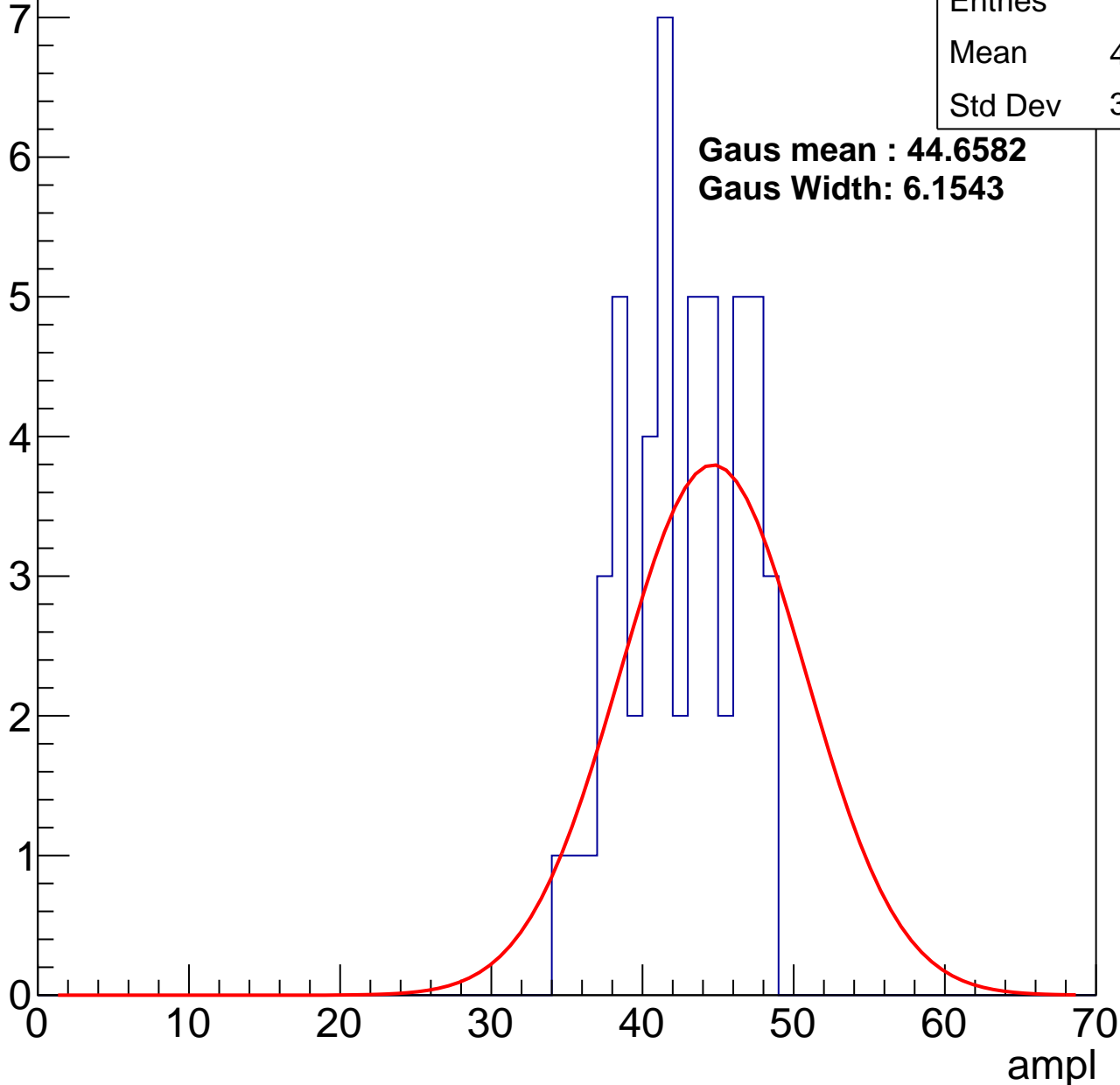
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	42.14
Std Dev	3.705

Gaus mean : 44.6582

Gaus Width: 6.1543

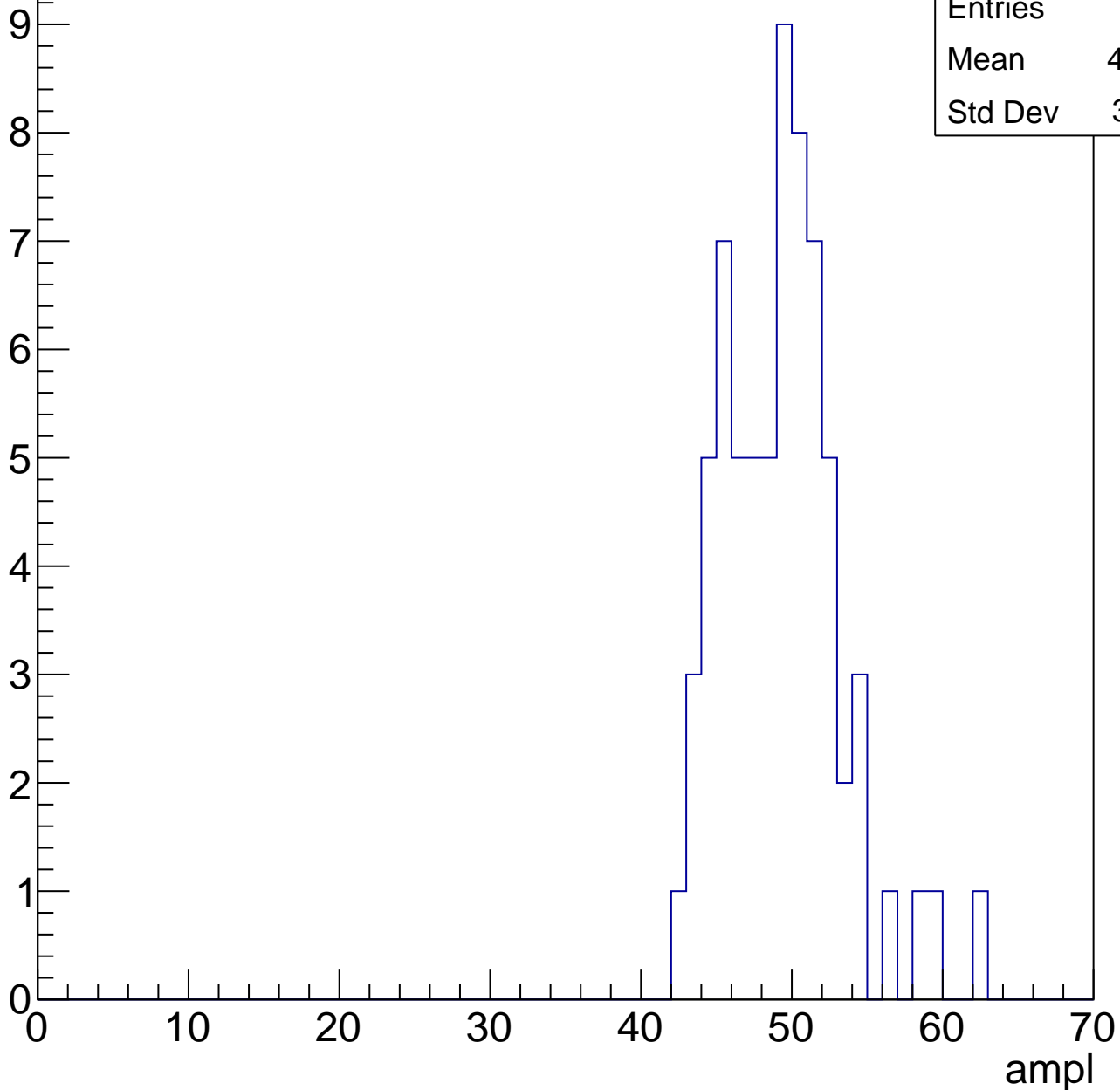


B1L103S, U17-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.87
Std Dev	3.901

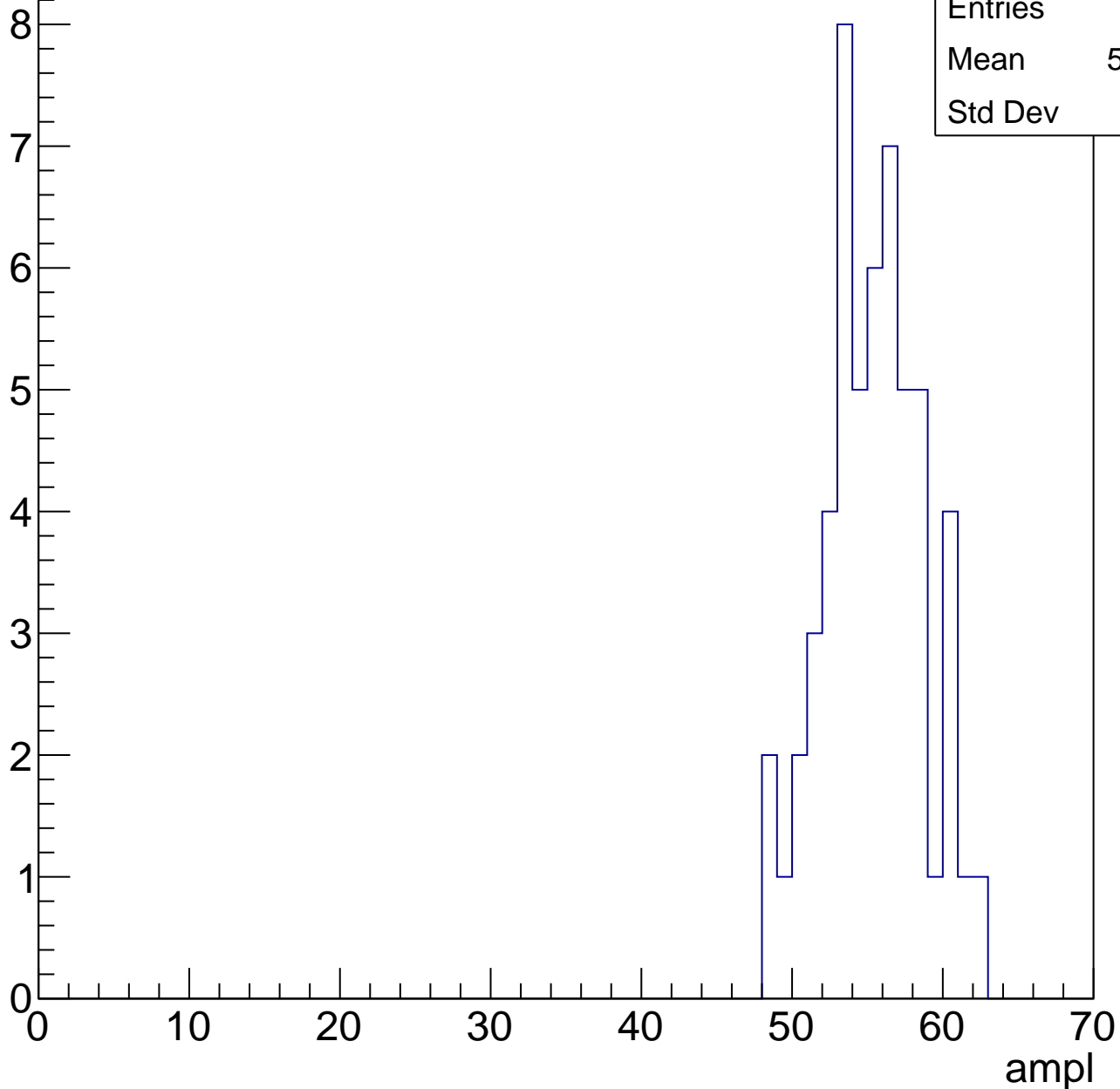


B1L103S, U17-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.89
Std Dev	3.24

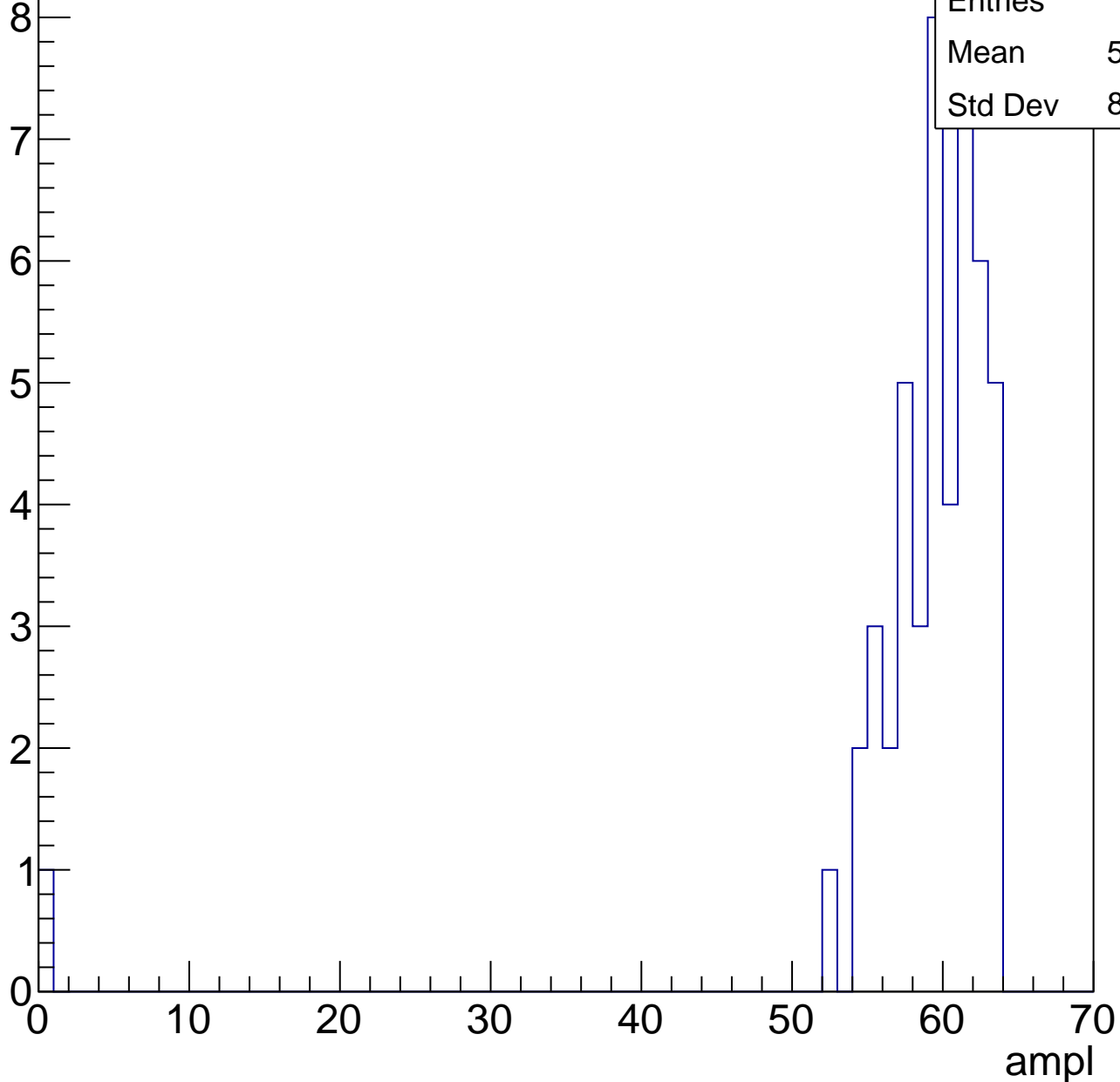


B1L103S, U17-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

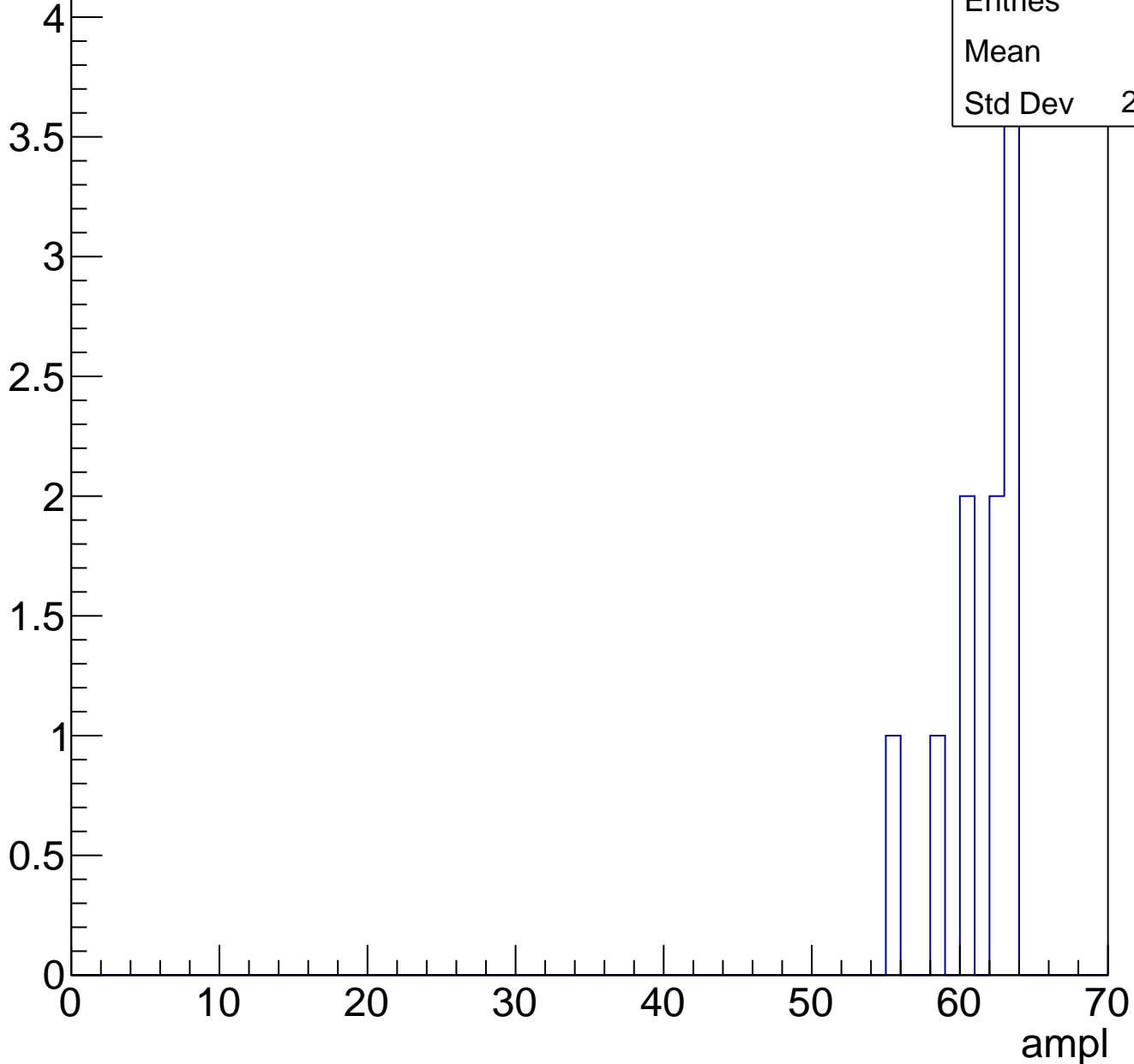
Entries	48
Mean	57.98
Std Dev	8.885



B1L103S, U17-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

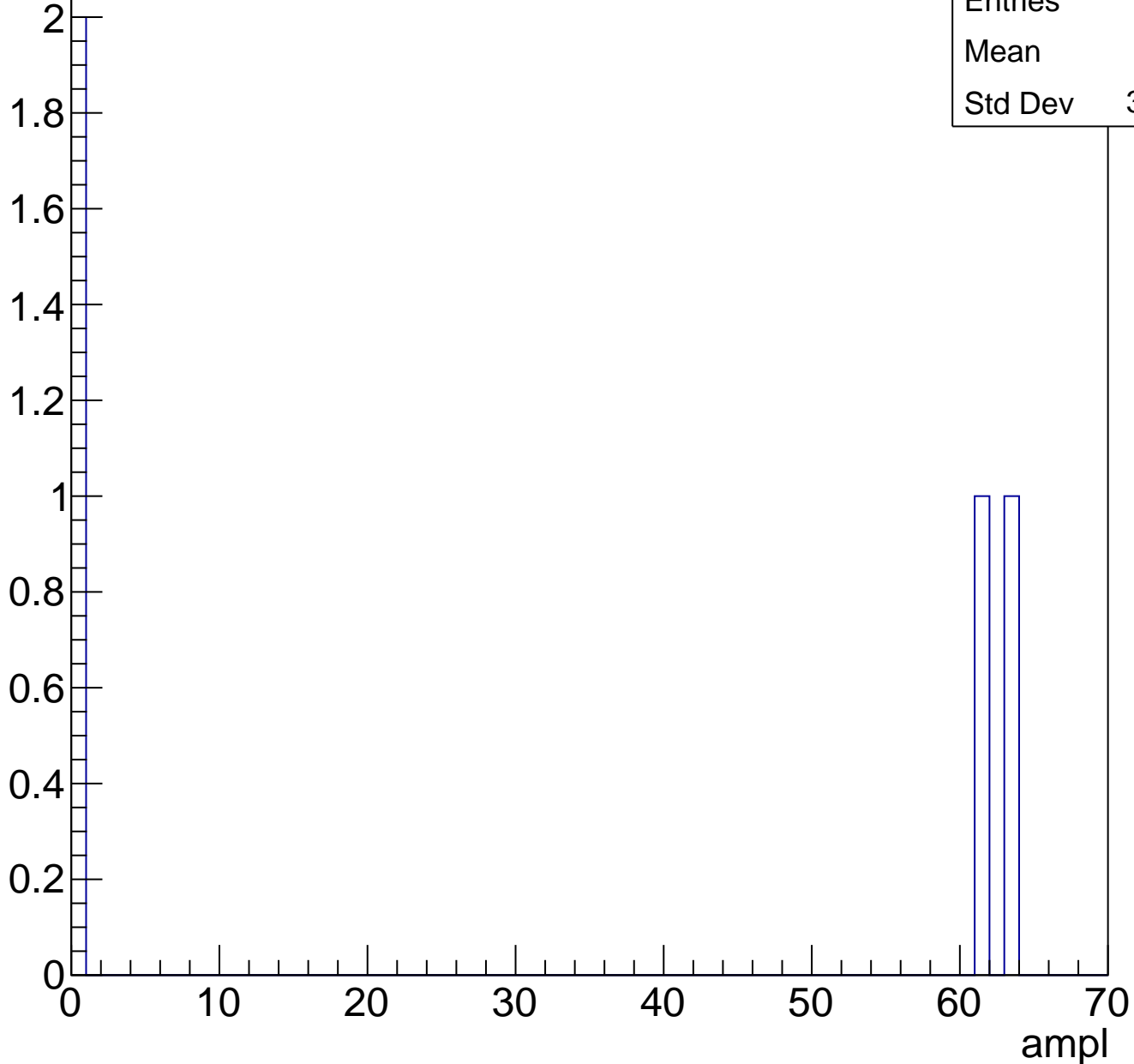
Entry



B1L103S, U17-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch121, adc0

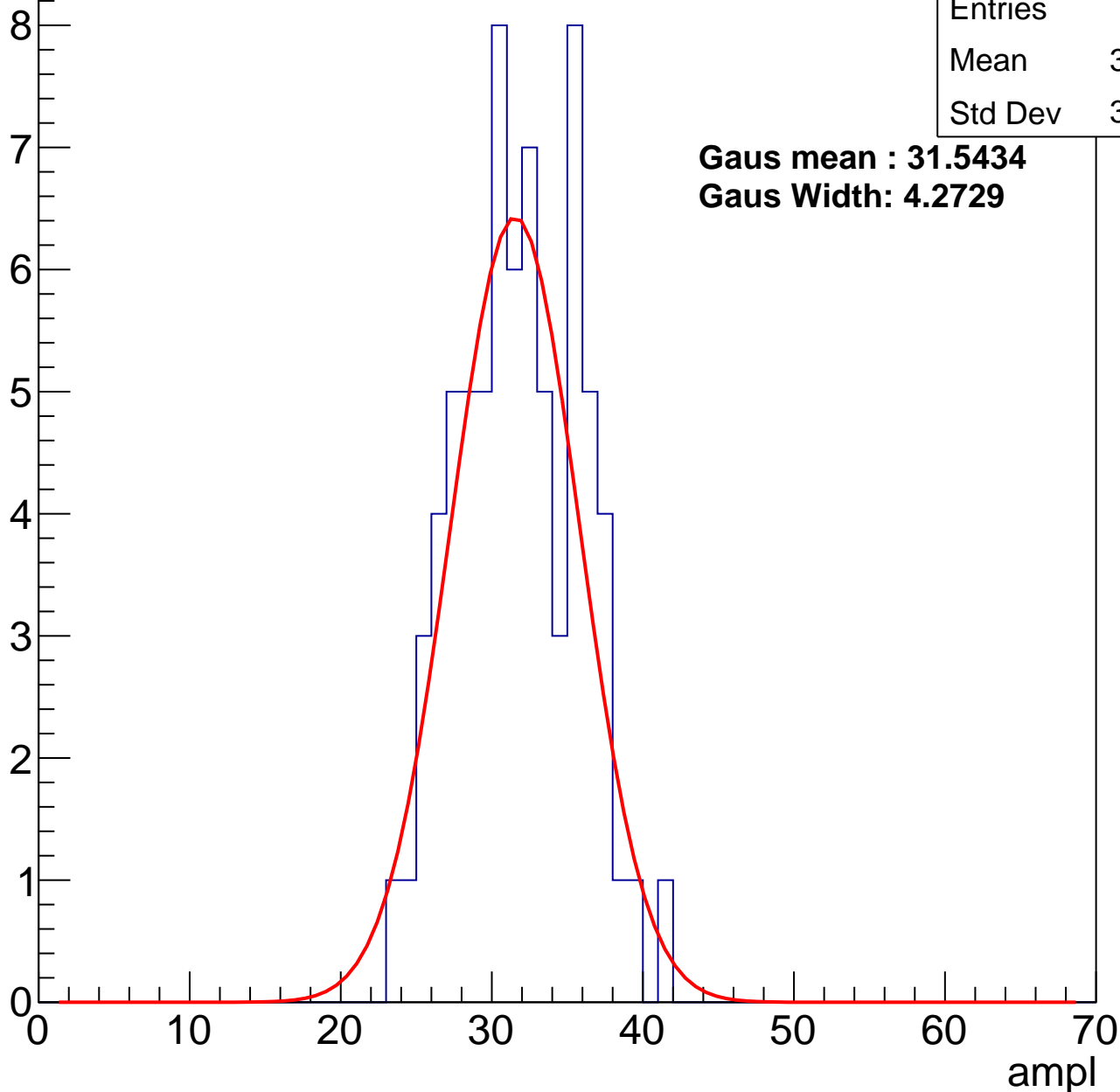
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	31.36
Std Dev	3.932

Gaus mean : 31.5434

Gaus Width: 4.2729



B1L103S, U17-ch121, adc1

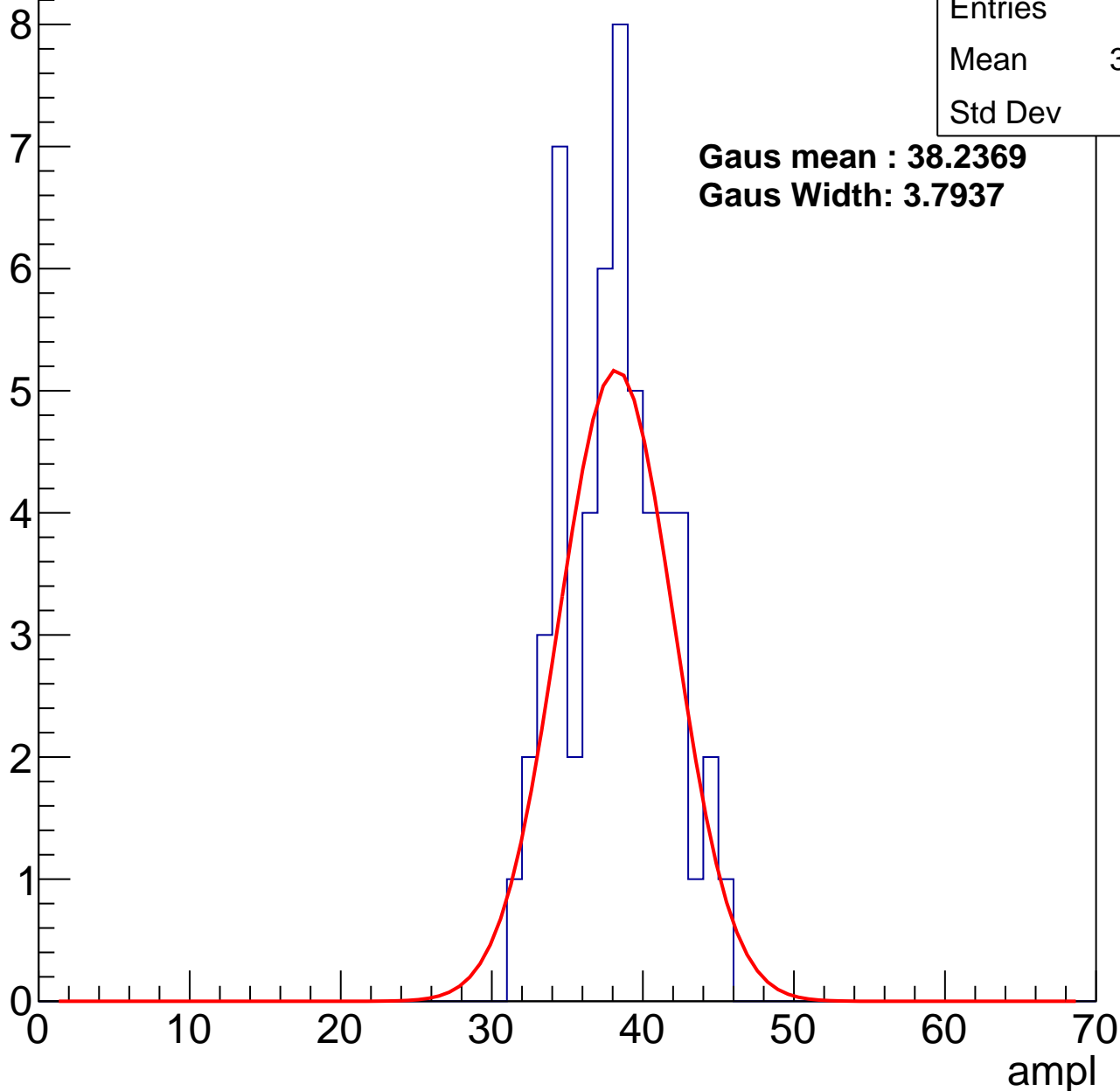
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	37.69
Std Dev	3.36

Gaus mean : 38.2369

Gaus Width: 3.7937



B1L103S, U17-ch121, adc2

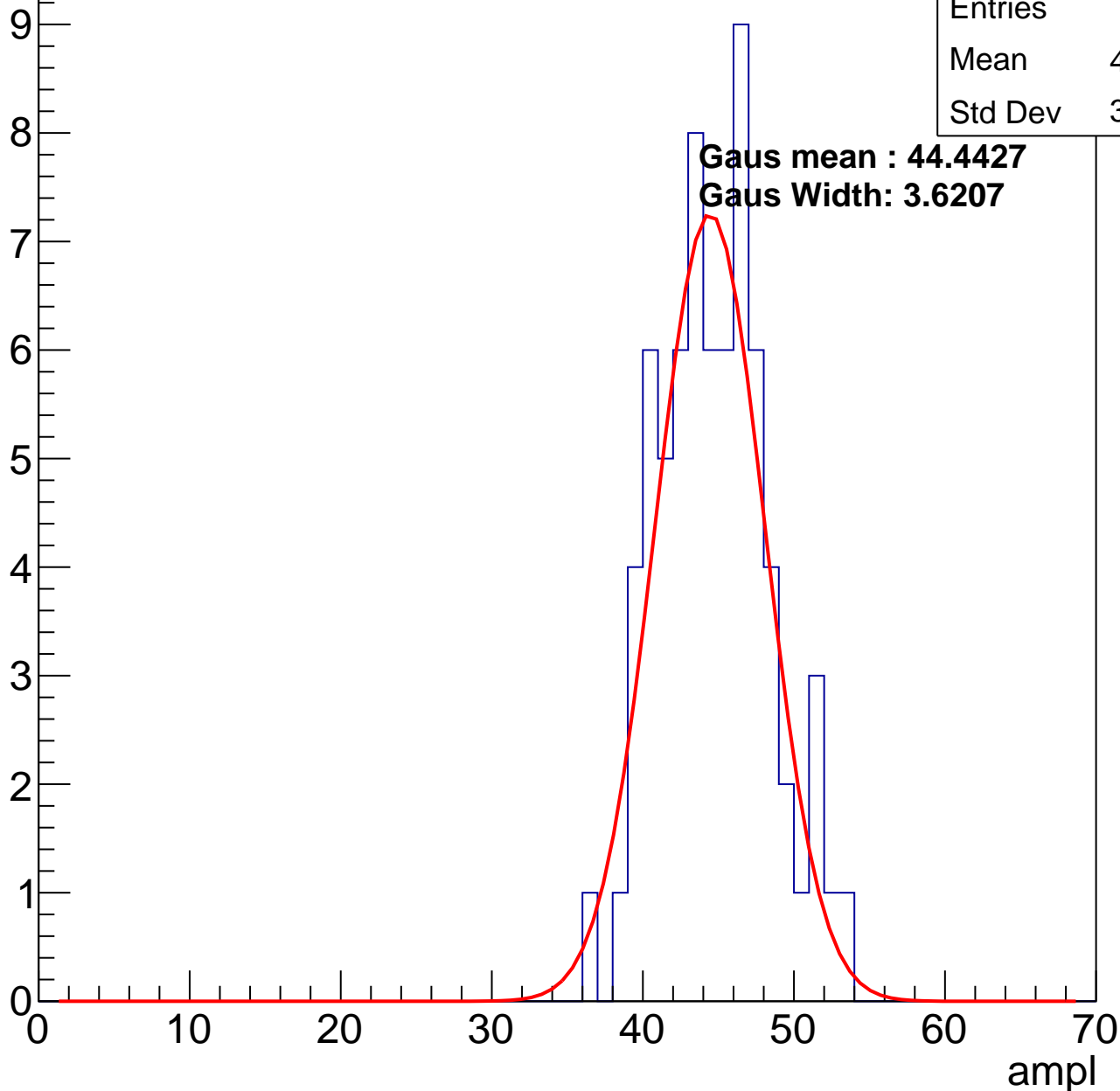
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.27
Std Dev	3.597

Gaus mean : 44.4427

Gaus Width: 3.6207

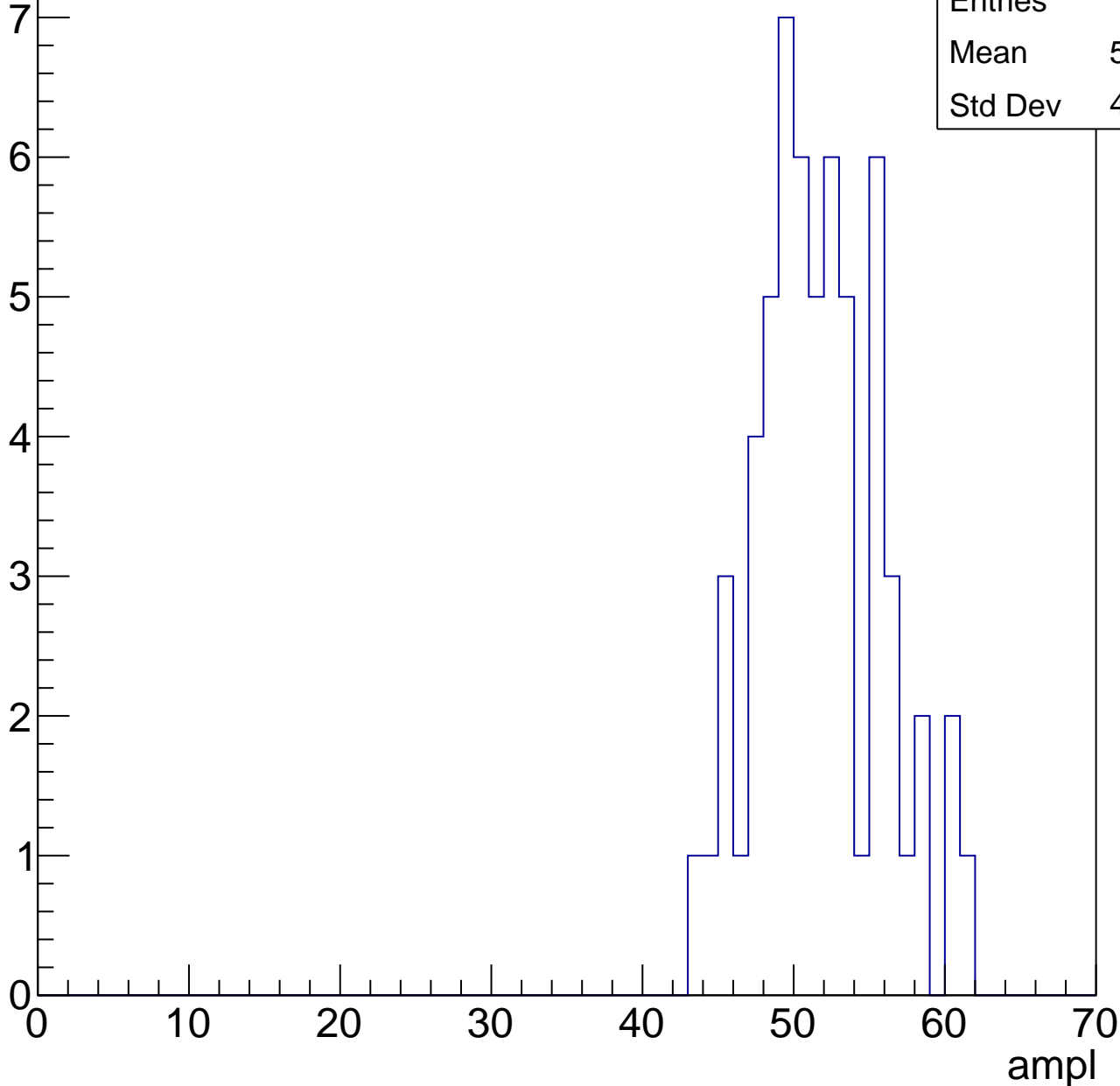


B1L103S, U17-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	51.28
Std Dev	4.054

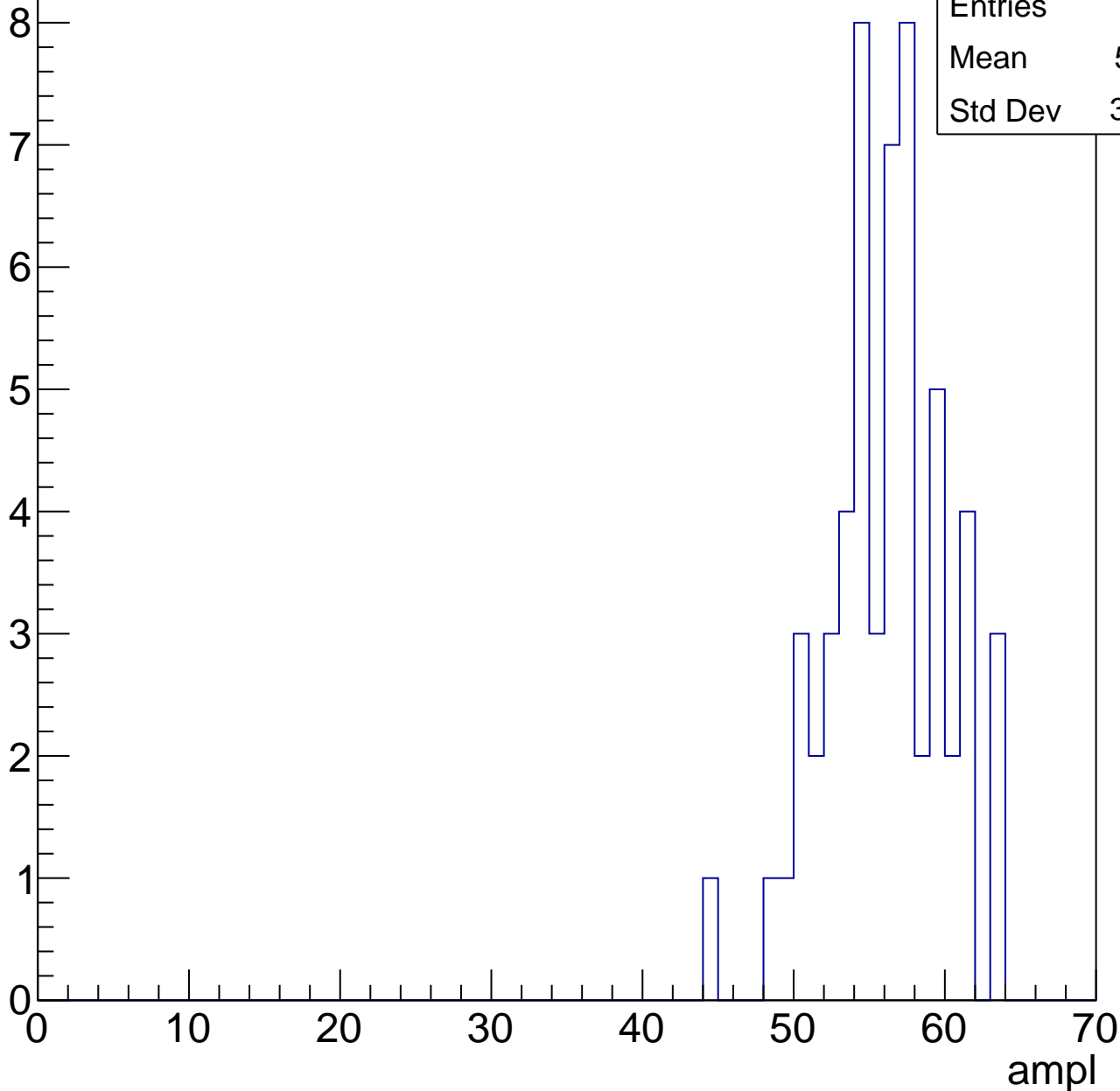


B1L103S, U17-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.61
Std Dev	3.897

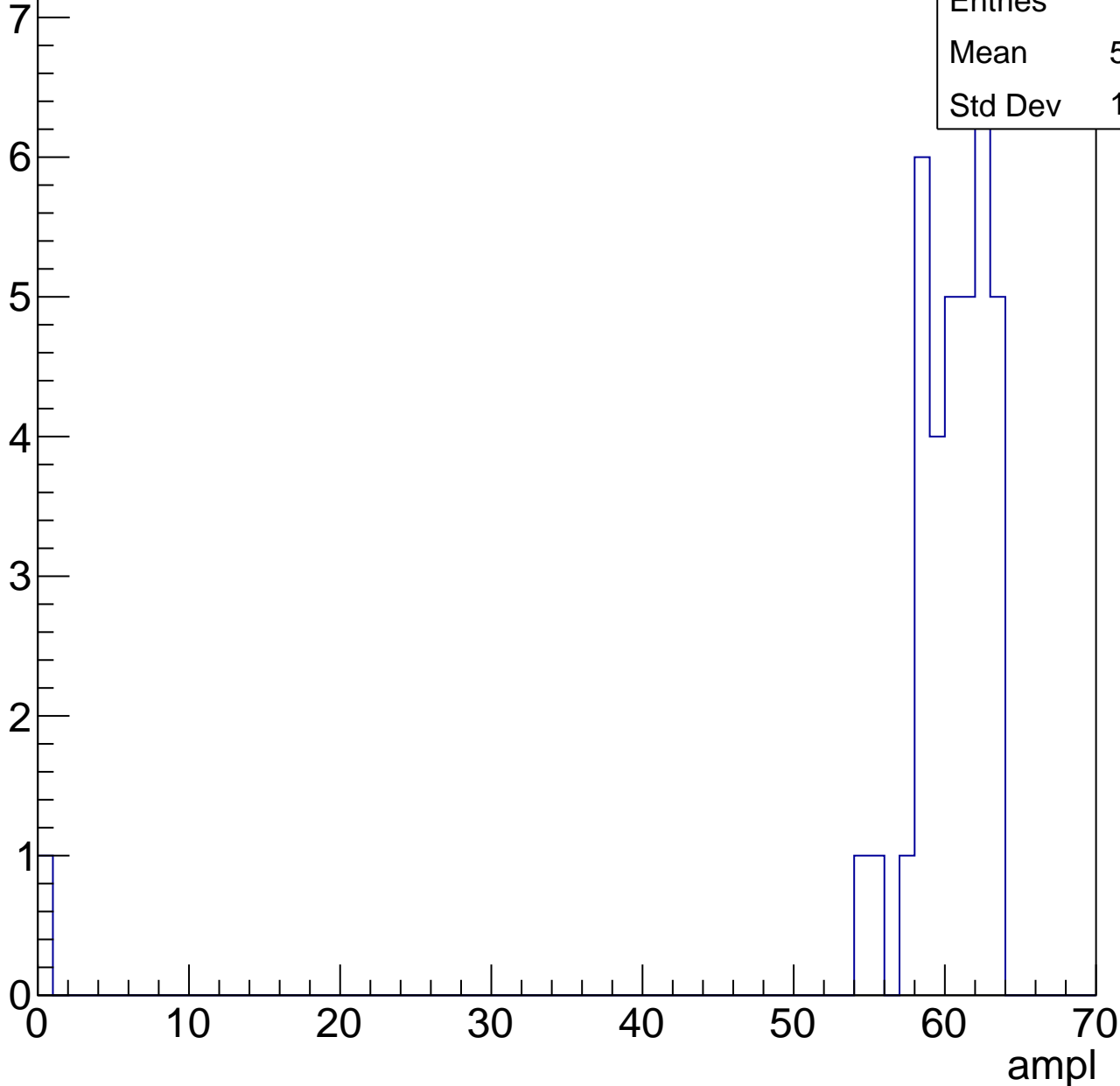


B1L103S, U17-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

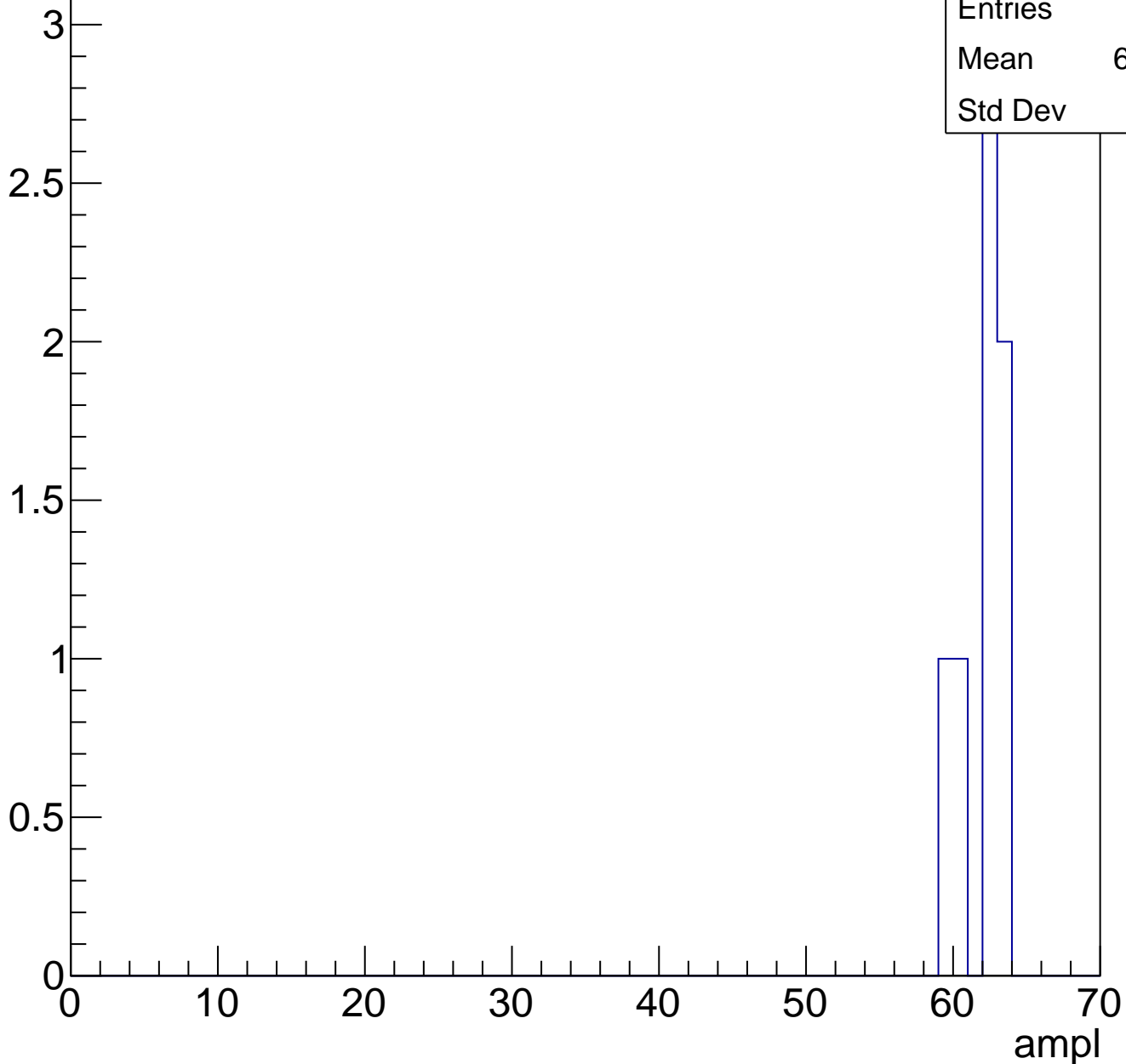
Entries	36
Mean	58.44
Std Dev	10.12



B1L103S, U17-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch122, adc0

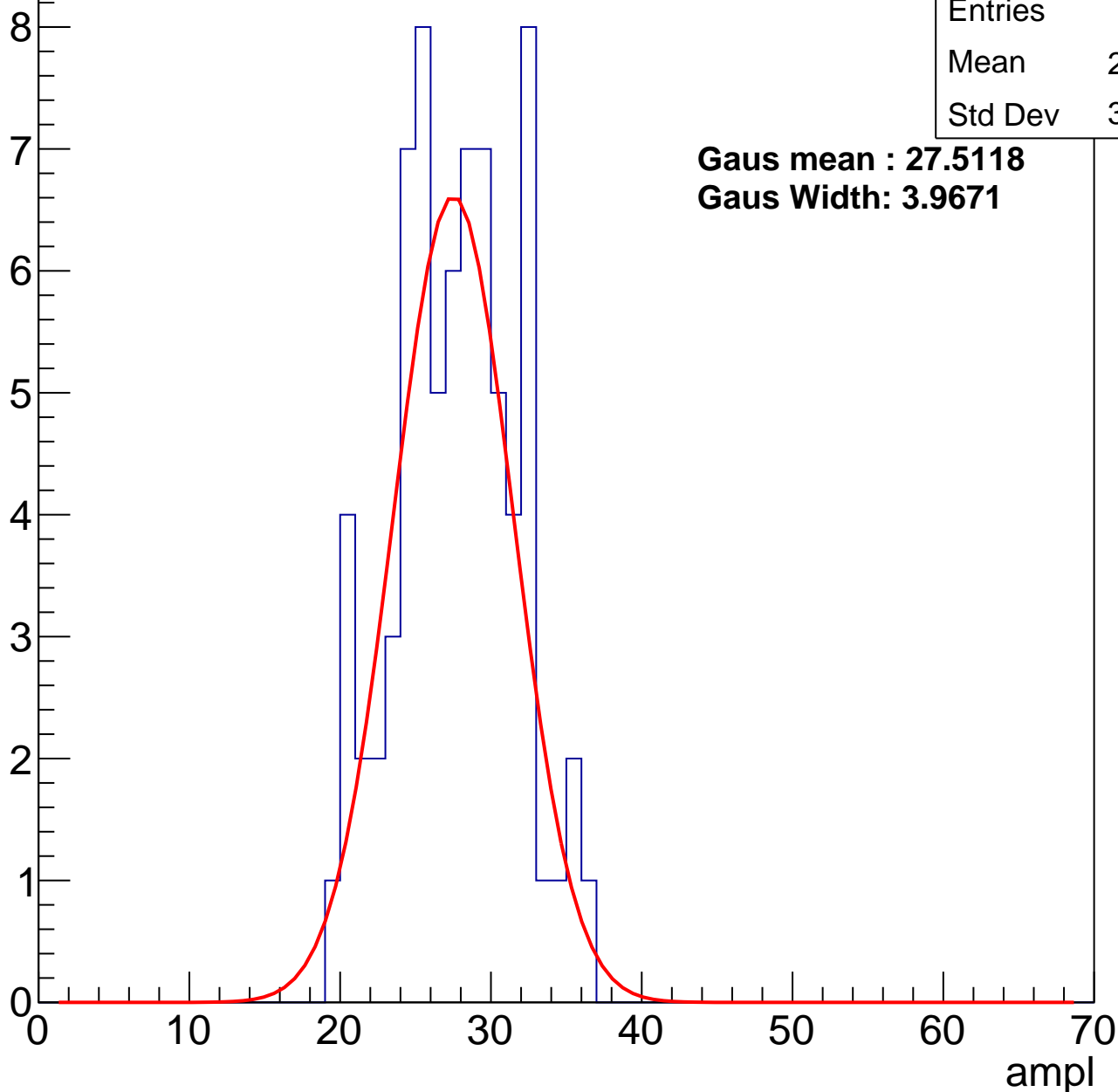
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.24
Std Dev	3.965

Gaus mean : 27.5118

Gaus Width: 3.9671



B1L103S, U17-ch122, adc1

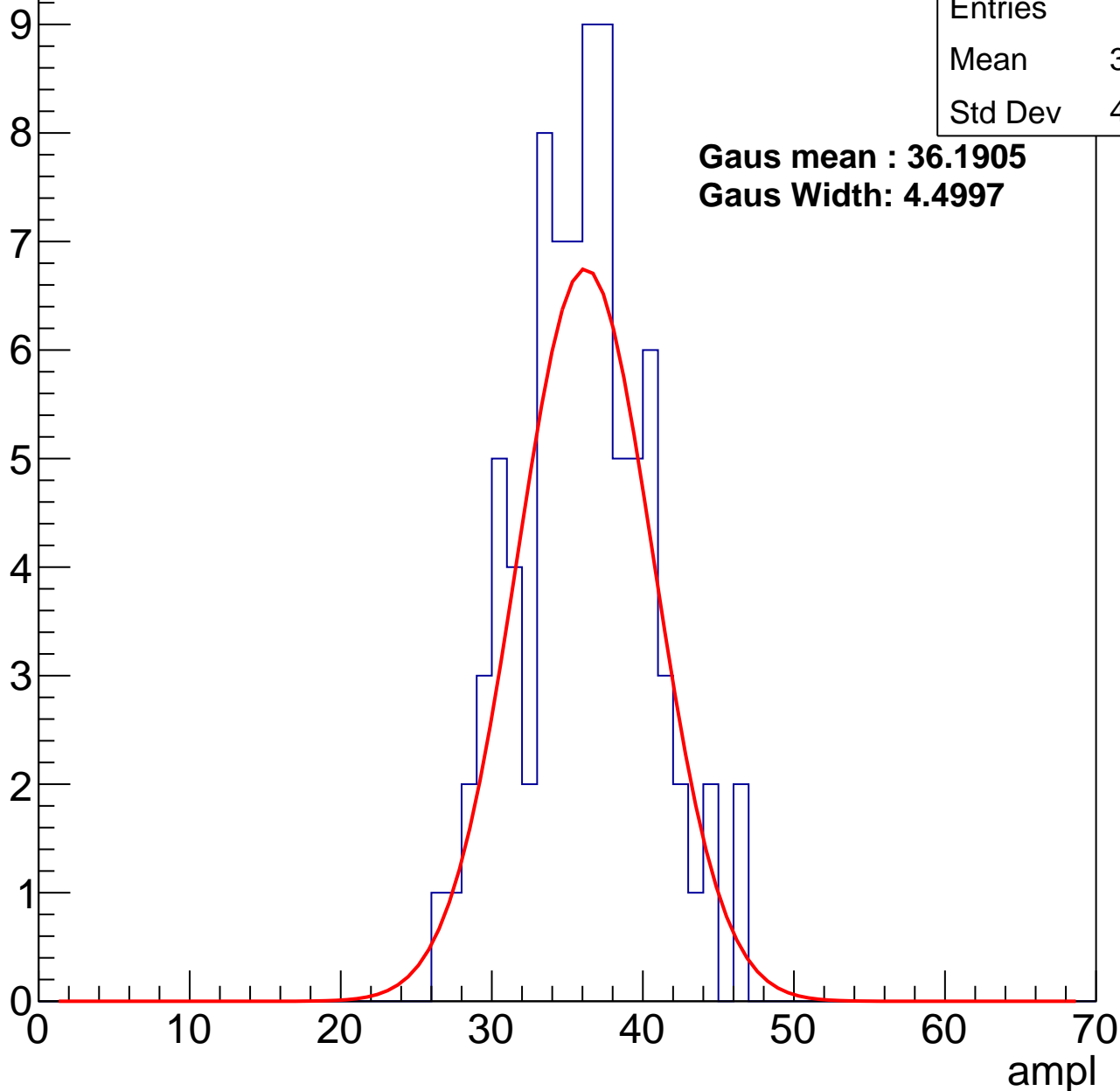
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	35.63
Std Dev	4.306

Gaus mean : 36.1905

Gaus Width: 4.4997



B1L103S, U17-ch122, adc2

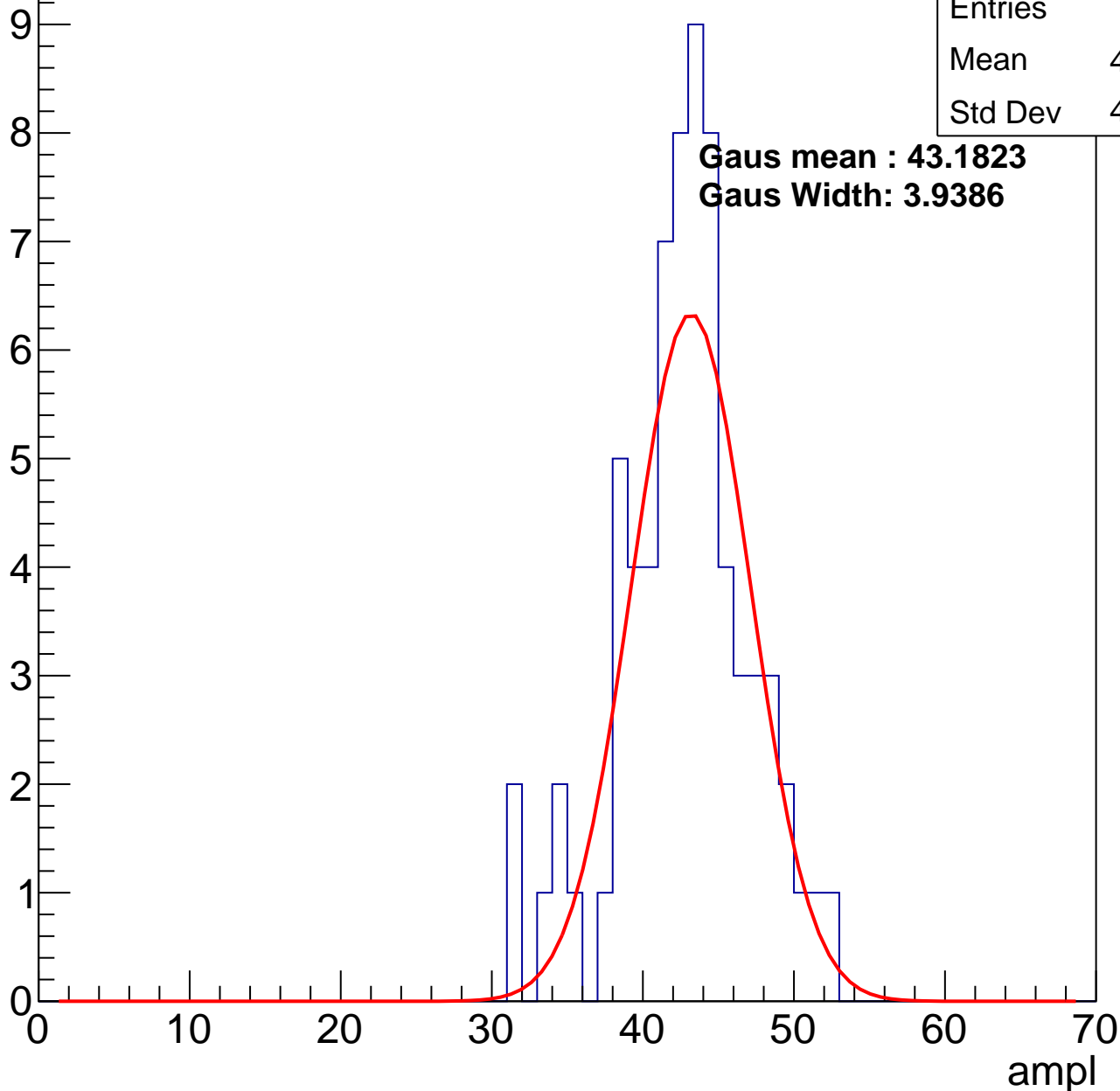
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.24
Std Dev	4.327

Gaus mean : 43.1823

Gaus Width: 3.9386

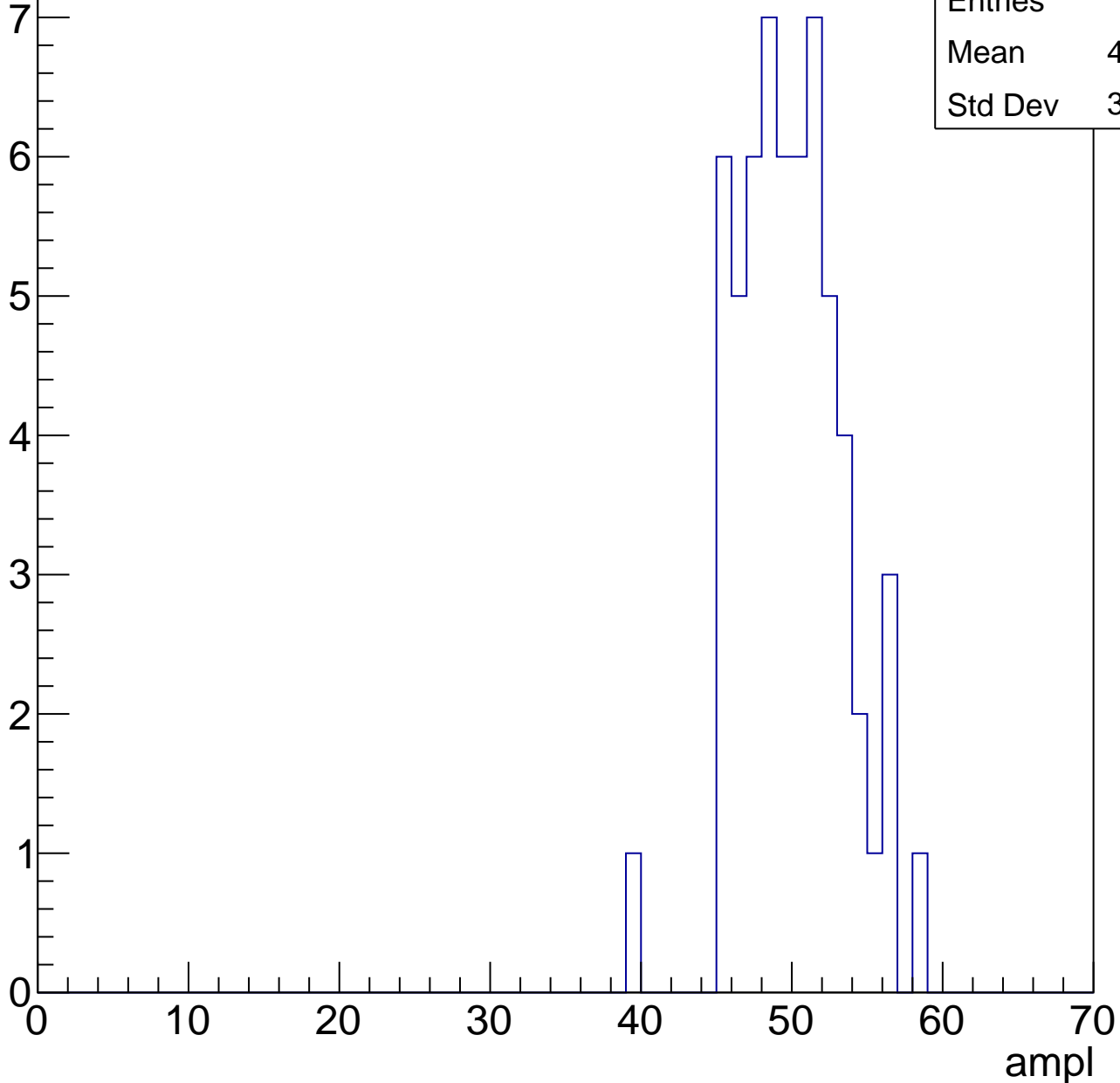


B1L103S, U17-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.48
Std Dev	3.452

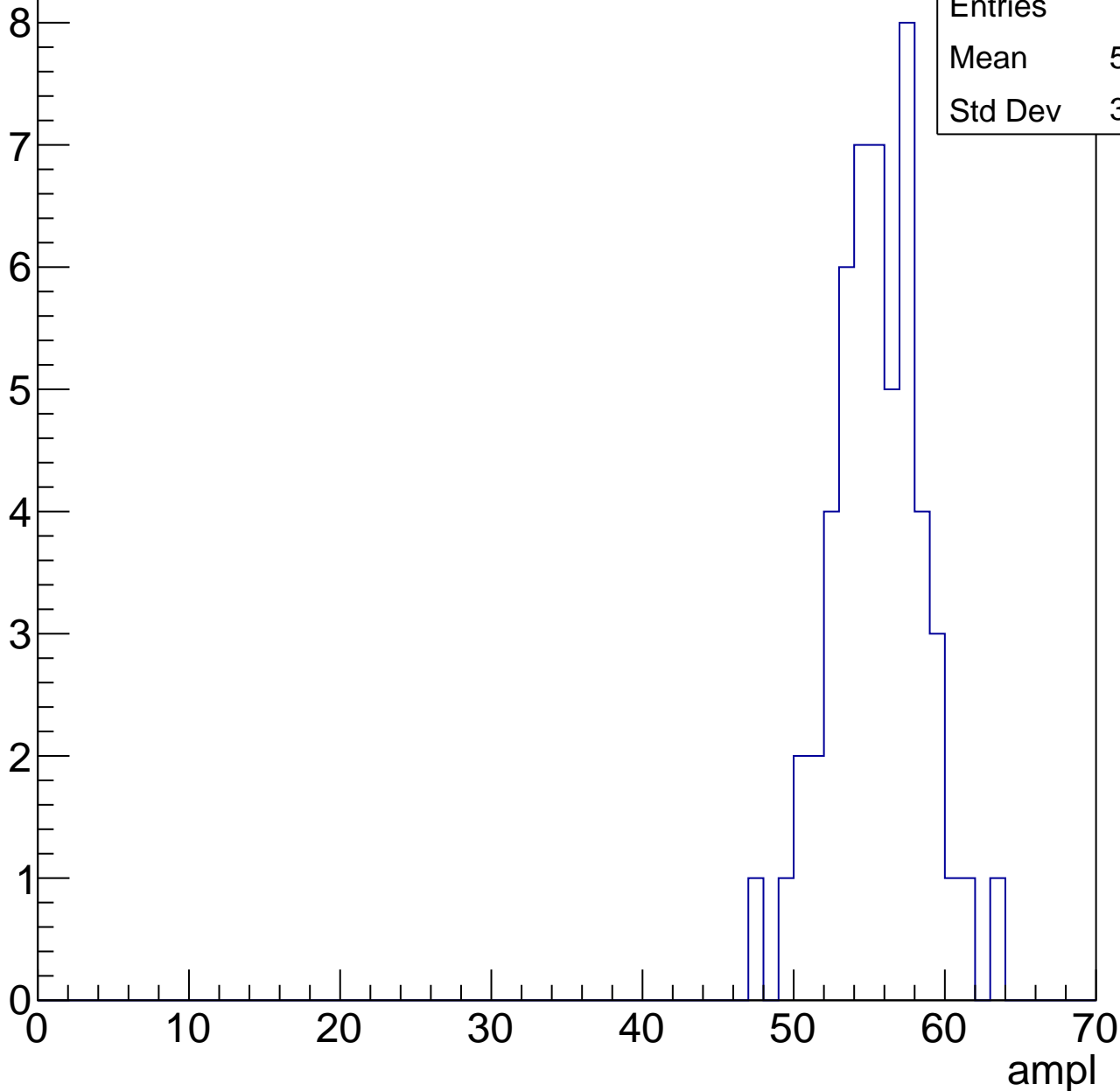


B1L103S, U17-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

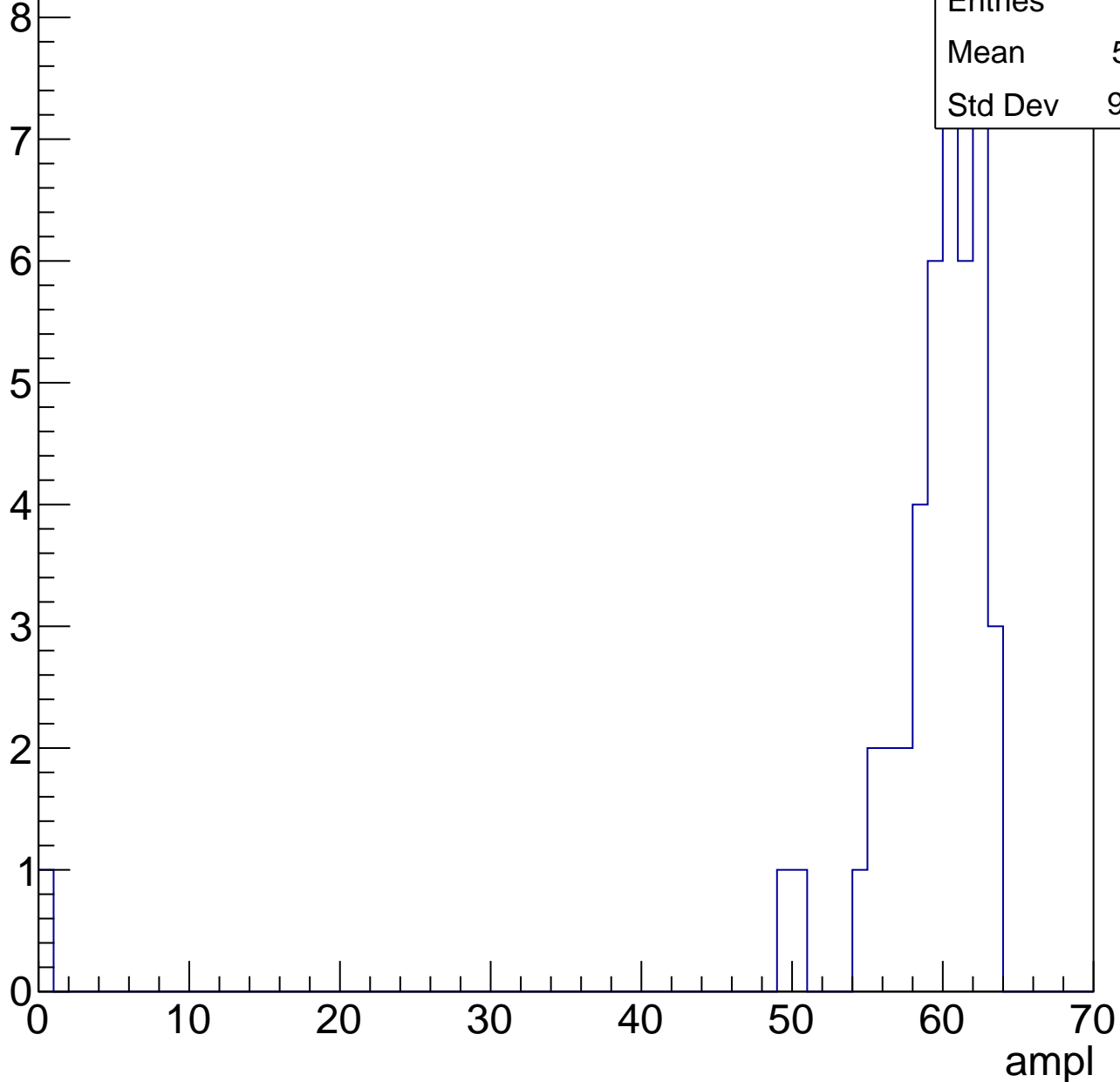
Entries	53
Mean	55.02
Std Dev	3.056



B1L103S, U17-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

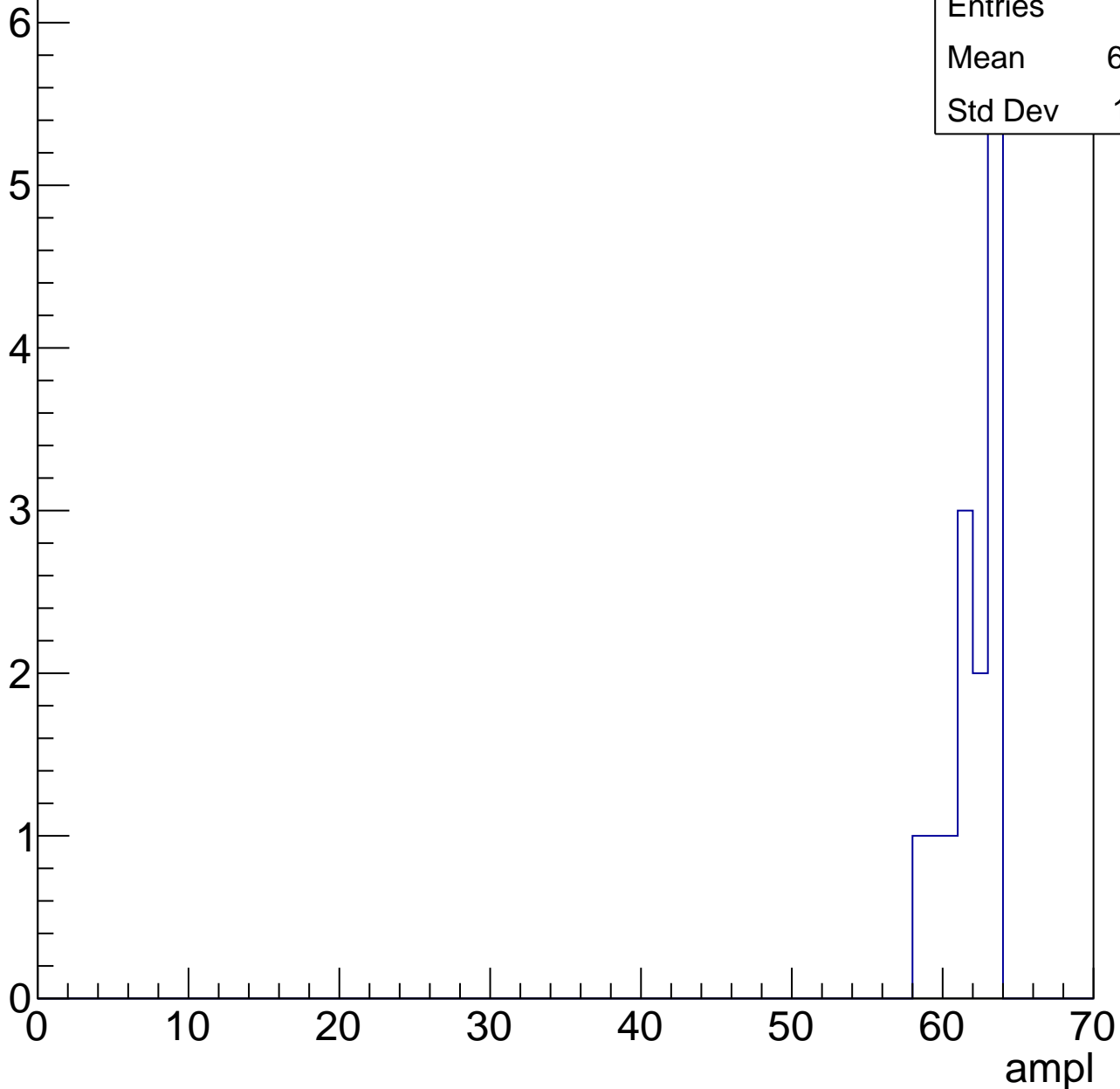


B1L103S, U17-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.57
Std Dev	1.591



B1L103S, U17-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U17-ch123, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	29.57
Std Dev	5.049

Gaus mean : 30.8562

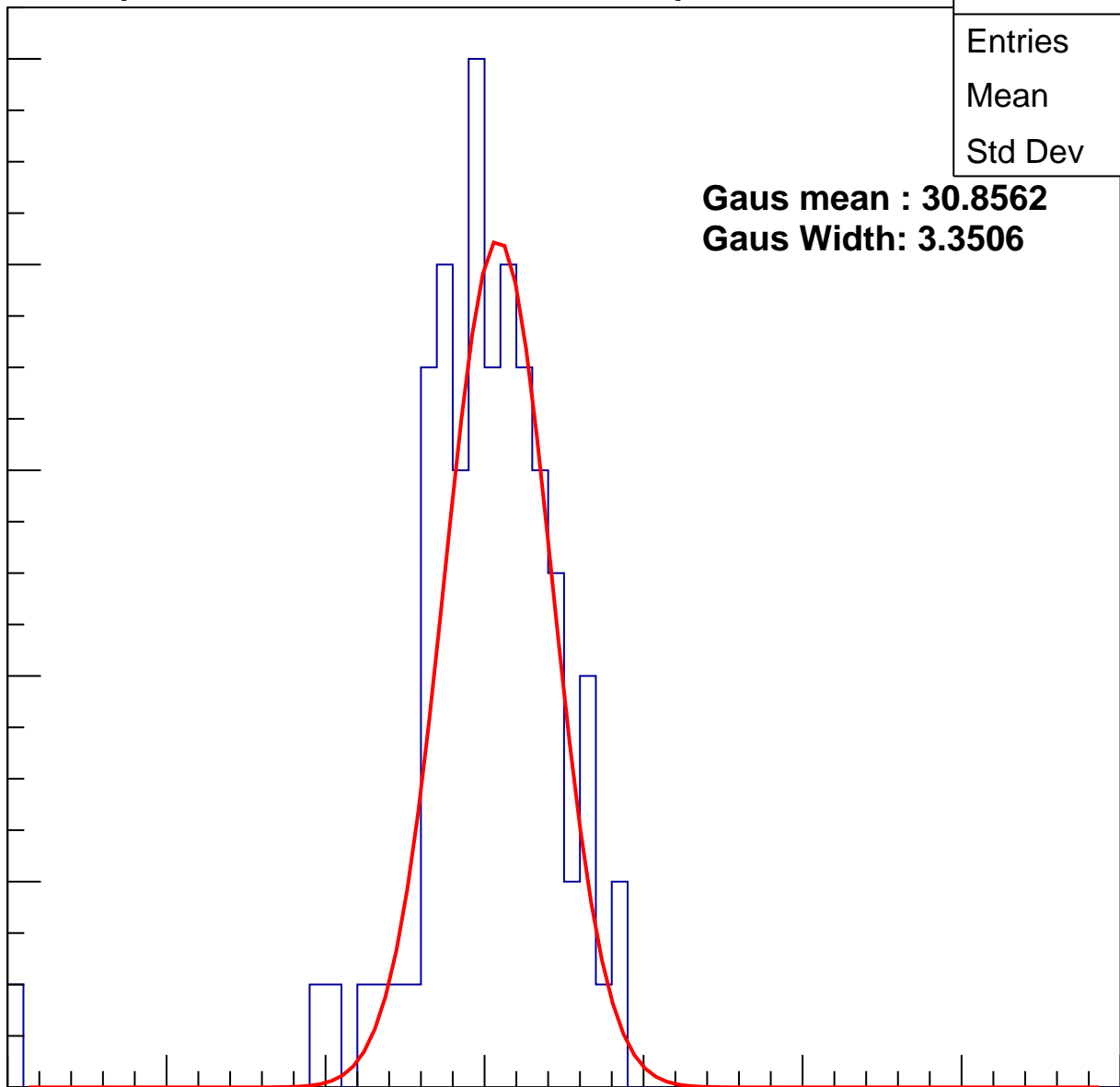
Gaus Width: 3.3506

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch123, adc1

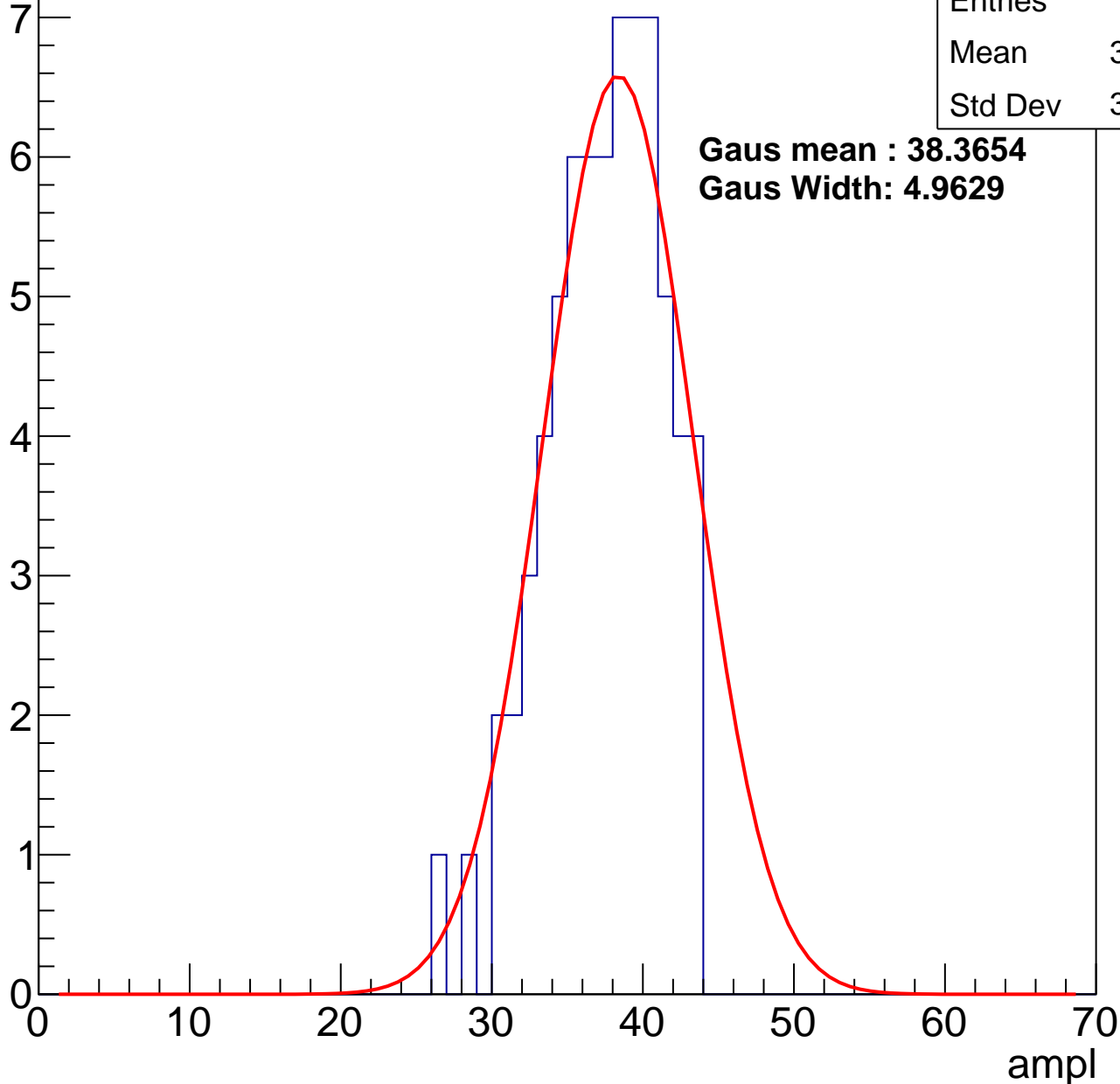
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.94
Std Dev	3.794

Gaus mean : 38.3654

Gaus Width: 4.9629



B1L103S, U17-ch123, adc2

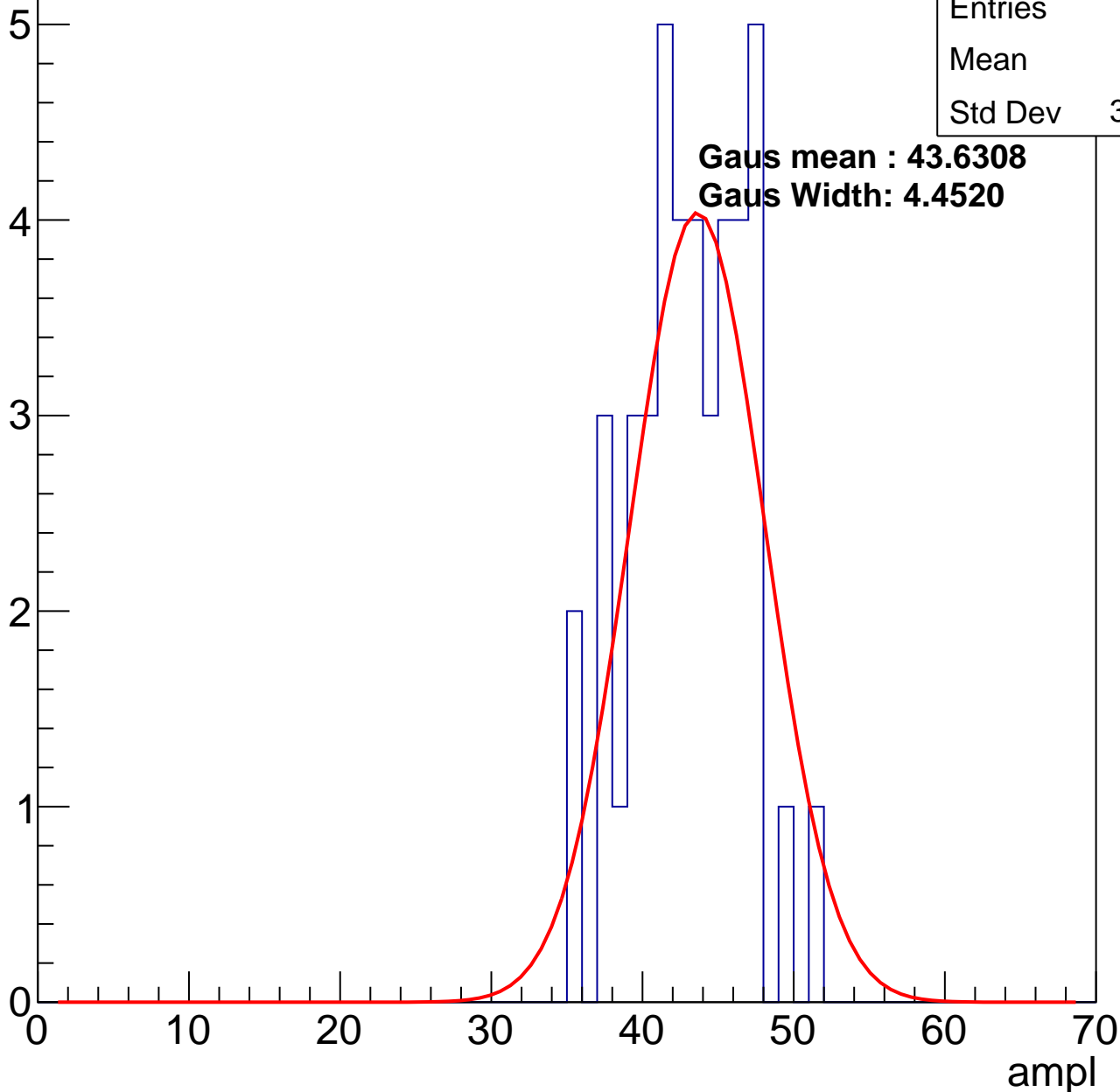
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	42.6
Std Dev	3.699

Gaus mean : 43.6308

Gaus Width: 4.4520

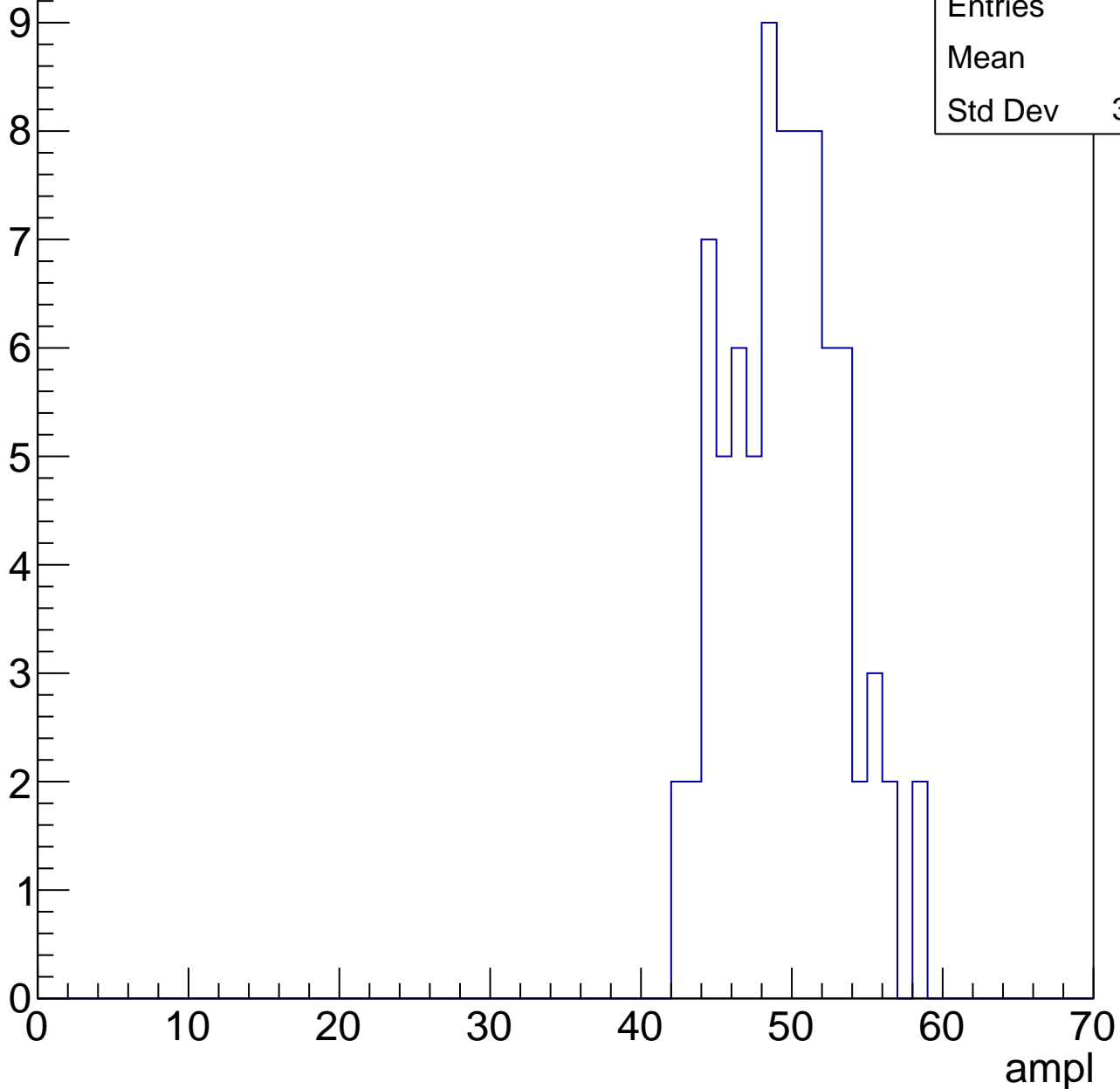


B1L103S, U17-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	49.1
Std Dev	3.711

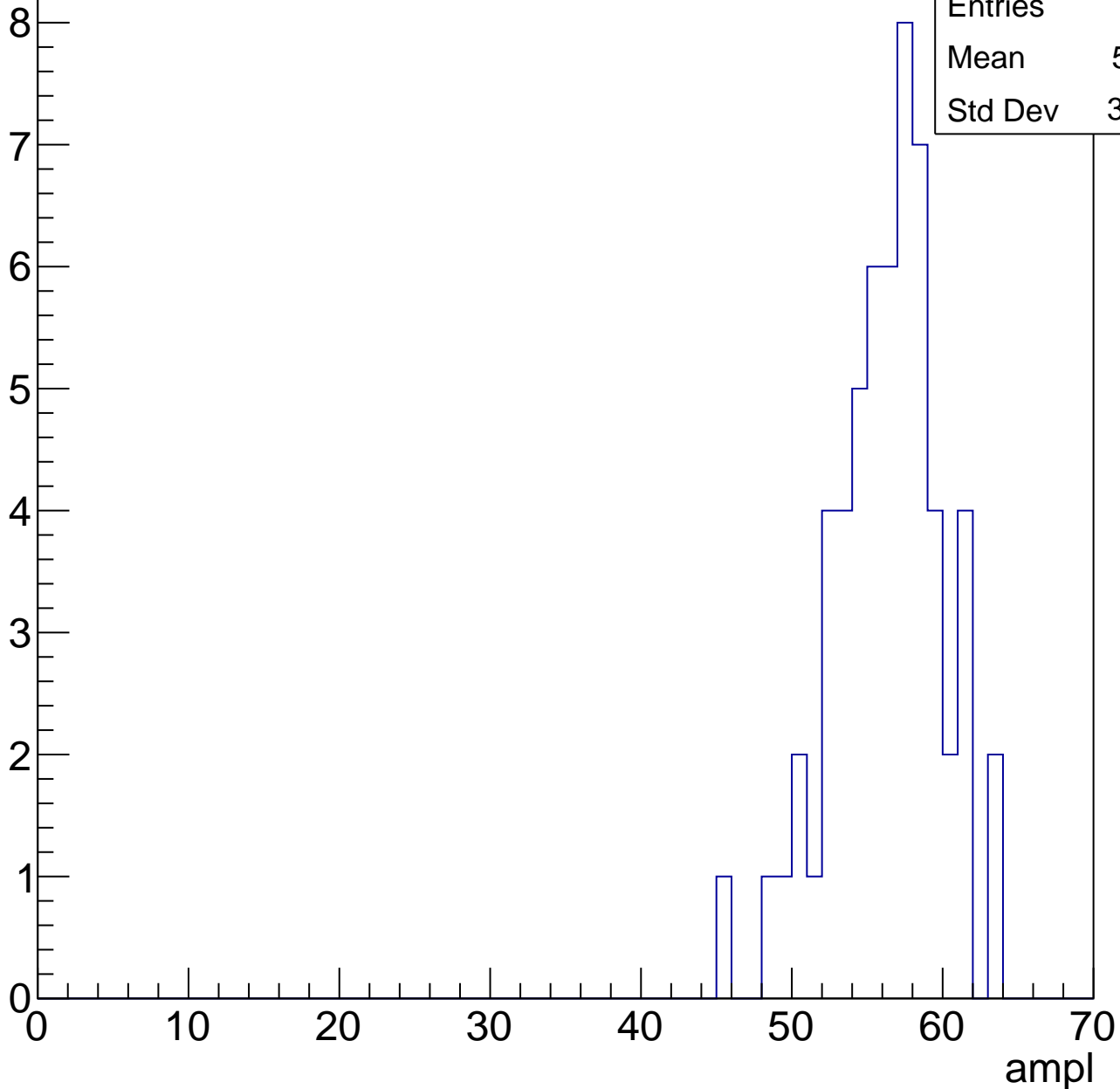


B1L103S, U17-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	55.81
Std Dev	3.613

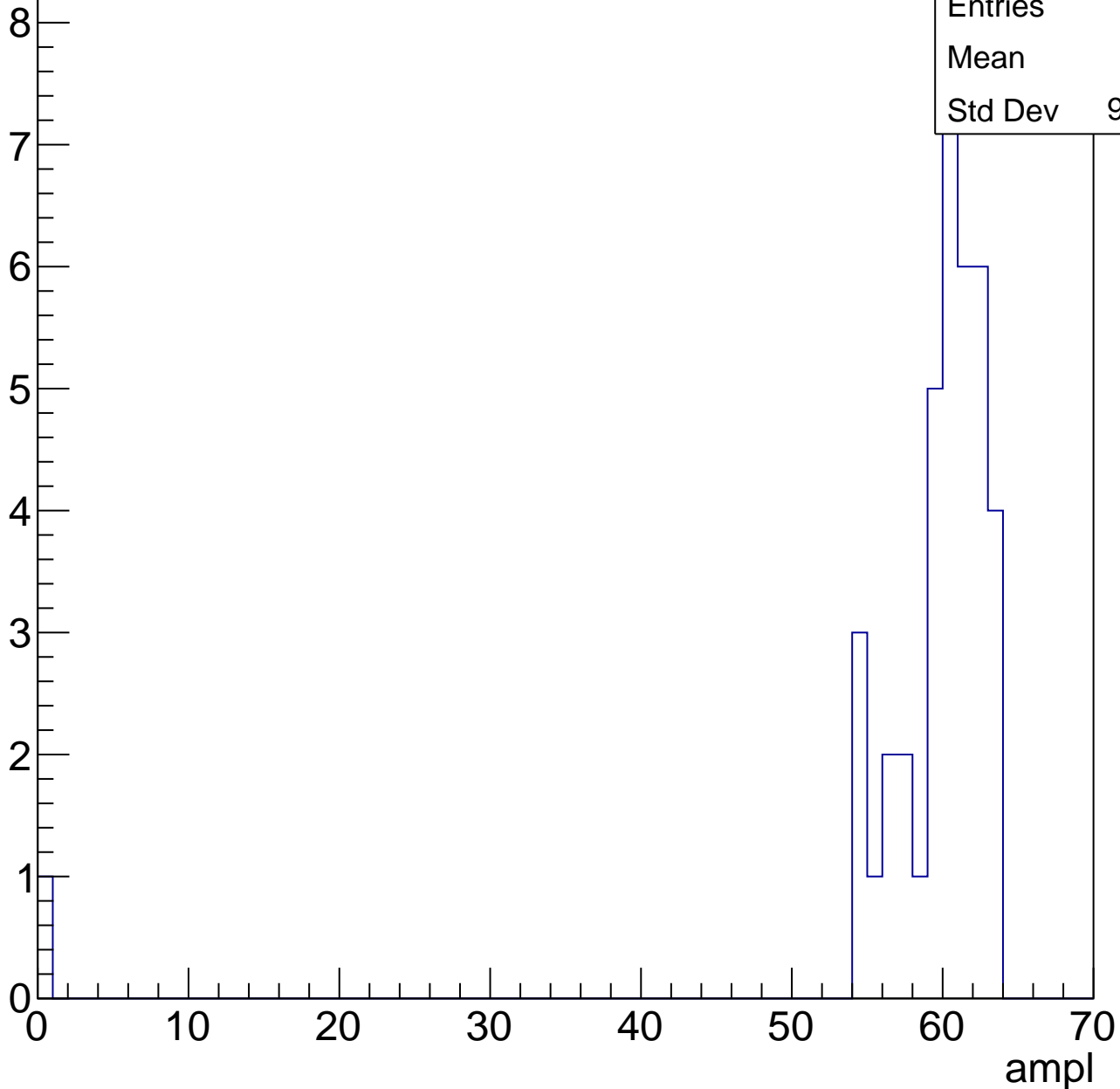


B1L103S, U17-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

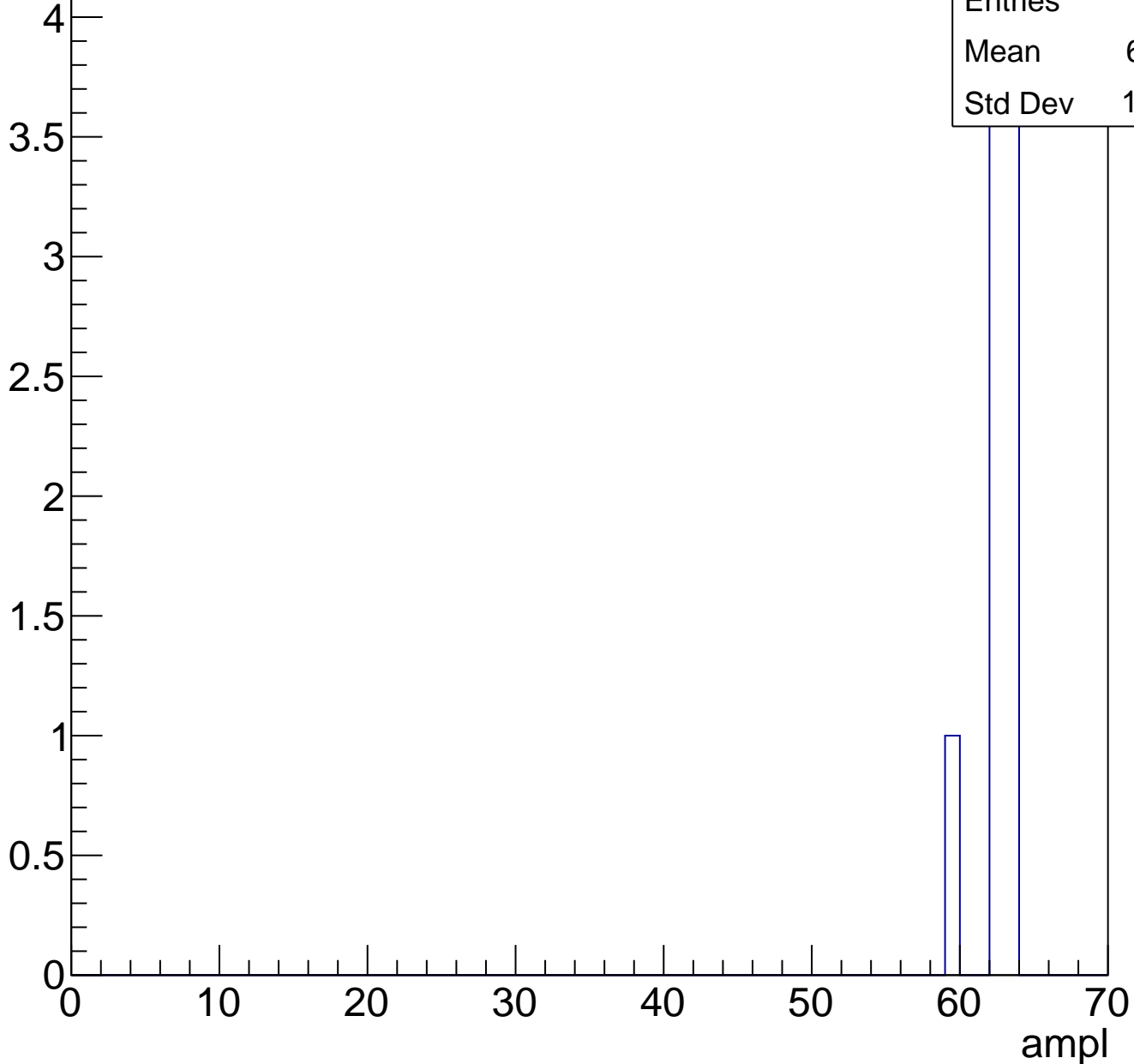
Entries	39
Mean	58.1
Std Dev	9.763



B1L103S, U17-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U17-ch124, adc0

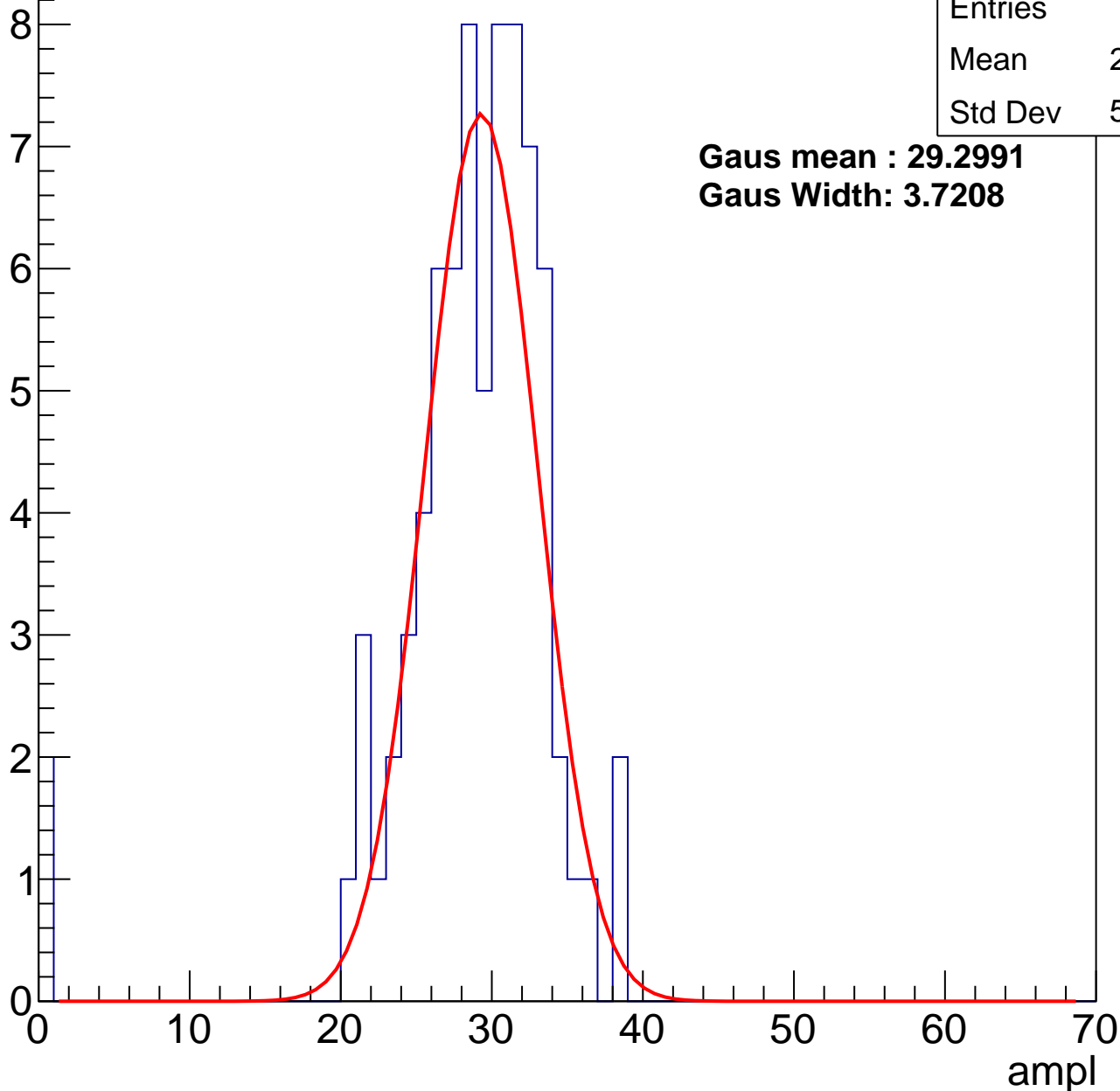
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.09
Std Dev	5.998

Gaus mean : 29.2991

Gaus Width: 3.7208



B1L103S, U17-ch124, adc1

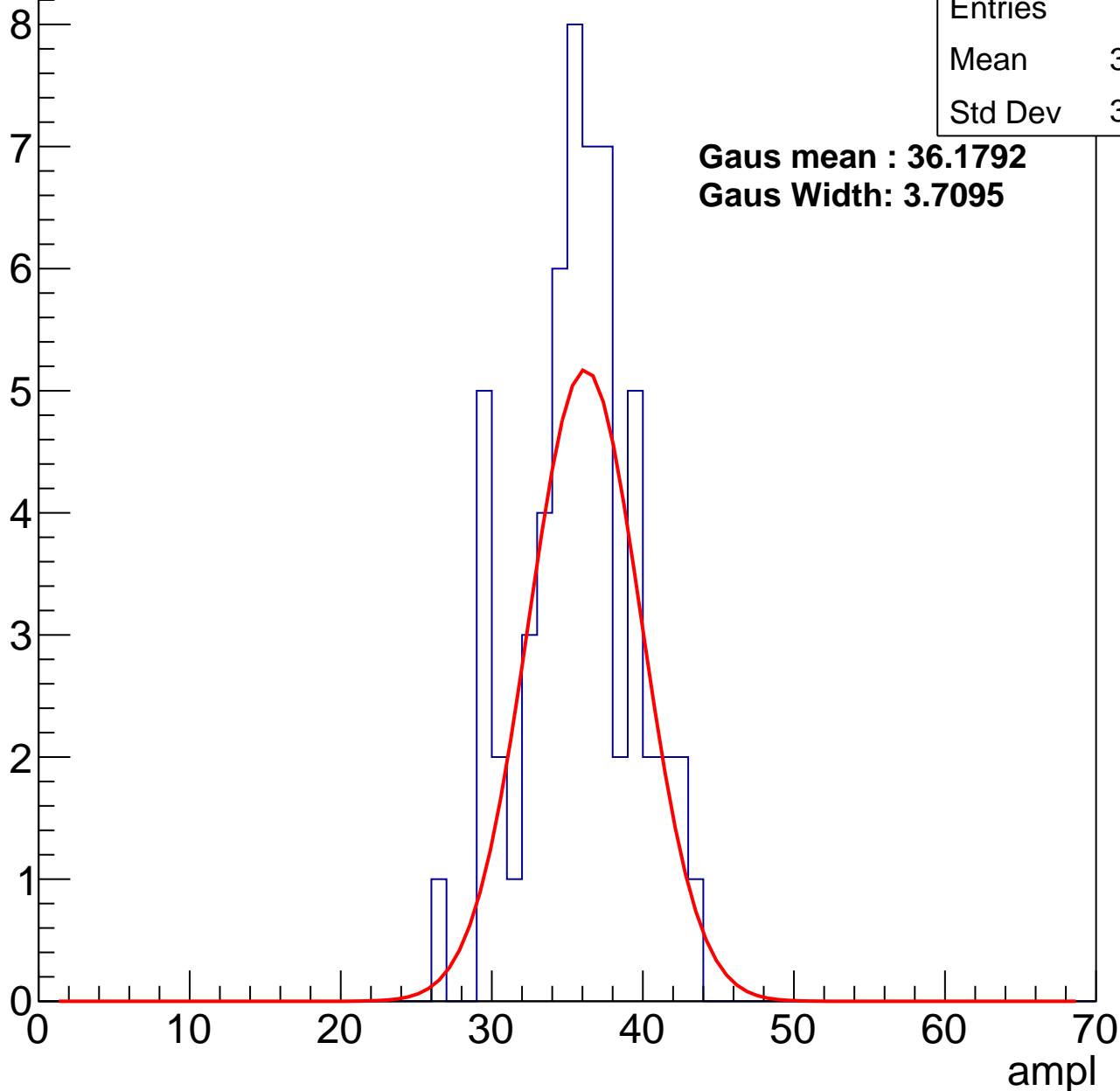
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.26
Std Dev	3.689

Gaus mean : 36.1792

Gaus Width: 3.7095



B1L103S, U17-ch124, adc2

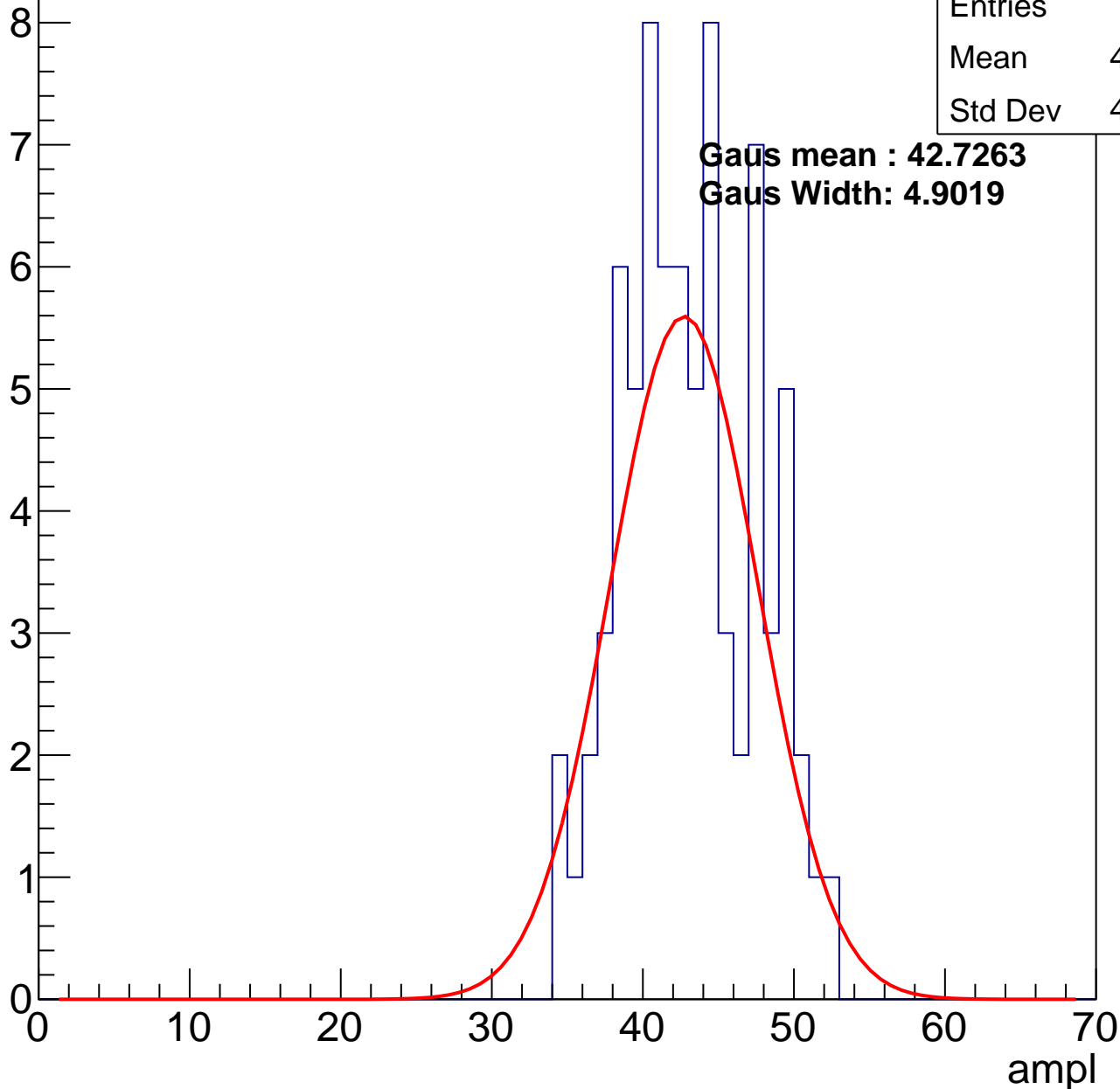
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	42.66
Std Dev	4.297

Gaus mean : 42.7263

Gaus Width: 4.9019

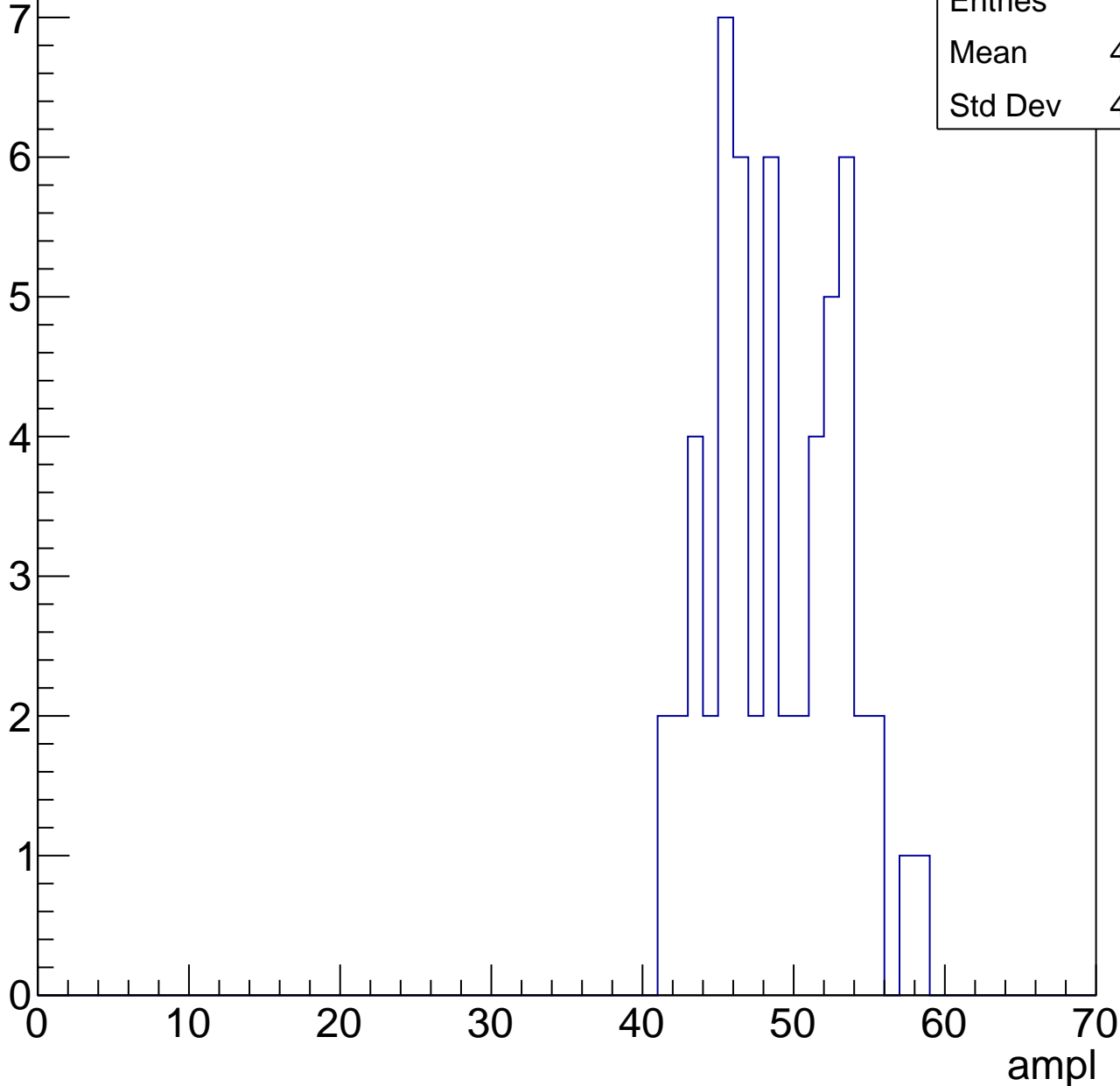


B1L103S, U17-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	48.43
Std Dev	4.242

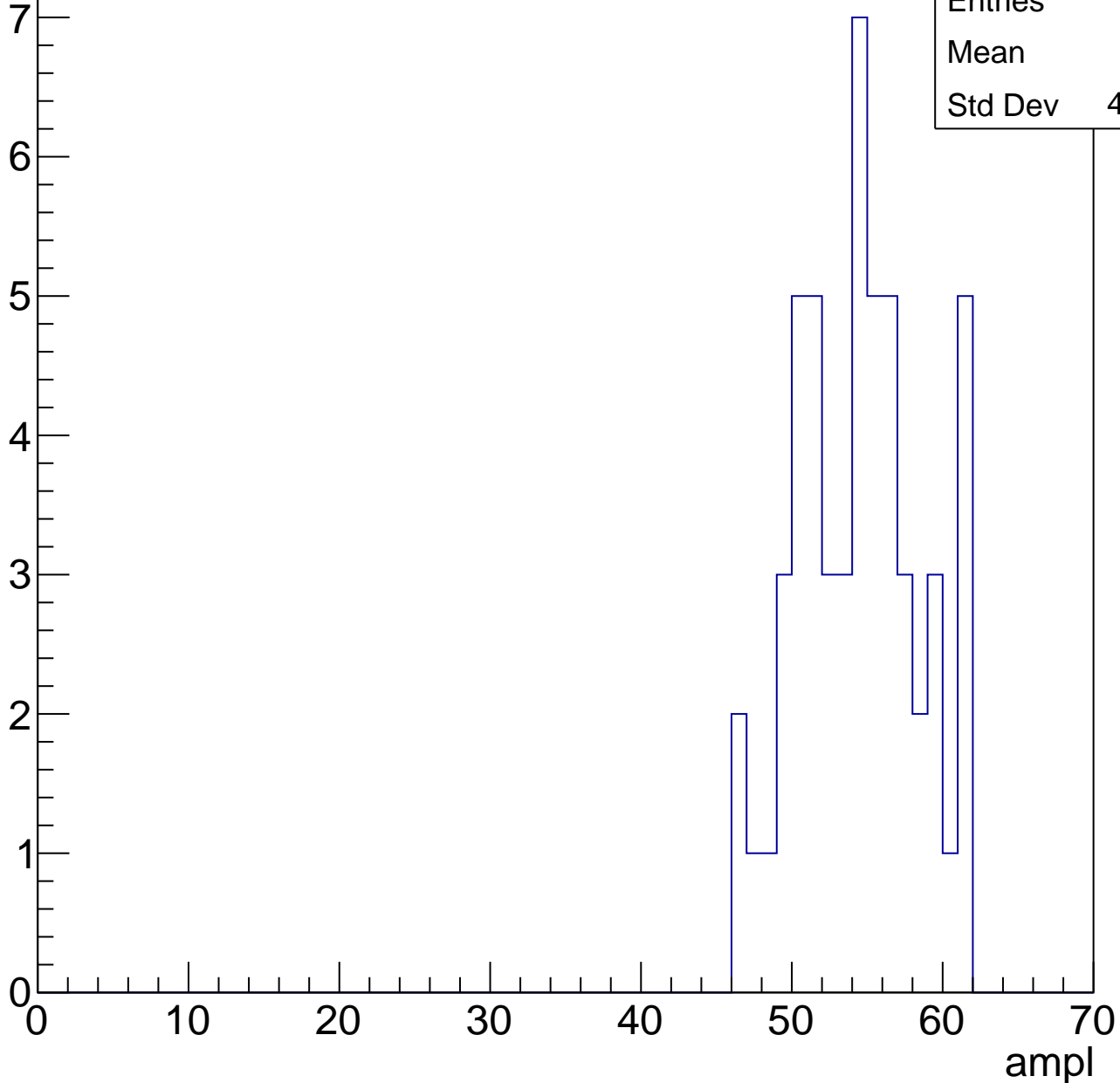


B1L103S, U17-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54
Std Dev	4.009

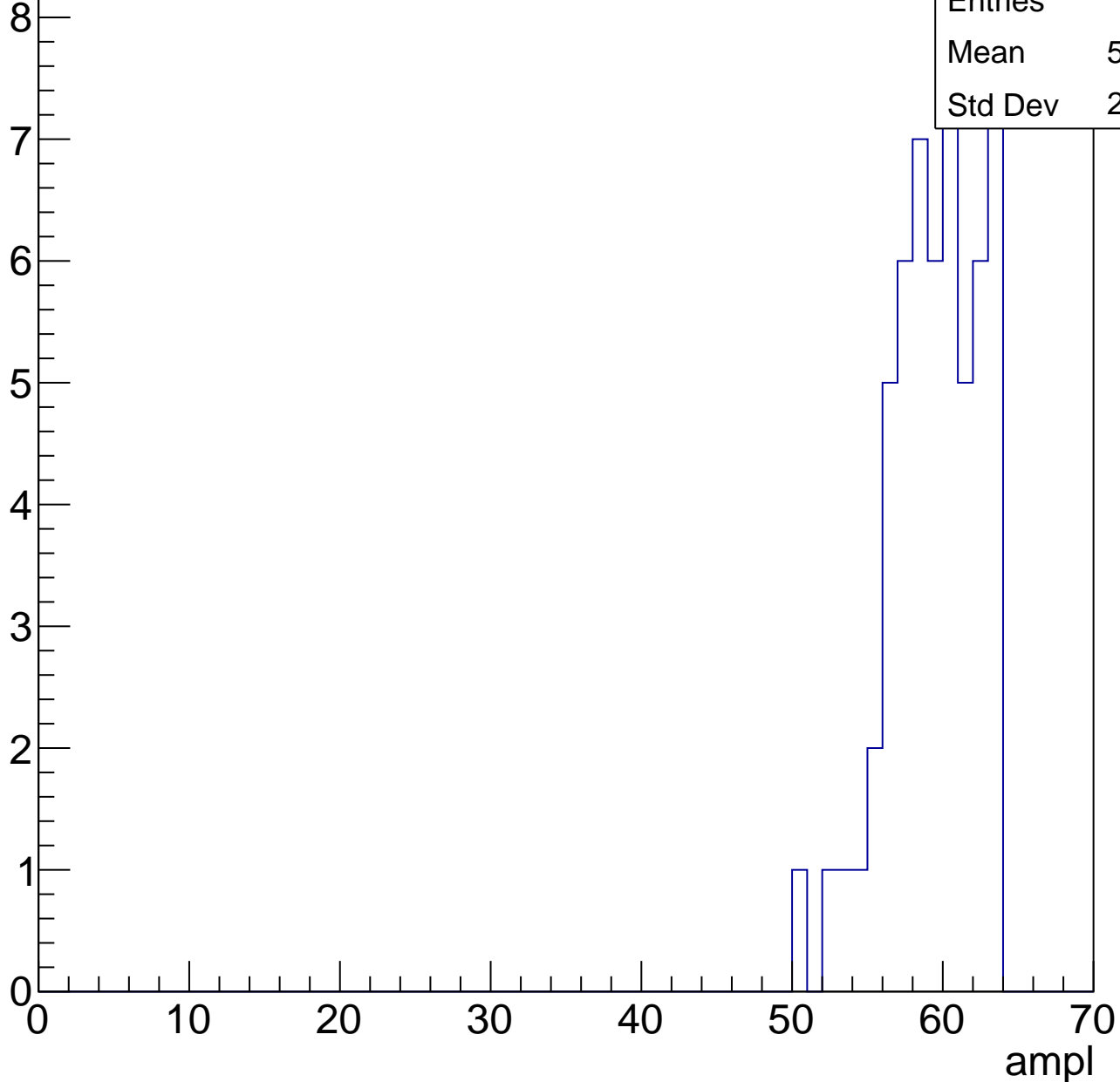


B1L103S, U17-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

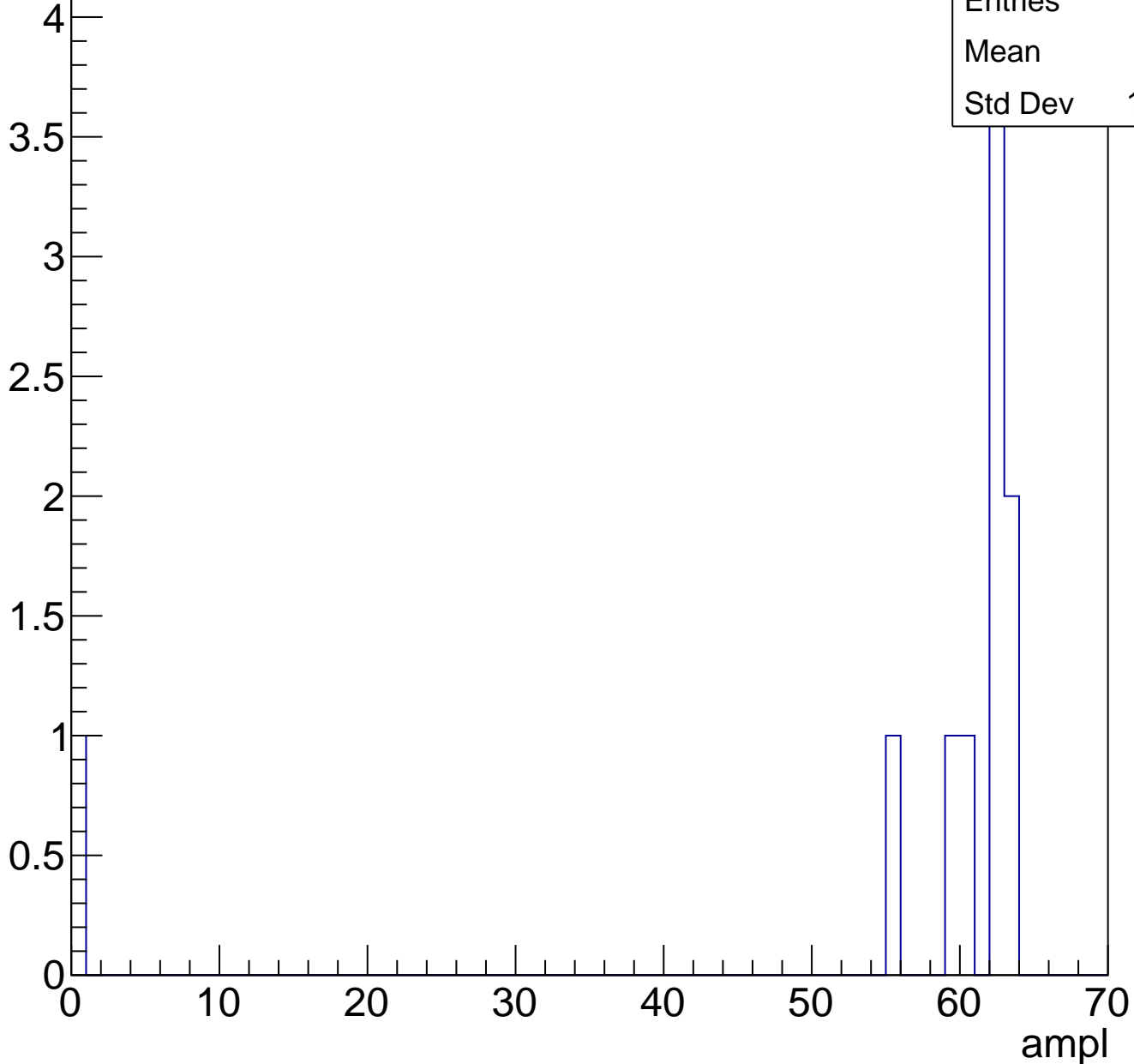
Entries	57
Mean	58.98
Std Dev	2.988



B1L103S, U17-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	54.8
Std Dev	18.41

B1L103S, U17-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch125, adc0

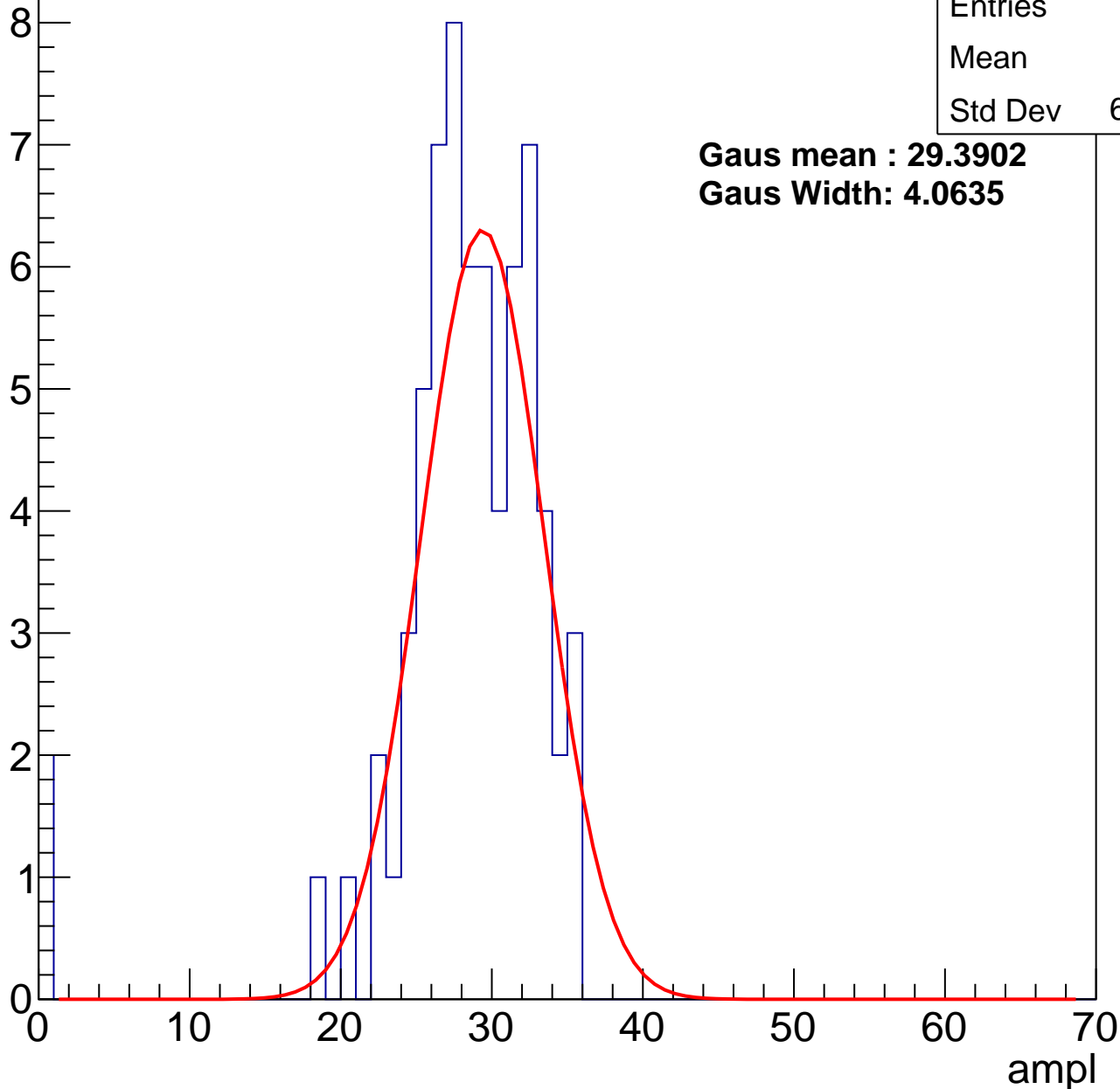
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.6
Std Dev	6.008

Gaus mean : 29.3902

Gaus Width: 4.0635



B1L103S, U17-ch125, adc1

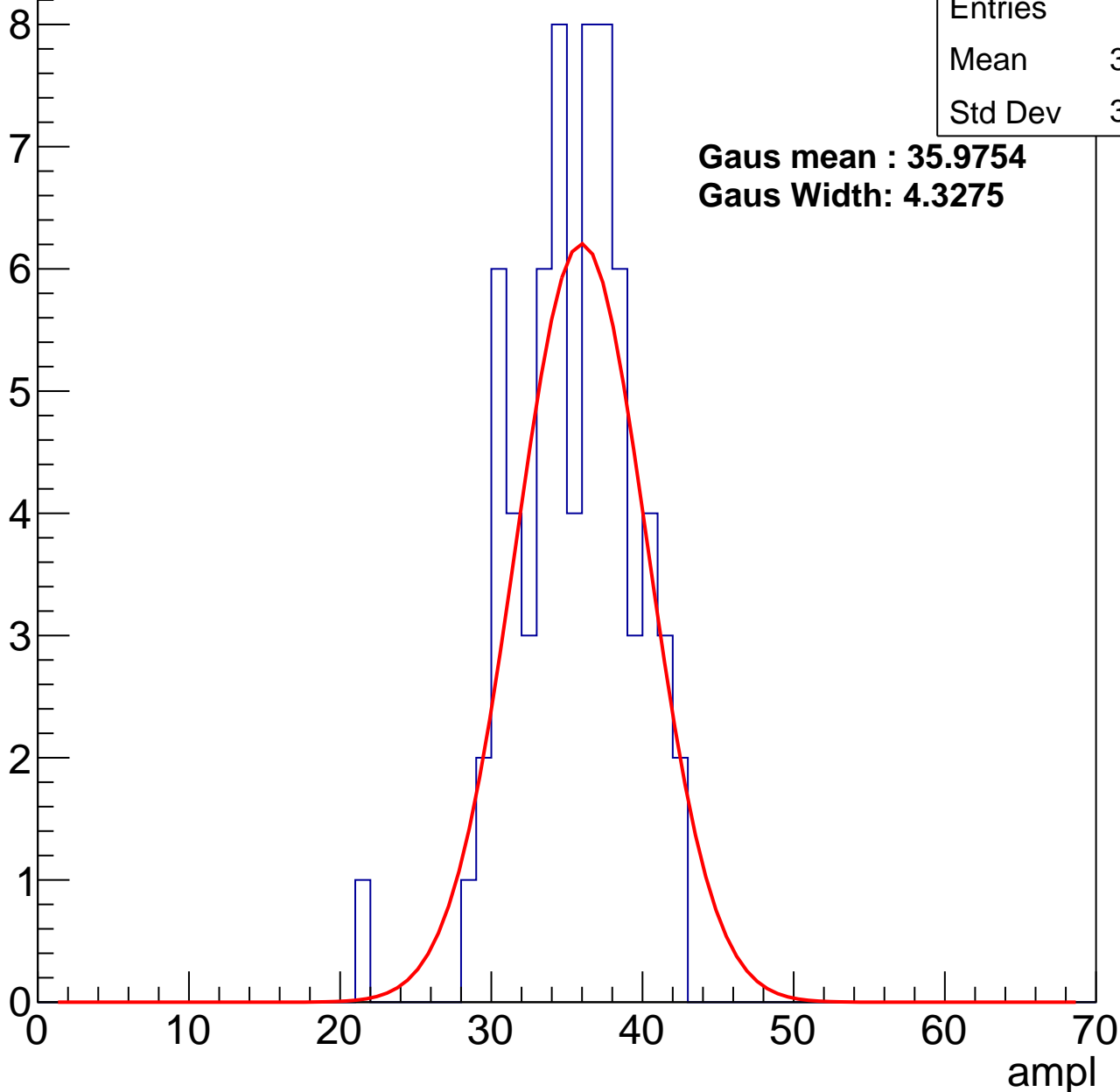
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.97
Std Dev	3.878

Gaus mean : 35.9754

Gaus Width: 4.3275



B1L103S, U17-ch125, adc2

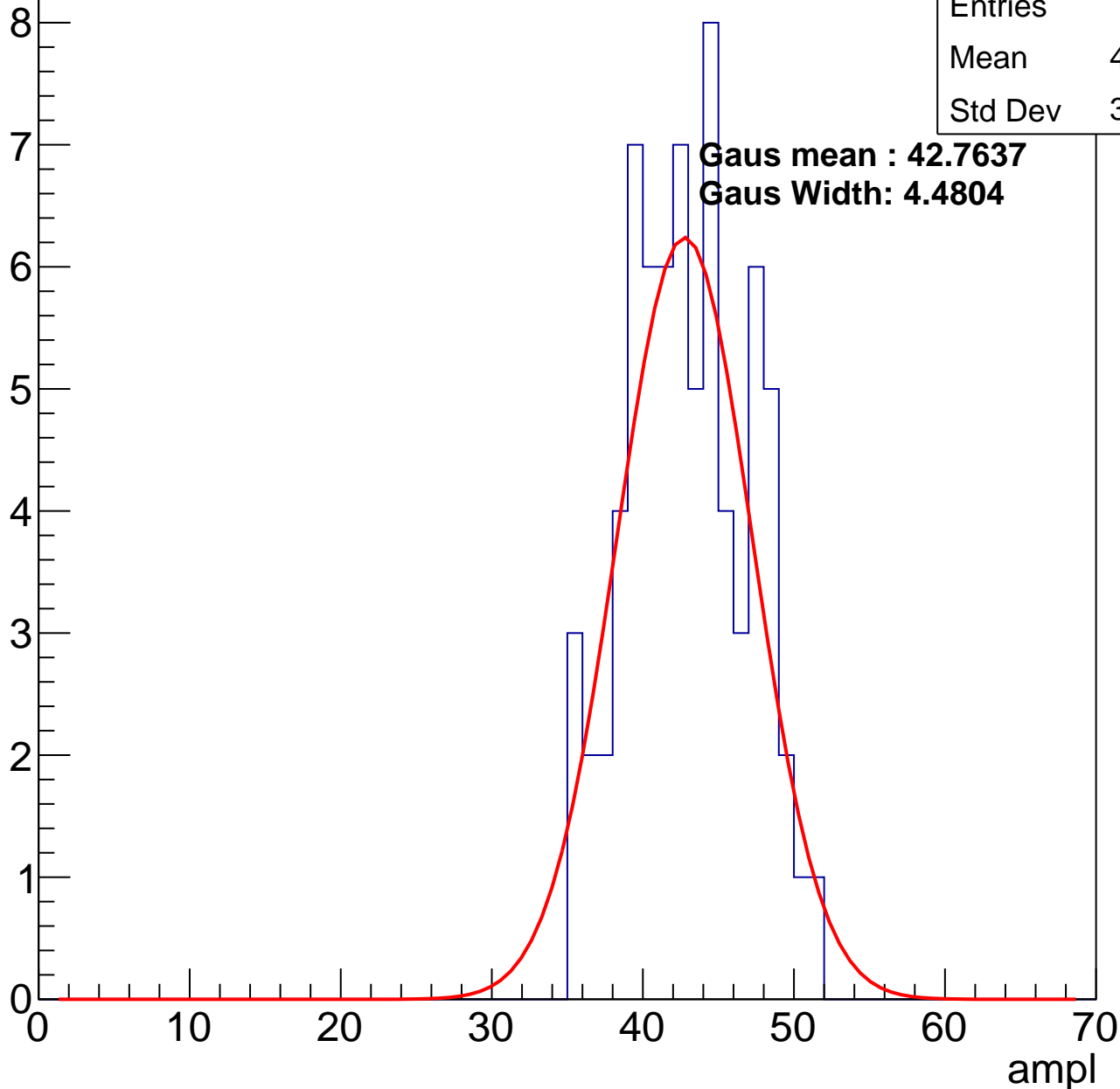
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.53
Std Dev	3.909

Gaus mean : 42.7637

Gaus Width: 4.4804

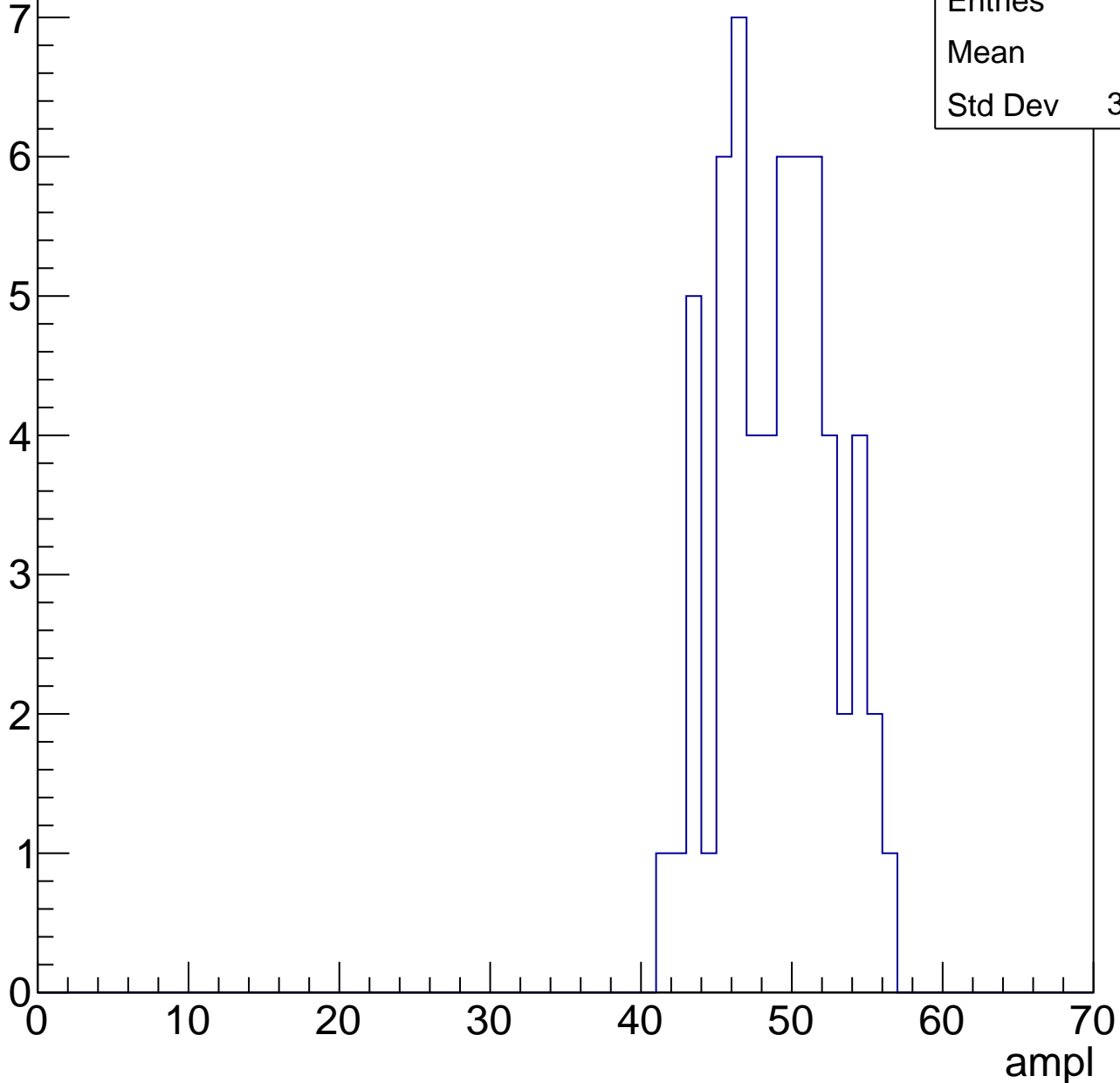


B1L103S, U17-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.5
Std Dev	3.654

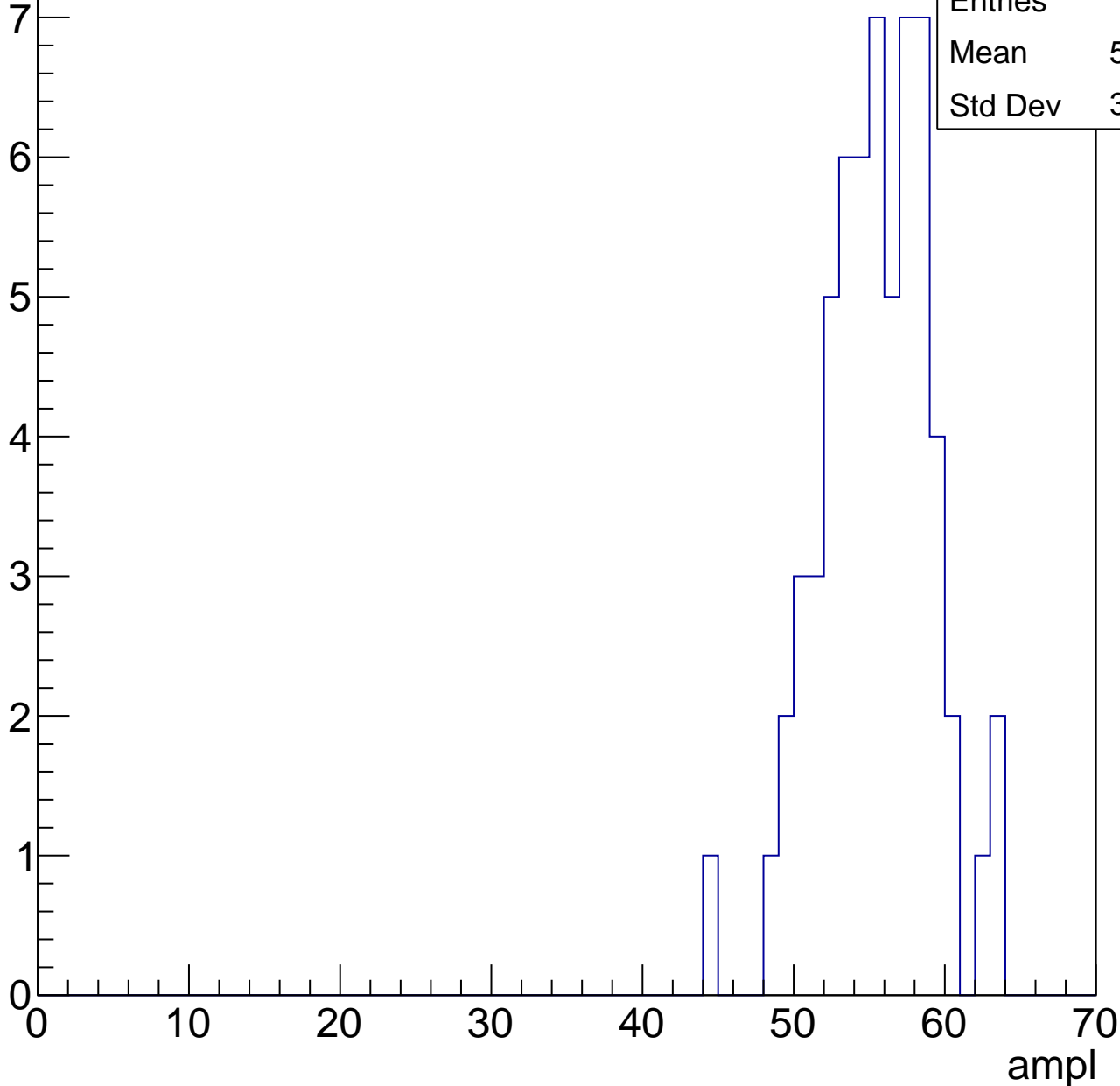


B1L103S, U17-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.98
Std Dev	3.652

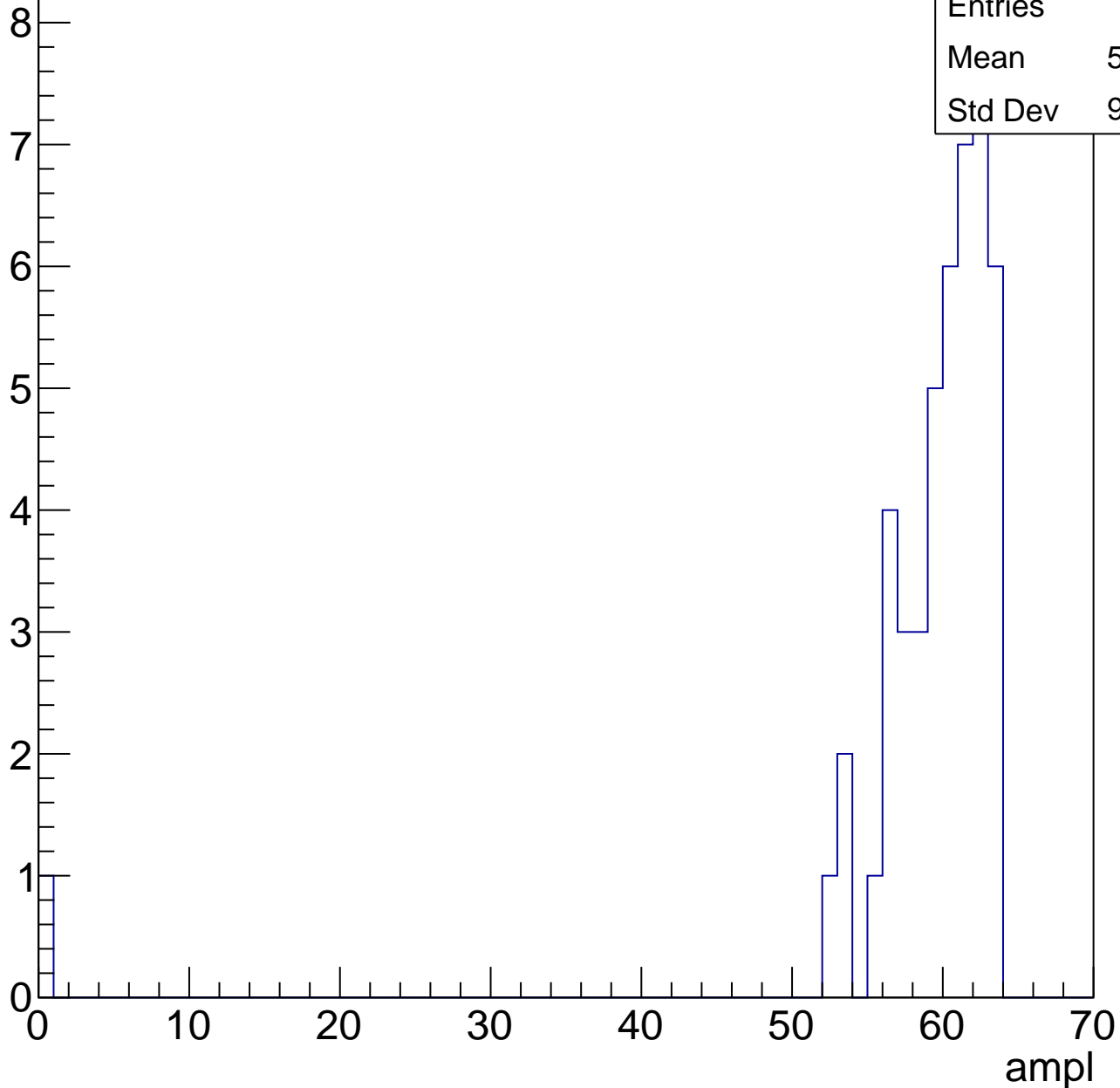


B1L103S, U17-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

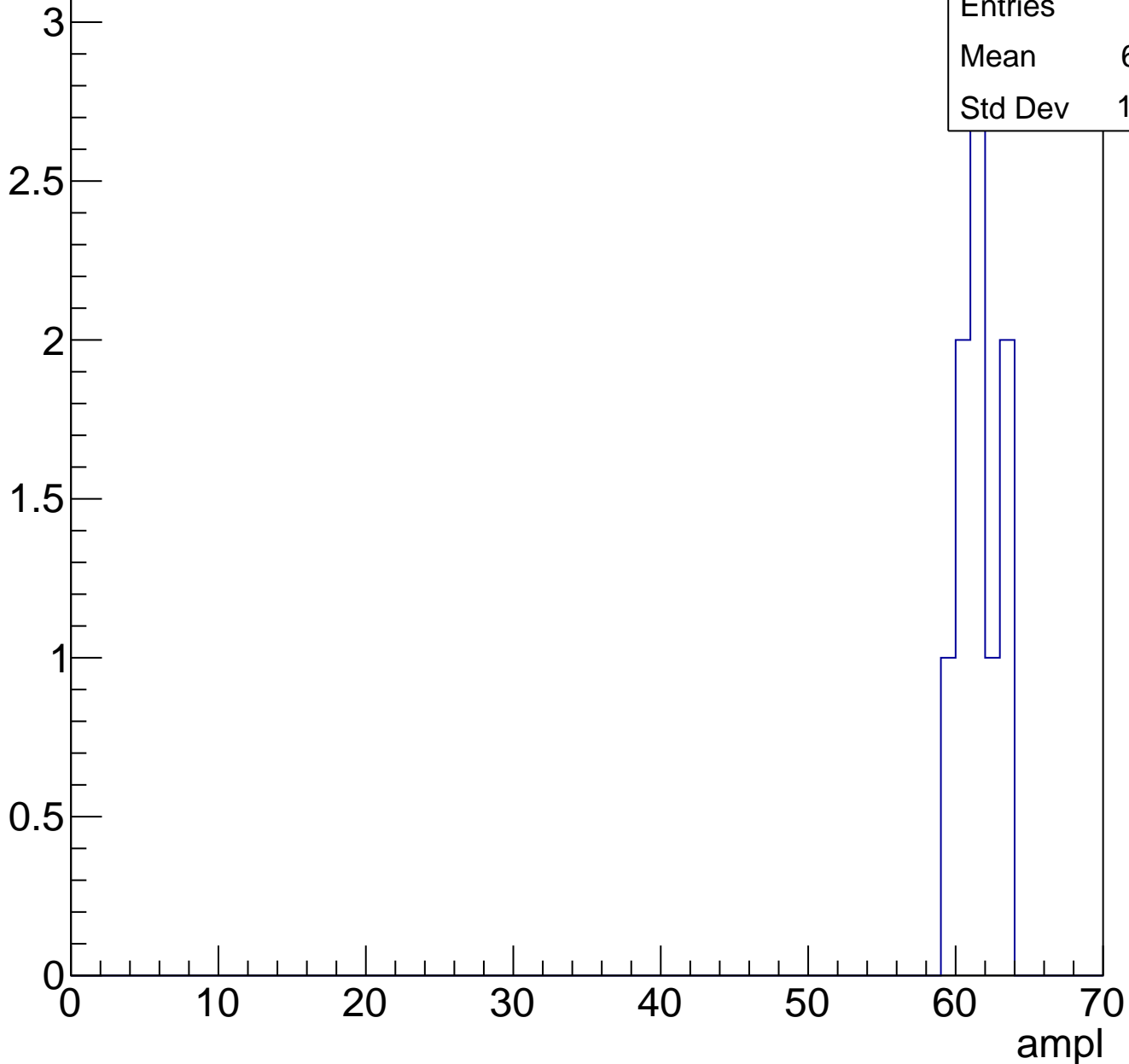
Entries	47
Mean	58.26
Std Dev	9.045



B1L103S, U17-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U17-ch126, adc0

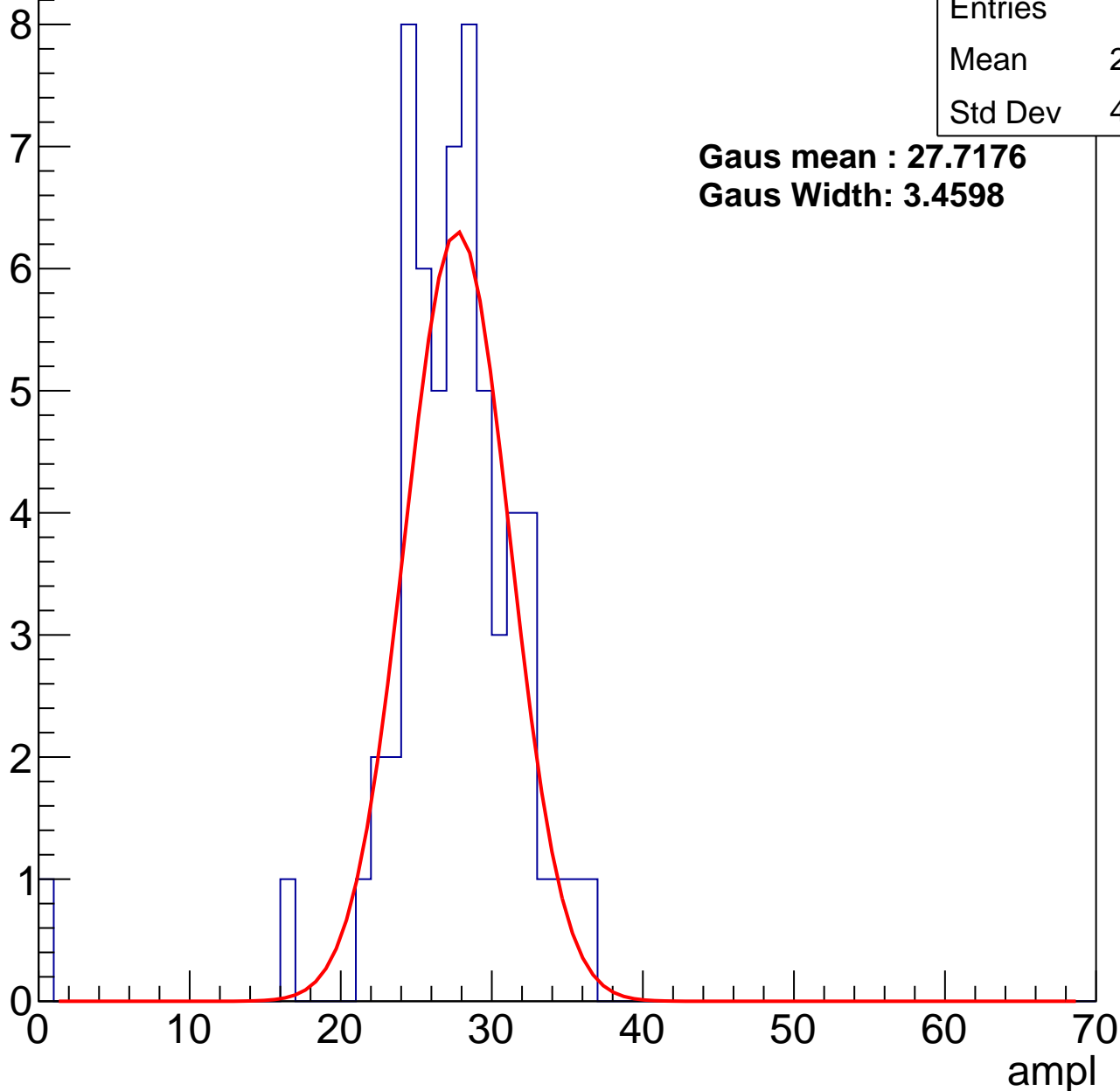
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	26.84
Std Dev	4.986

Gaus mean : 27.7176

Gaus Width: 3.4598



B1L103S, U17-ch126, adc1

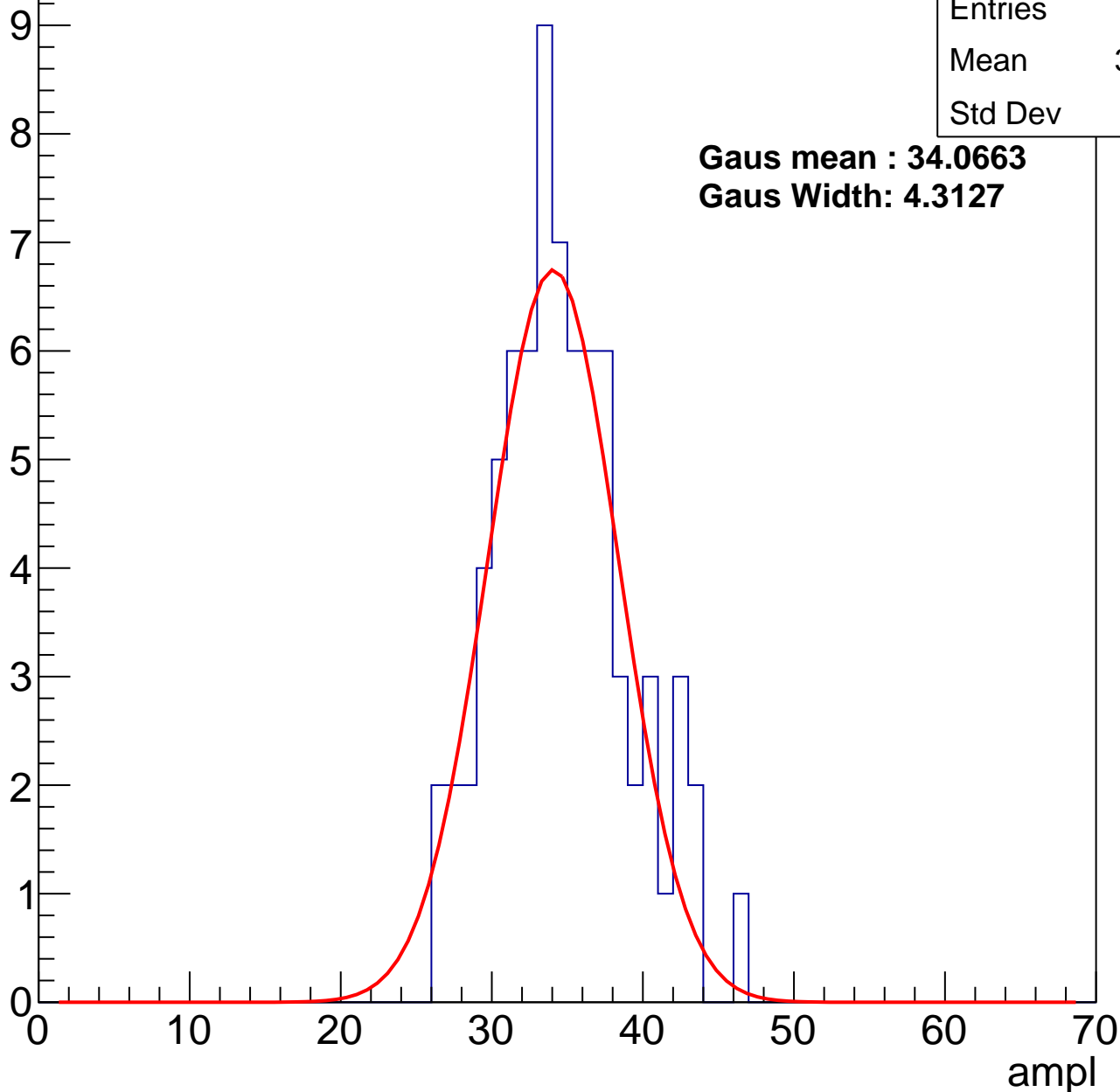
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	34.21
Std Dev	4.29

Gaus mean : 34.0663

Gaus Width: 4.3127



B1L103S, U17-ch126, adc2

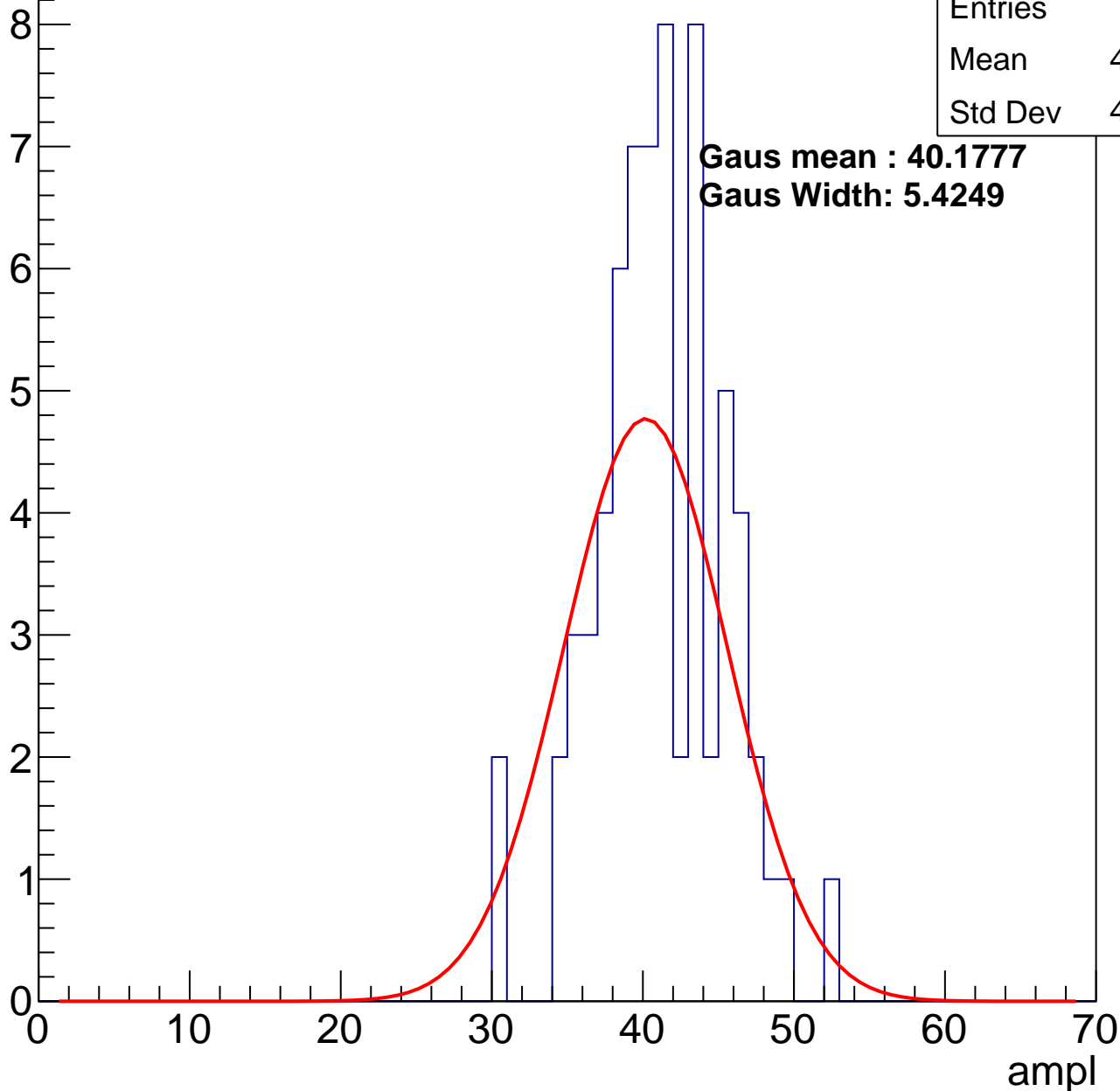
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	40.68
Std Dev	4.213

Gaus mean : 40.1777

Gaus Width: 5.4249

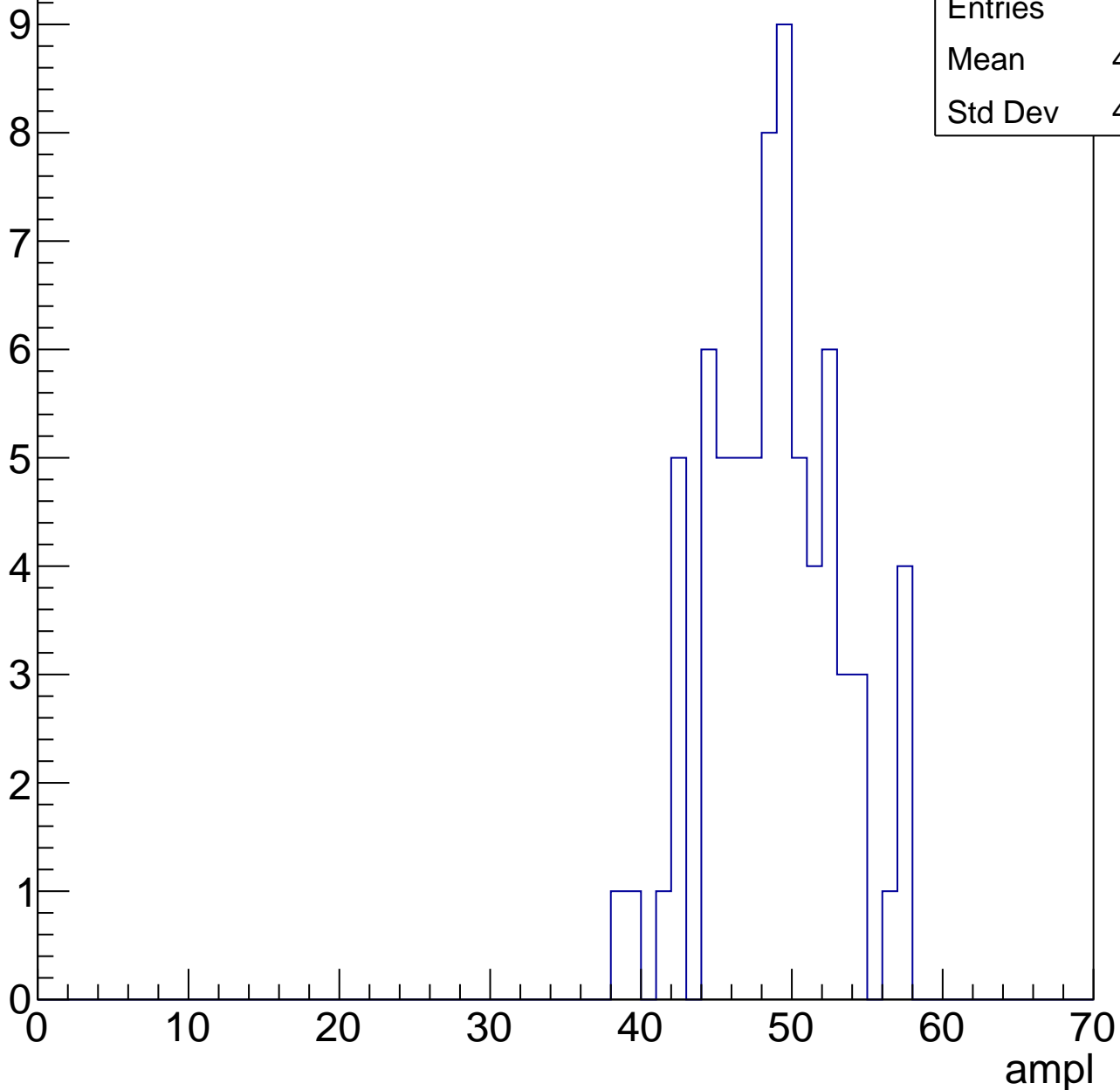


B1L103S, U17-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.31
Std Dev	4.251

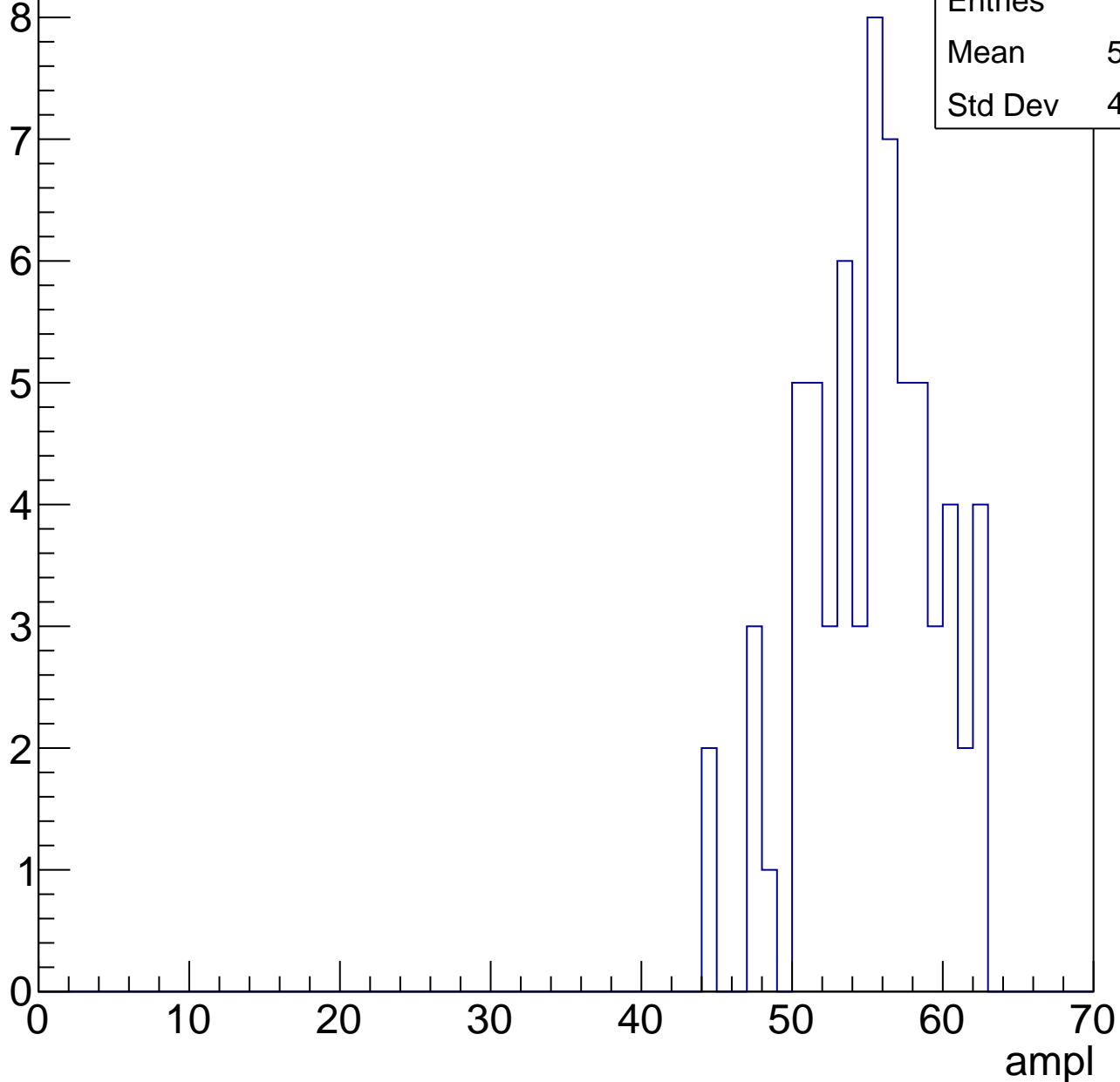


B1L103S, U17-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

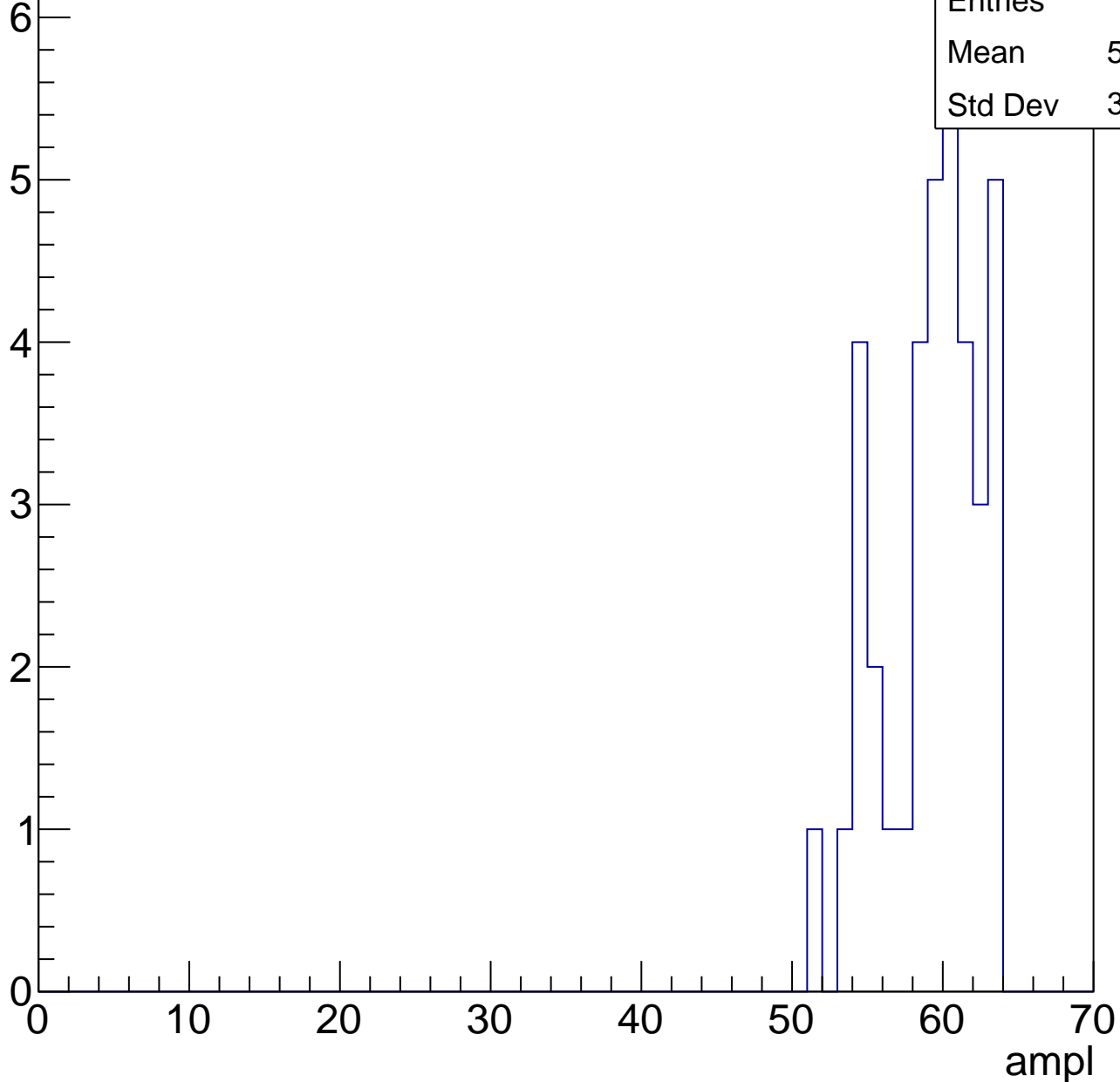
Entries	66
Mean	54.73
Std Dev	4.308



B1L103S, U17-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

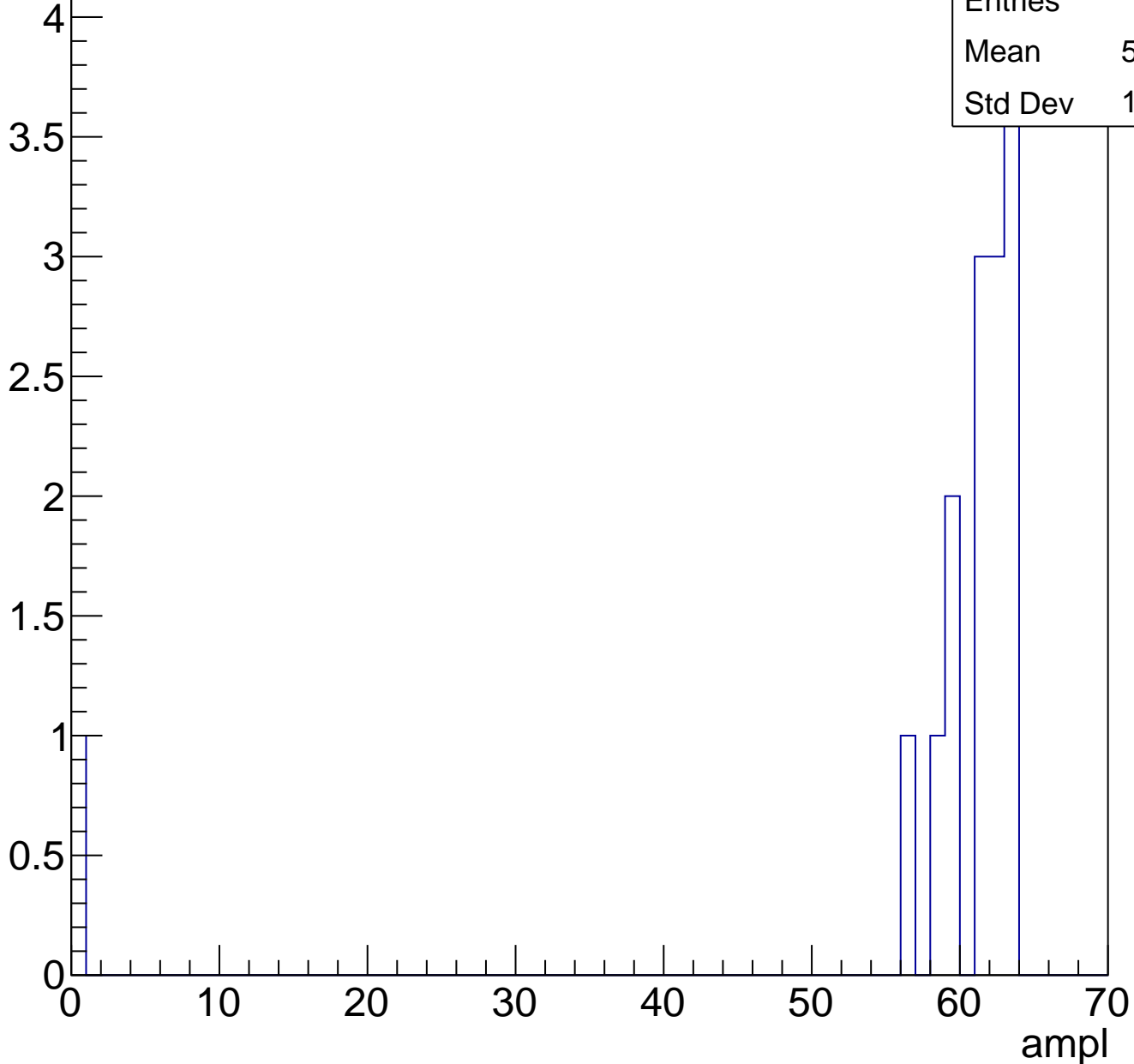


Entries	37
Mean	58.78
Std Dev	3.189

B1L103S, U17-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

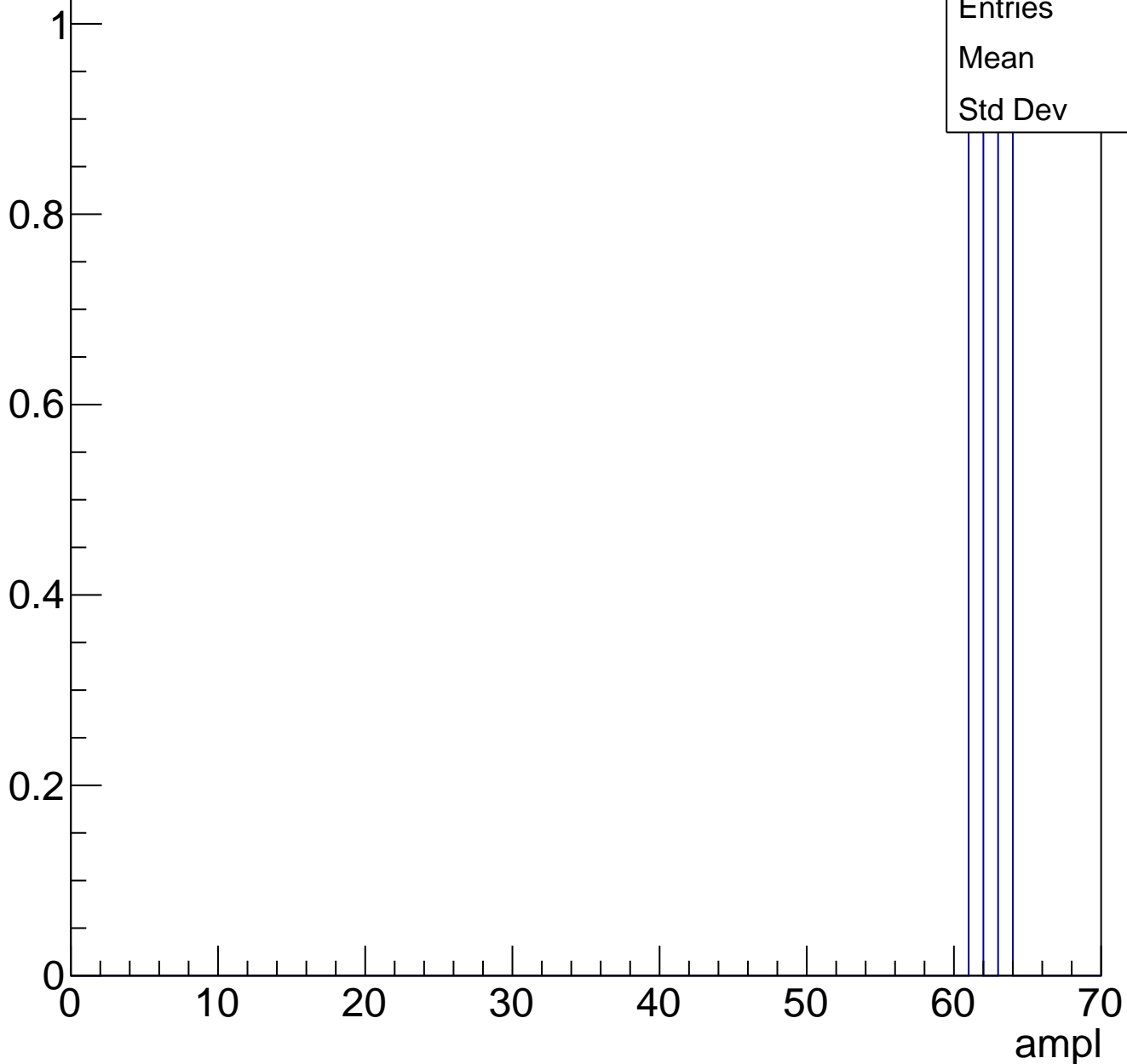
Entry



B1L103S, U17-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U17-ch127, adc0

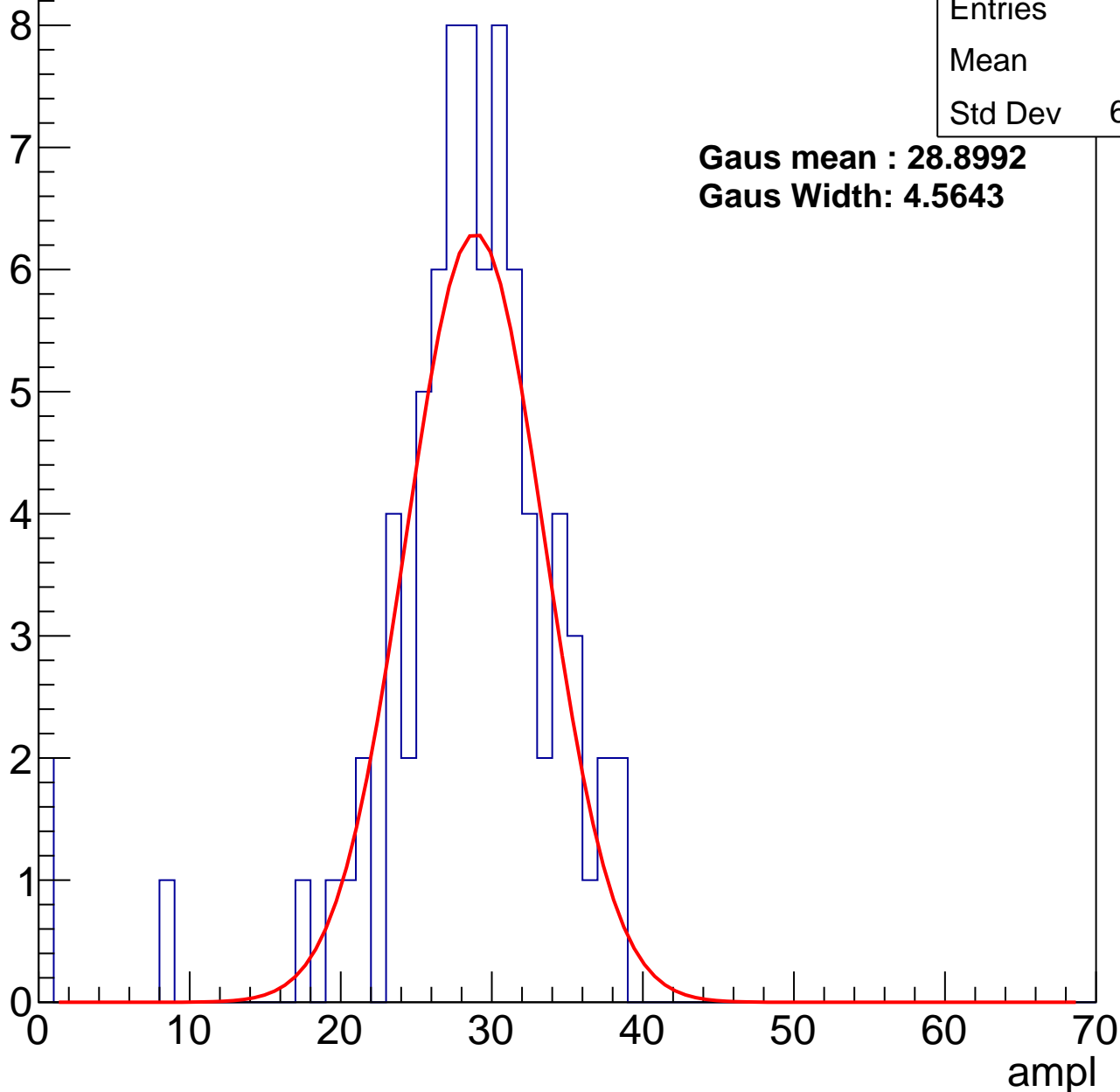
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	27.7
Std Dev	6.615

Gaus mean : 28.8992

Gaus Width: 4.5643



B1L103S, U17-ch127, adc1

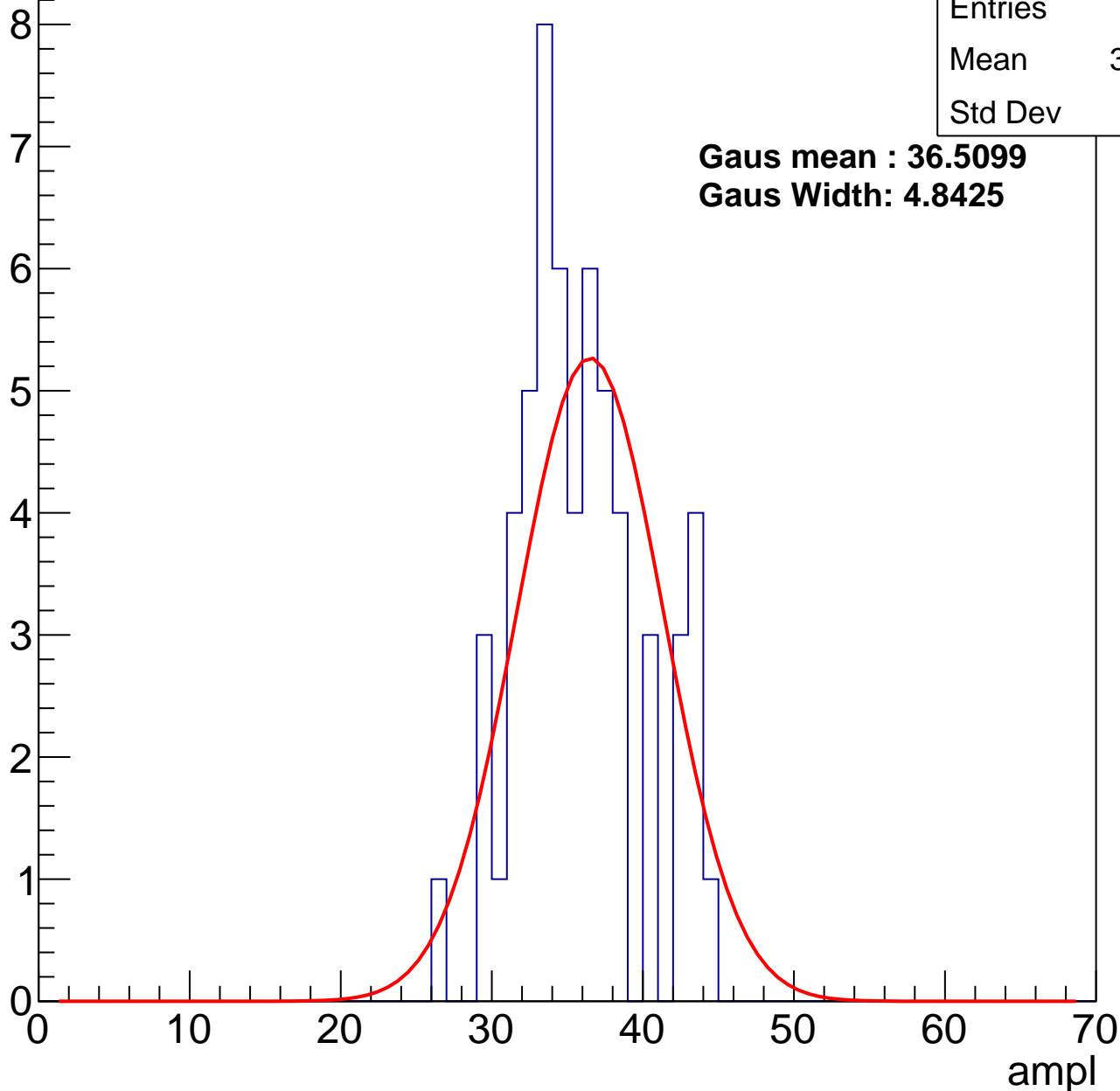
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.34
Std Dev	4.1

Gaus mean : 36.5099

Gaus Width: 4.8425



B1L103S, U17-ch127, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	41.35
Std Dev	3.451

Gaus mean : 41.3210

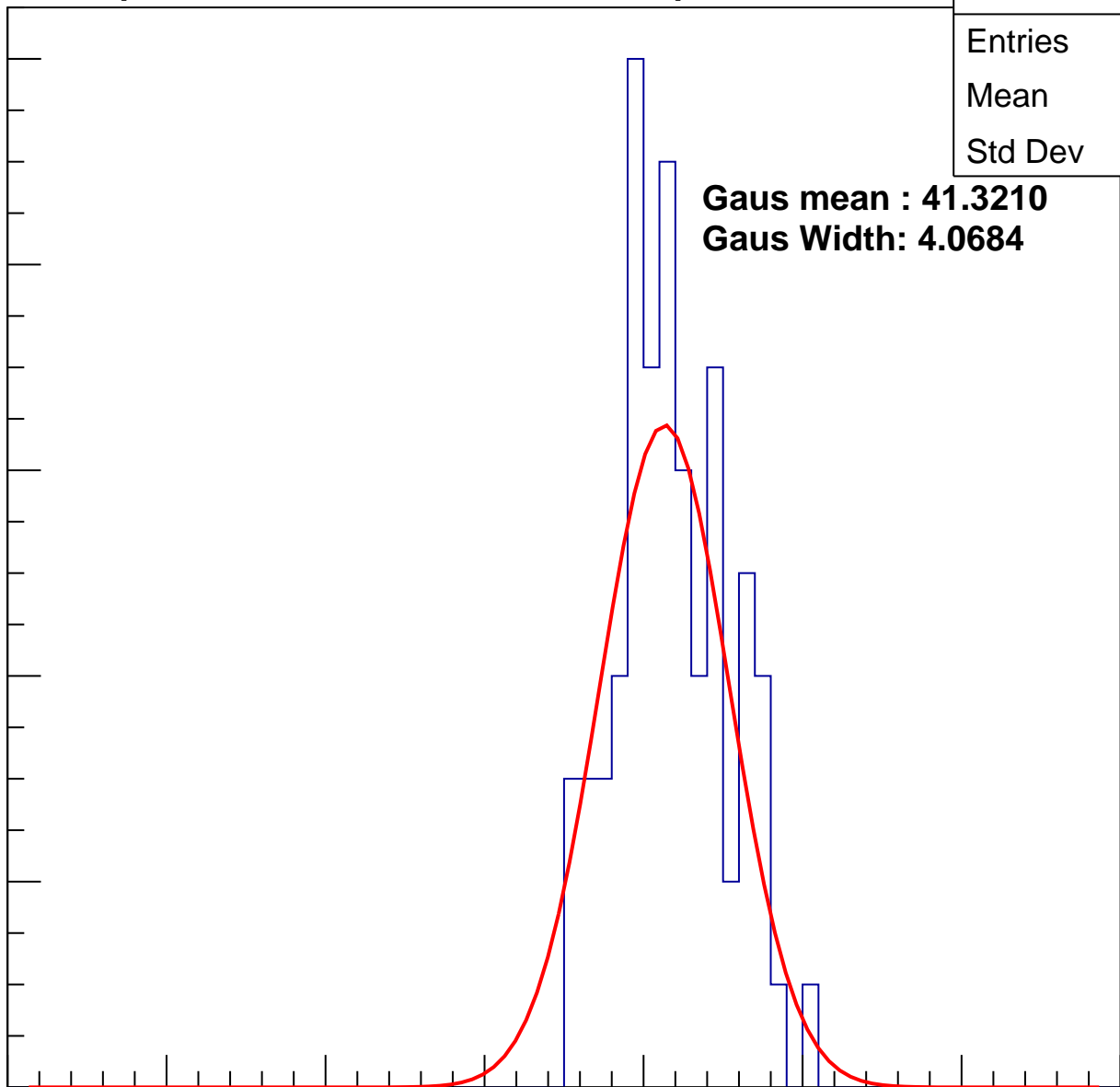
Gaus Width: 4.0684

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

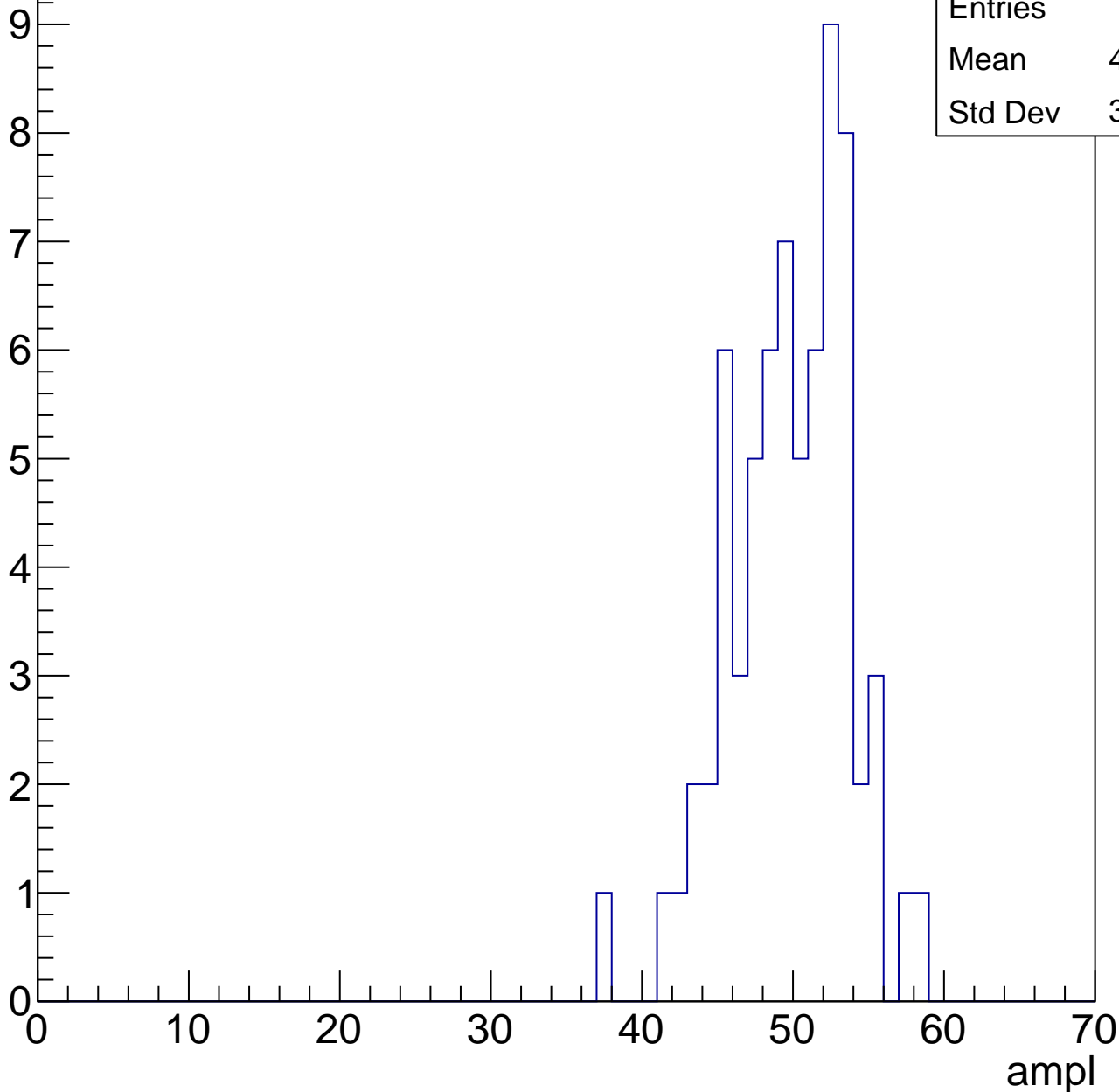


B1L103S, U17-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.33
Std Dev	3.922

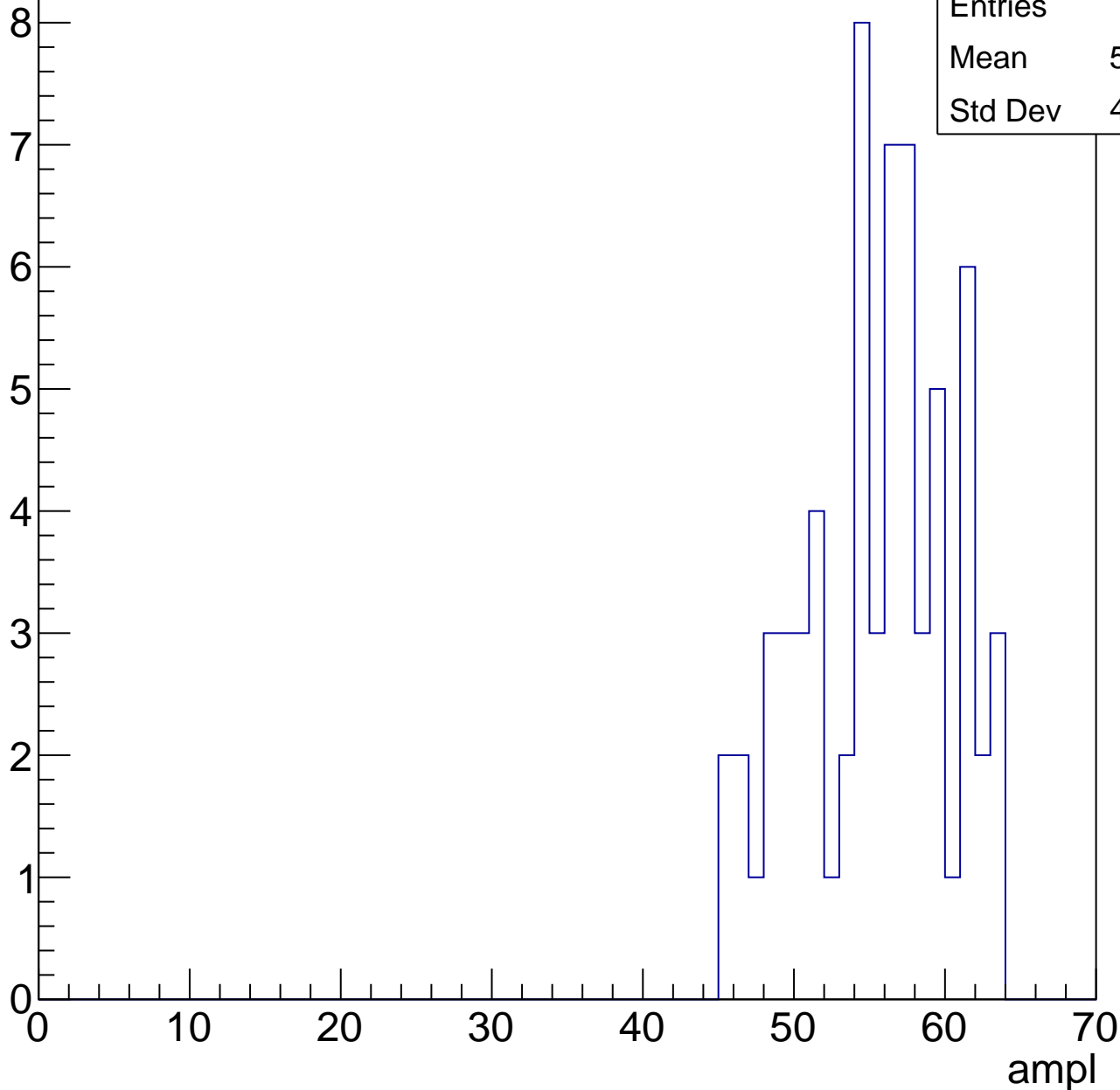


B1L103S, U17-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.97
Std Dev	4.796

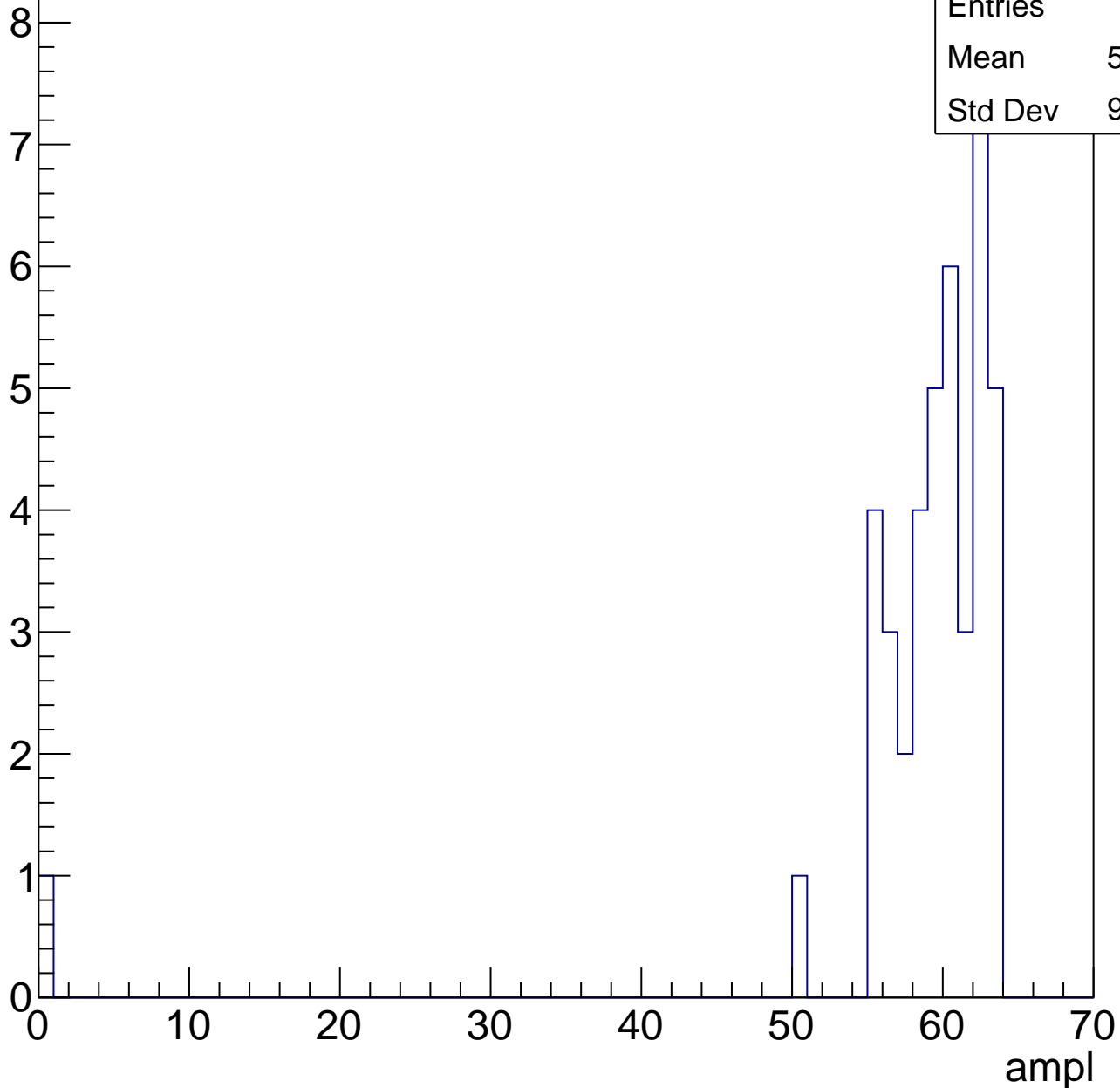


B1L103S, U17-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	57.93
Std Dev	9.495



B1L103S, U17-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

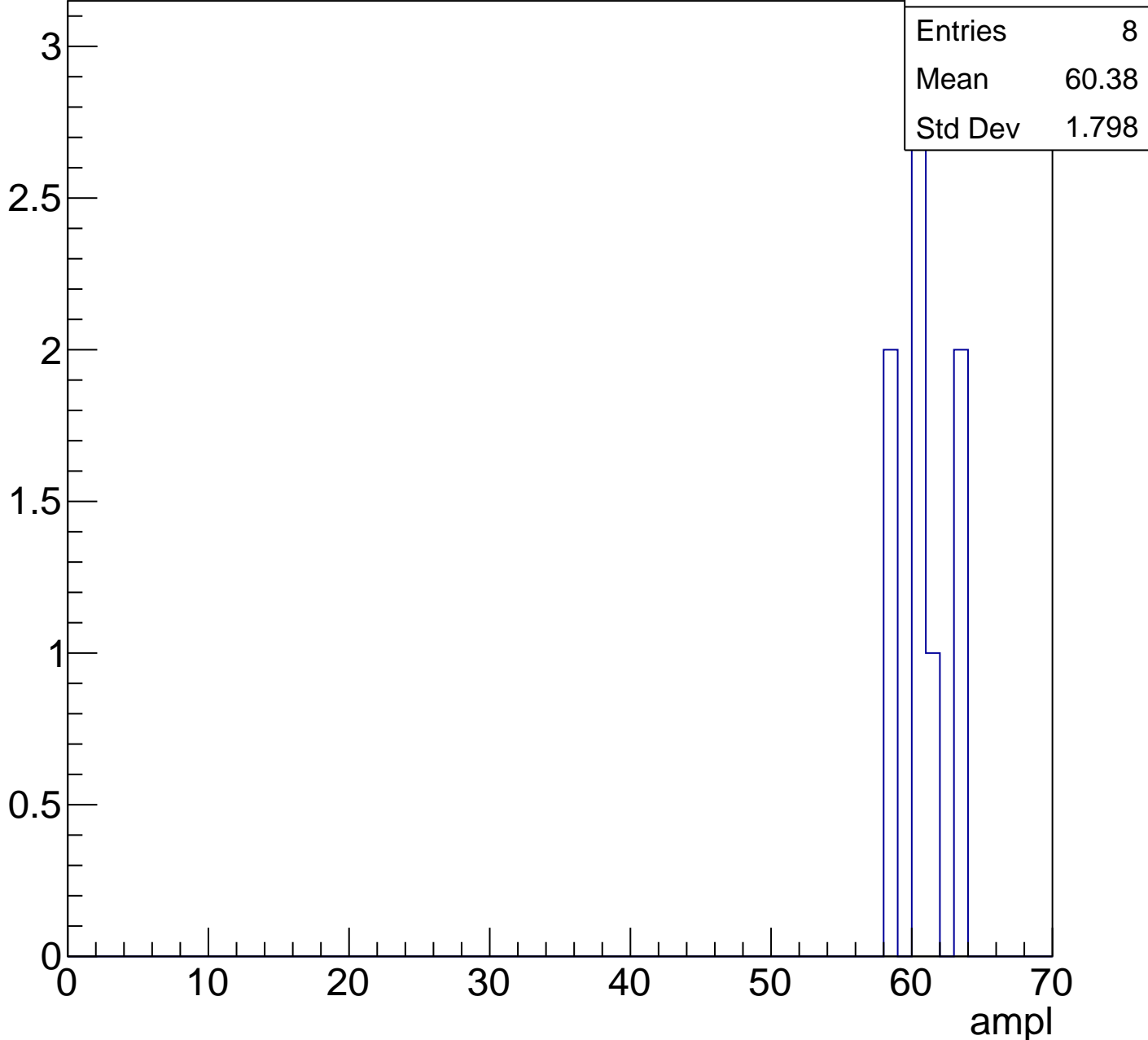
8

Mean

60.38

Std Dev

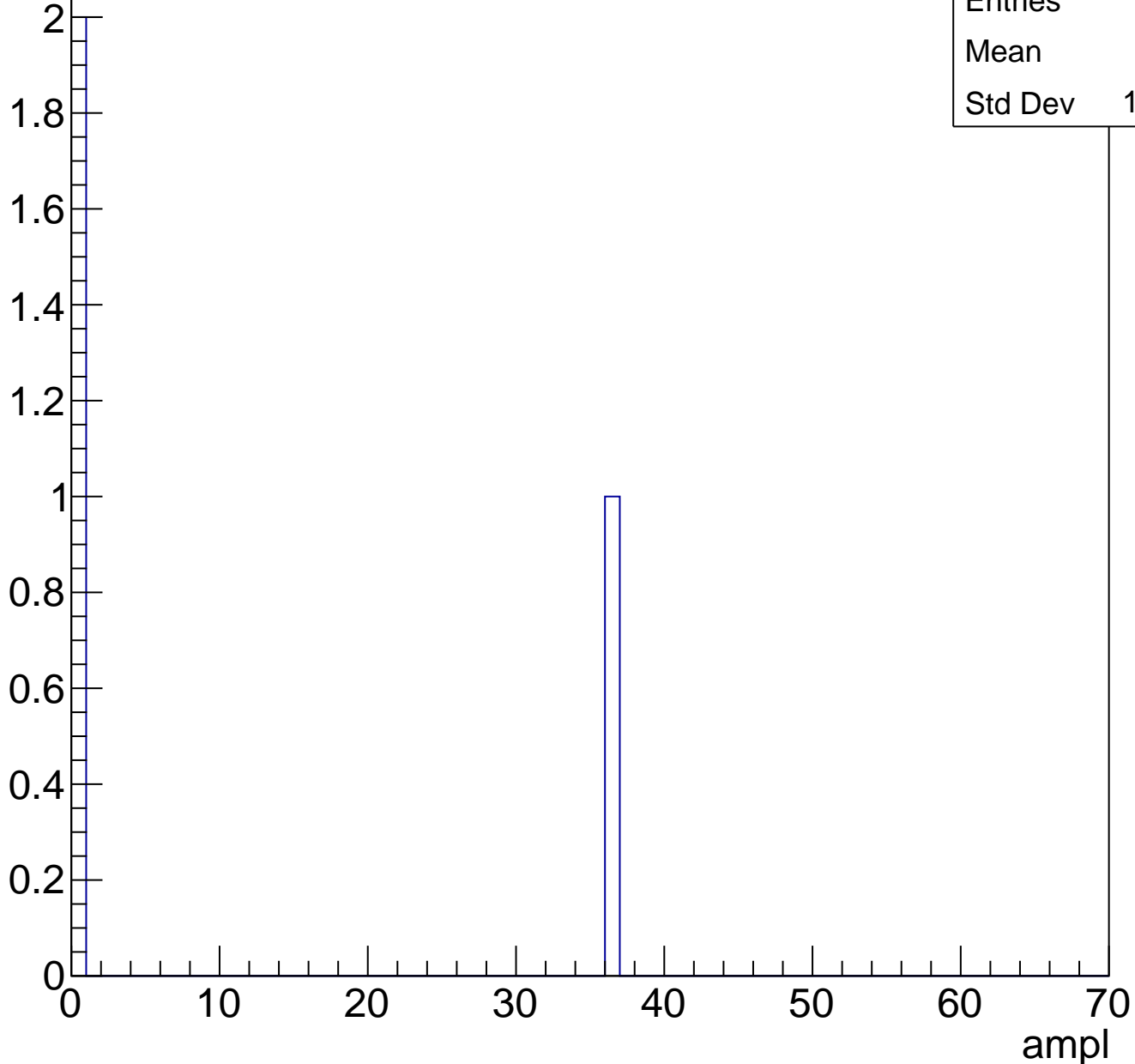
1.798



B1L103S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

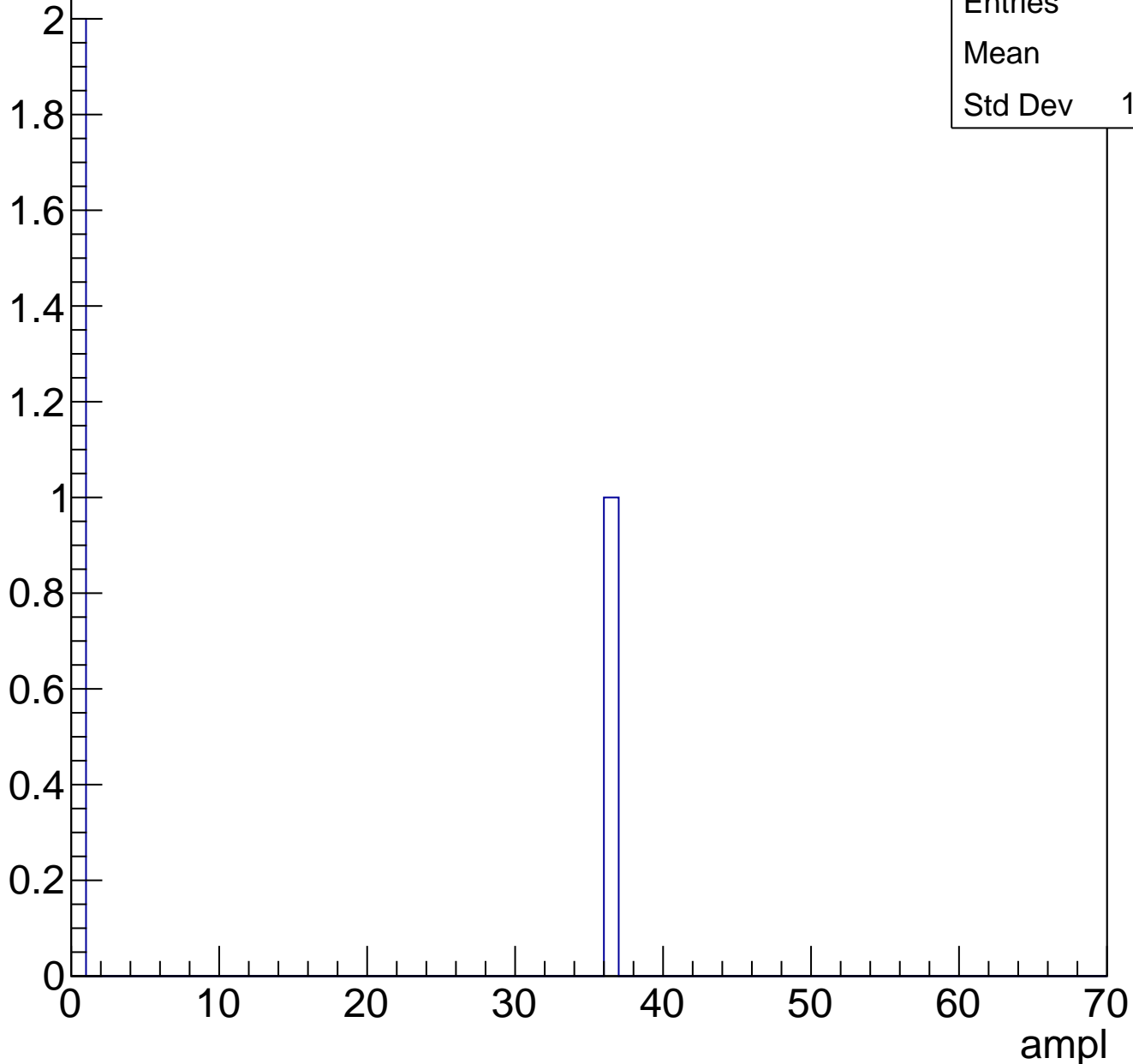


Entries	3
Mean	12
Std Dev	16.97

B1L103S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	12
Std Dev	16.97