

B1L000S, U20-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.86
Std Dev	11.21

Turn on : 27.7471

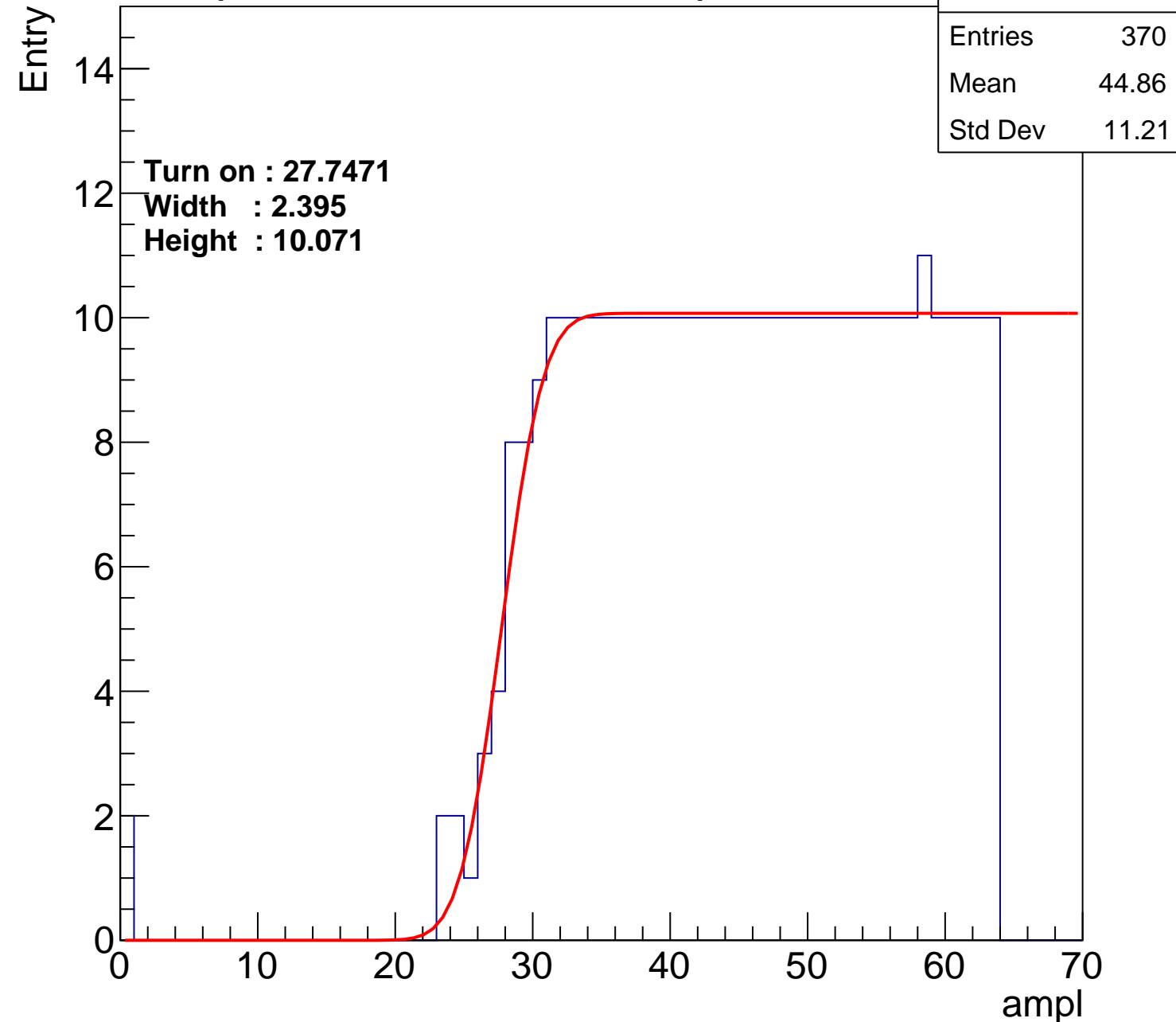
Width : 2.395

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.07
Std Dev	11.44

Turn on : 25.9046

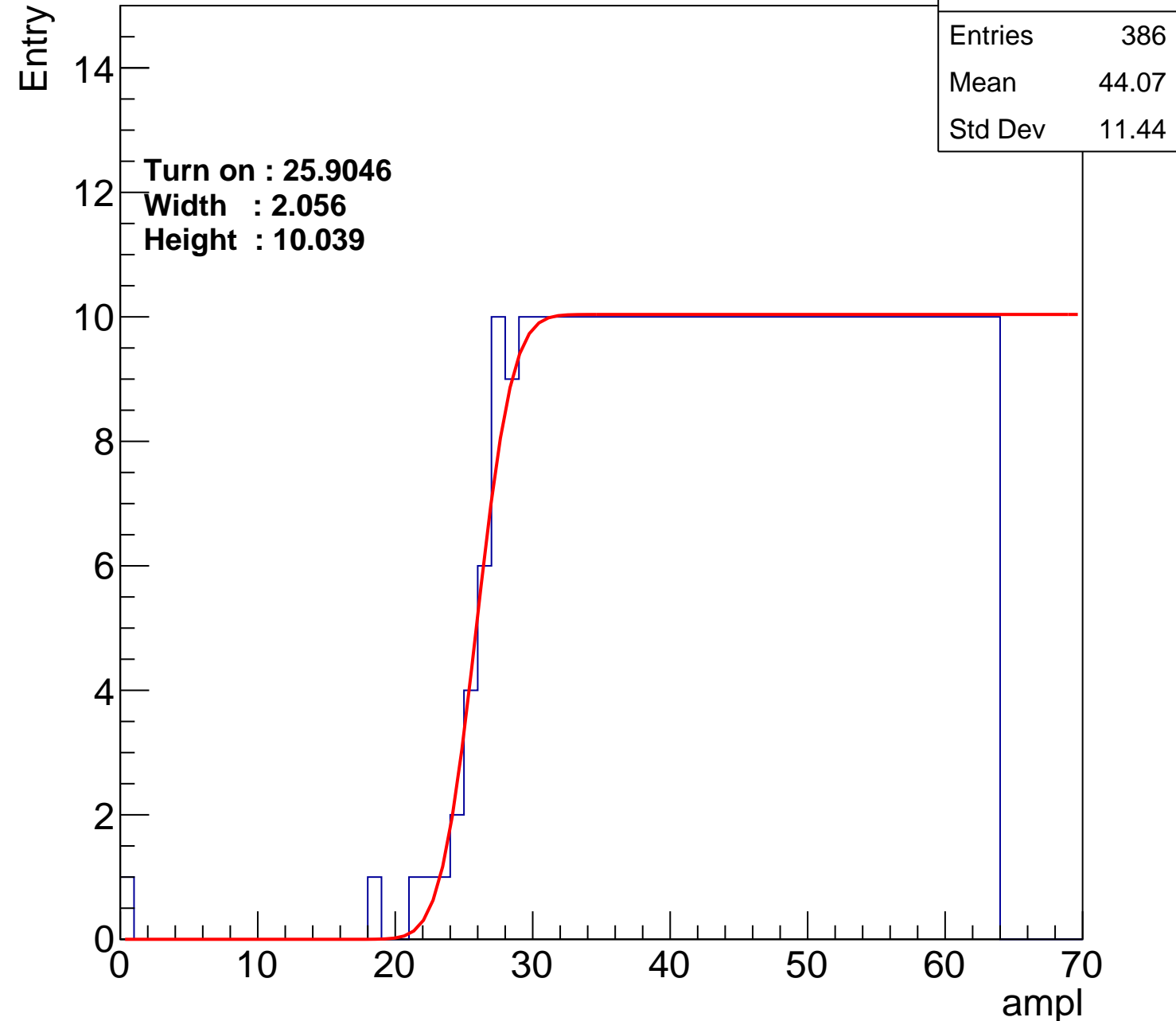
Width : 2.056

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 25.9385

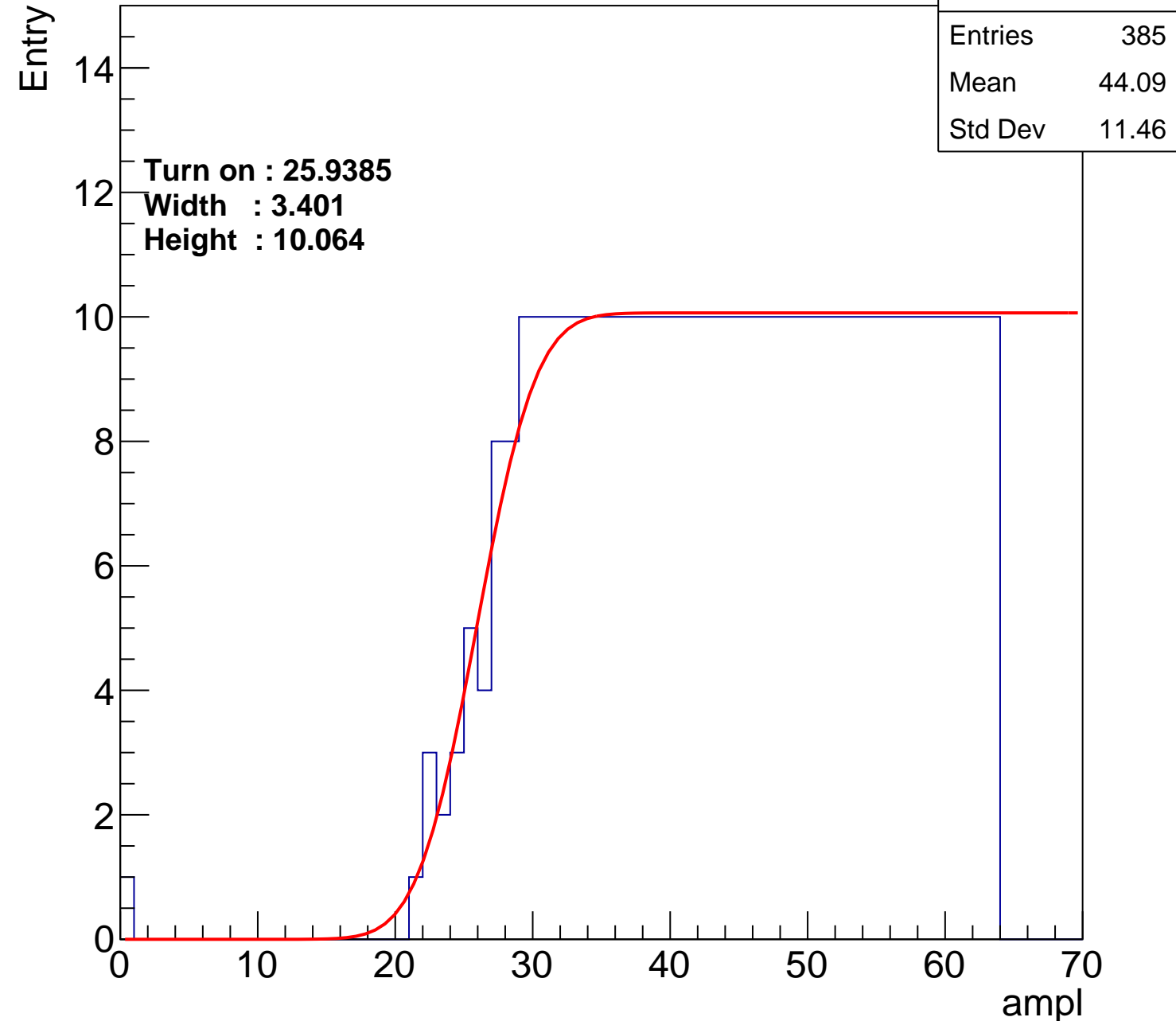
Width : 3.401

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 25.9727

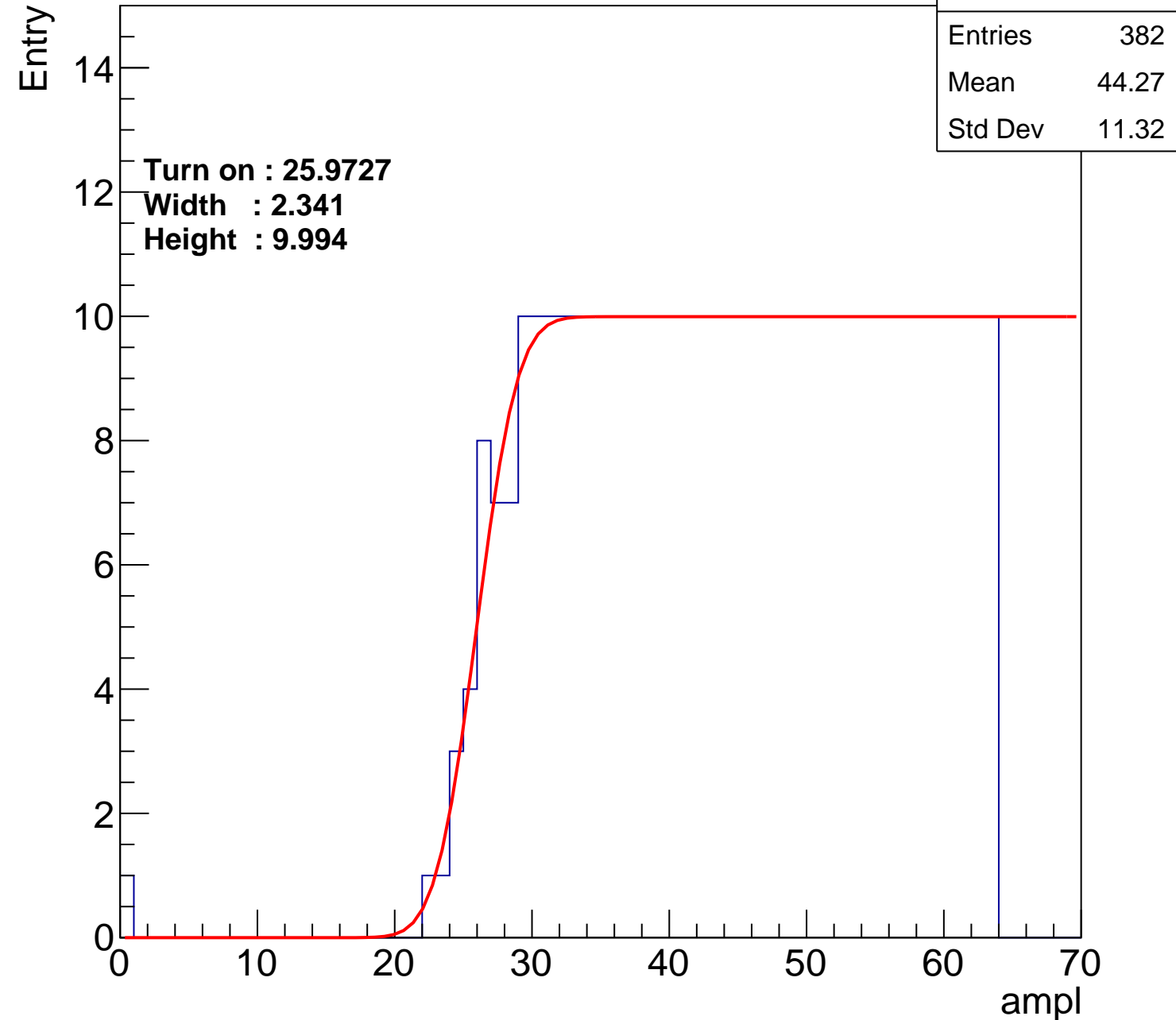
Width : 2.341

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch4

calib_packv5_042523_0143.root, FC#3, port B3

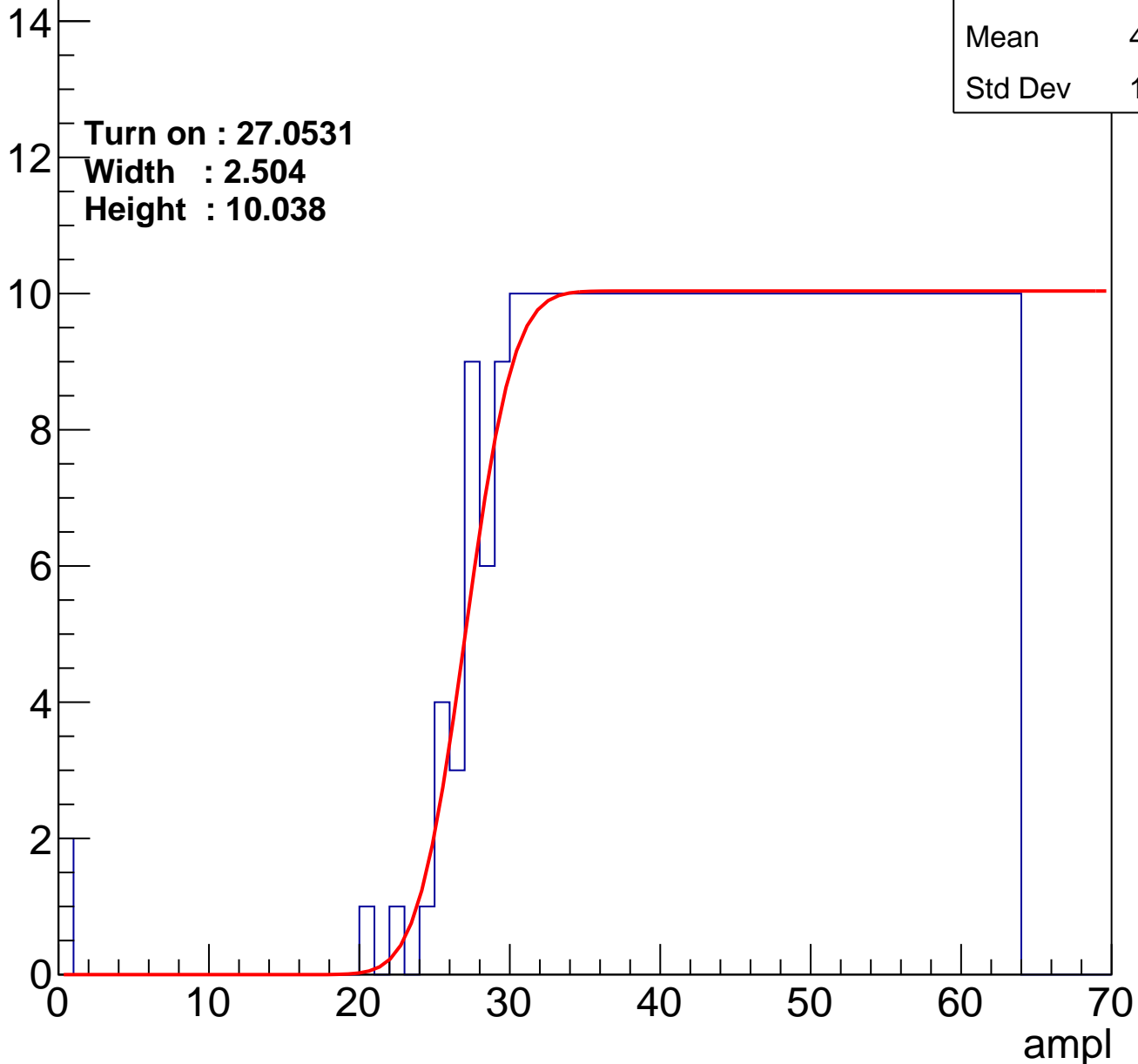
Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 27.0531

Width : 2.504

Height : 10.038

Entry



B1L000S, U20-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.37

Turn on : 26.8068

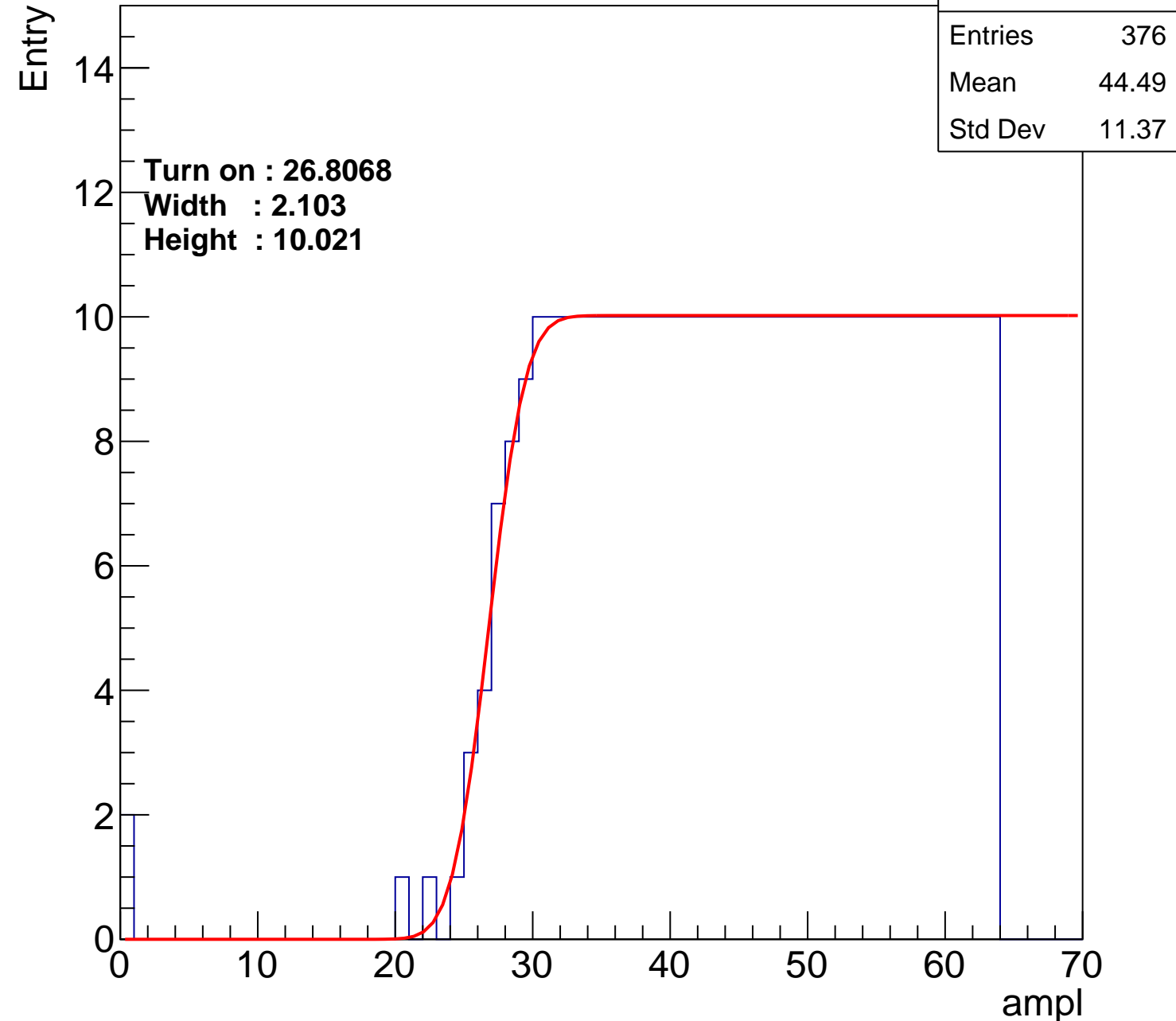
Width : 2.103

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.11
Std Dev	11.67

Turn on : 25.9466

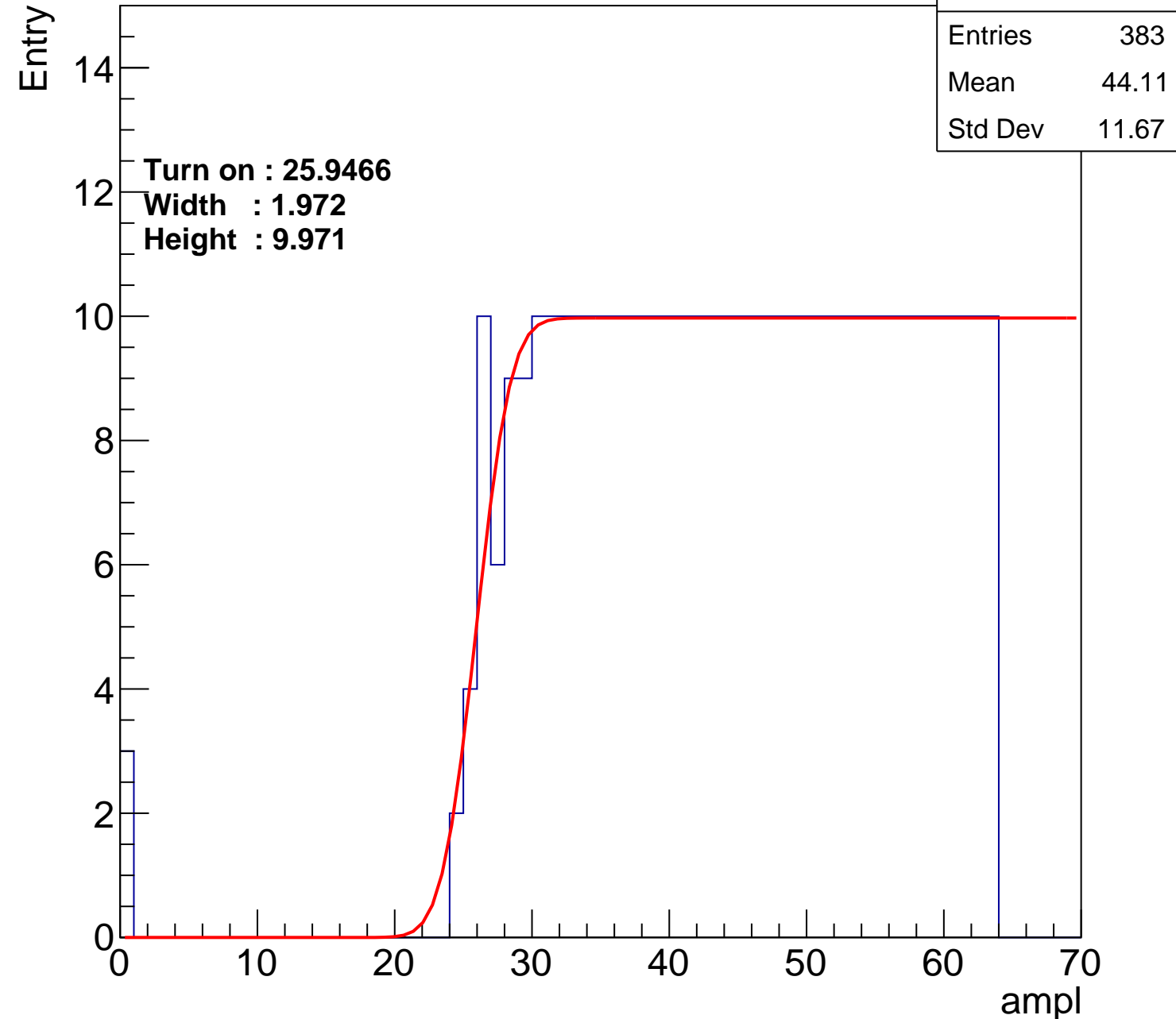
Width : 1.972

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch7

calib_packv5_042523_0143.root, FC#3, port B3

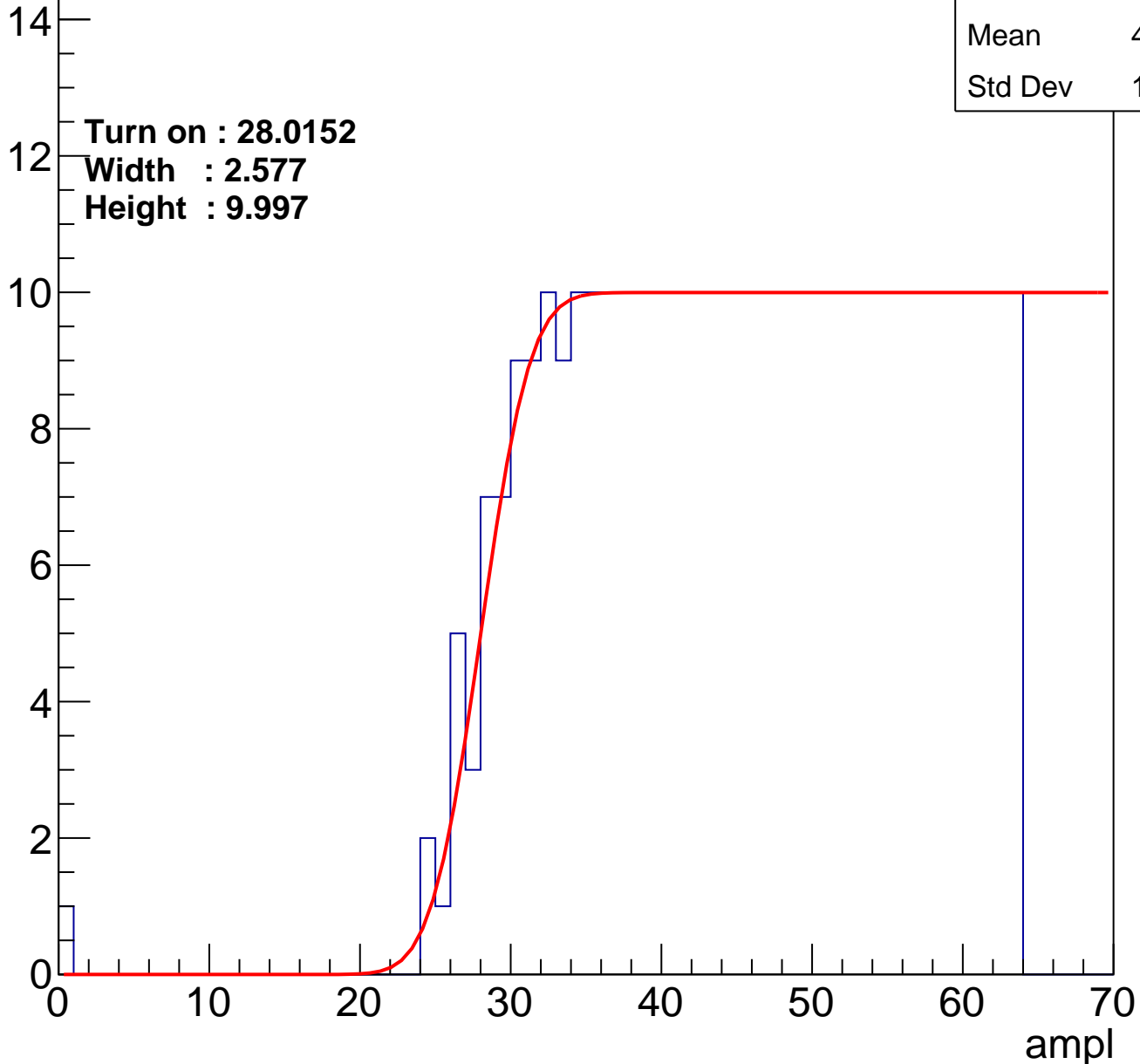
Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 28.0152

Width : 2.577

Height : 9.997

Entry



B1L000S, U20-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.6230

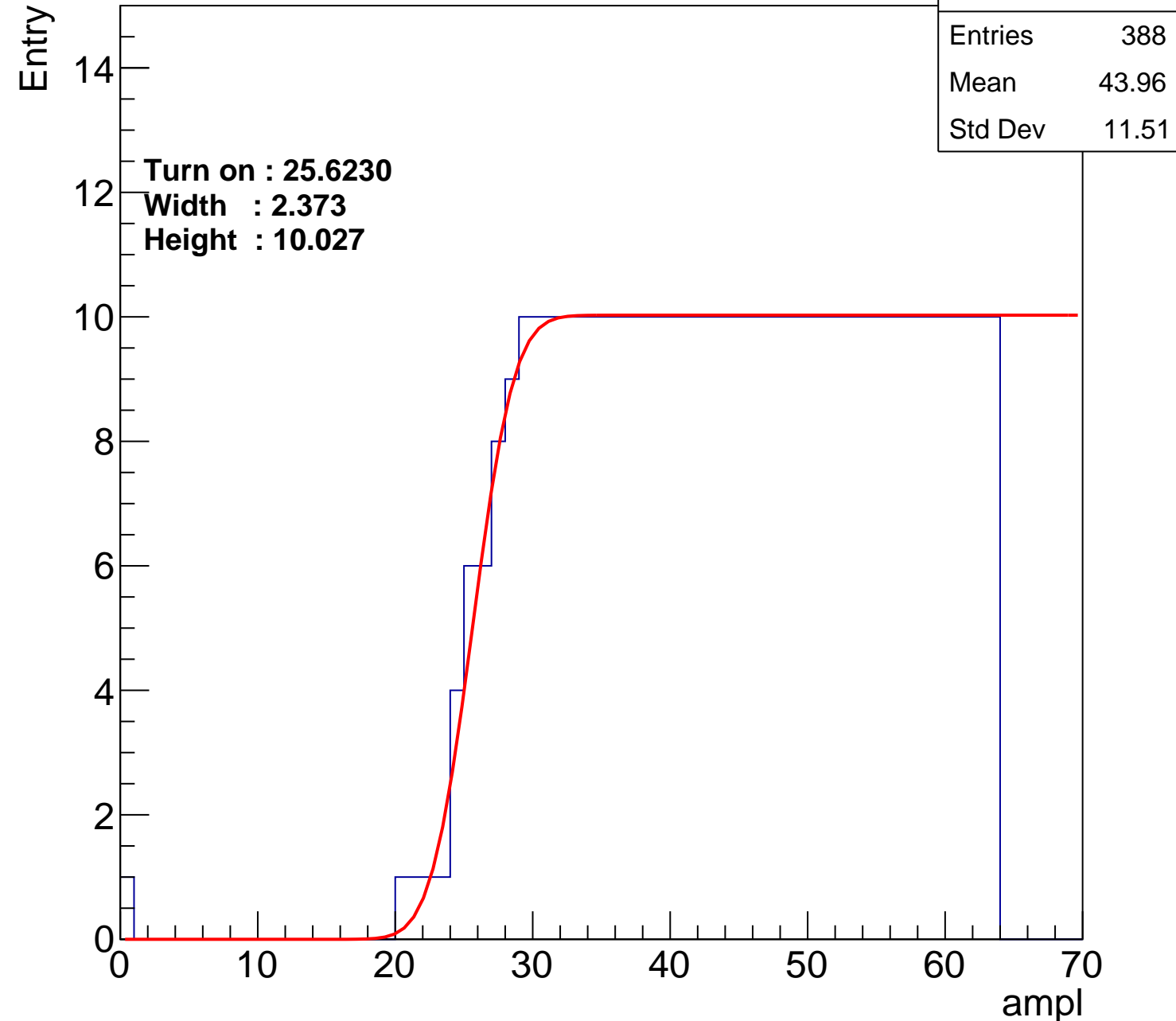
Width : 2.373

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch9

calib_packv5_042523_0143.root, FC#3, port B3

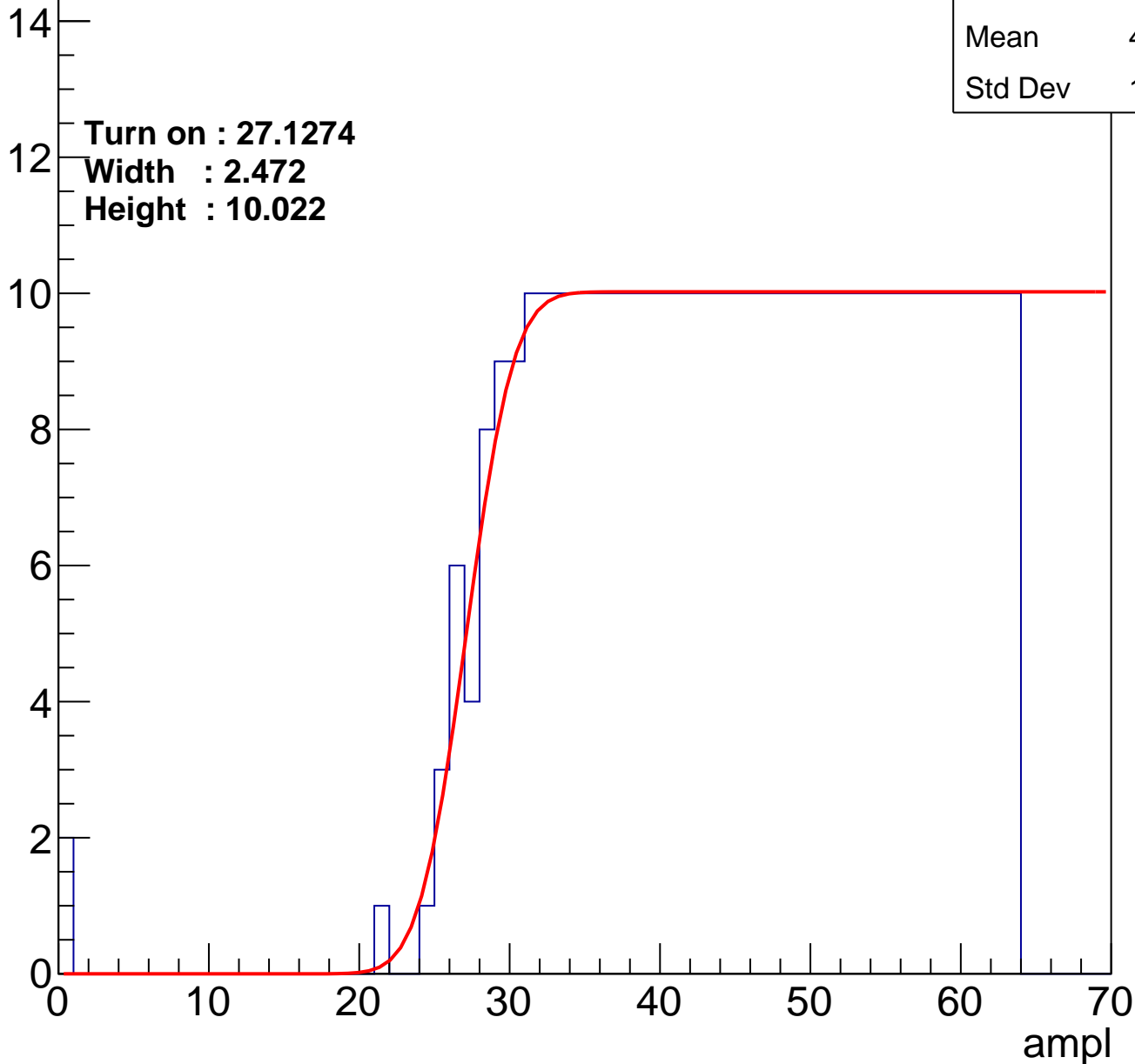
Entry

Entries	373
Mean	44.64
Std Dev	11.29

Turn on : 27.1274

Width : 2.472

Height : 10.022



B1L000S, U20-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 25.4907

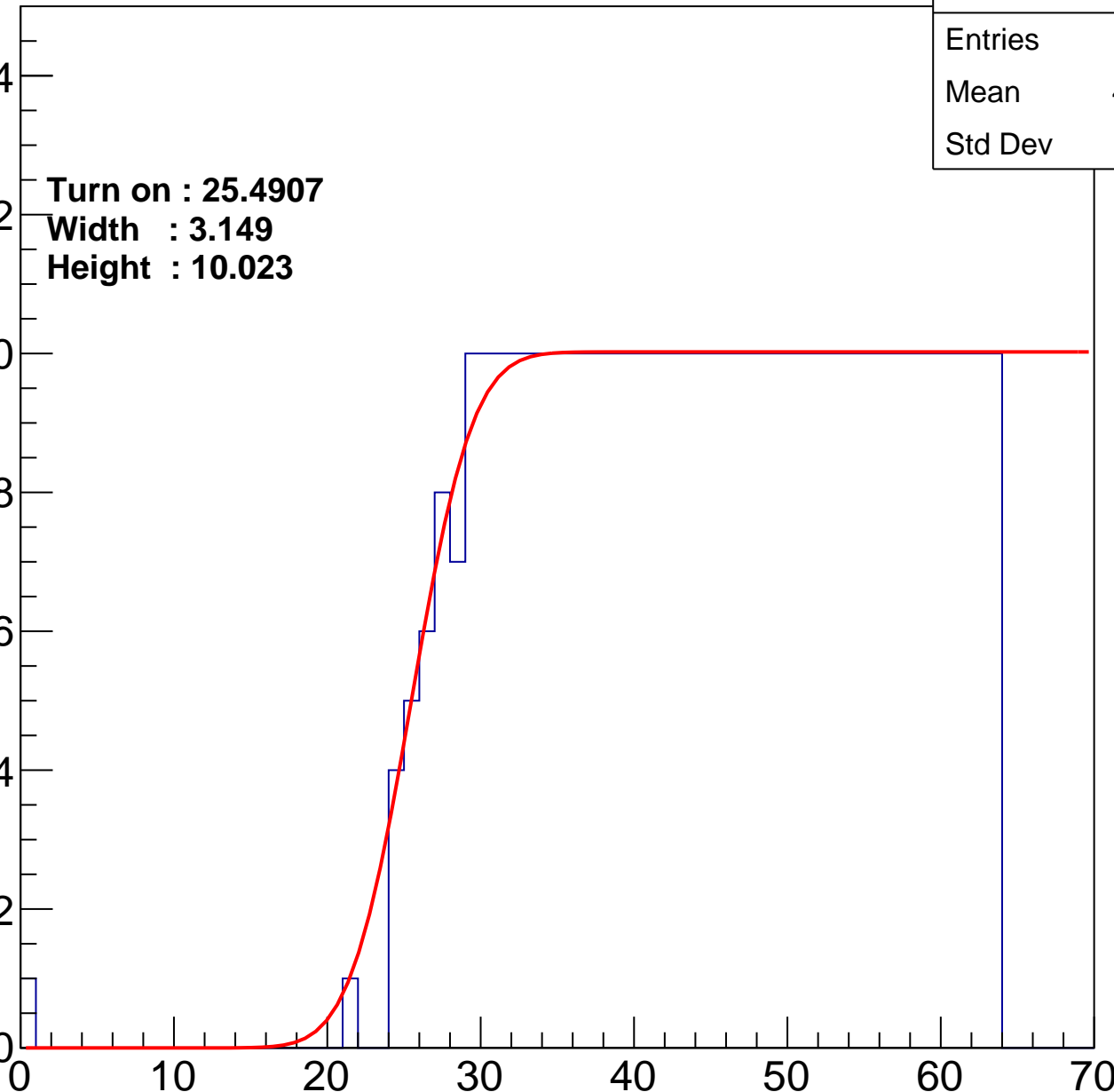
Width : 3.149

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	45.02
Std Dev	10.94

Turn on : 27.4836

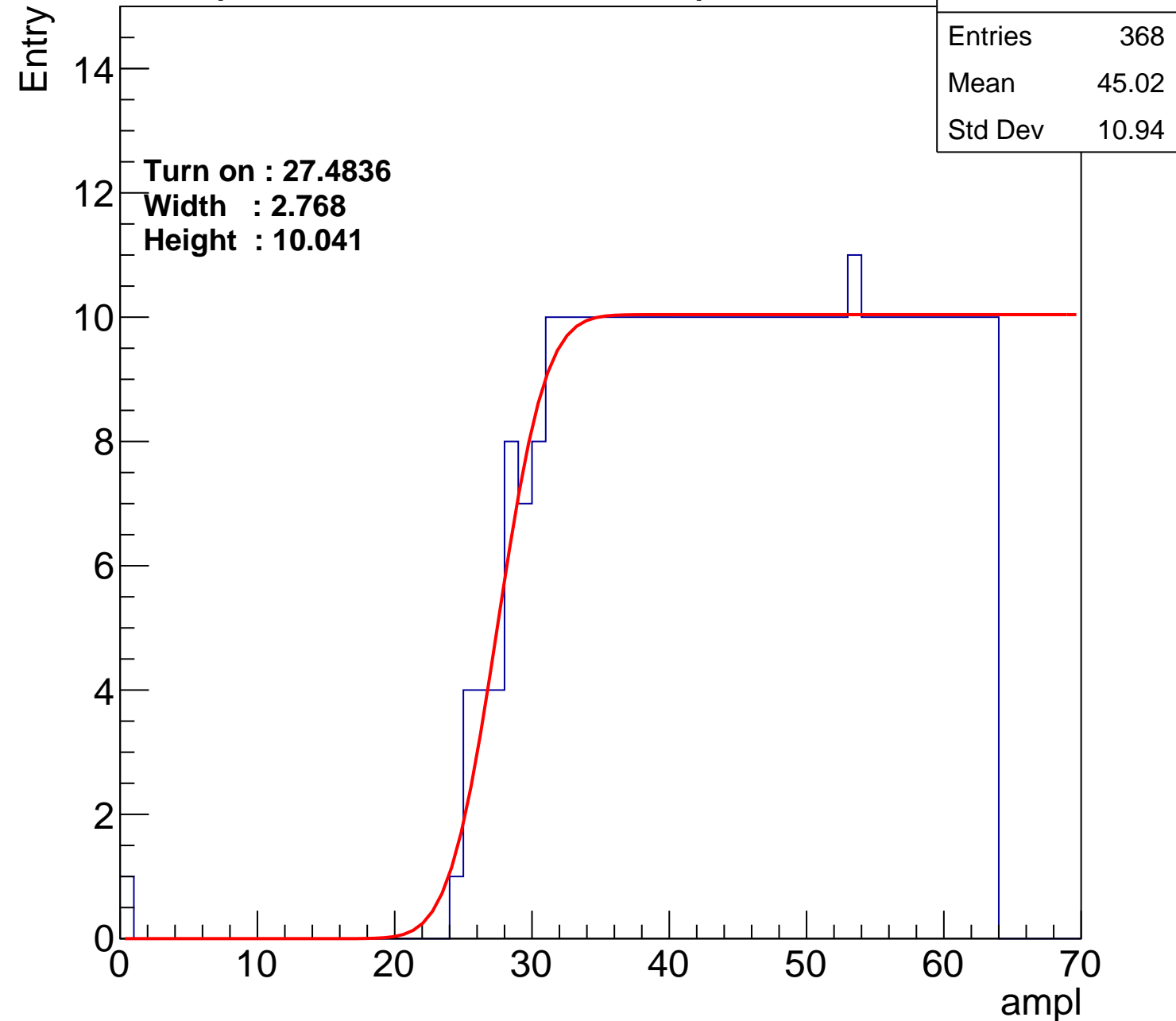
Width : 2.768

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch12

calib_packv5_042523_0143.root, FC#3, port B3

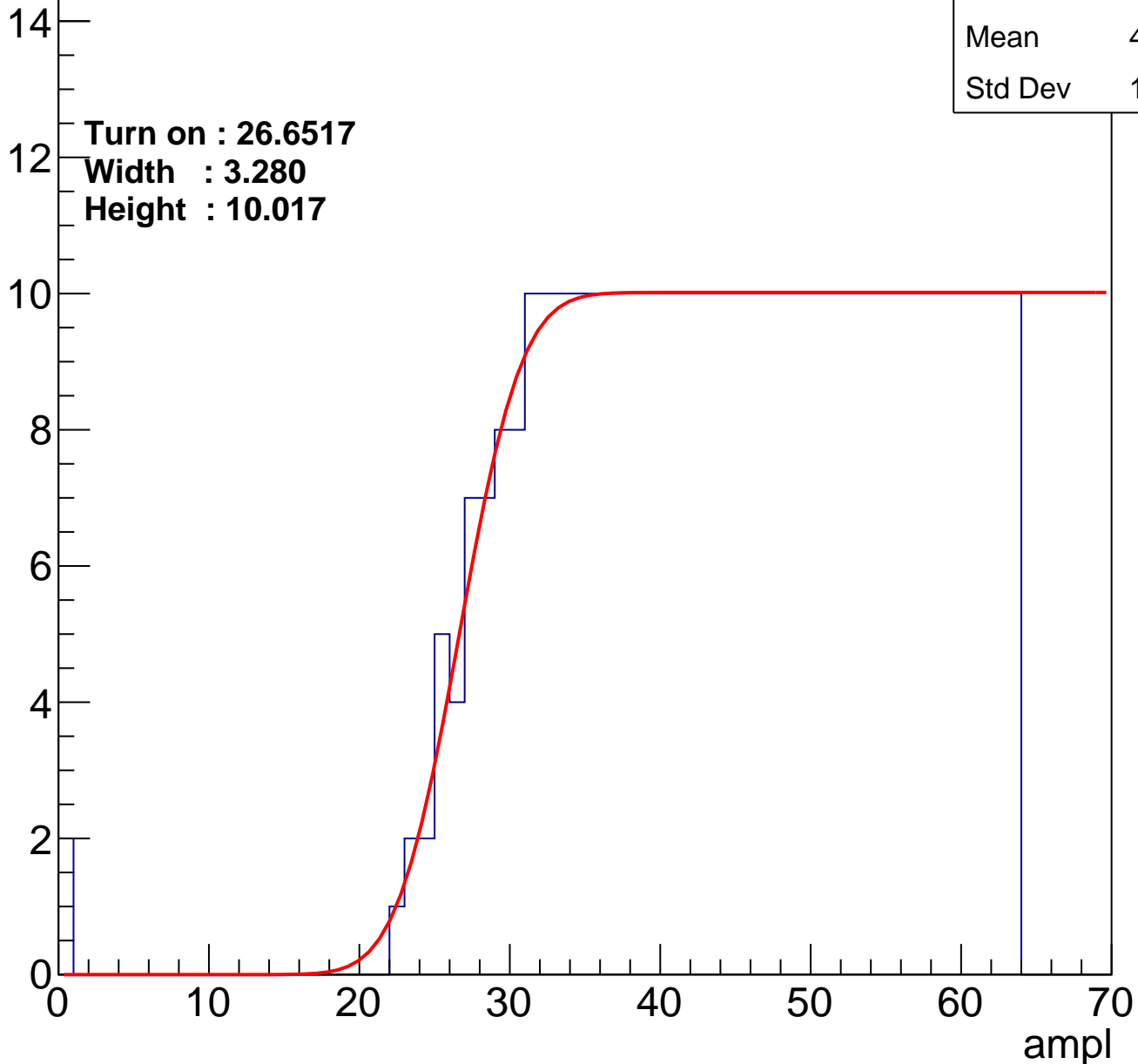
Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.6517

Width : 3.280

Height : 10.017

Entry



B1L000S, U20-ch13

calib_packv5_042523_0143.root, FC#3, port B3

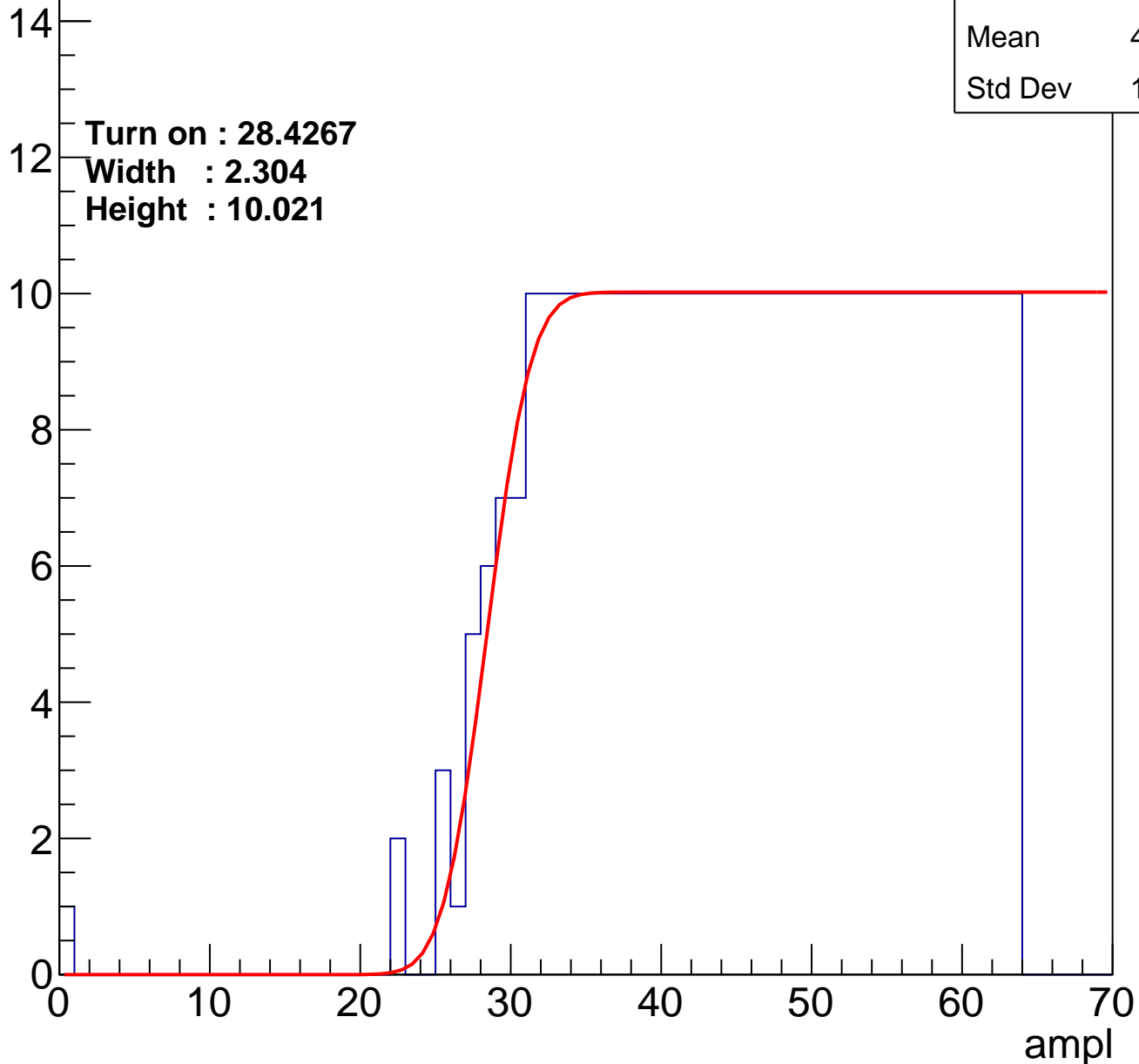
Entries	362
Mean	45.22
Std Dev	10.85

Turn on : 28.4267

Width : 2.304

Height : 10.021

Entry



B1L000S, U20-ch14

calib_packv5_042523_0143.root, FC#3, port B3

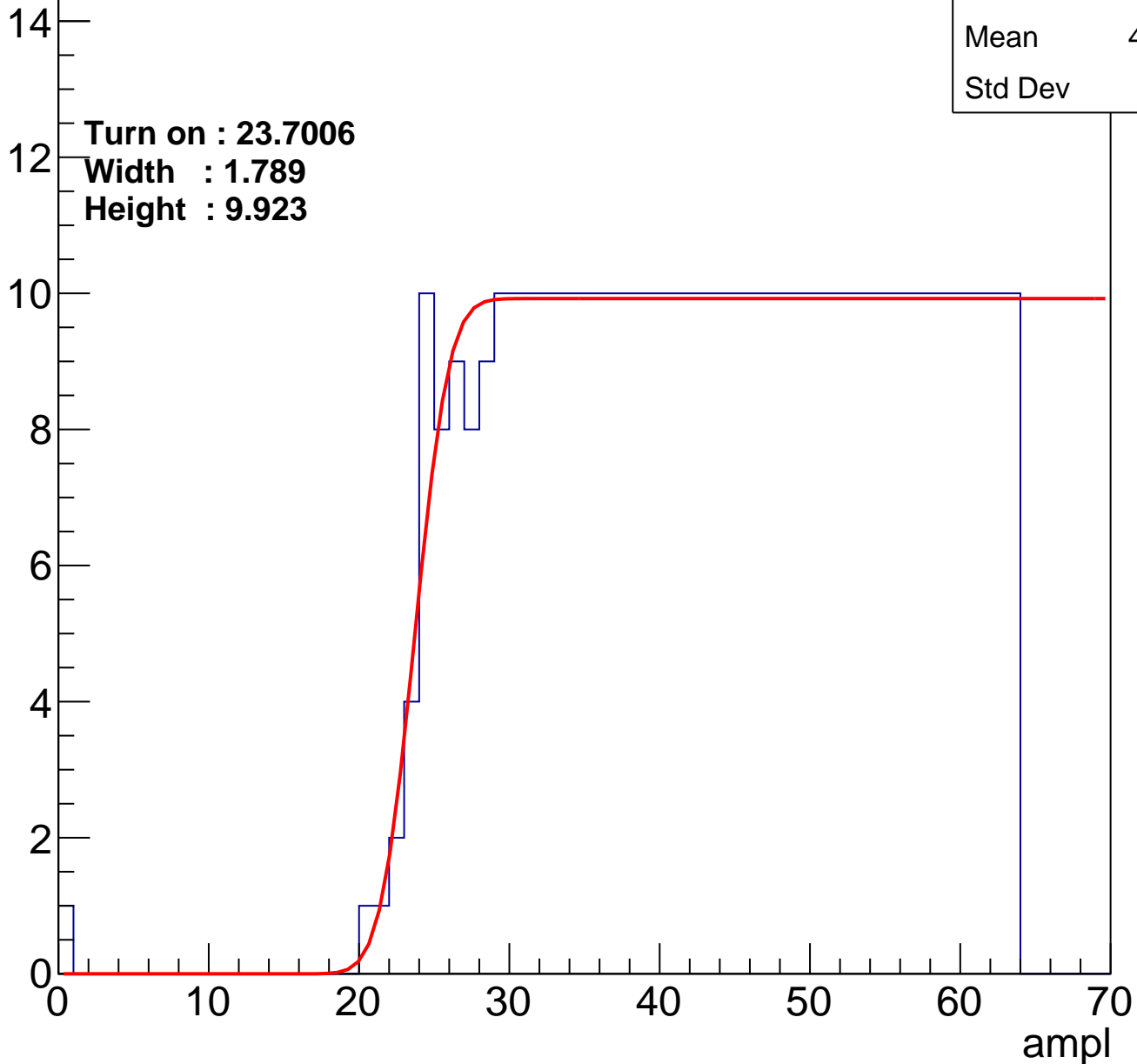
Entries	403
Mean	43.22
Std Dev	11.9

Turn on : 23.7006

Width : 1.789

Height : 9.923

Entry



B1L000S, U20-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.2913

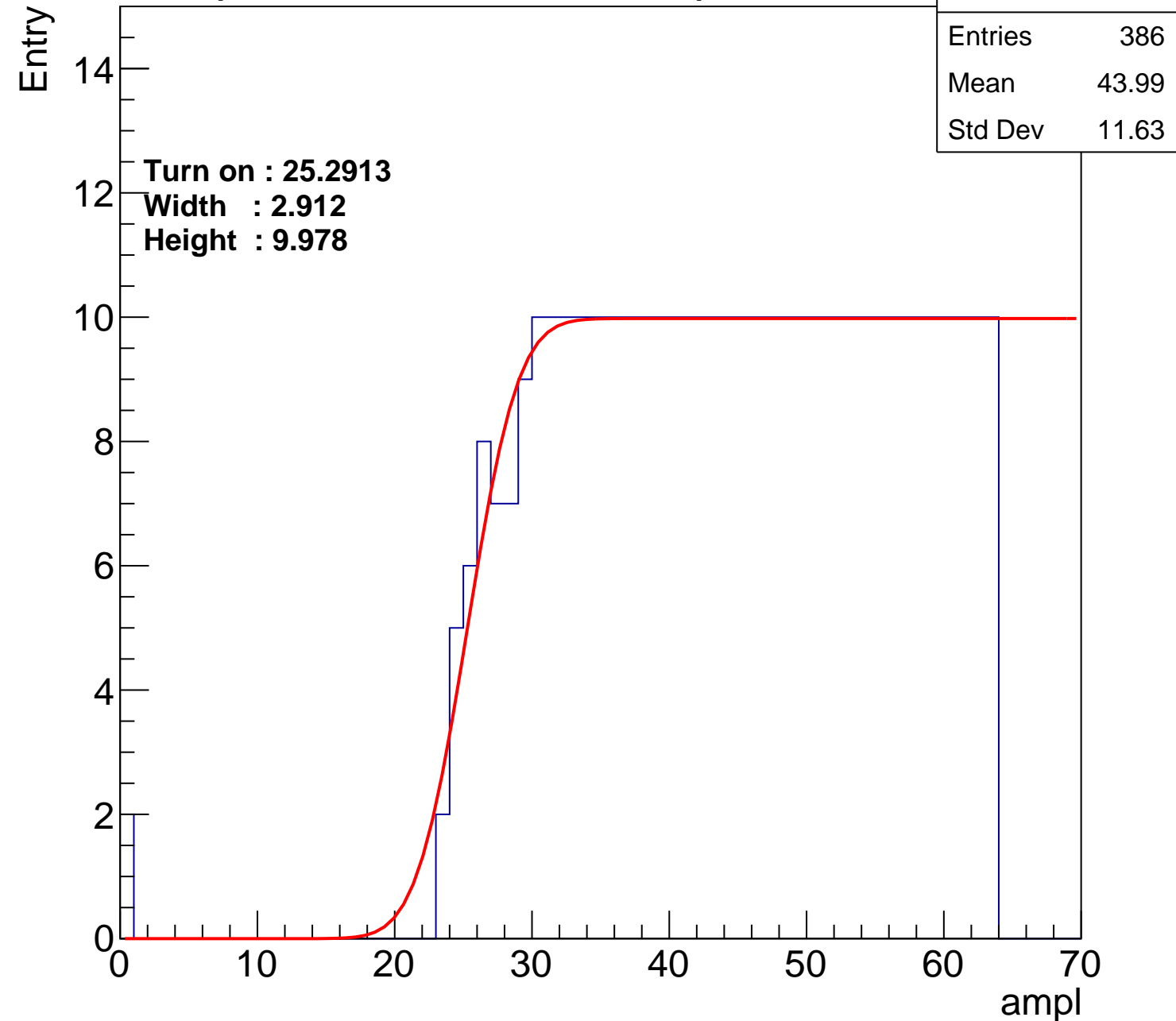
Width : 2.912

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch16

calib_packv5_042523_0143.root, FC#3, port B3

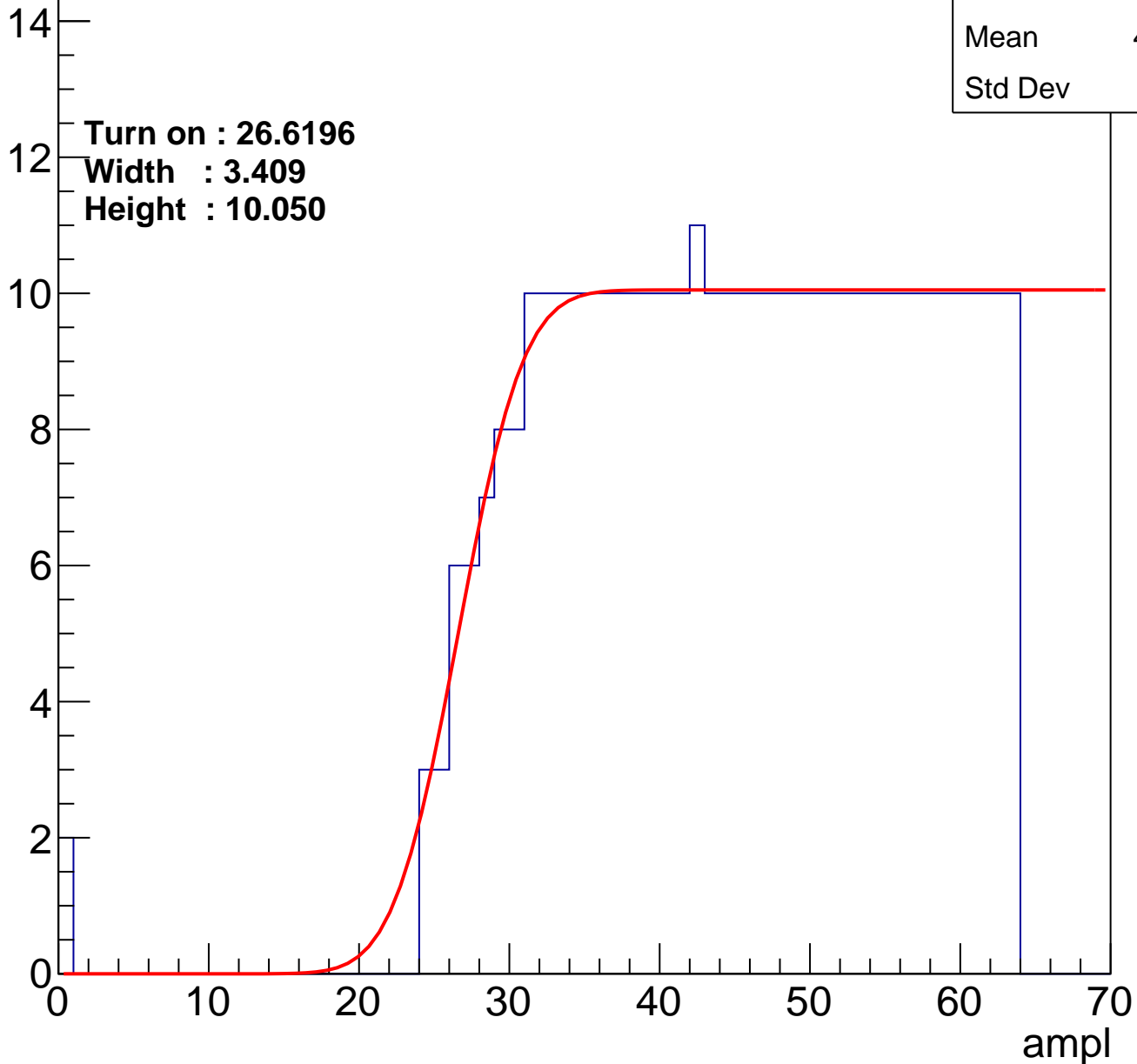
Entries	374
Mean	44.61
Std Dev	11.3

Turn on : 26.6196

Width : 3.409

Height : 10.050

Entry



B1L000S, U20-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.7499

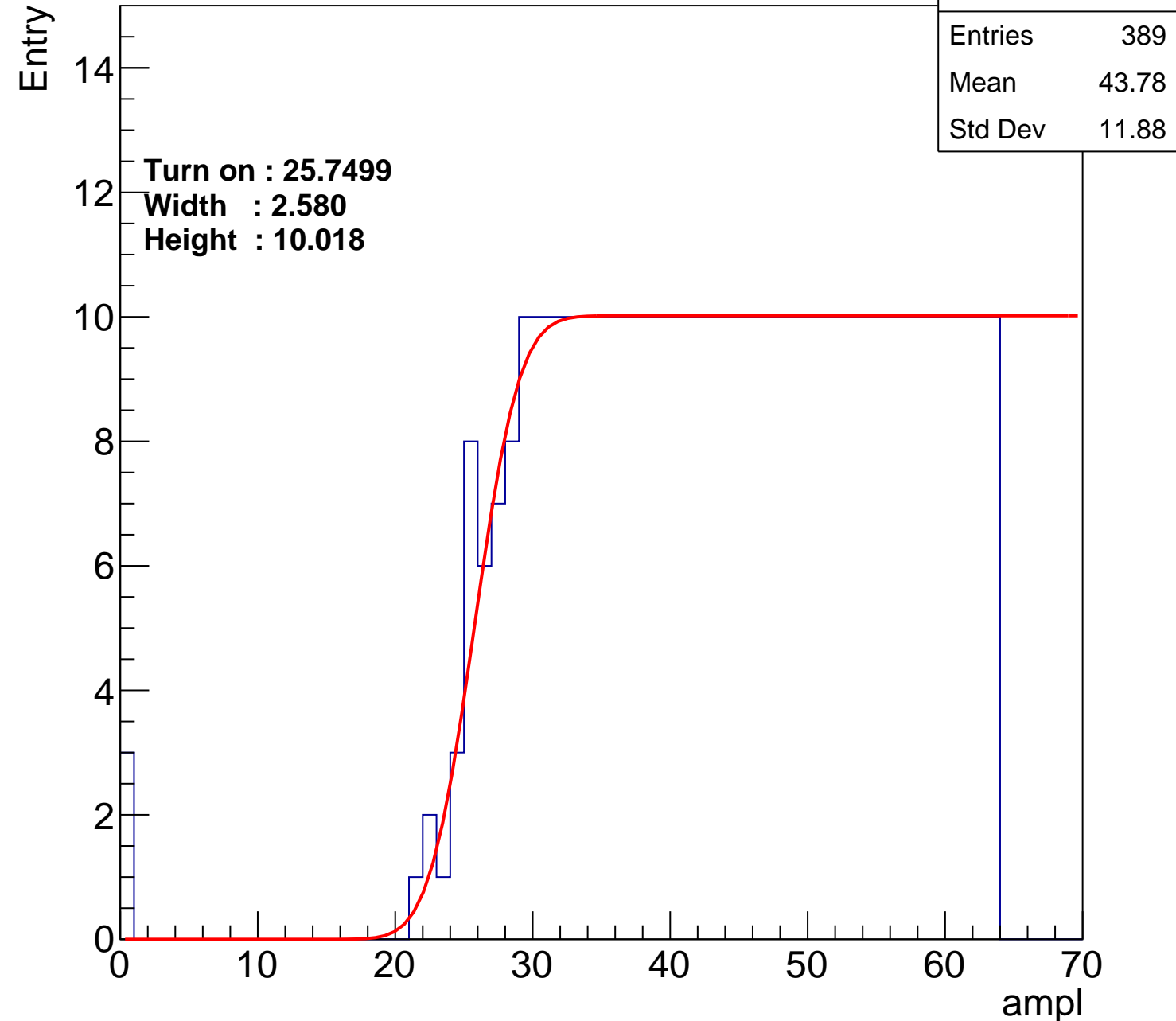
Width : 2.580

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.22
Std Dev	11.34

Turn on : 26.4488

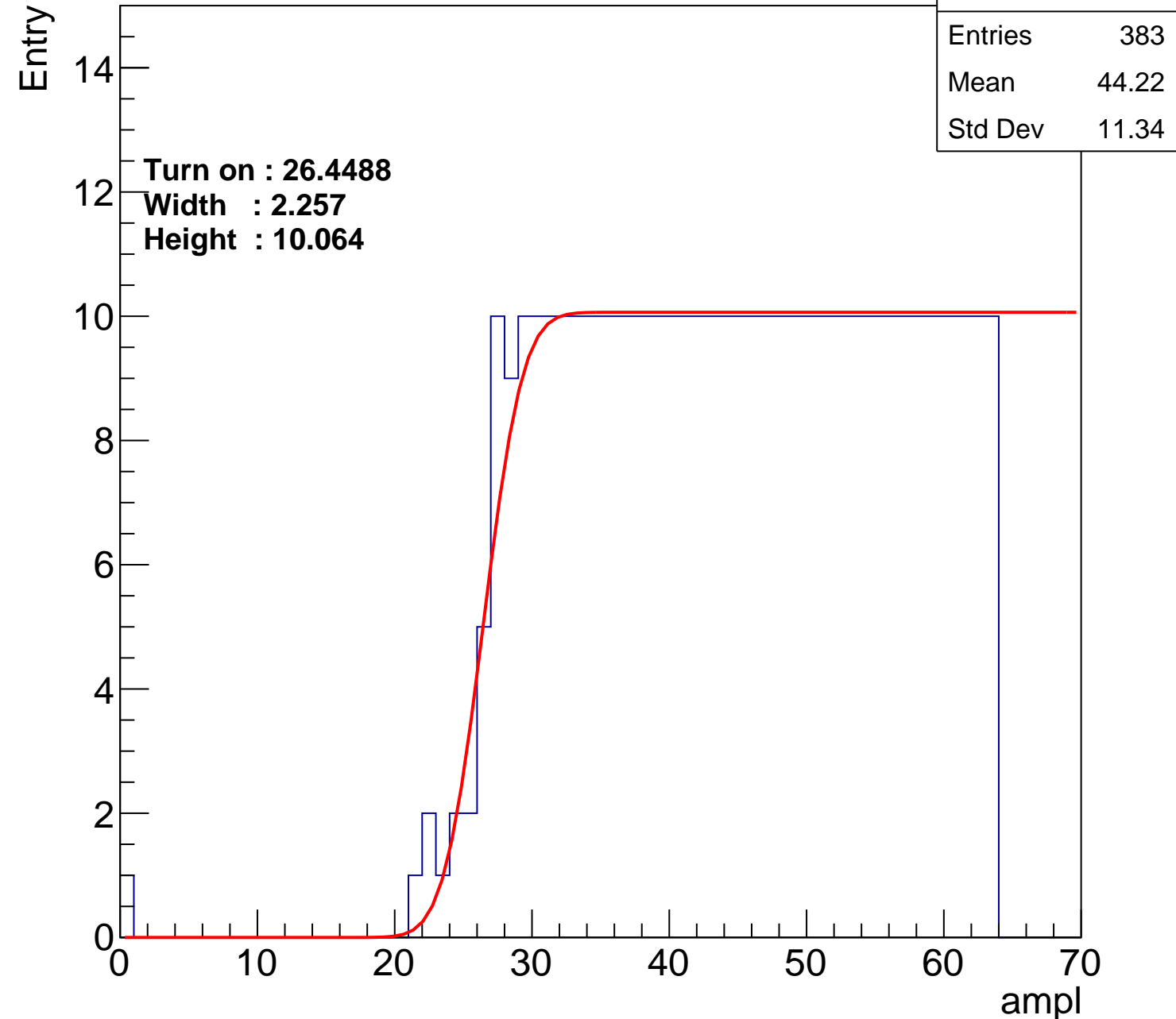
Width : 2.257

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch19

calib_packv5_042523_0143.root, FC#3, port B3

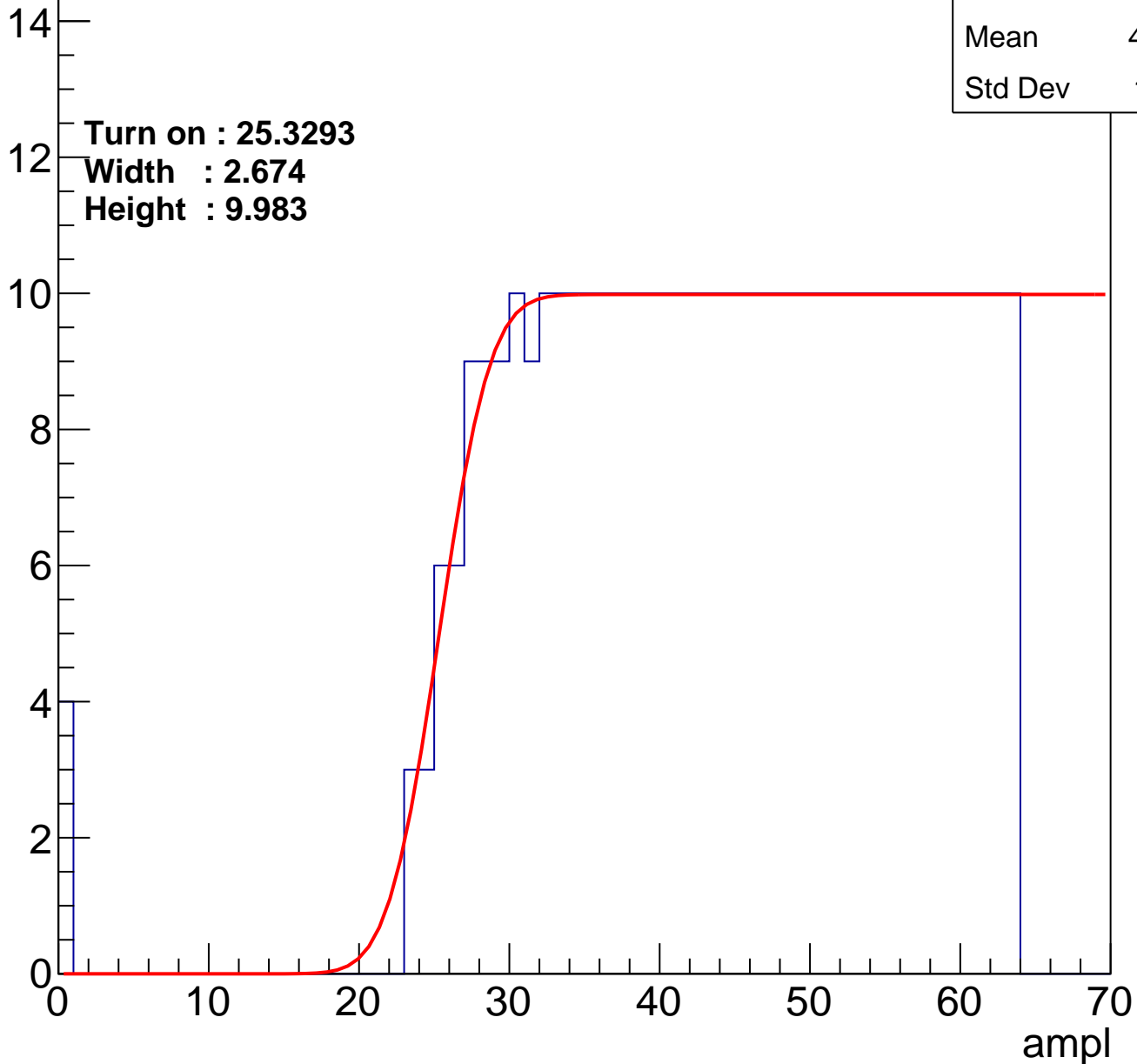
Entries	388
Mean	43.77
Std Dev	12.01

Turn on : 25.3293

Width : 2.674

Height : 9.983

Entry



B1L000S, U20-ch20

calib_packv5_042523_0143.root, FC#3, port B3

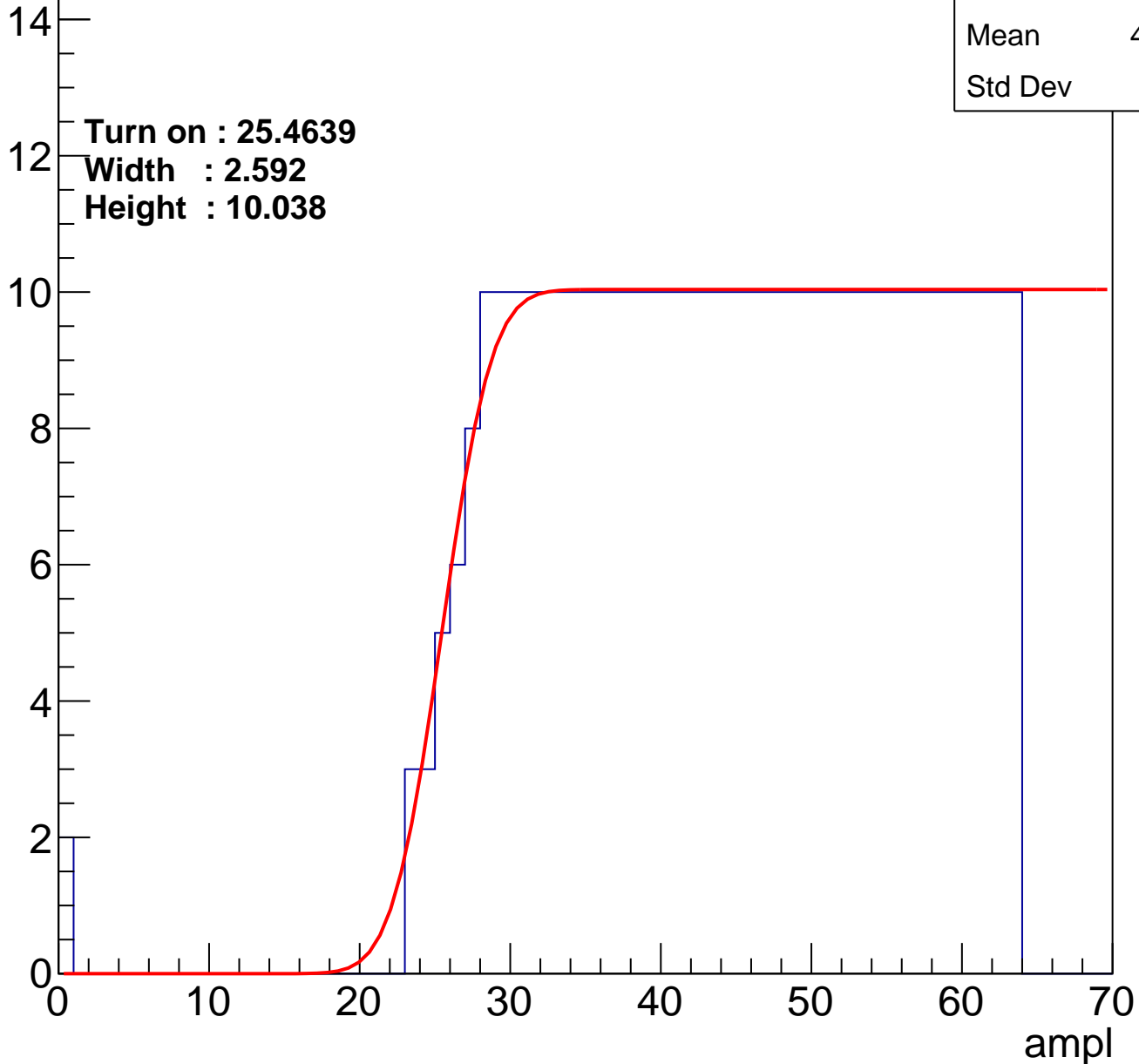
Entries	387
Mean	43.97
Std Dev	11.6

Turn on : 25.4639

Width : 2.592

Height : 10.038

Entry



B1L000S, U20-ch21

calib_packv5_042523_0143.root, FC#3, port B3

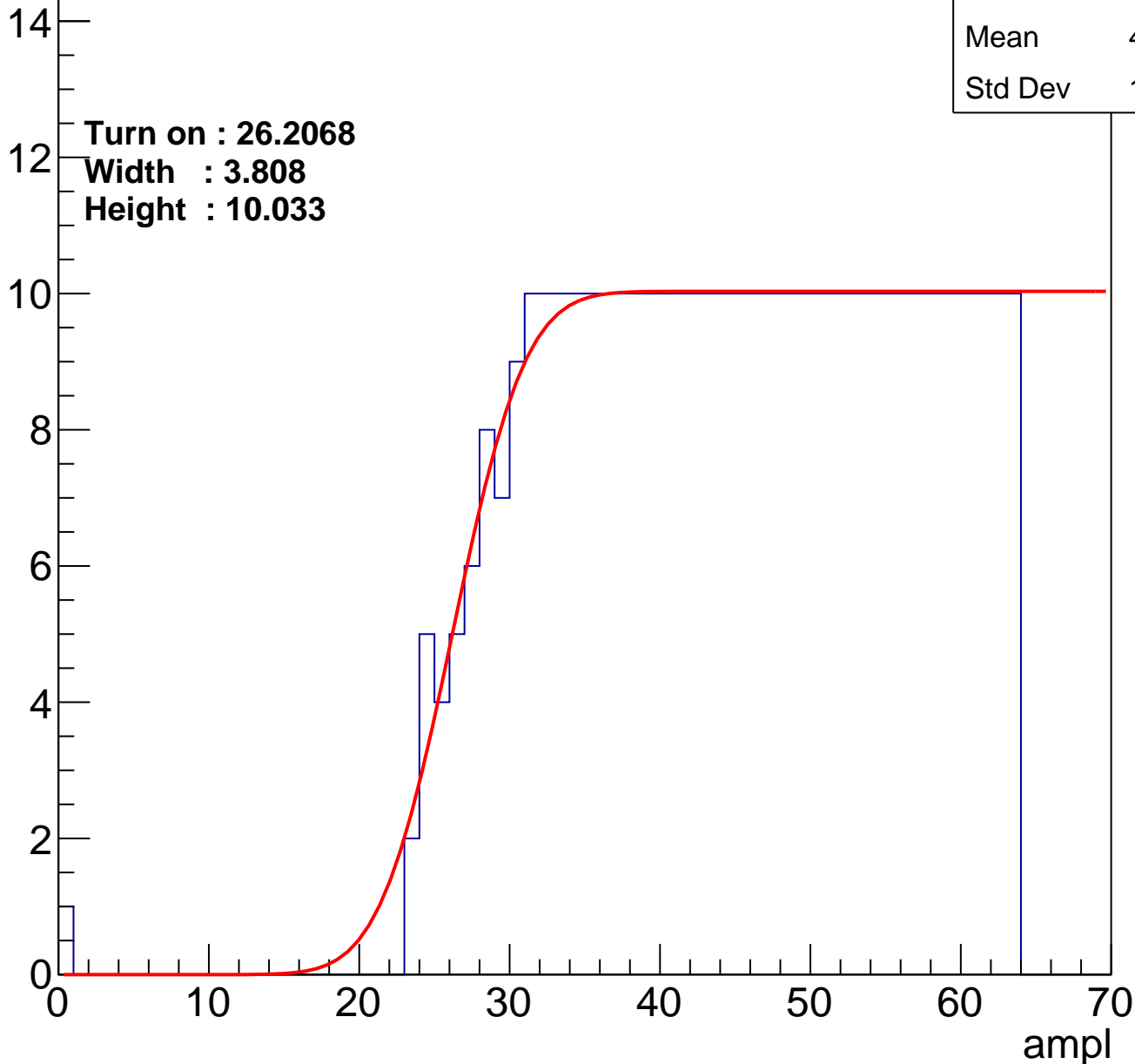
Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.2068

Width : 3.808

Height : 10.033

Entry



B1L000S, U20-ch22

calib_packv5_042523_0143.root, FC#3, port B3

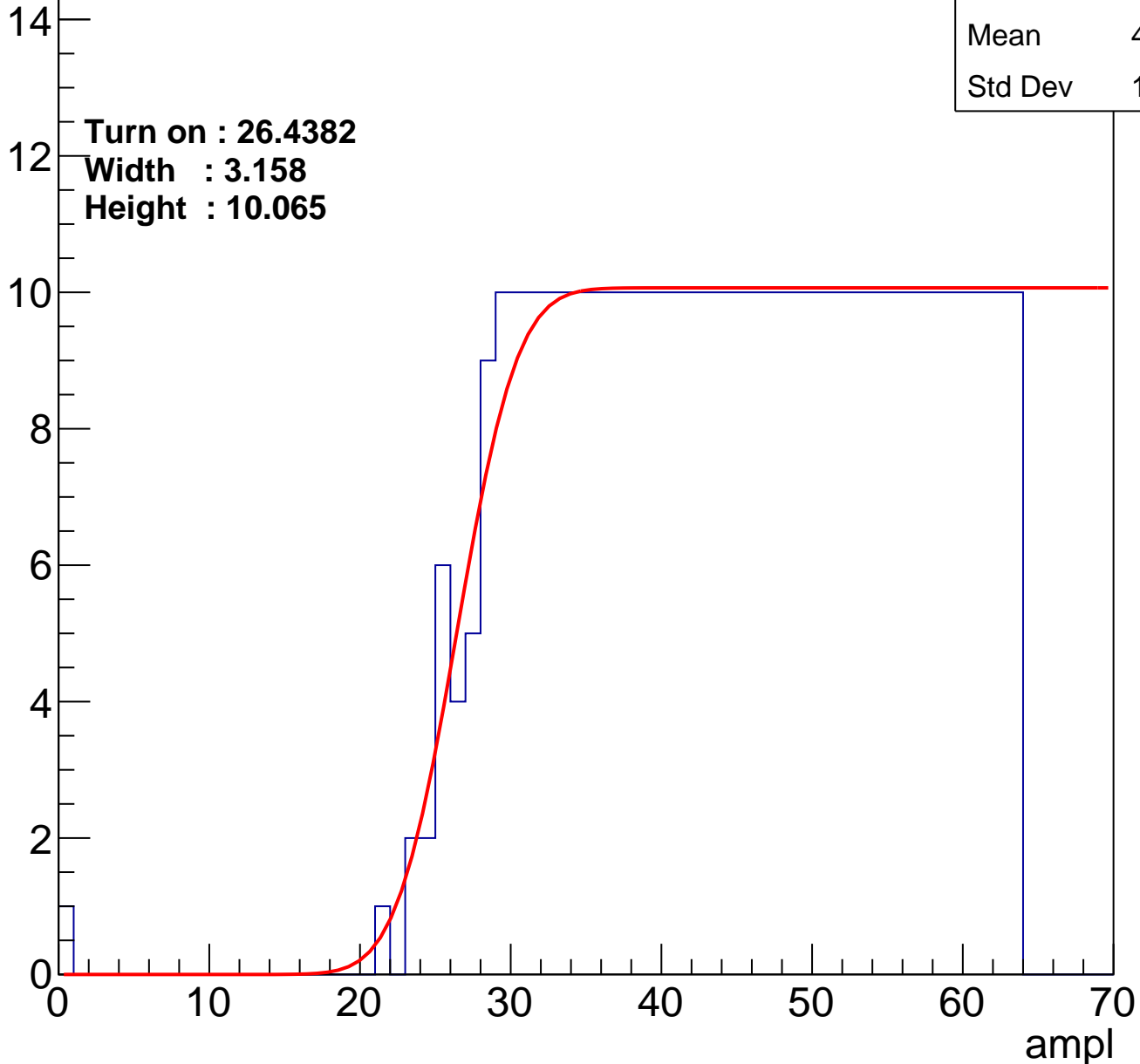
Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 26.4382

Width : 3.158

Height : 10.065

Entry



B1L000S, U20-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.5460

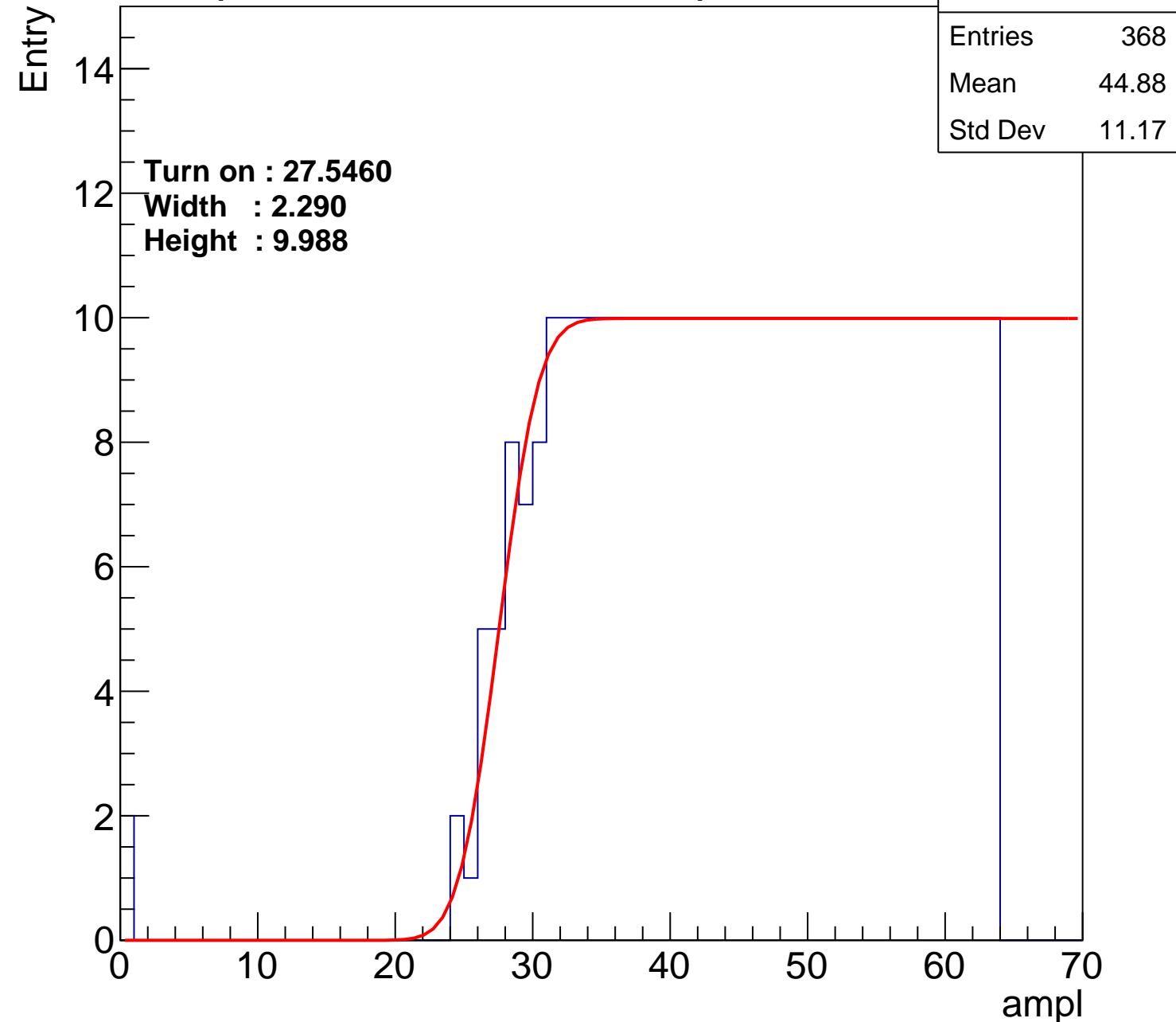
Width : 2.290

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.08
Std Dev	11.61

Turn on : 26.4364

Width : 2.820

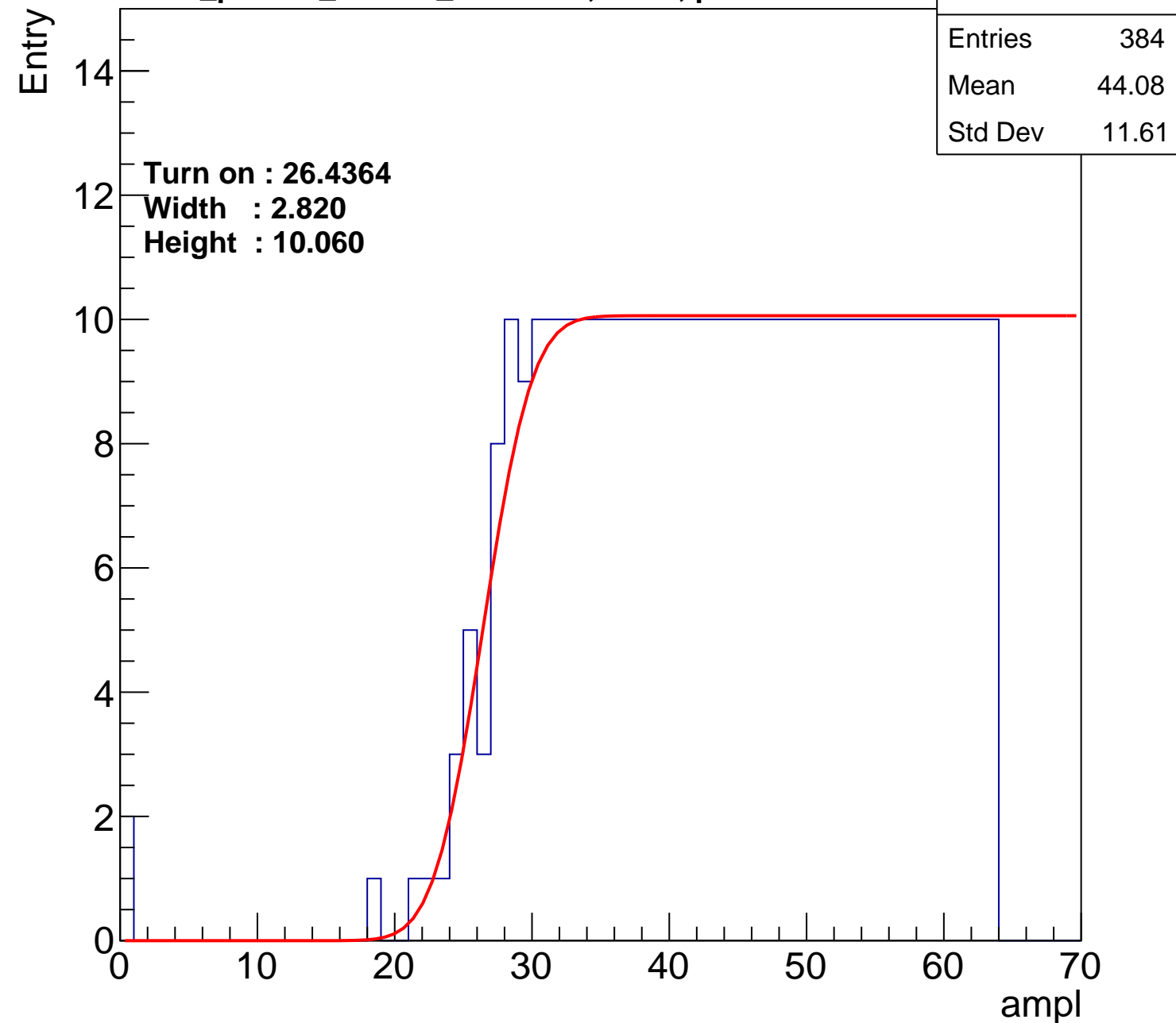
Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U20-ch25

calib_packv5_042523_0143.root, FC#3, port B3

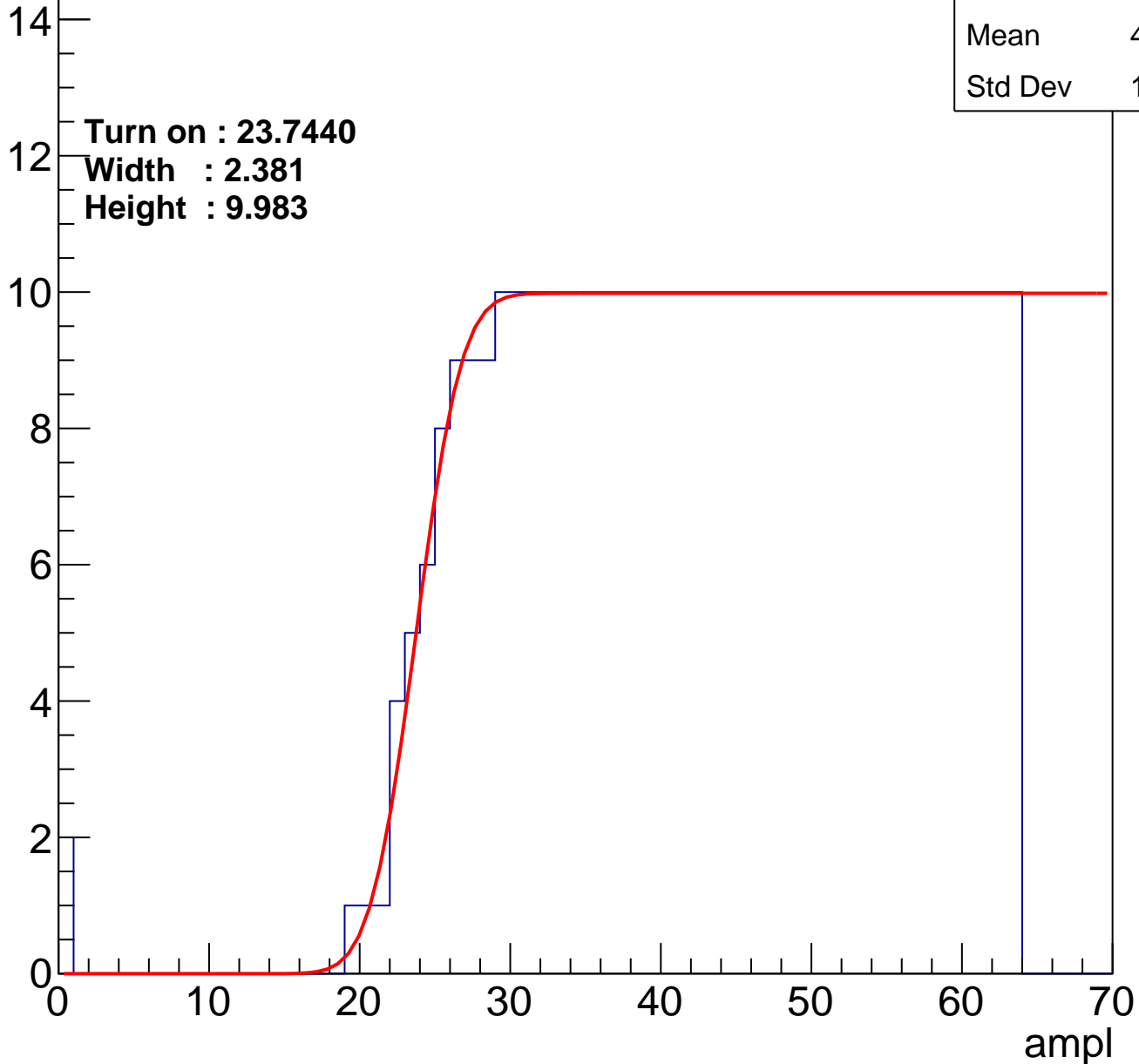
Entries	405
Mean	43.05
Std Dev	12.13

Turn on : 23.7440

Width : 2.381

Height : 9.983

Entry



B1L000S, U20-ch26

calib_packv5_042523_0143.root, FC#3, port B3

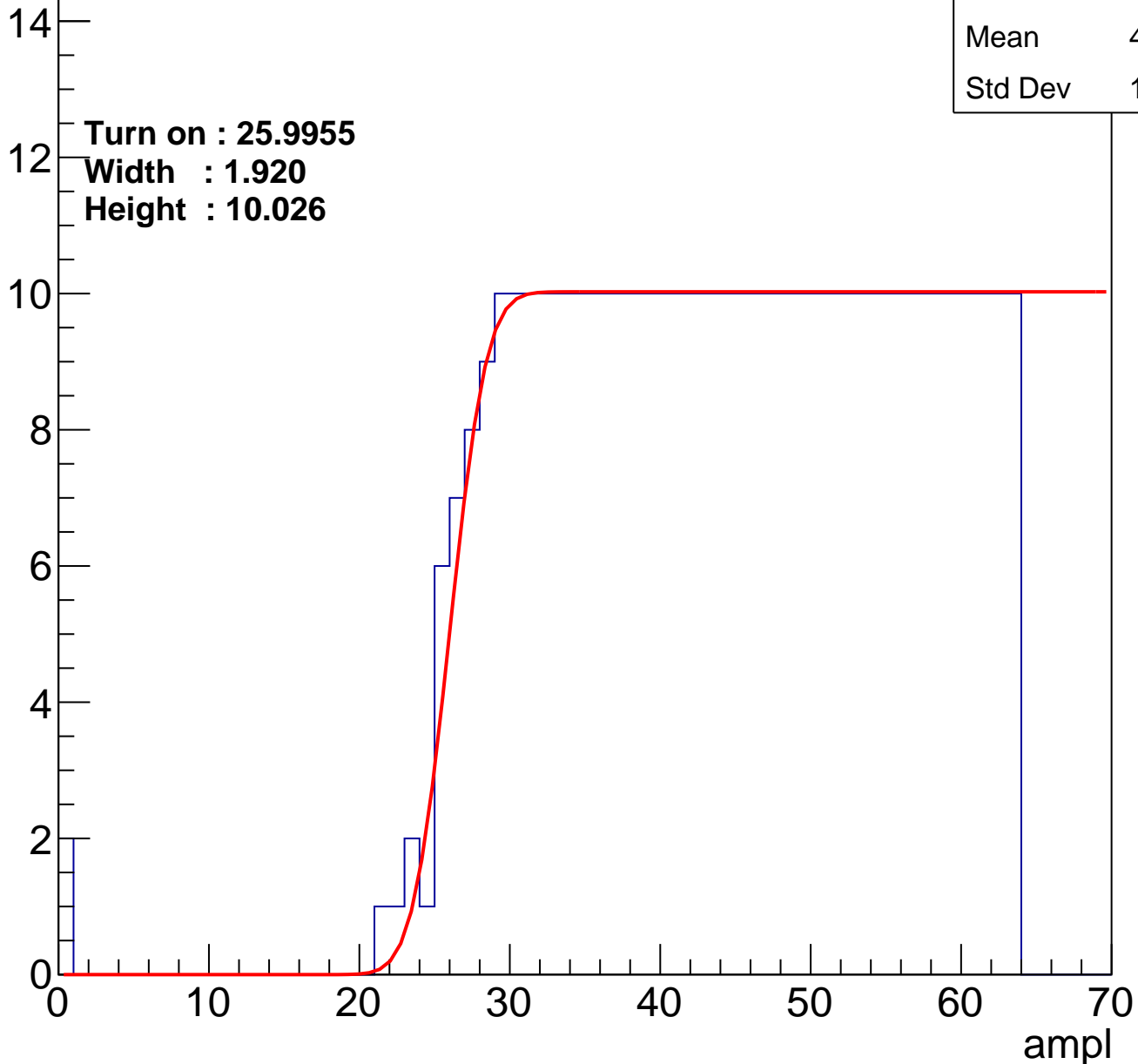
Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.9955

Width : 1.920

Height : 10.026

Entry



B1L000S, U20-ch27

calib_packv5_042523_0143.root, FC#3, port B3

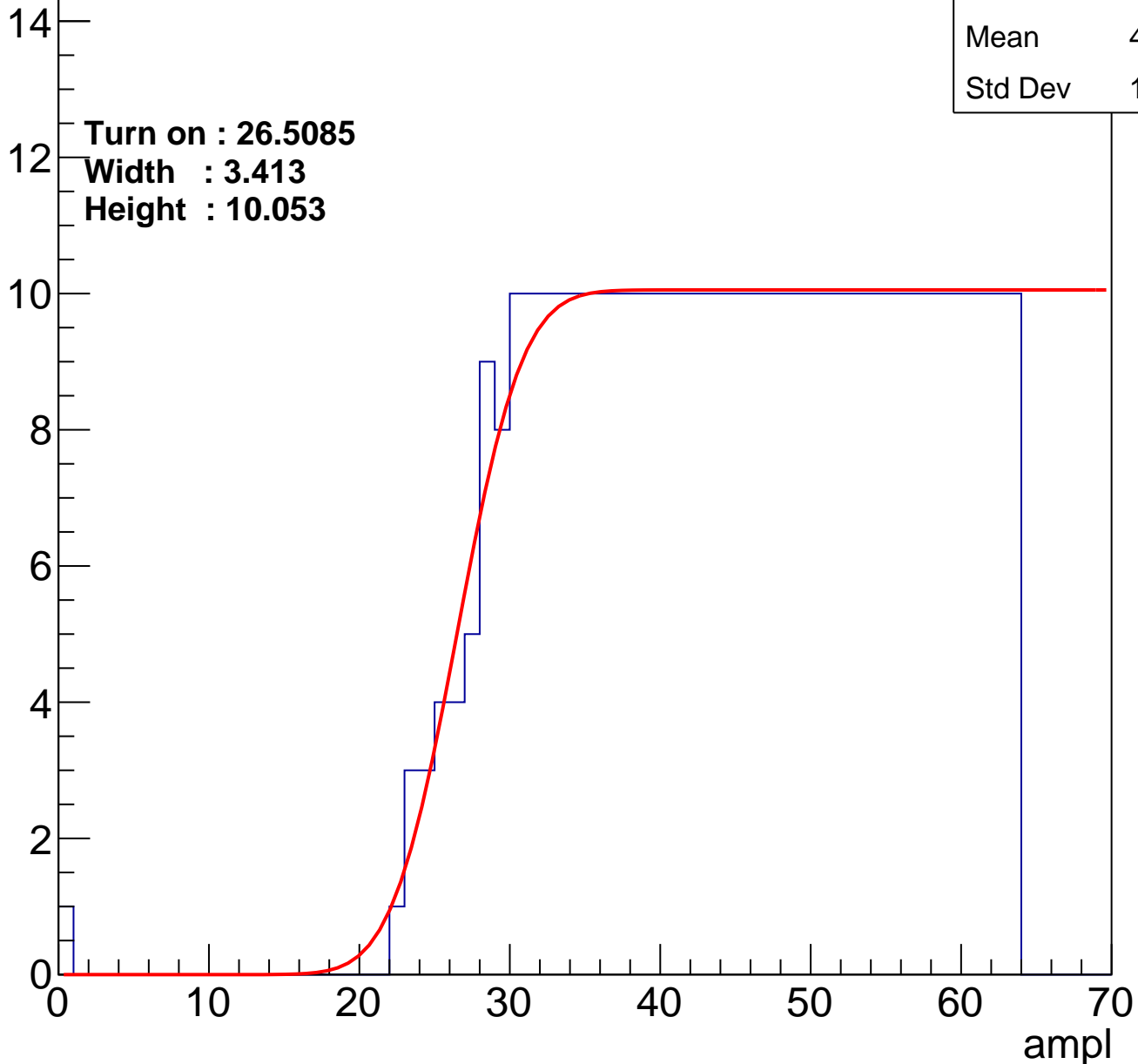
Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.5085

Width : 3.413

Height : 10.053

Entry



B1L000S, U20-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.21
Std Dev	11.16

Turn on : 28.2255

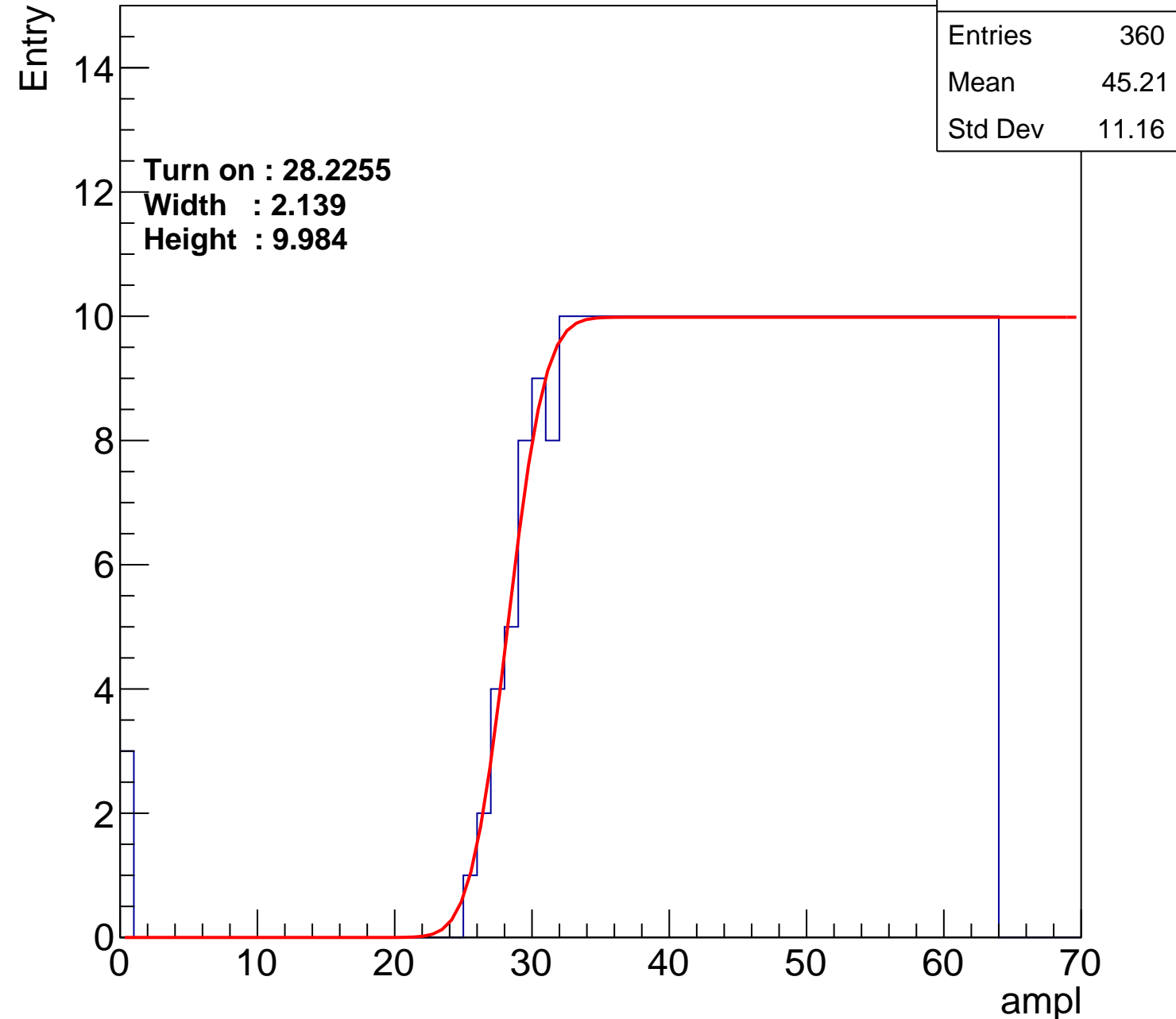
Width : 2.139

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch29

calib_packv5_042523_0143.root, FC#3, port B3

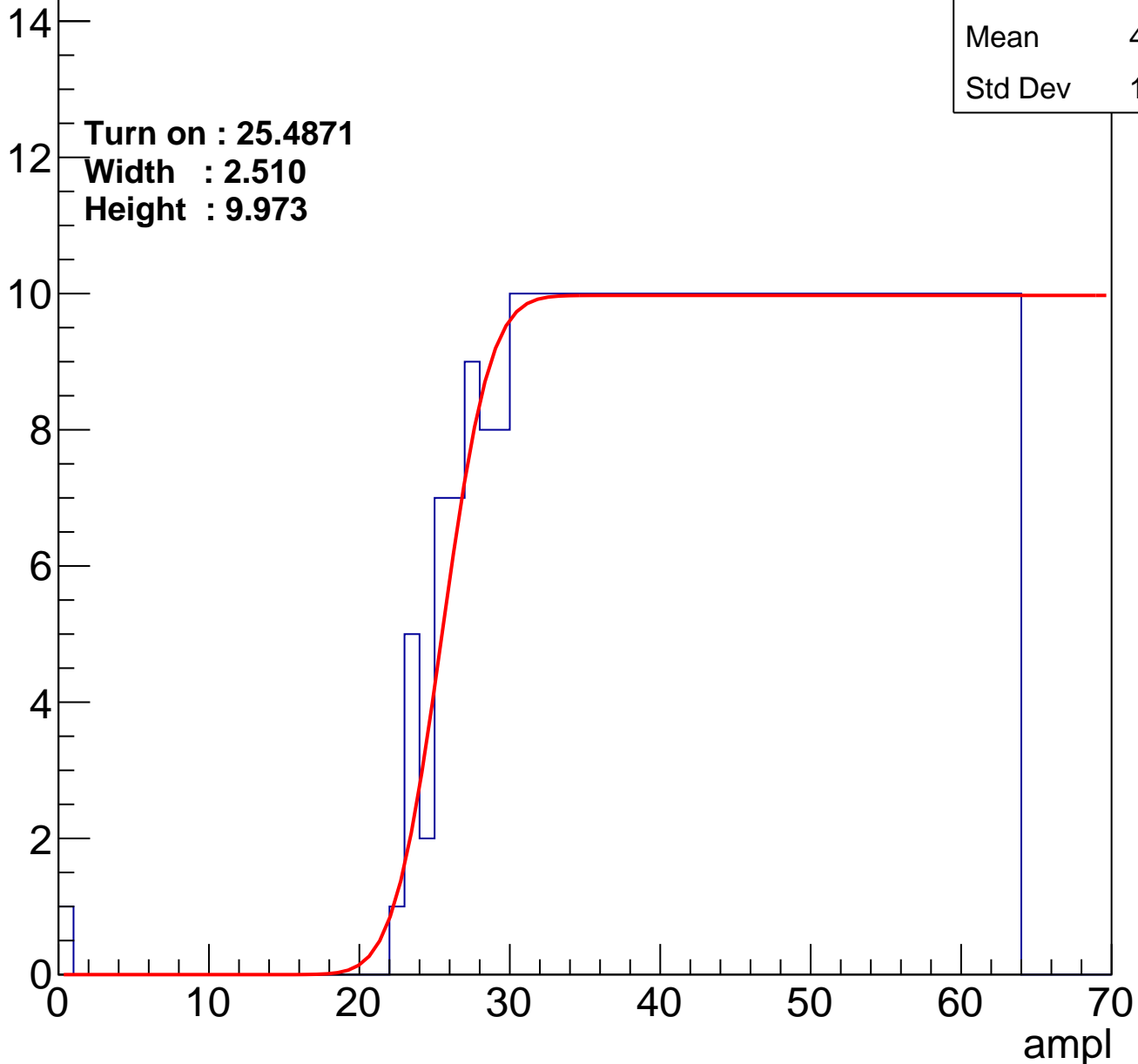
Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 25.4871

Width : 2.510

Height : 9.973

Entry



B1L000S, U20-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.96
Std Dev	10.96

Turn on : 27.4706

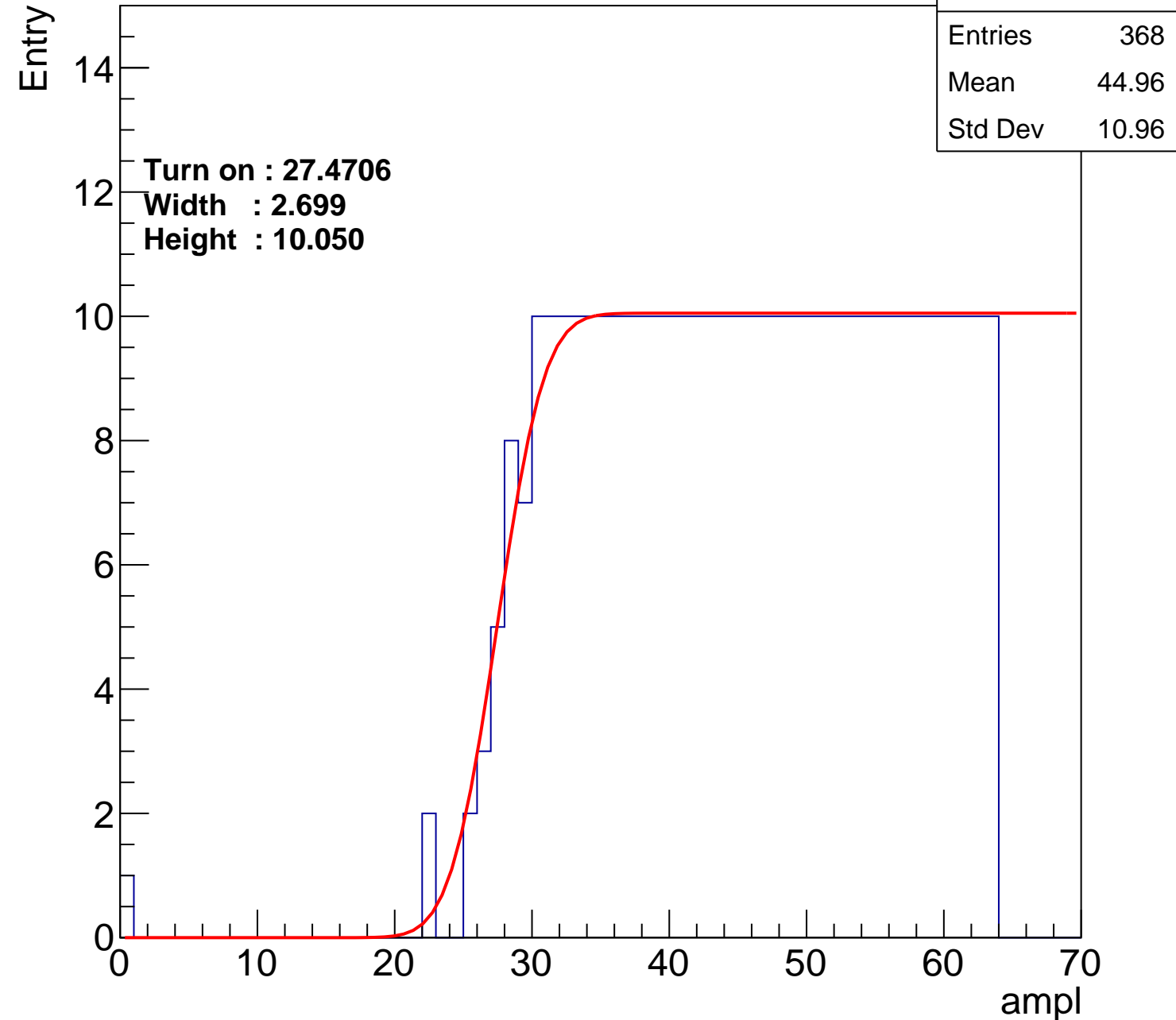
Width : 2.699

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.2250

Width : 2.755

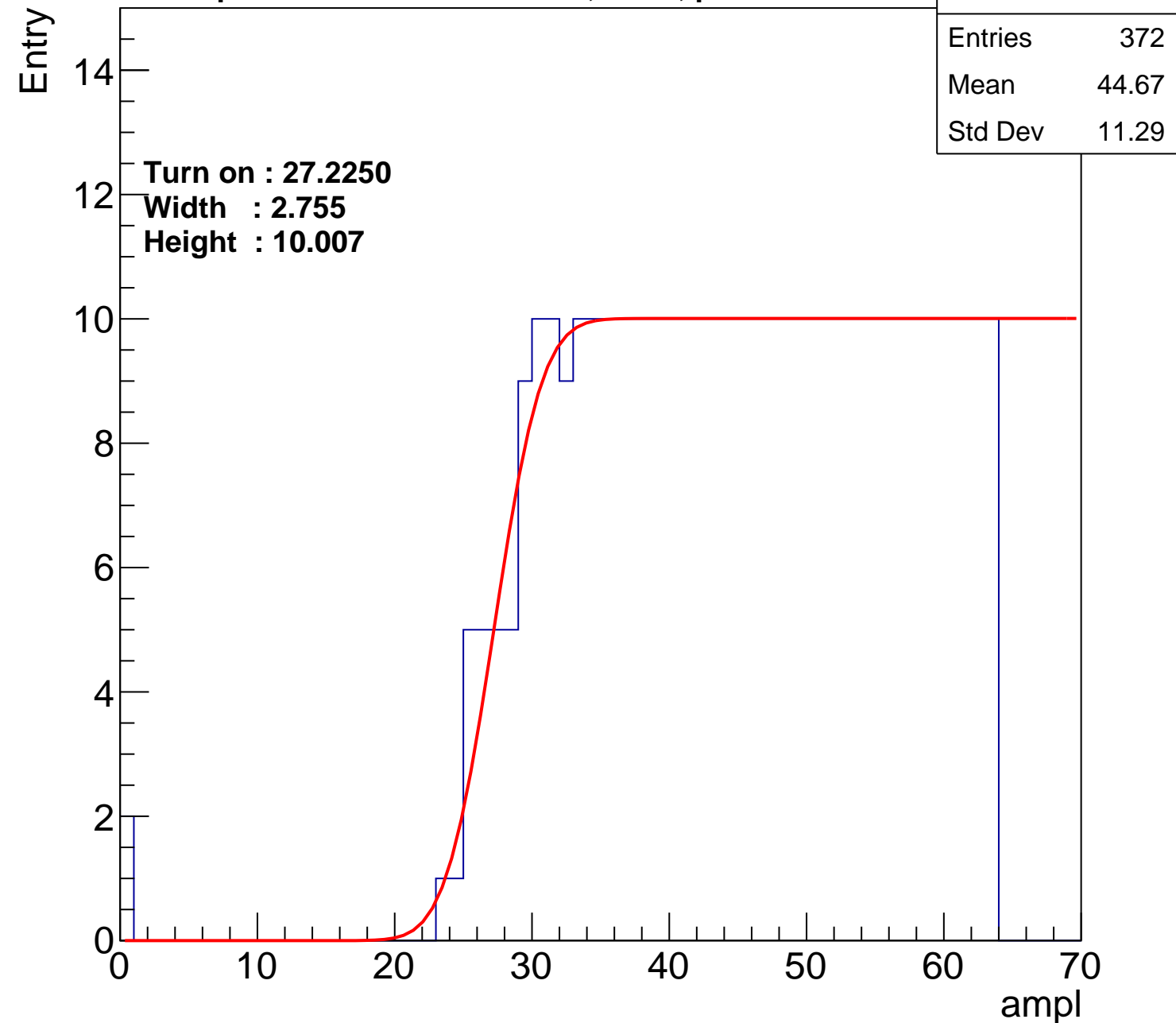
Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U20-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.83
Std Dev	11.55

Turn on : 25.1179

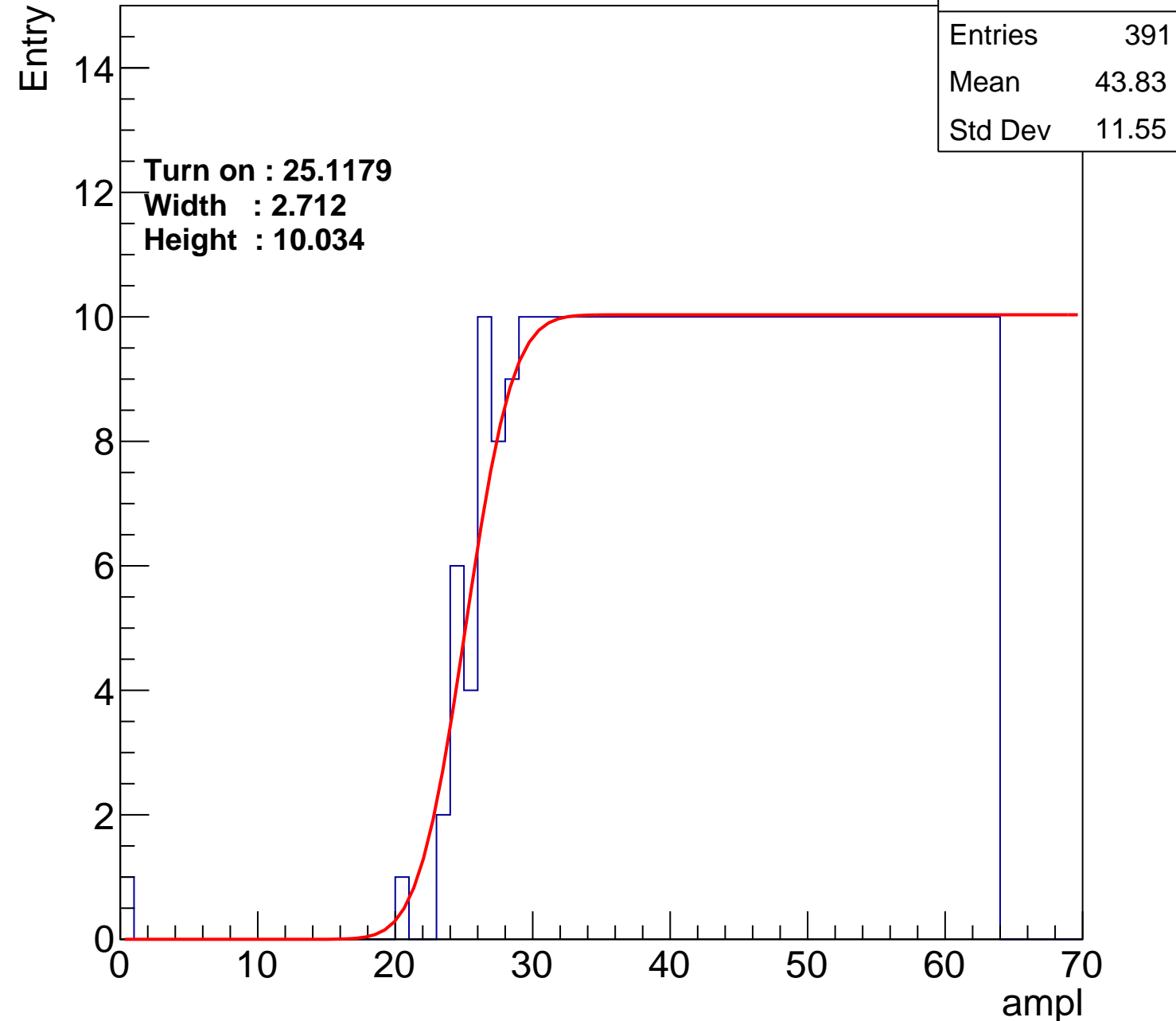
Width : 2.712

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch33

calib_packv5_042523_0143.root, FC#3, port B3

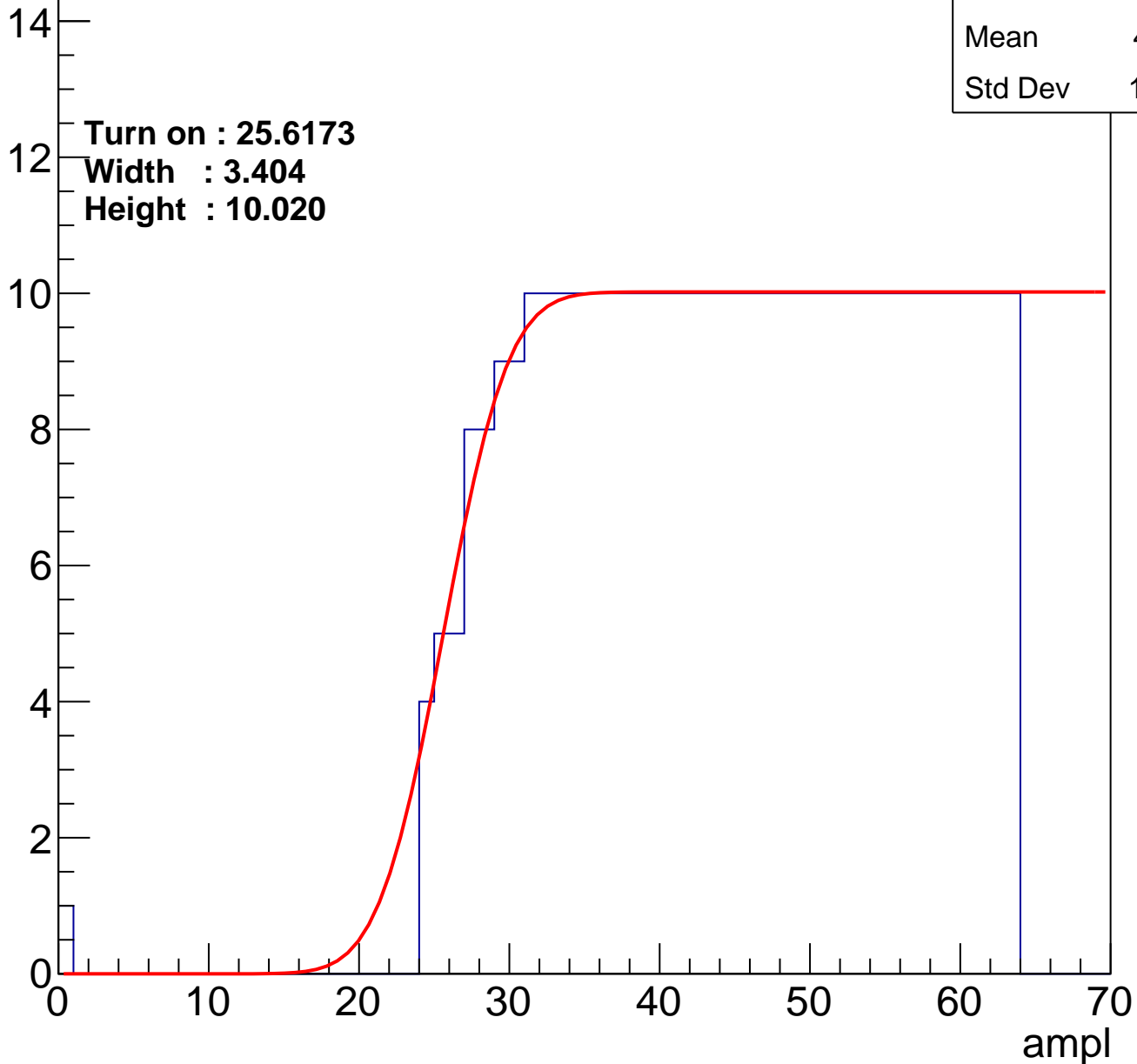
Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 25.6173

Width : 3.404

Height : 10.020

Entry



B1L000S, U20-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45
Std Dev	11.28

Turn on : 28.3542

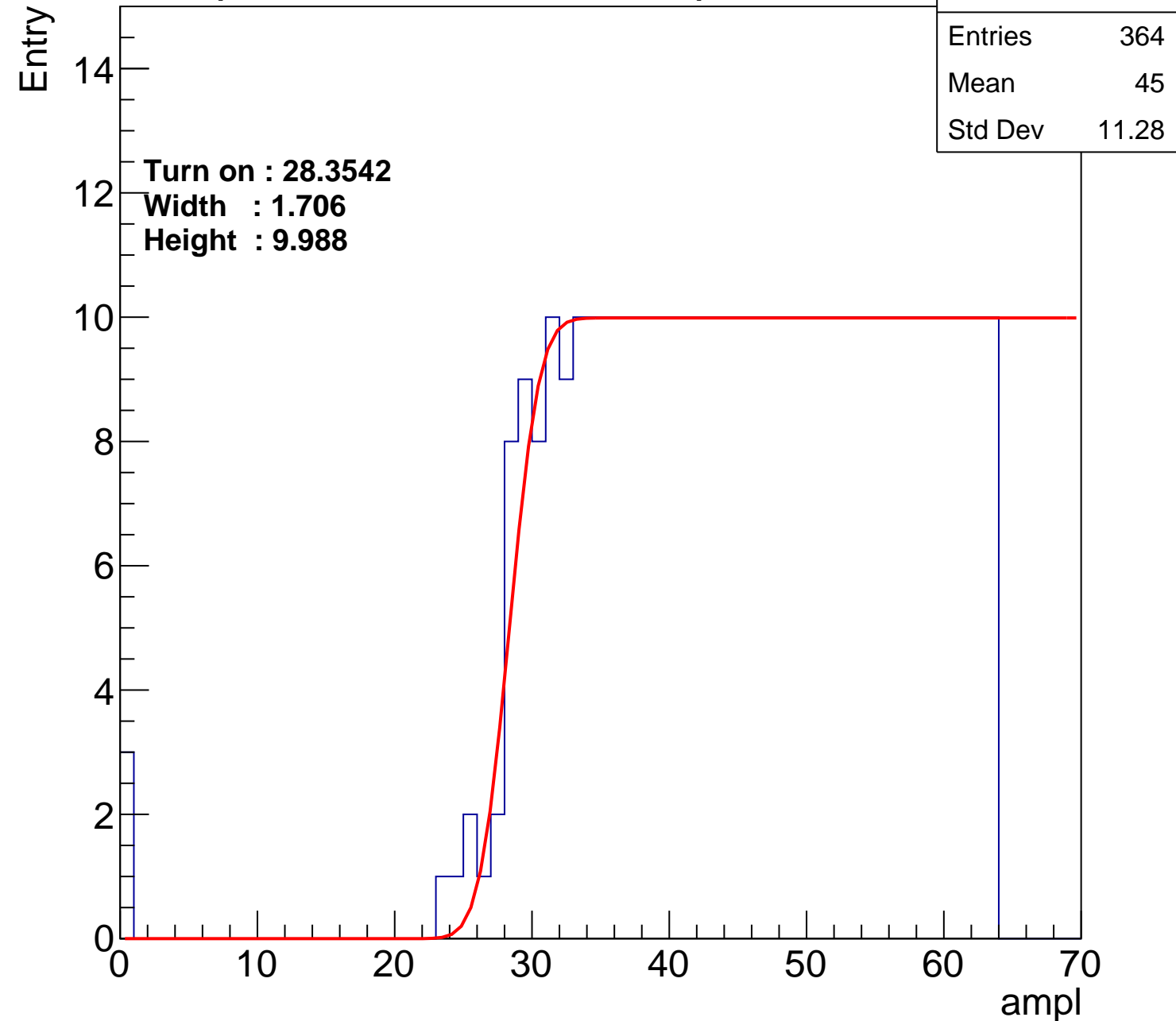
Width : 1.706

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.5054

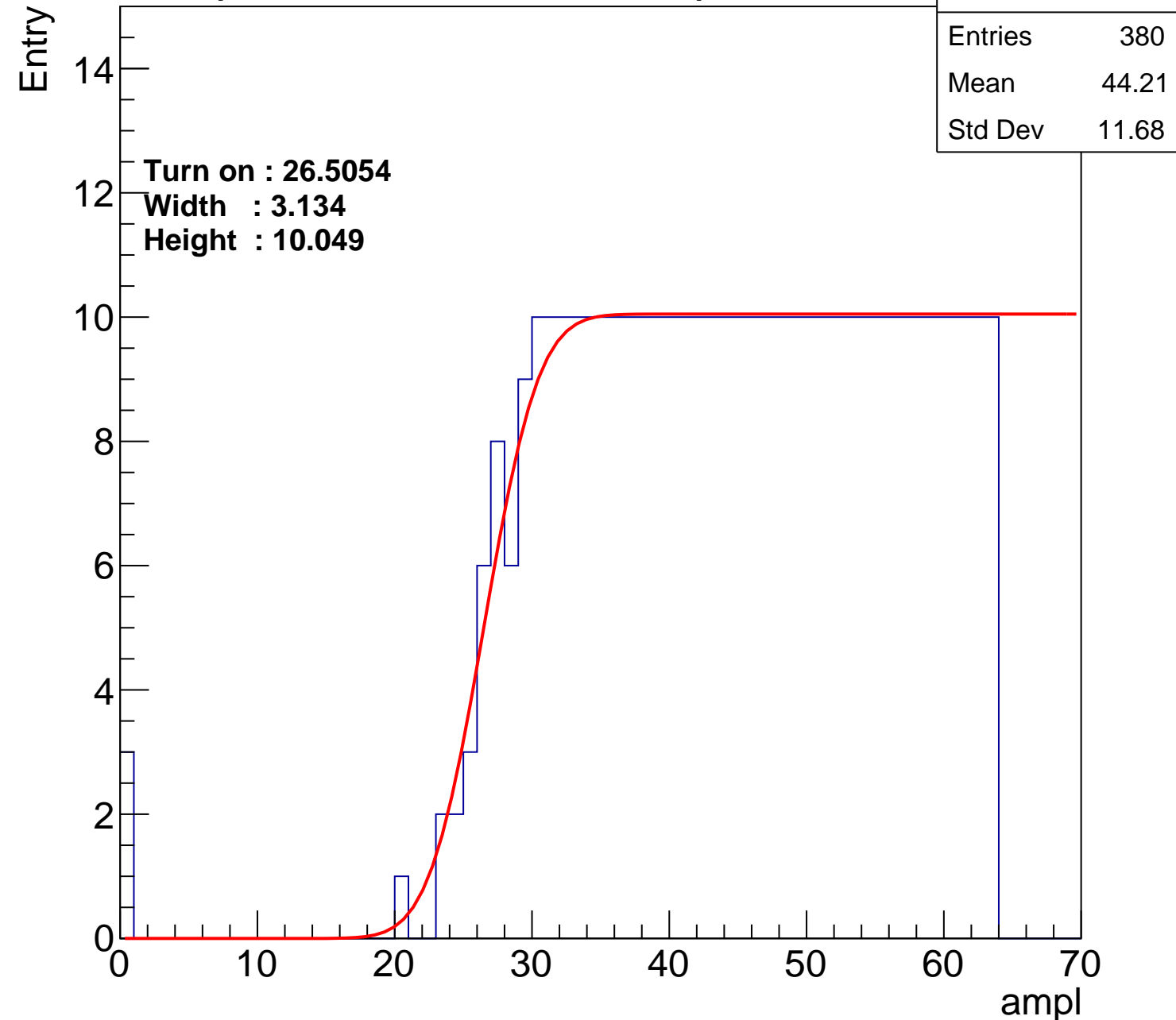
Width : 3.134

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch36

calib_packv5_042523_0143.root, FC#3, port B3

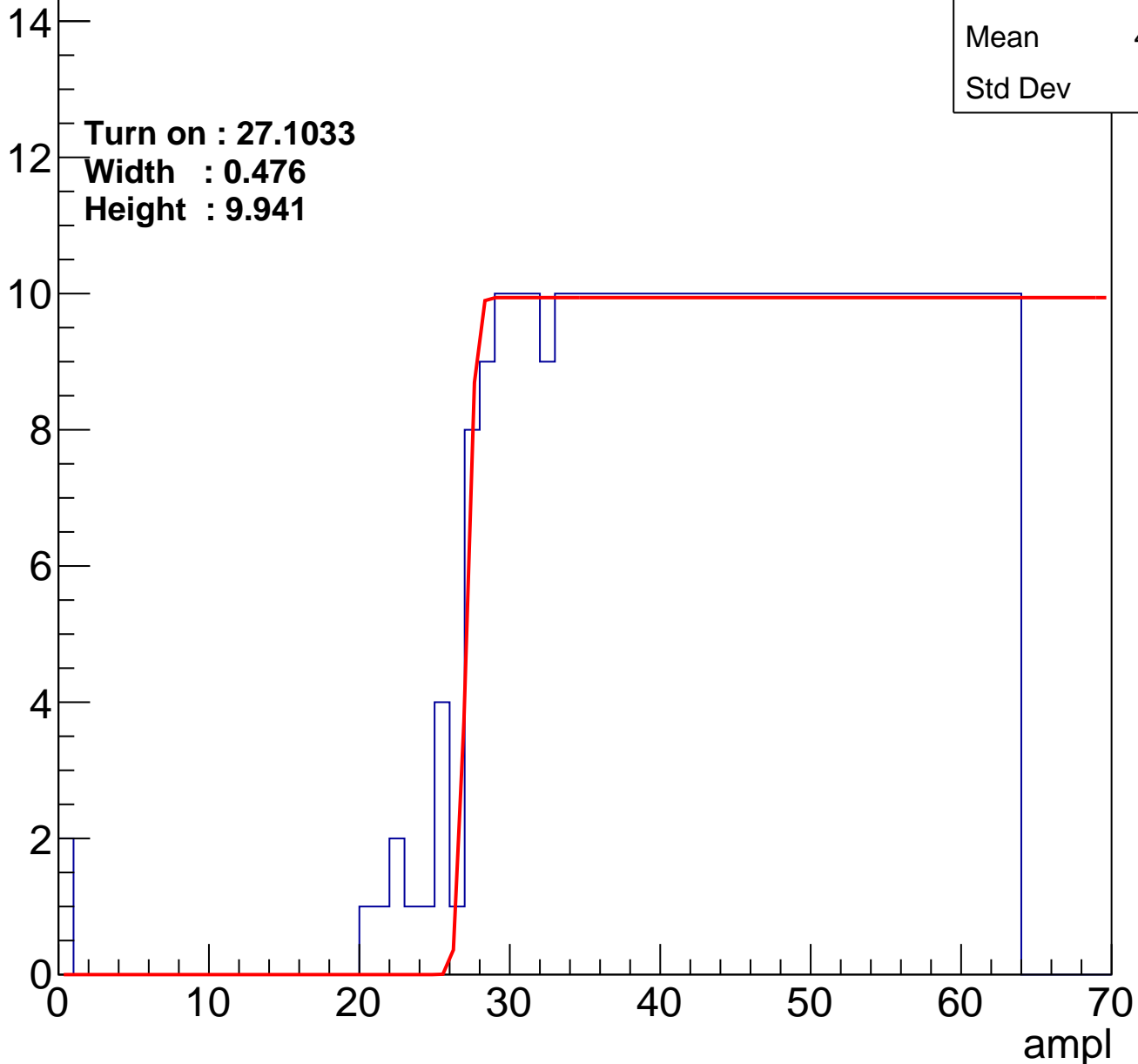
Entries	379
Mean	44.31
Std Dev	11.5

Turn on : 27.1033

Width : 0.476

Height : 9.941

Entry



B1L000S, U20-ch37

calib_packv5_042523_0143.root, FC#3, port B3

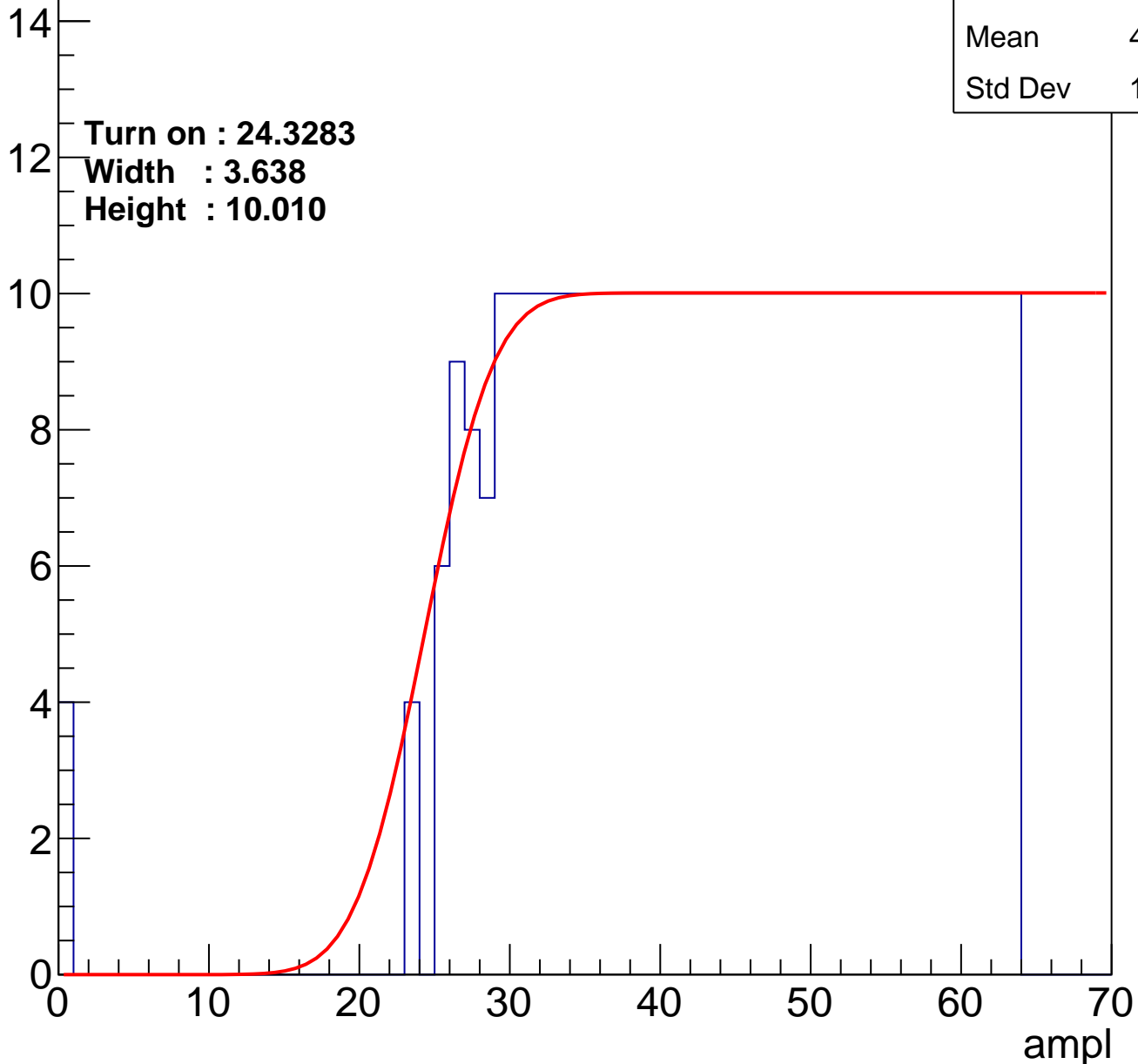
Entries	388
Mean	43.78
Std Dev	11.99

Turn on : 24.3283

Width : 3.638

Height : 10.010

Entry



B1L000S, U20-ch38

calib_packv5_042523_0143.root, FC#3, port B3

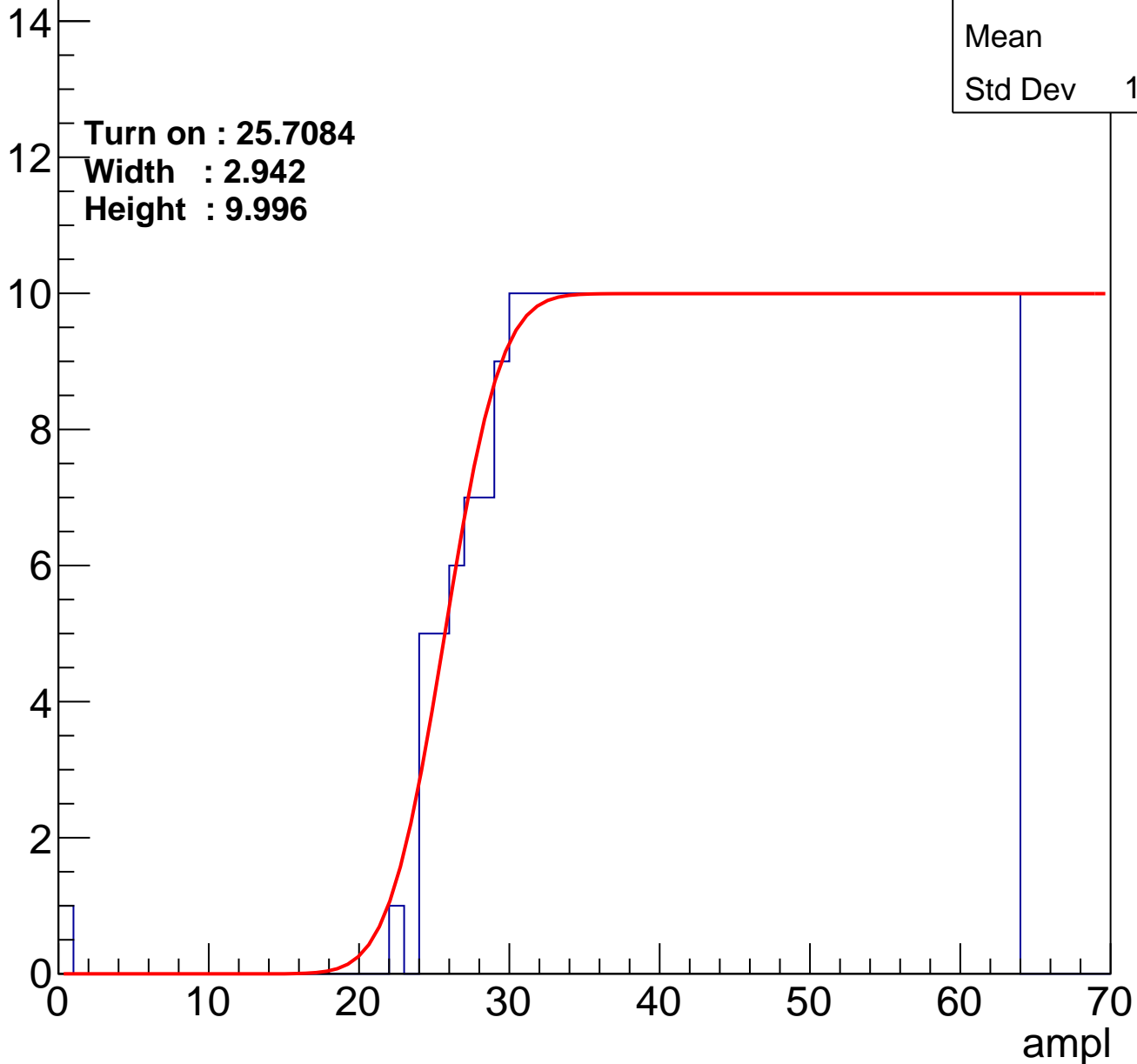
Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 25.7084

Width : 2.942

Height : 9.996

Entry



B1L000S, U20-ch39

calib_packv5_042523_0143.root, FC#3, port B3

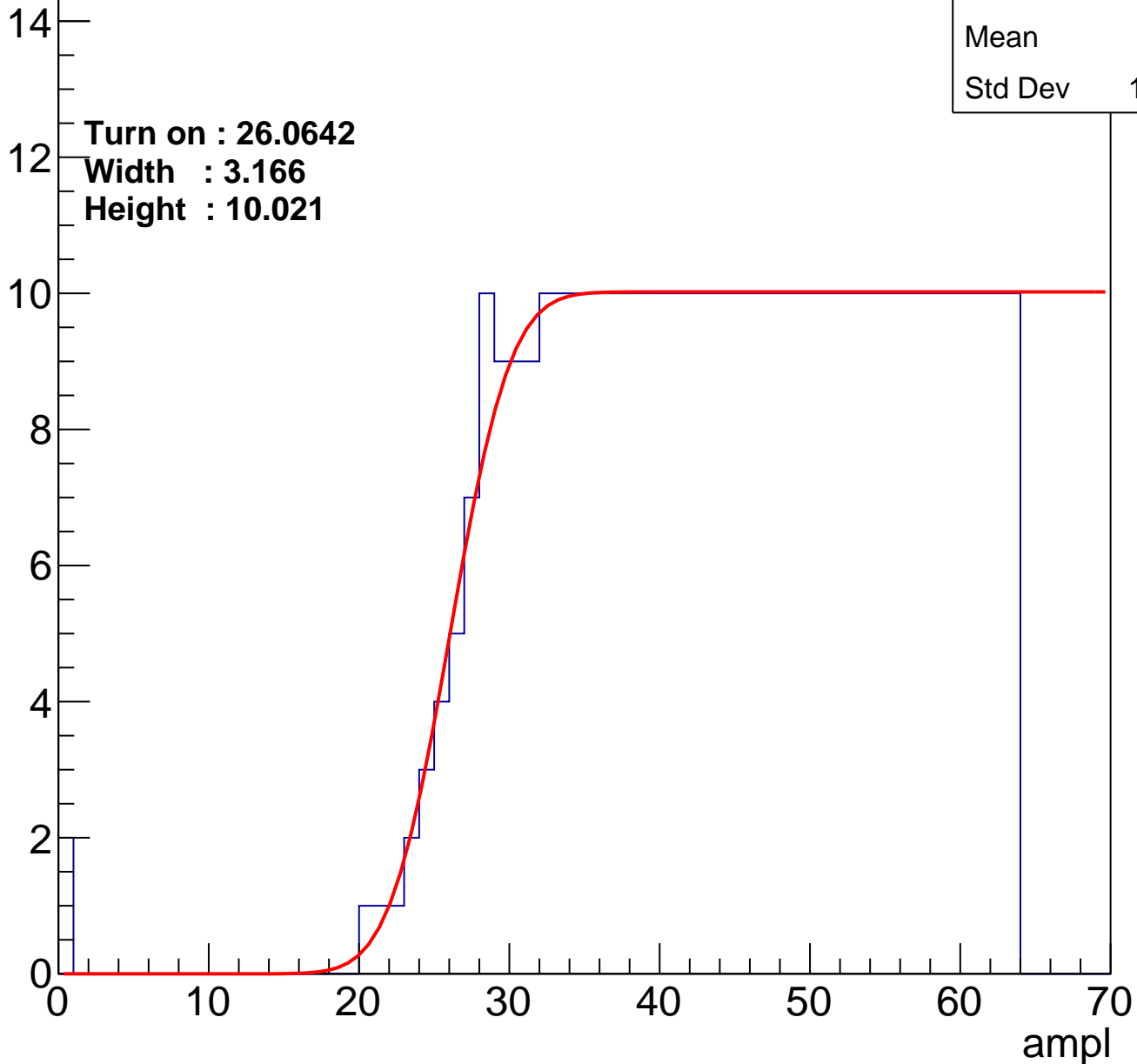
Entries	383
Mean	44.1
Std Dev	11.62

Turn on : 26.0642

Width : 3.166

Height : 10.021

Entry



B1L000S, U20-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.3142

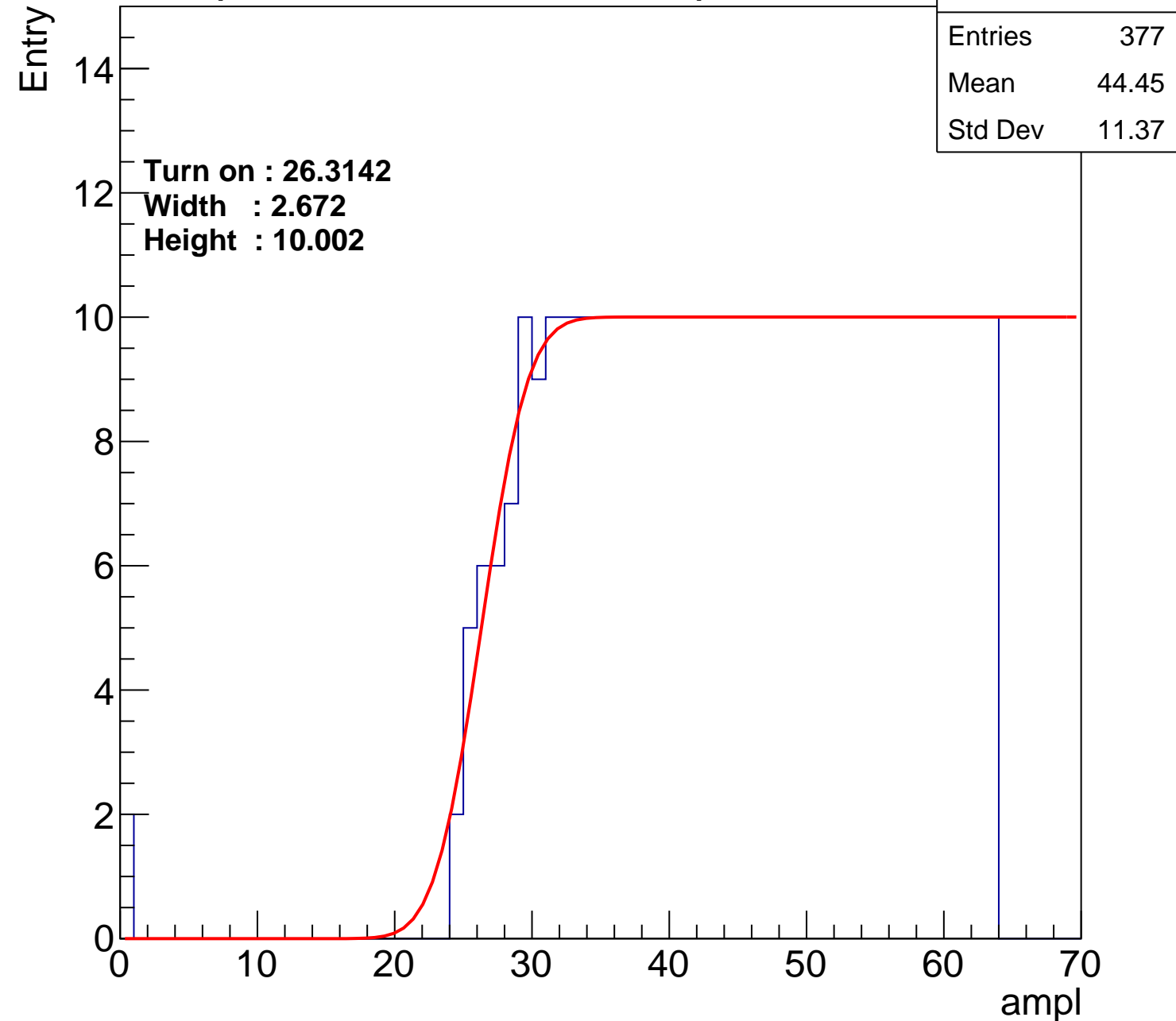
Width : 2.672

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.1
Std Dev	11.99

Turn on : 26.5670

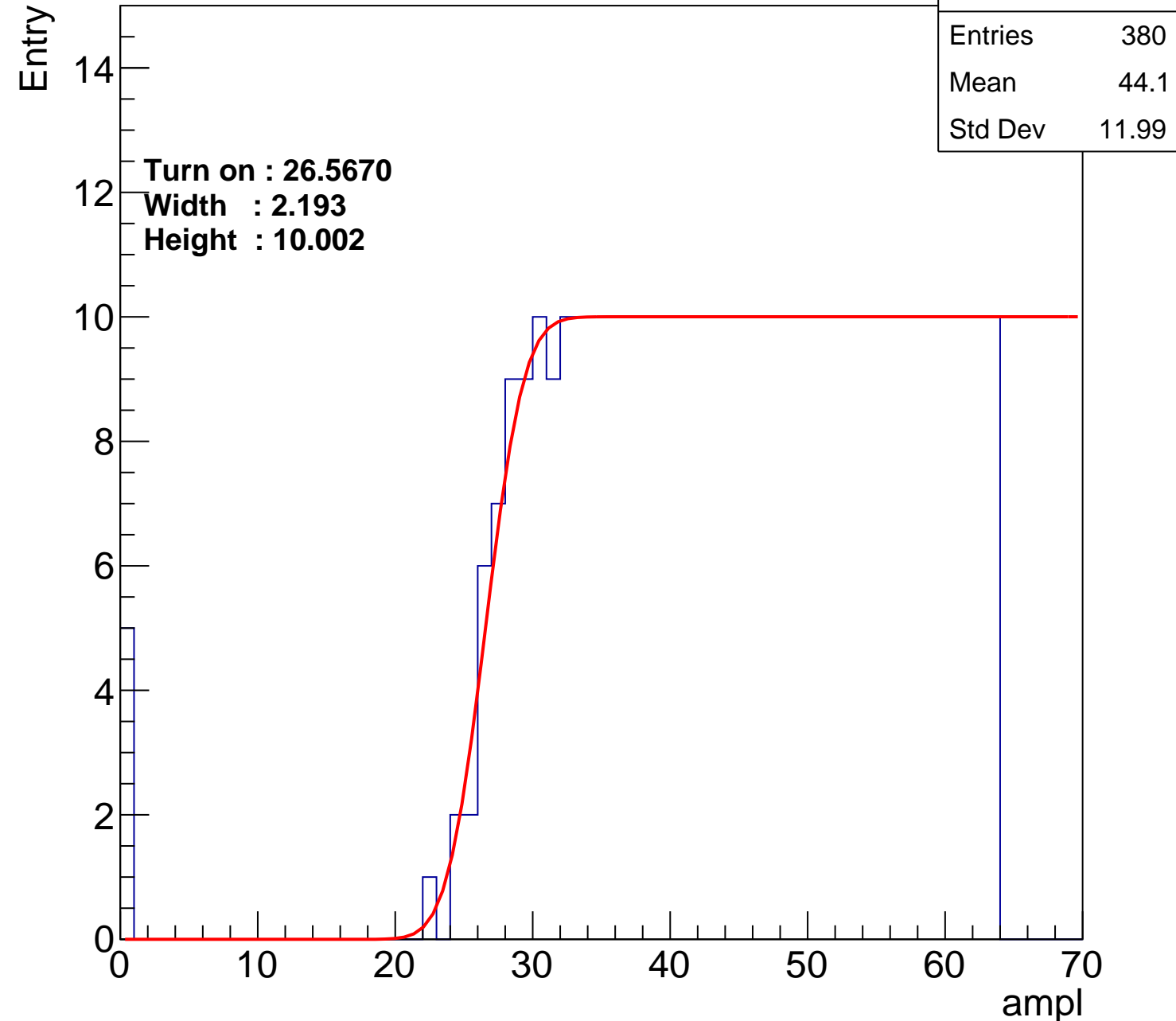
Width : 2.193

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 25.9554

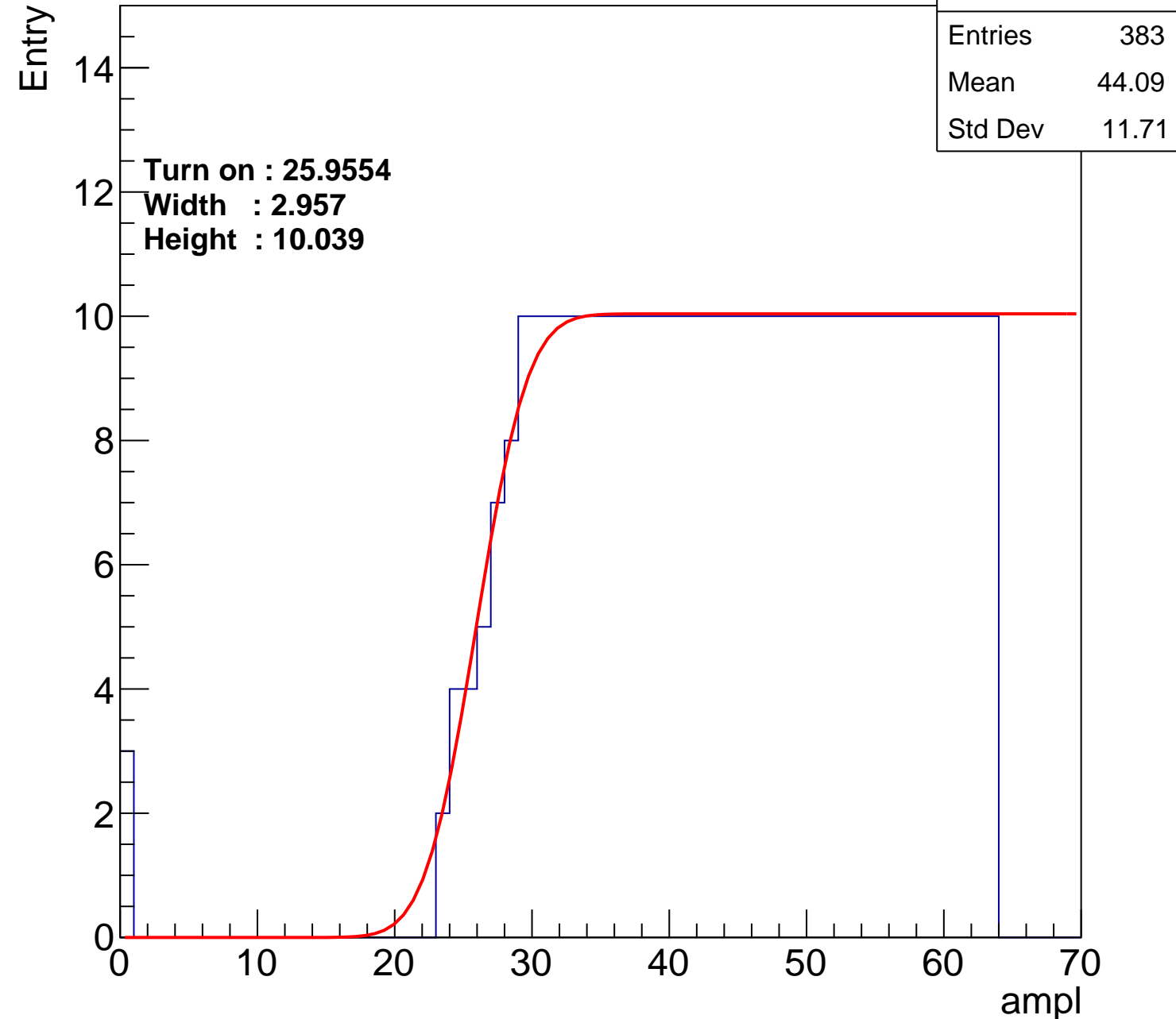
Width : 2.957

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch43

calib_packv5_042523_0143.root, FC#3, port B3

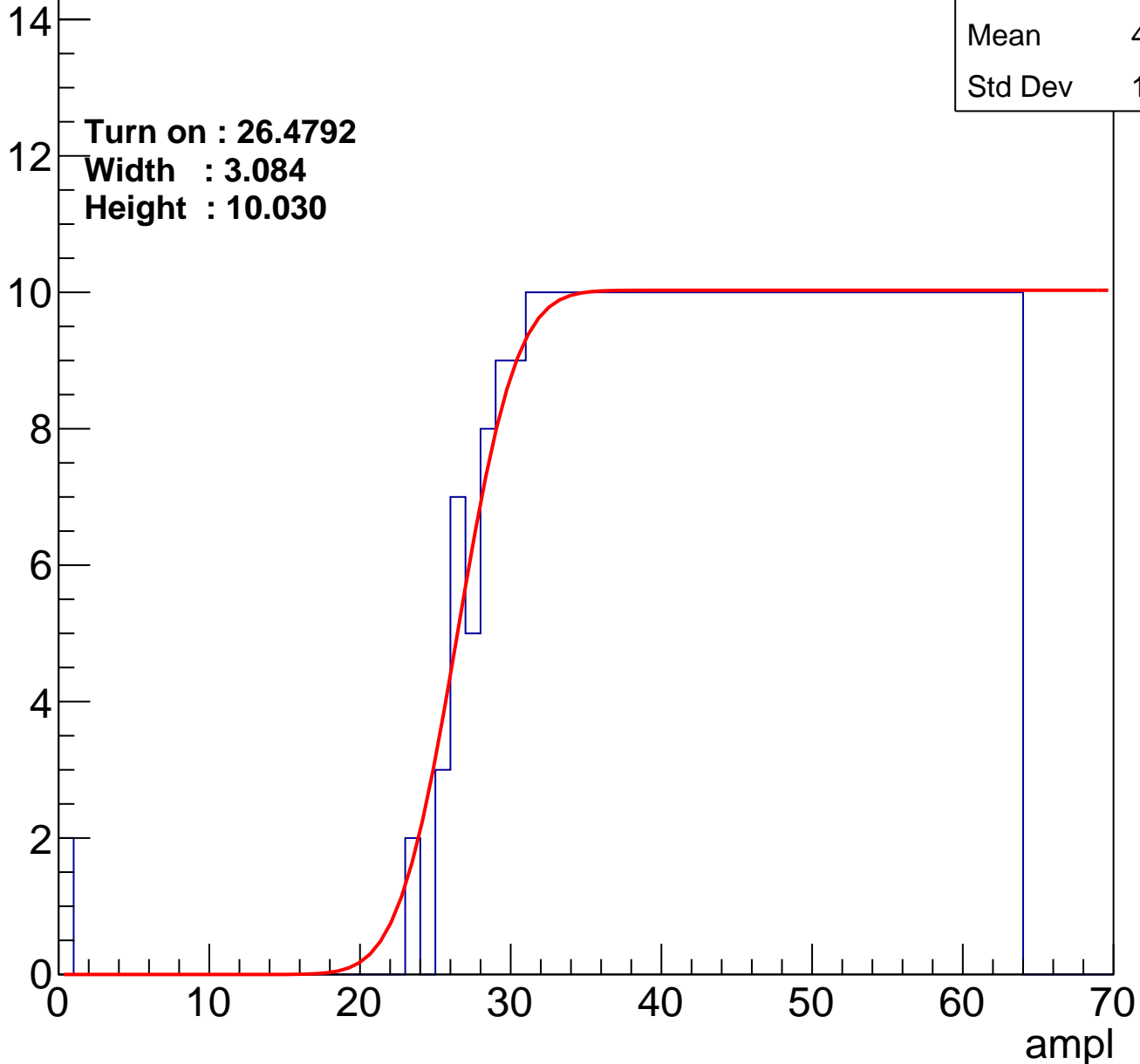
Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.4792

Width : 3.084

Height : 10.030

Entry



calib_packv5_042523_0143.root, FC#3, port B3

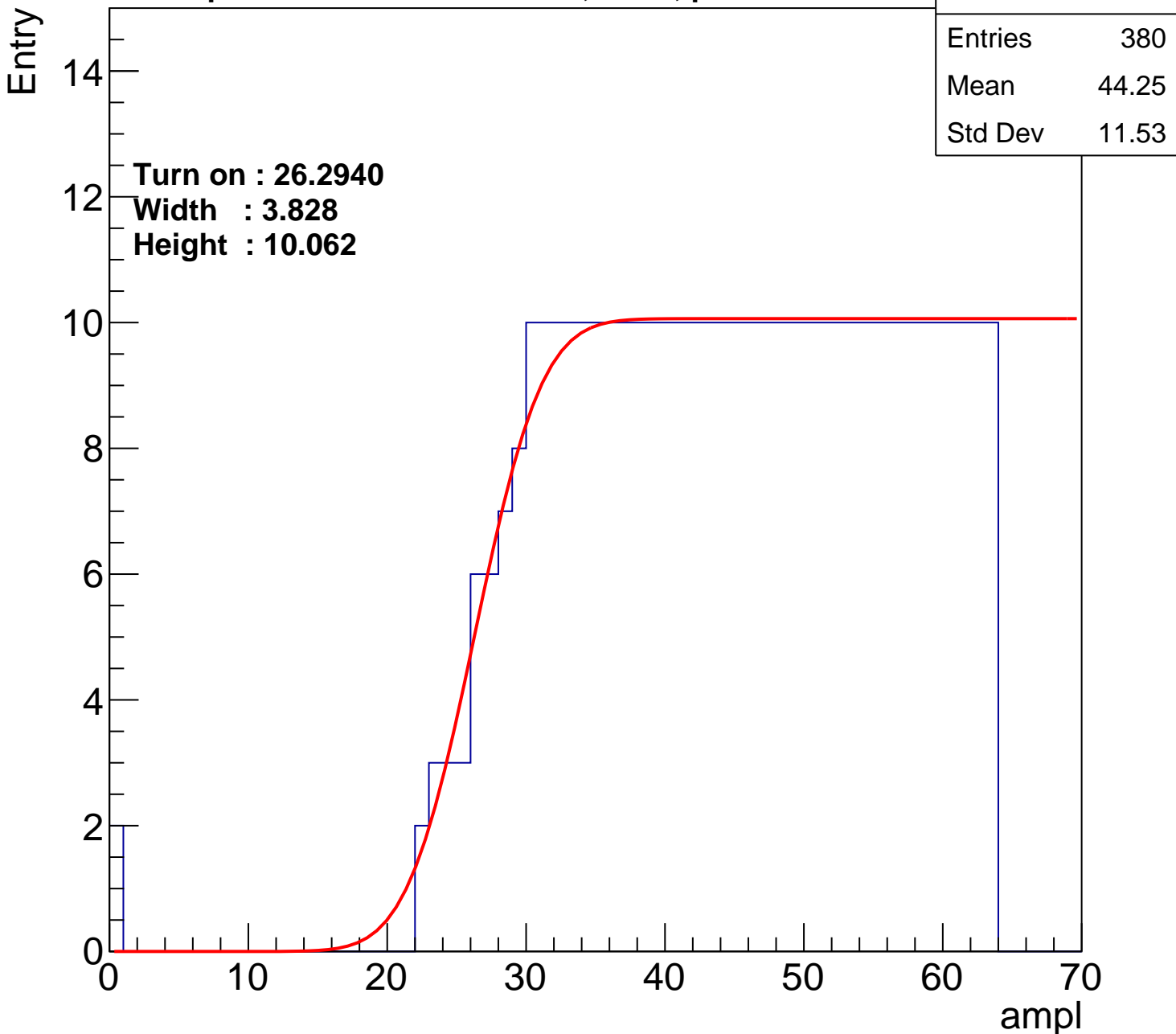
Entries	380
Mean	44.25
Std Dev	11.53

Mean	44.25
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Std Dev	11.53
---------	-------

Width : 3.828

Height : 10.062



B1L000S, U20-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.93
Std Dev	11.68

Turn on : 25.5781

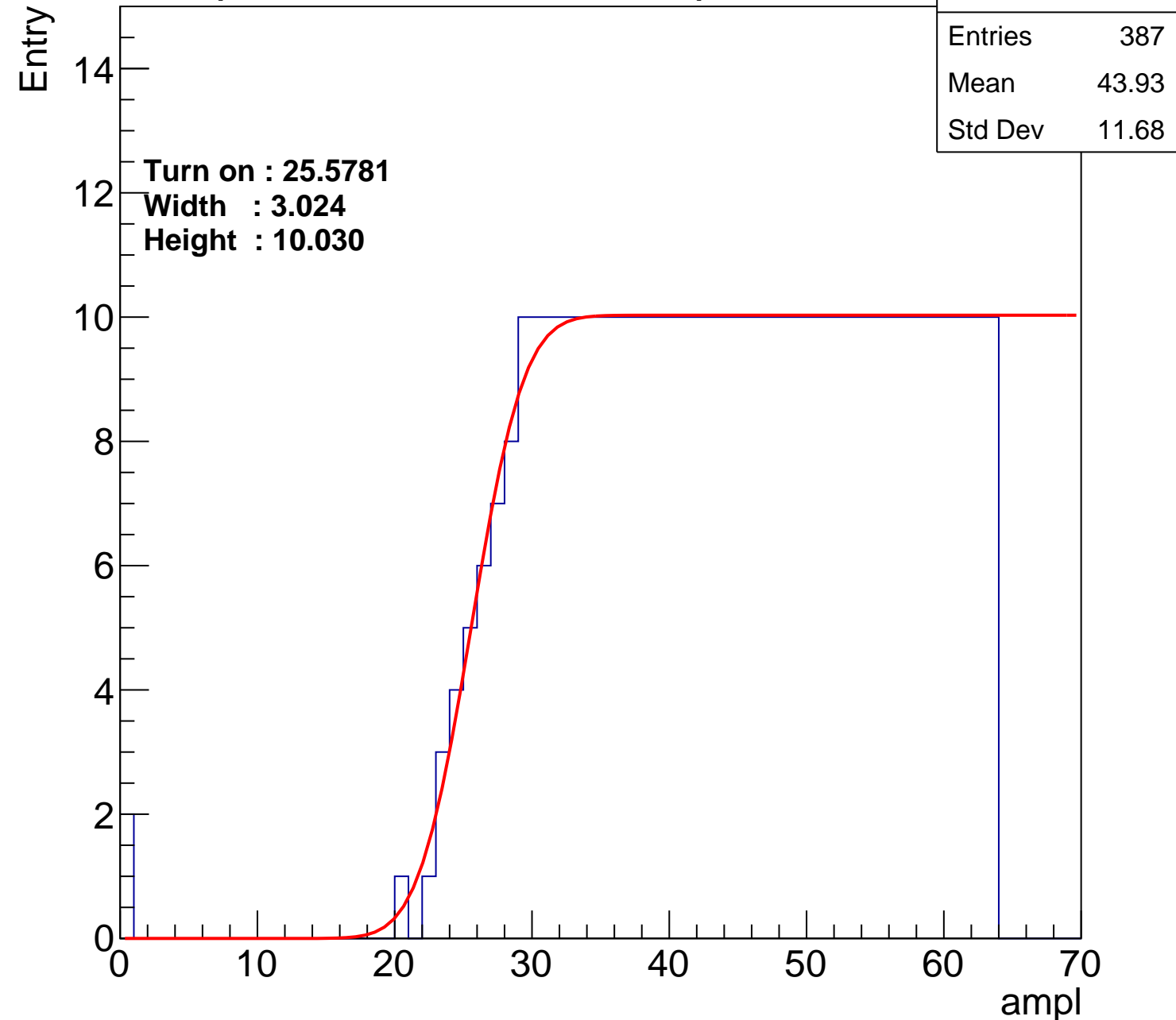
Width : 3.024

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 24.2265
Width : 2.653
Height : 10.033



B1L000S, U20-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.2139

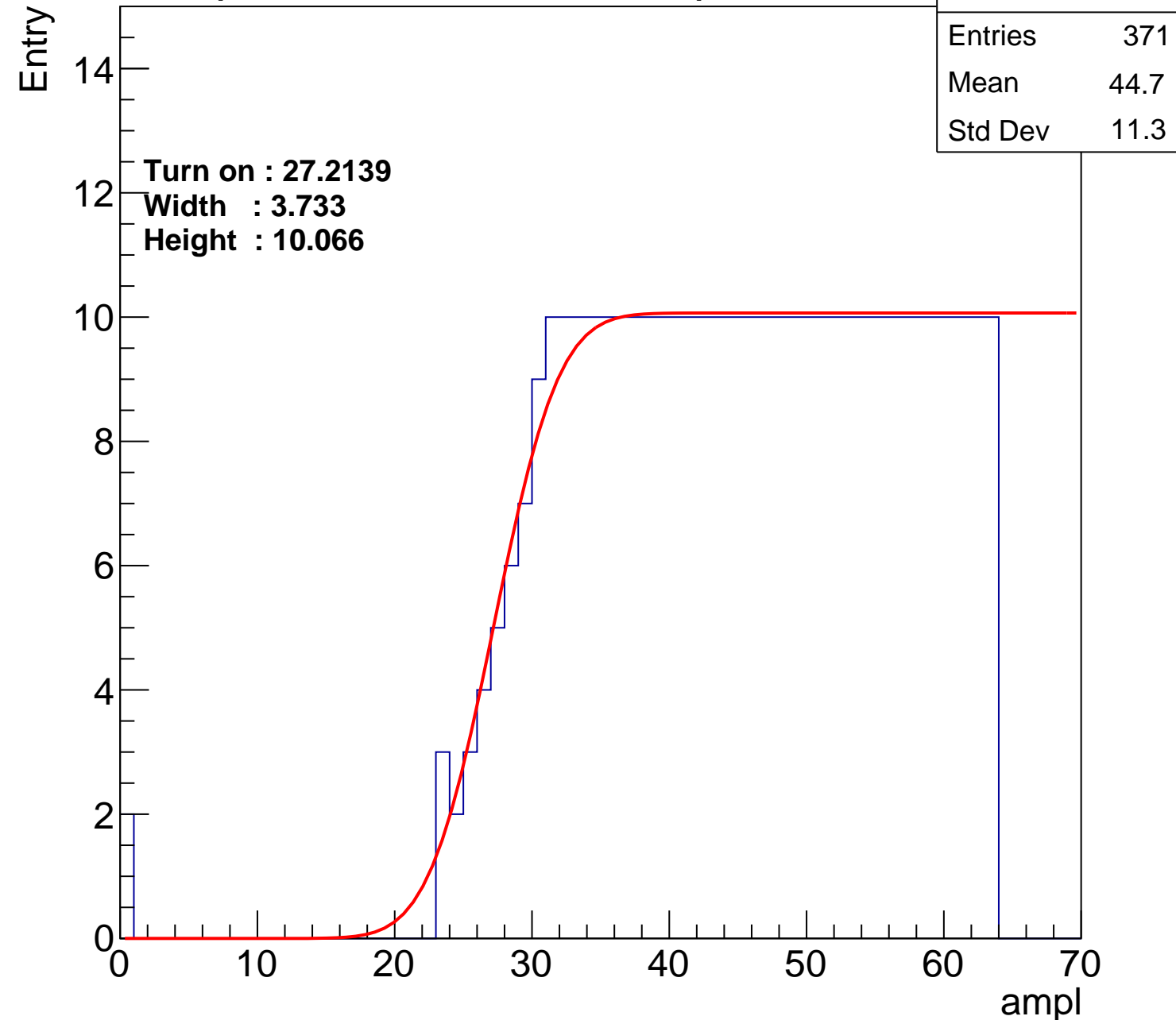
Width : 3.733

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch48

calib_packv5_042523_0143.root, FC#3, port B3

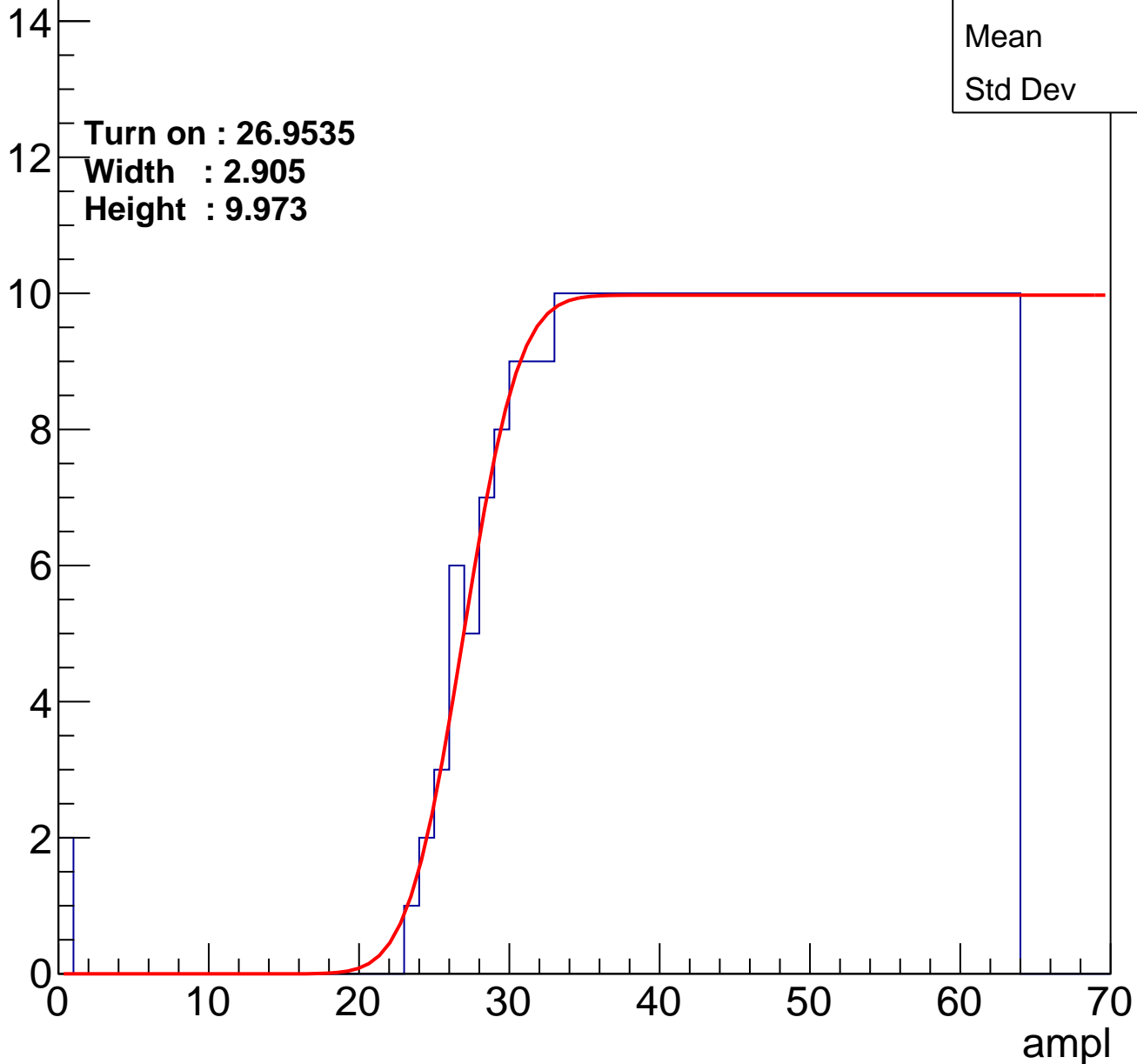
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 26.9535

Width : 2.905

Height : 9.973

Entry



B1L000S, U20-ch49

calib_packv5_042523_0143.root, FC#3, port B3

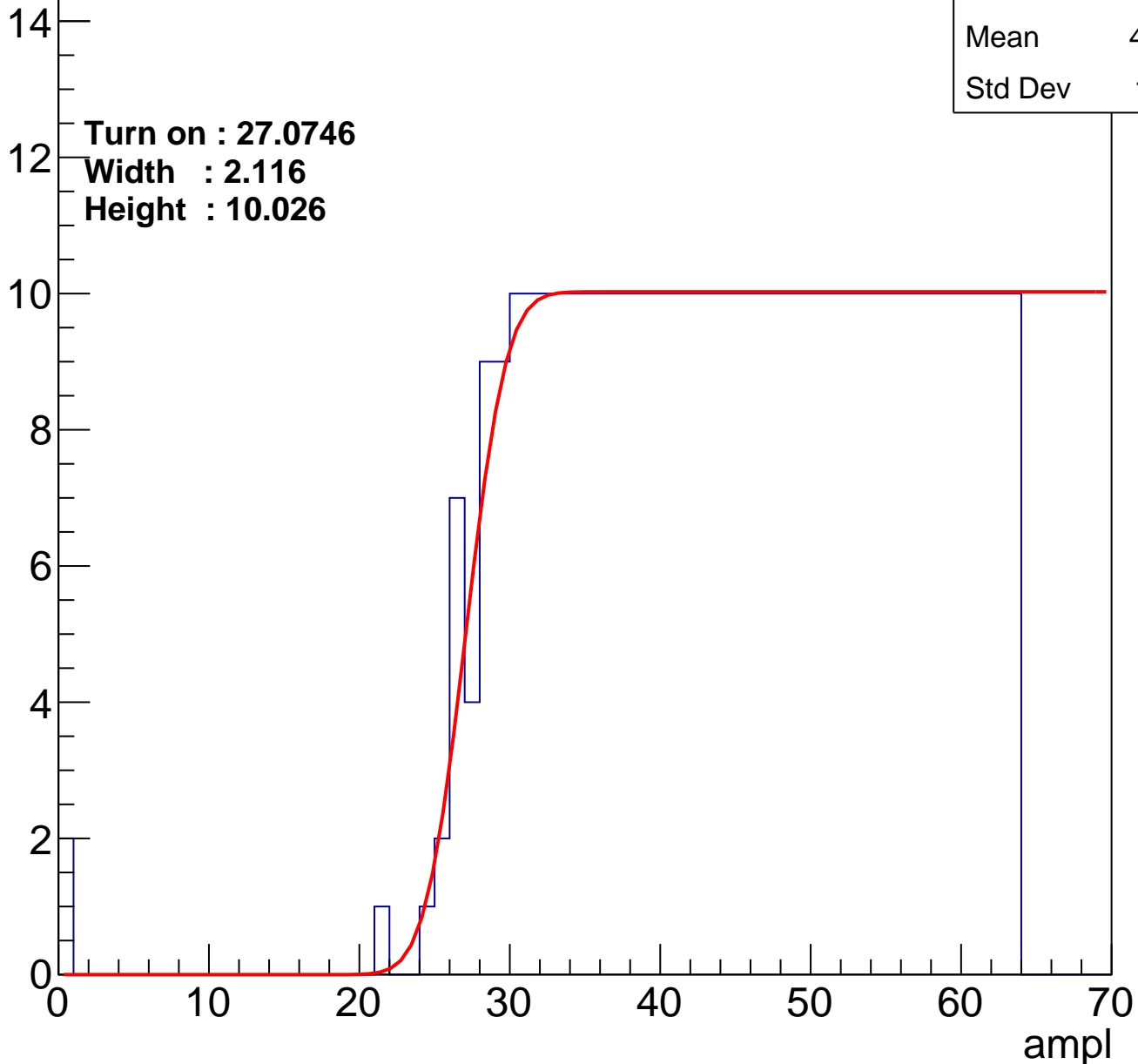
Entries	375
Mean	44.55
Std Dev	11.31

Turn on : 27.0746

Width : 2.116

Height : 10.026

Entry



B1L000S, U20-ch50

calib_packv5_042523_0143.root, FC#3, port B3

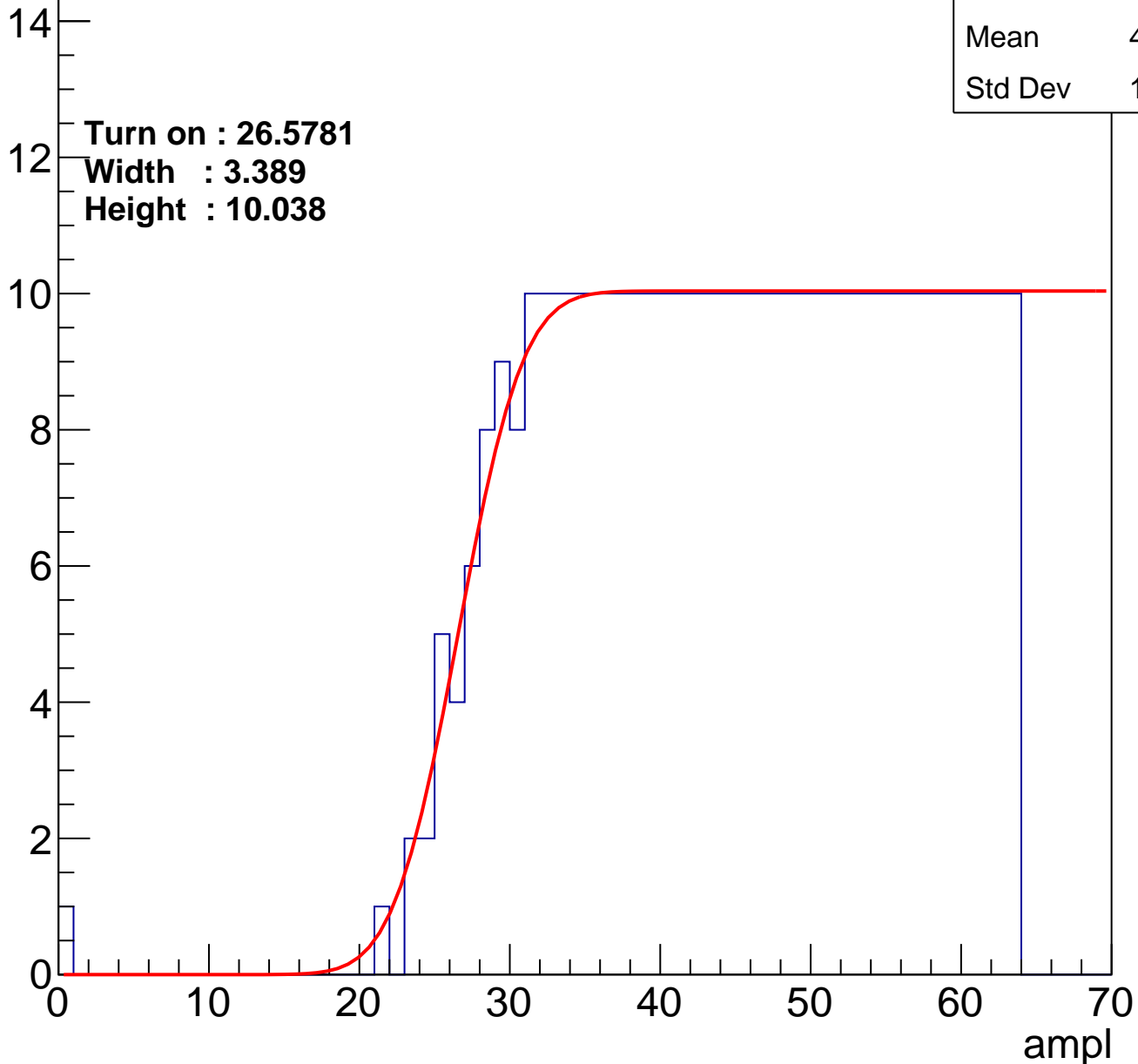
Entries	376
Mean	44.52
Std Dev	11.23

Turn on : 26.5781

Width : 3.389

Height : 10.038

Entry



B1L000S, U20-ch51

calib_packv5_042523_0143.root, FC#3, port B3

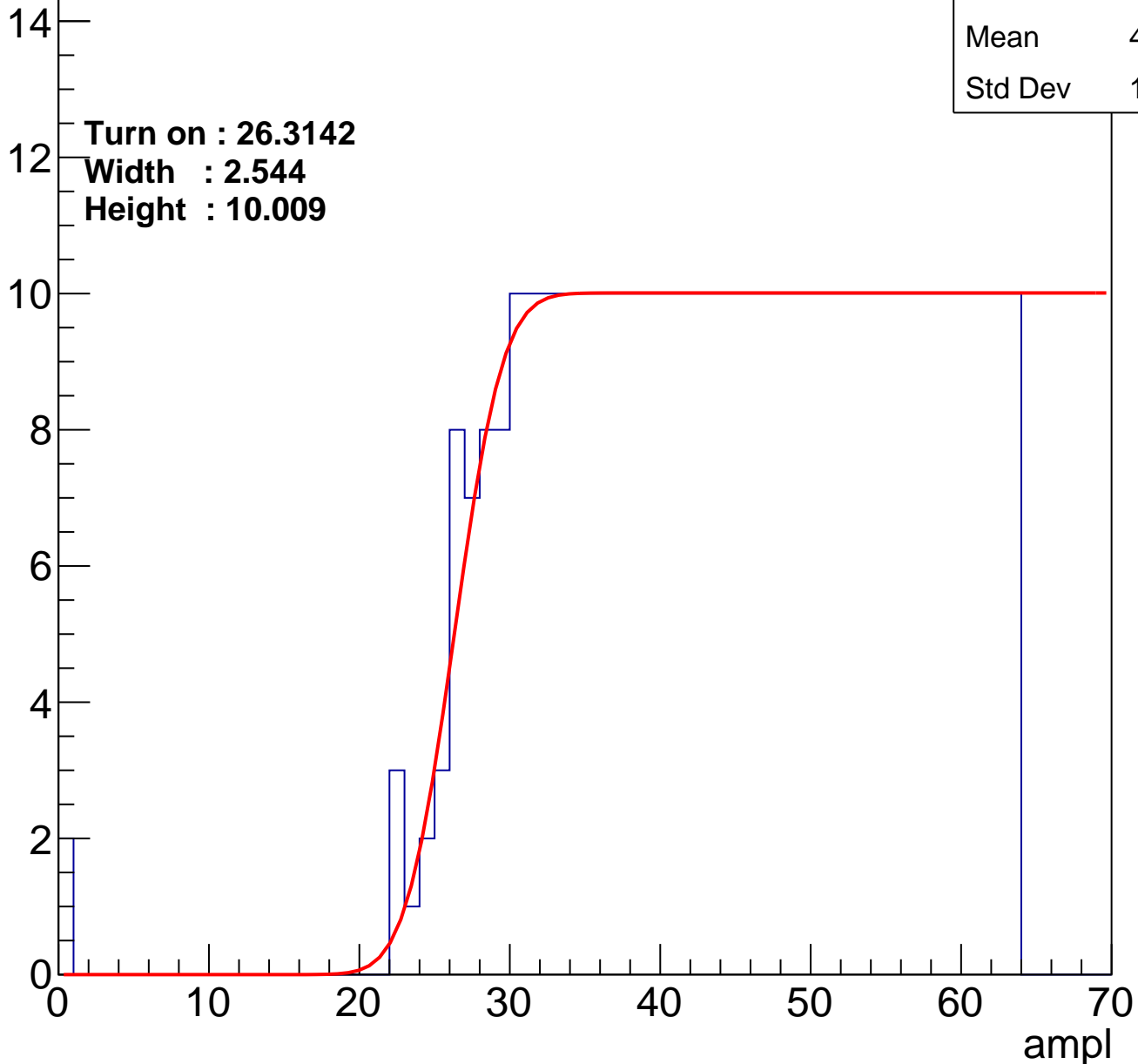
Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.3142

Width : 2.544

Height : 10.009

Entry



B1L000S, U20-ch52

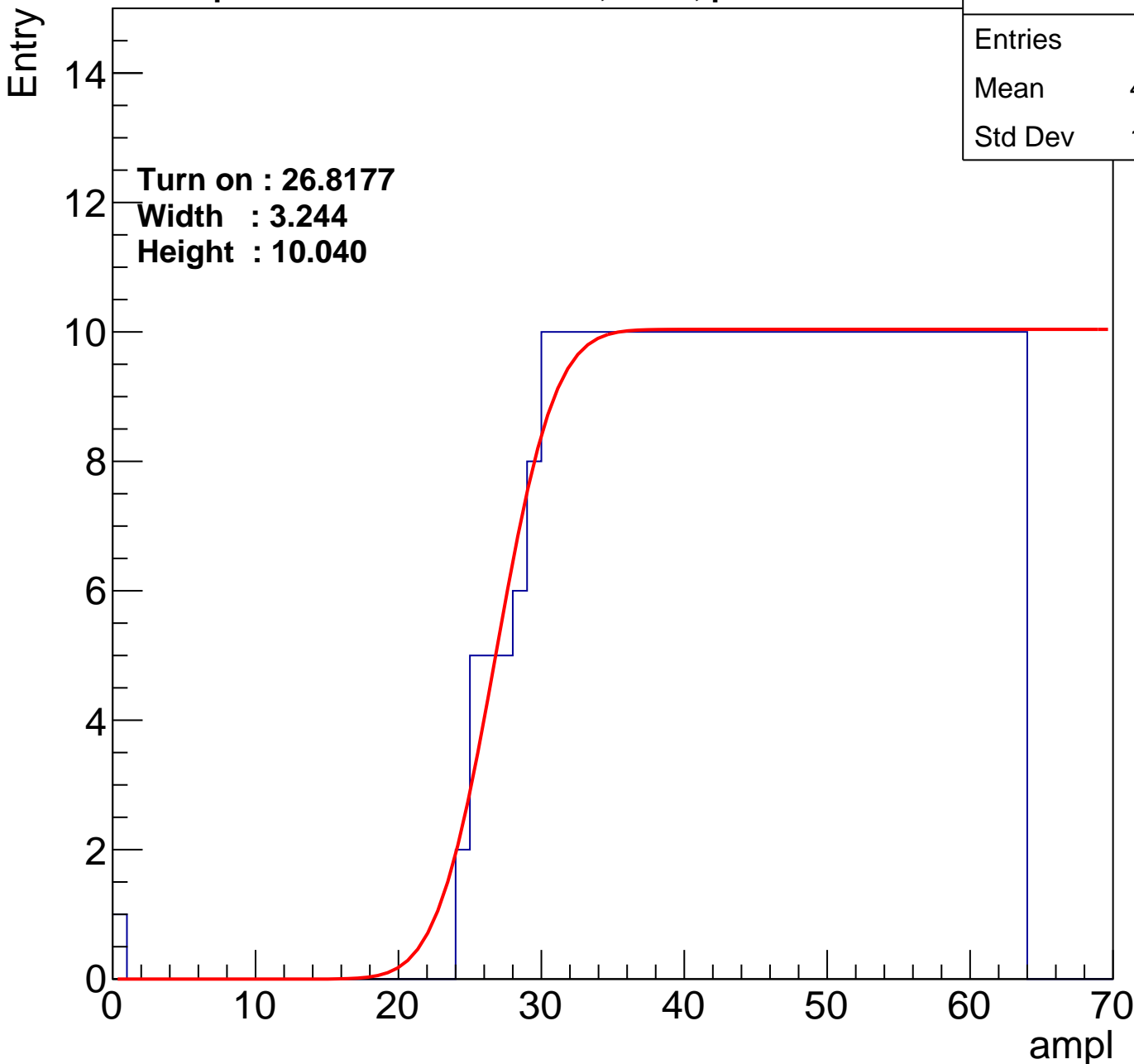
calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.75
Std Dev	11.07

Turn on : 26.8177

Width : 3.244

Height : 10.040



B1L000S, U20-ch53

calib_packv5_042523_0143.root, FC#3, port B3

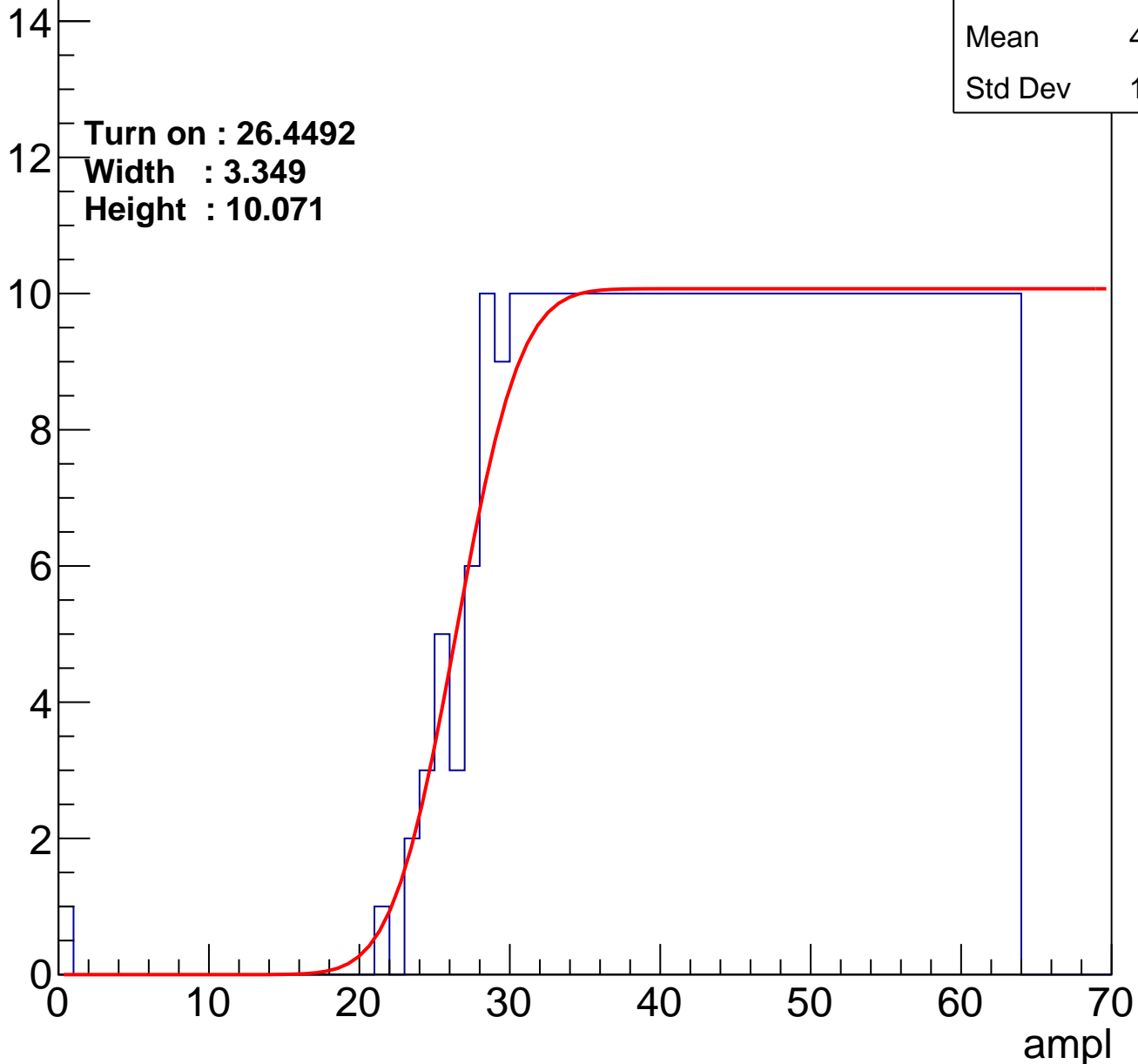
Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 26.4492

Width : 3.349

Height : 10.071

Entry



B1L000S, U20-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.47
Std Dev	11.35

Turn on : 26.5842

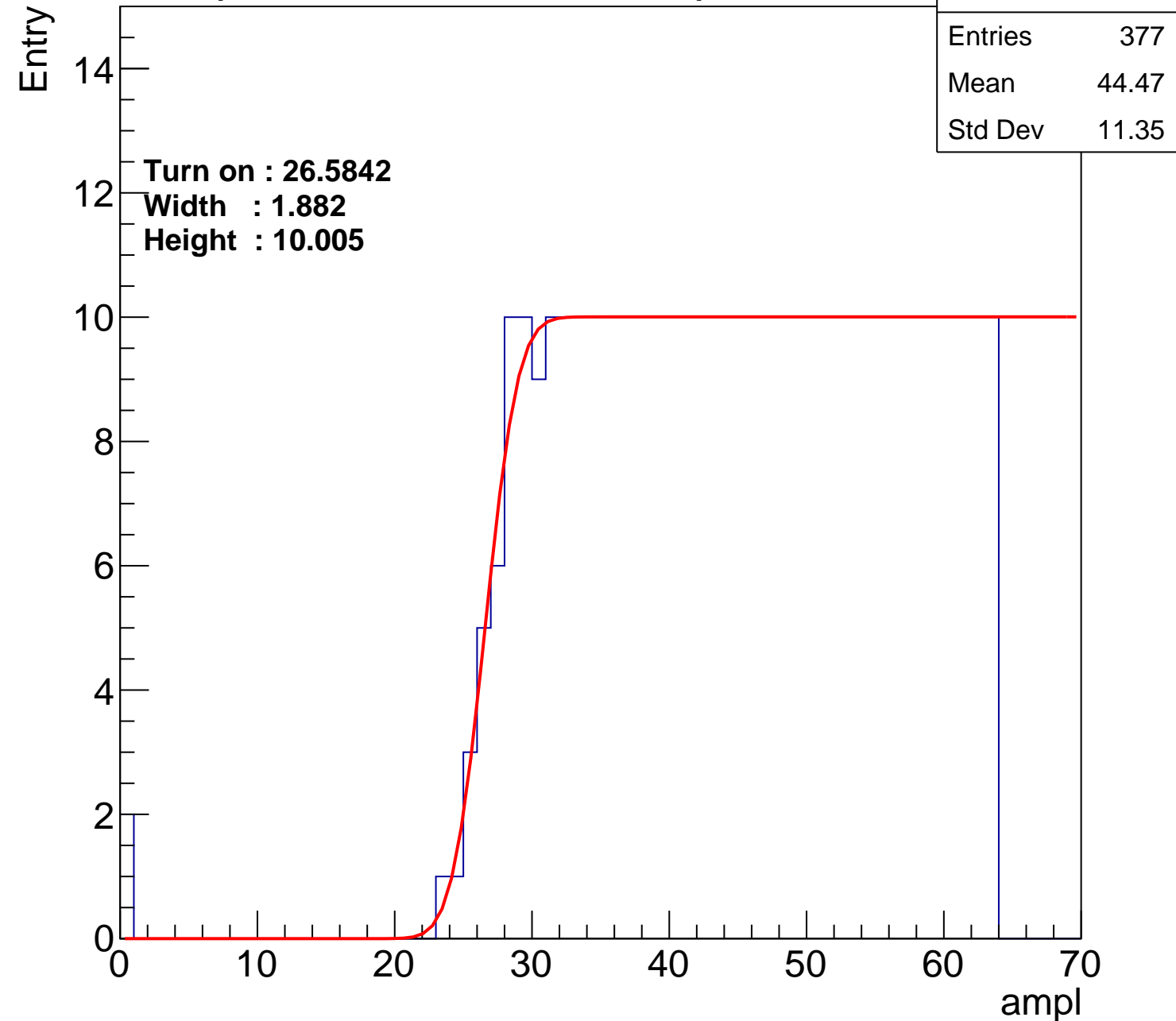
Width : 1.882

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 24.9810

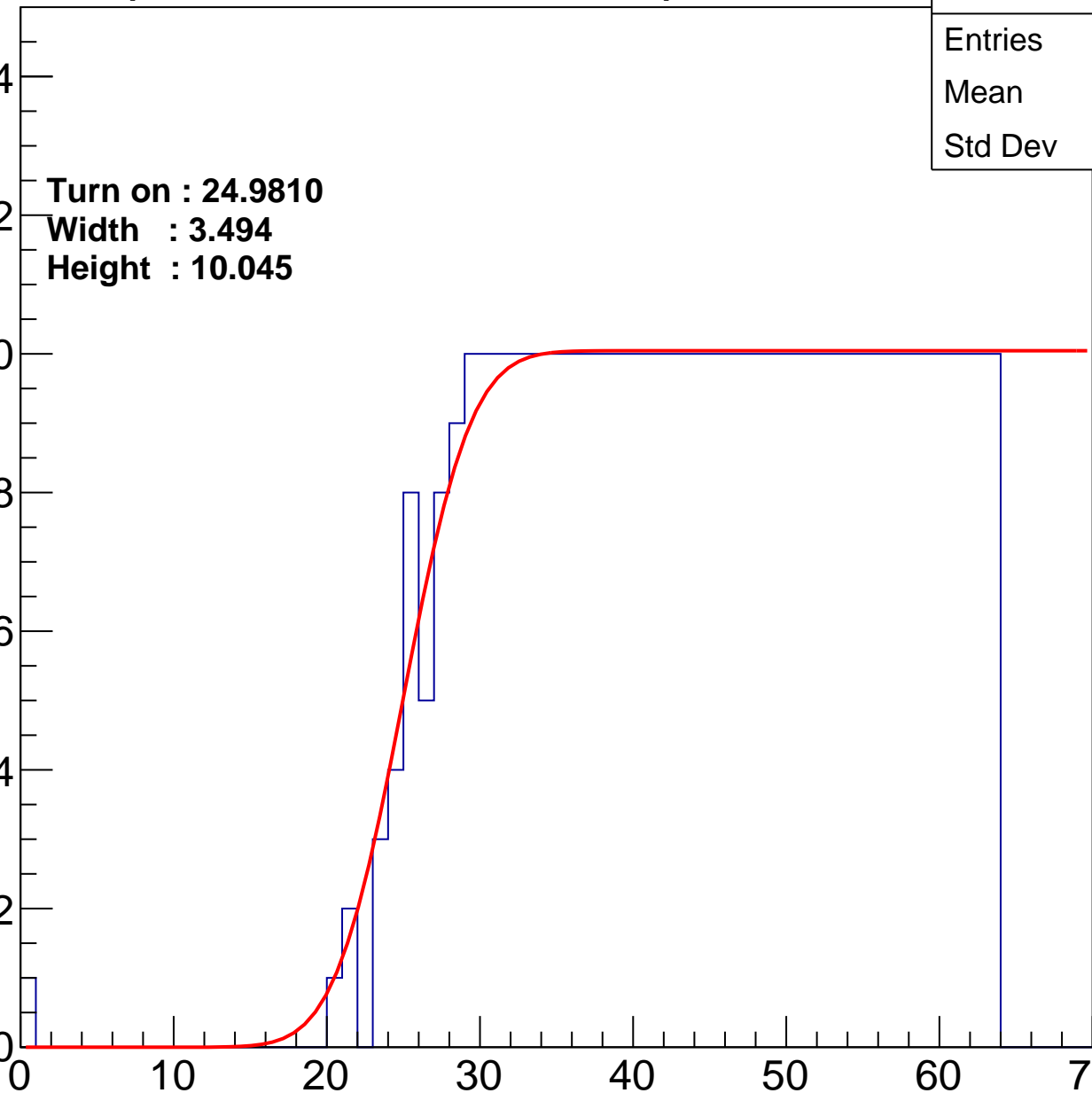
Width : 3.494

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 27.1376

Width : 3.013

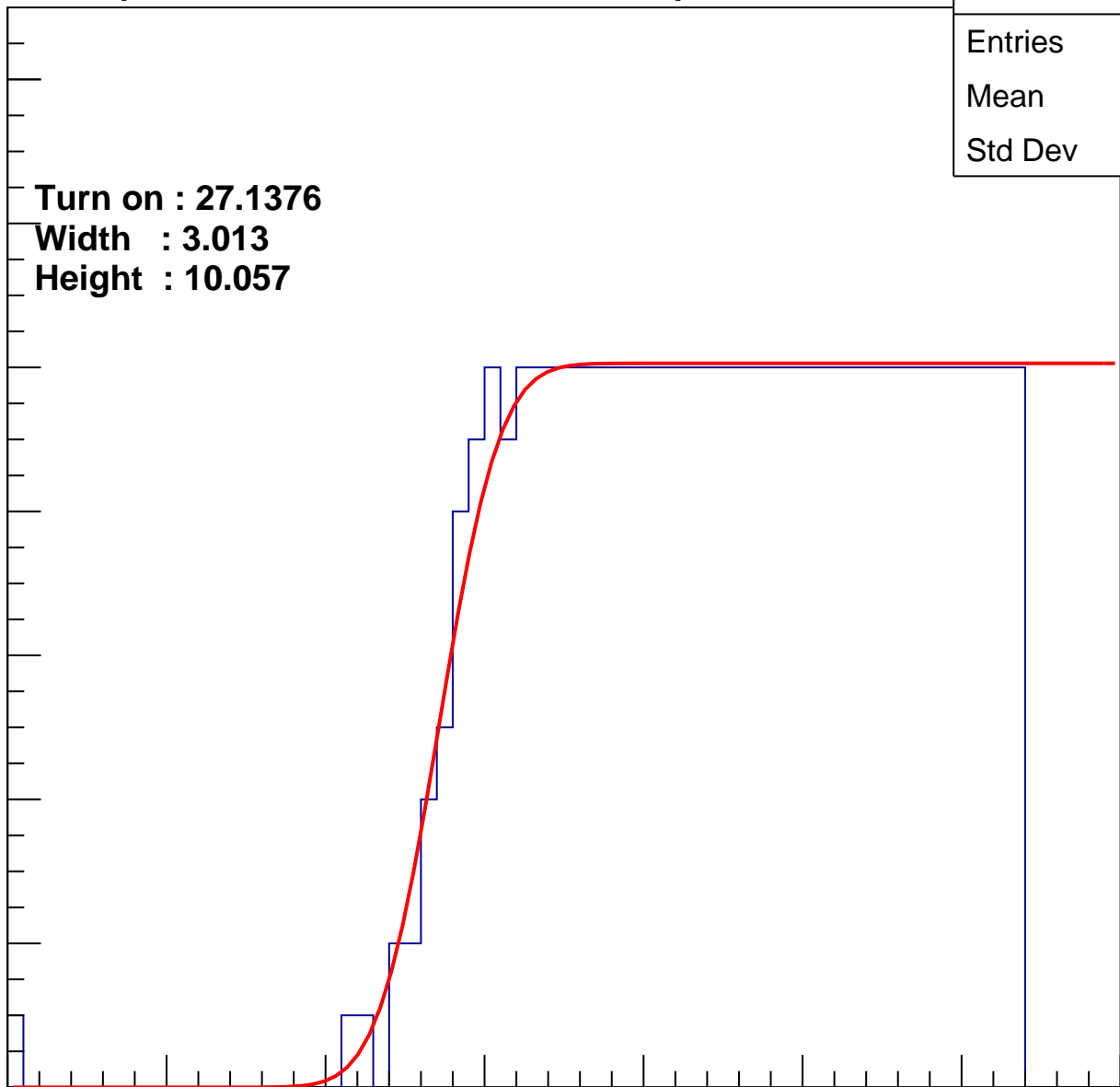
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U20-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.3022

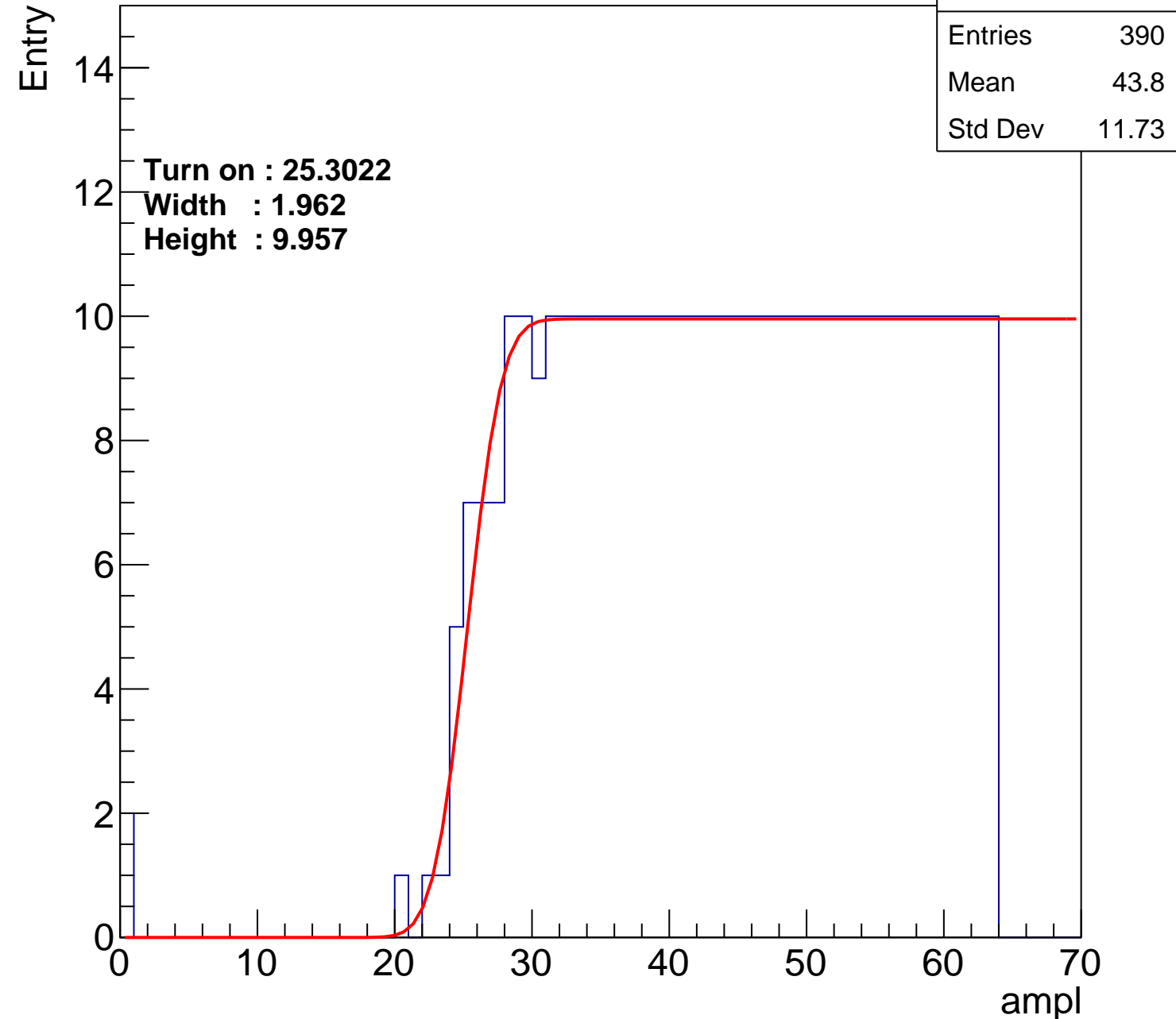
Width : 1.962

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 26.8694

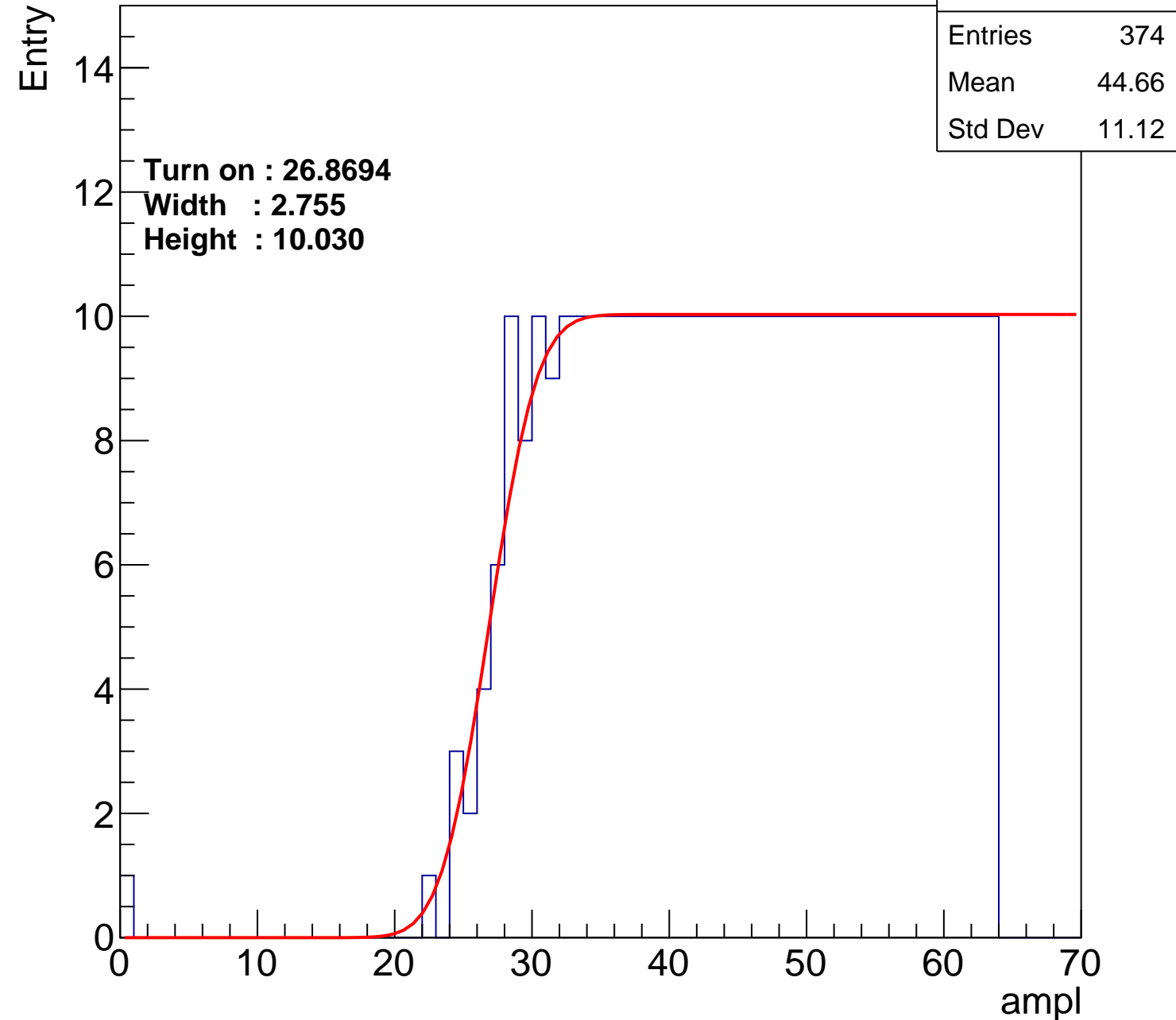
Width : 2.755

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch59

calib_packv5_042523_0143.root, FC#3, port B3

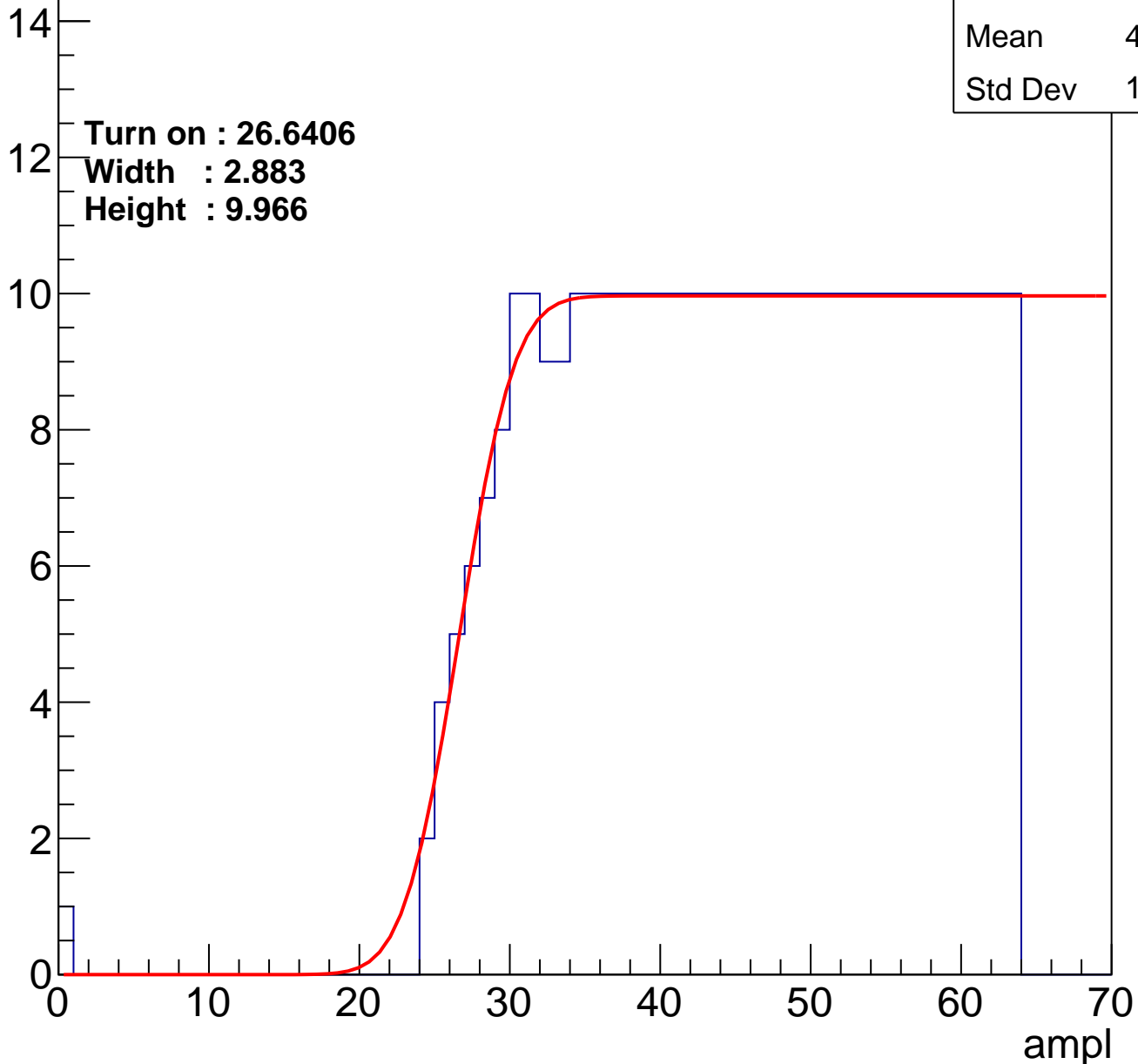
Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 26.6406

Width : 2.883

Height : 9.966

Entry



B1L000S, U20-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.2456

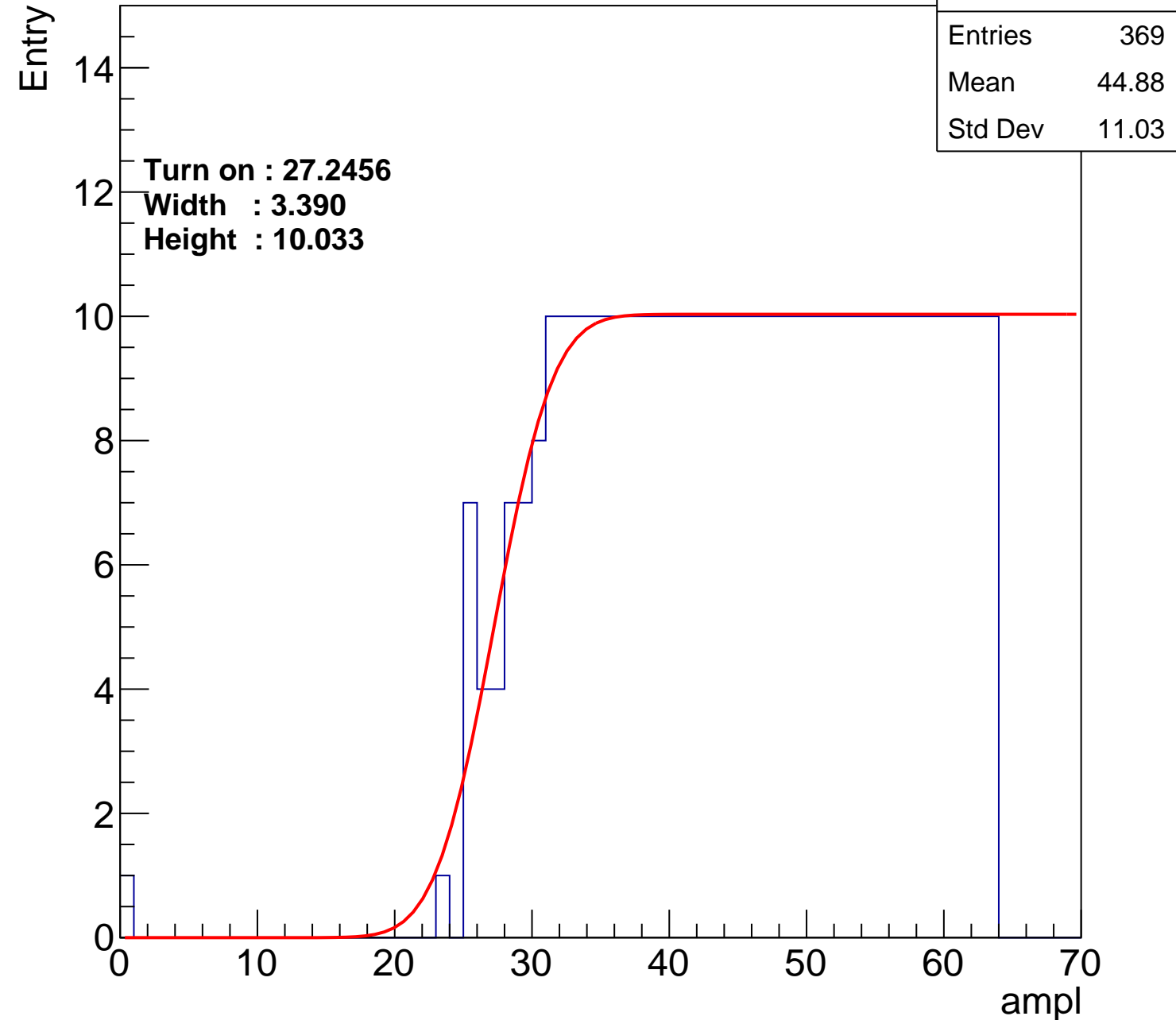
Width : 3.390

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.4166

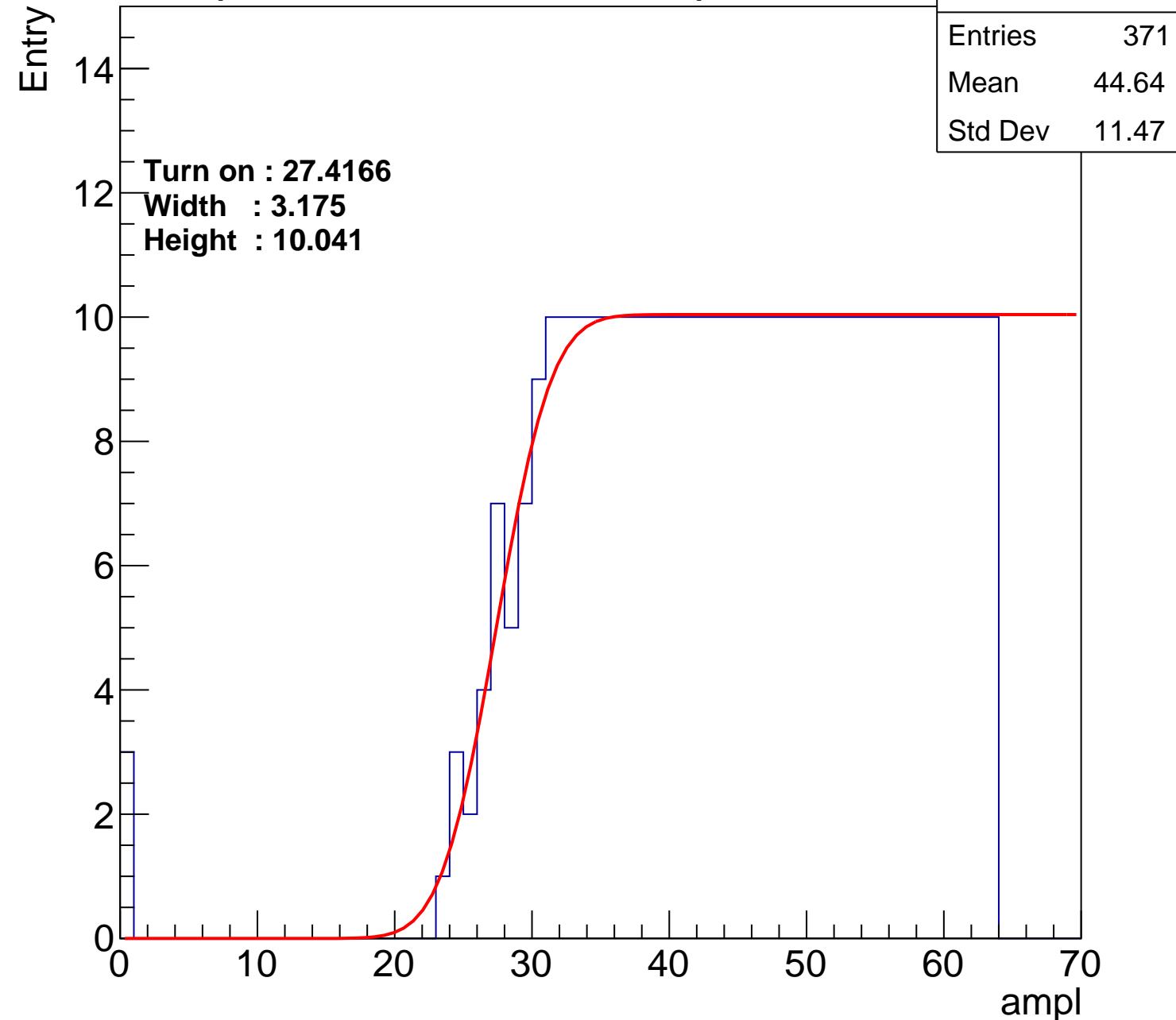
Width : 3.175

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch62

calib_packv5_042523_0143.root, FC#3, port B3

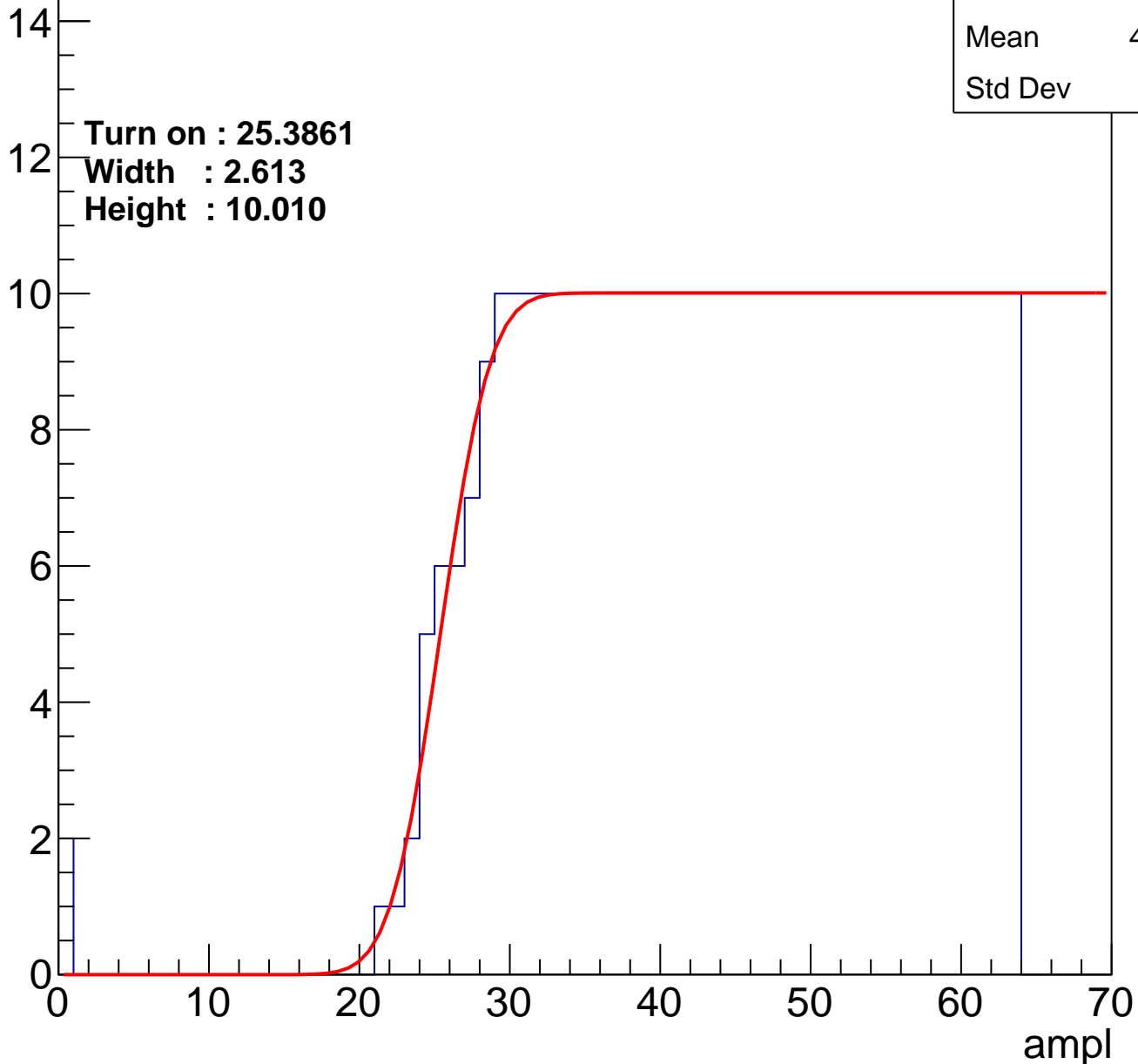
Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.3861

Width : 2.613

Height : 10.010

Entry



B1L000S, U20-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.69
Std Dev	11.25

Turn on : 27.3684

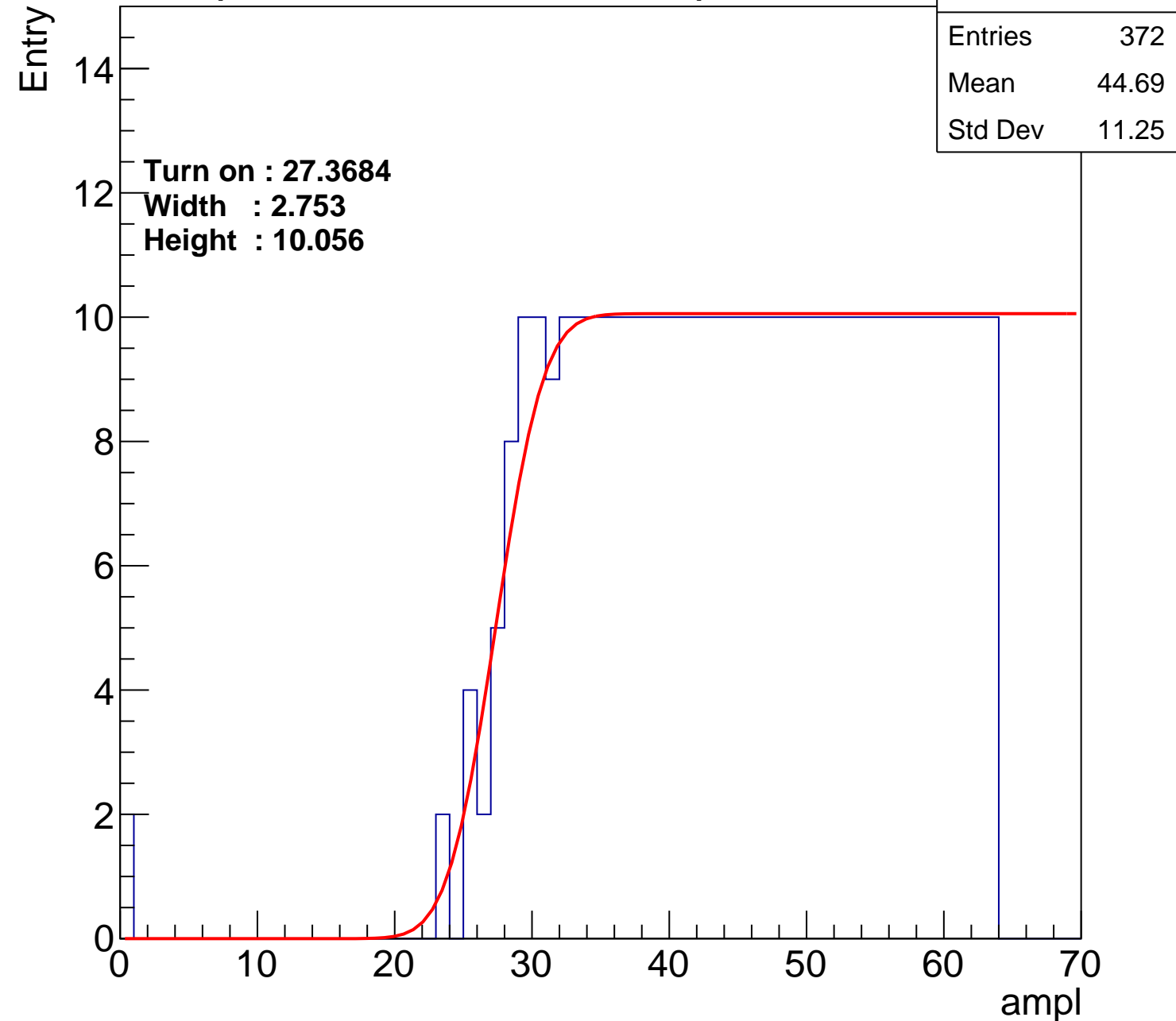
Width : 2.753

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.84
Std Dev	11.08

Turn on : 27.1977

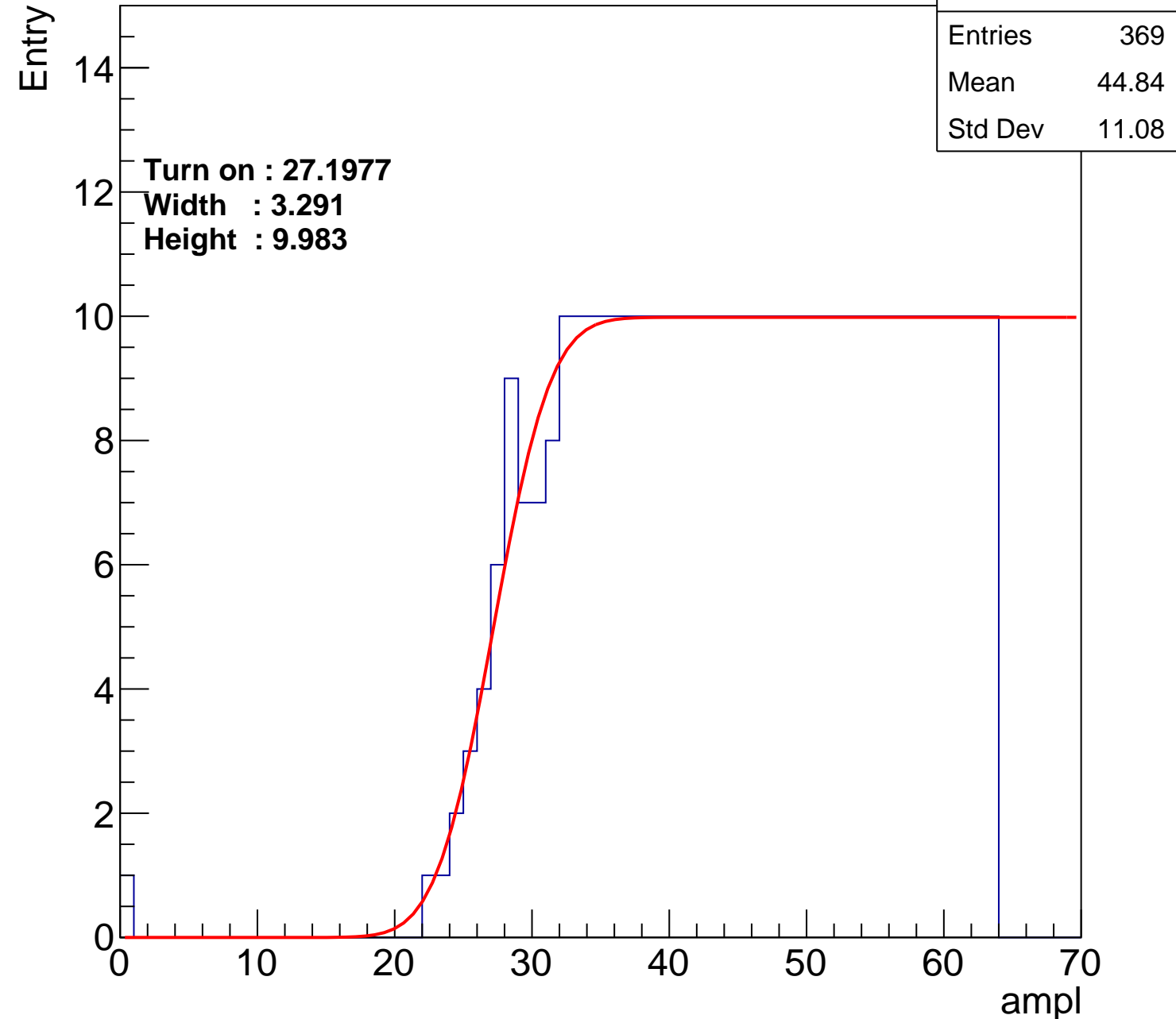
Width : 3.291

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.5194

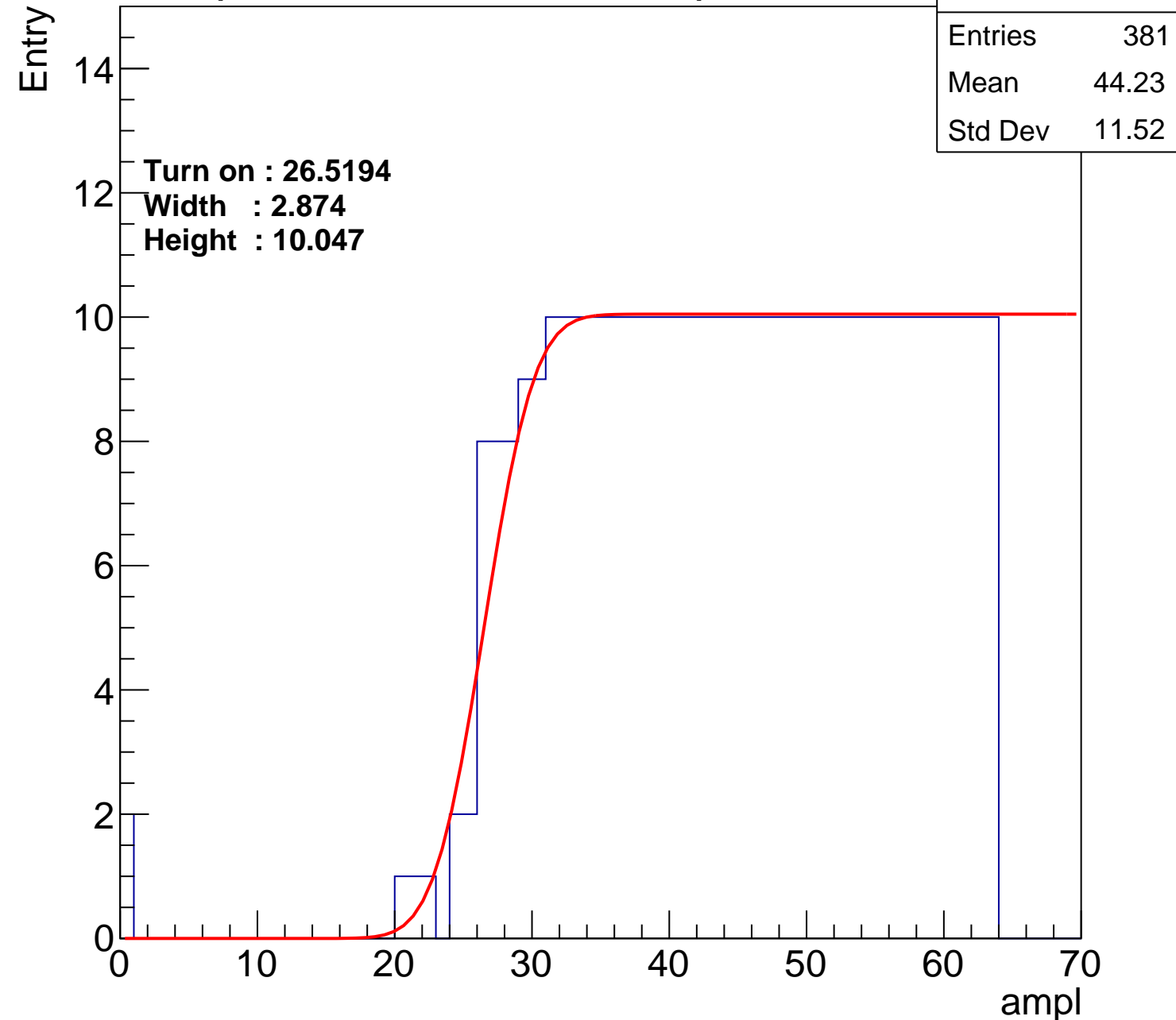
Width : 2.874

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch66

calib_packv5_042523_0143.root, FC#3, port B3

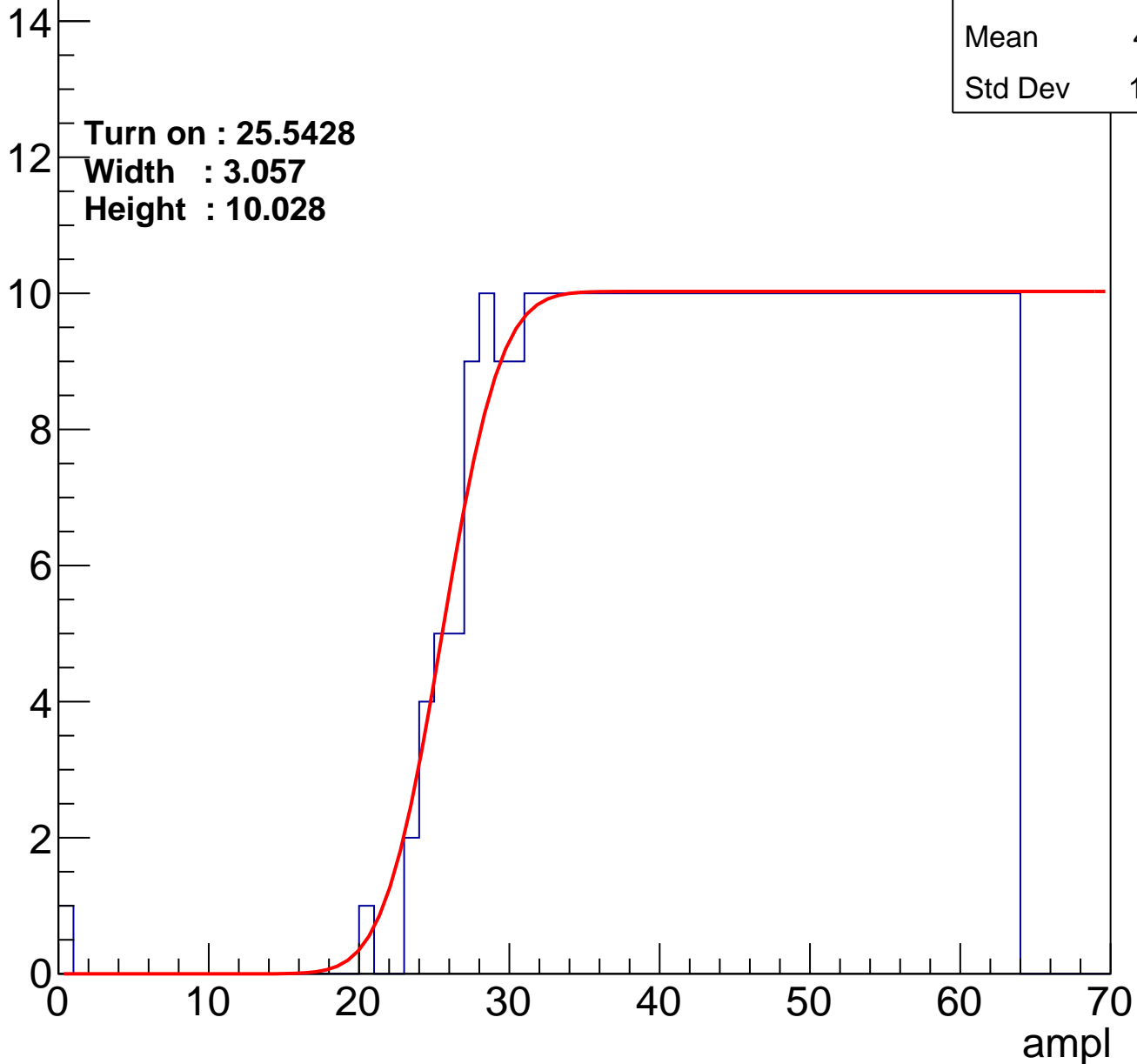
Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.5428

Width : 3.057

Height : 10.028

Entry



B1L000S, U20-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.6086

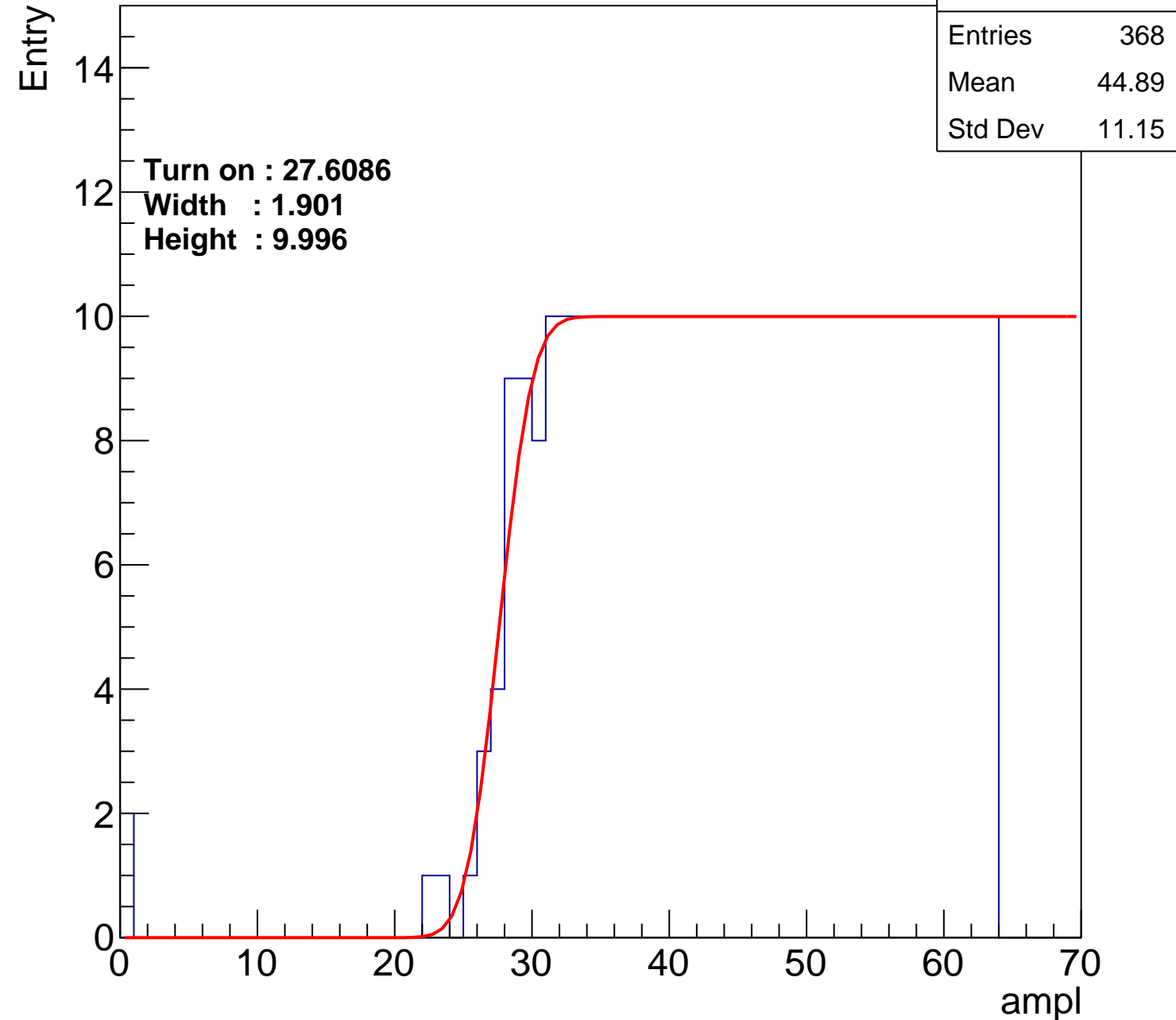
Width : 1.901

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 26.2070

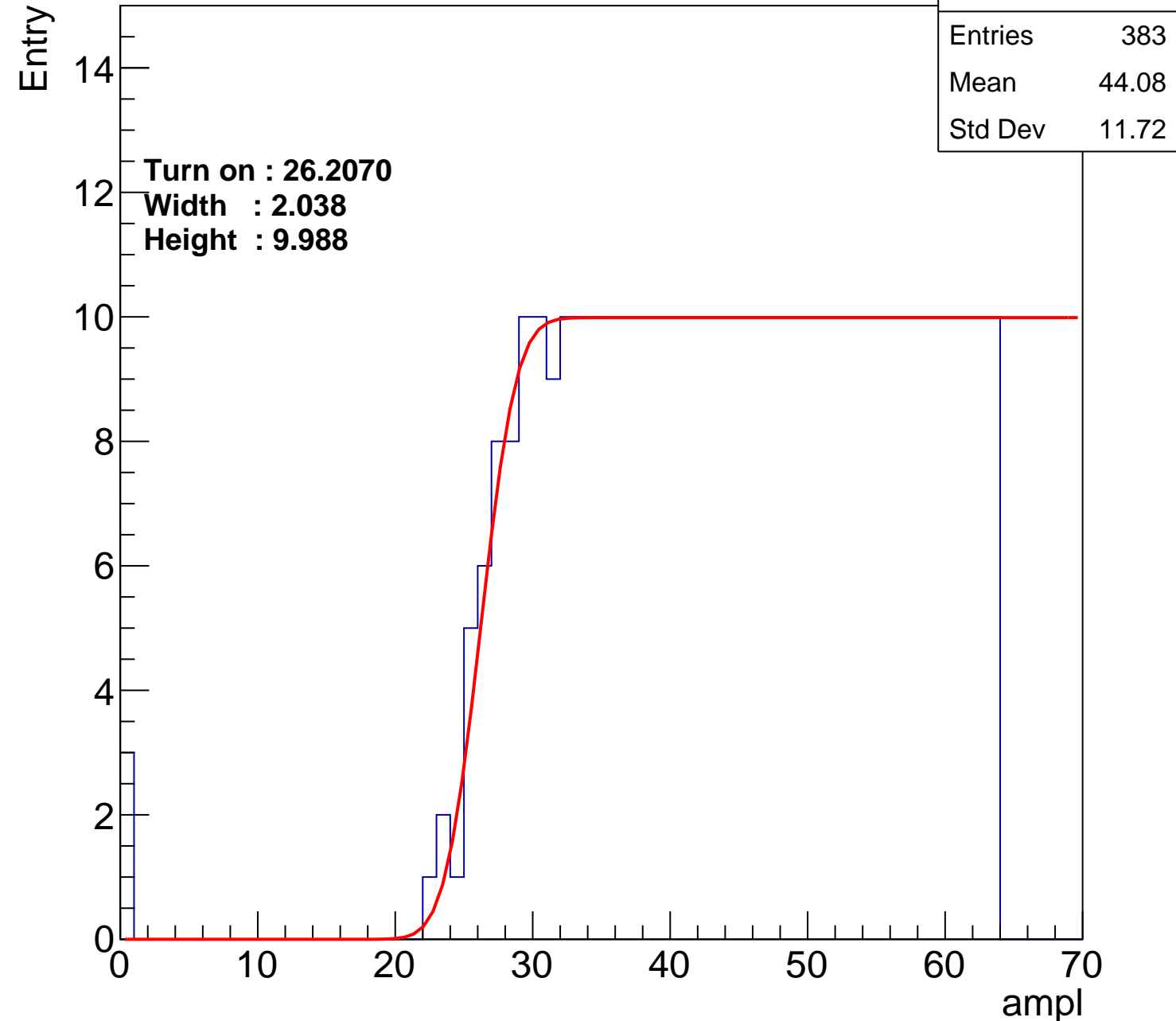
Width : 2.038

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch69

calib_packv5_042523_0143.root, FC#3, port B3

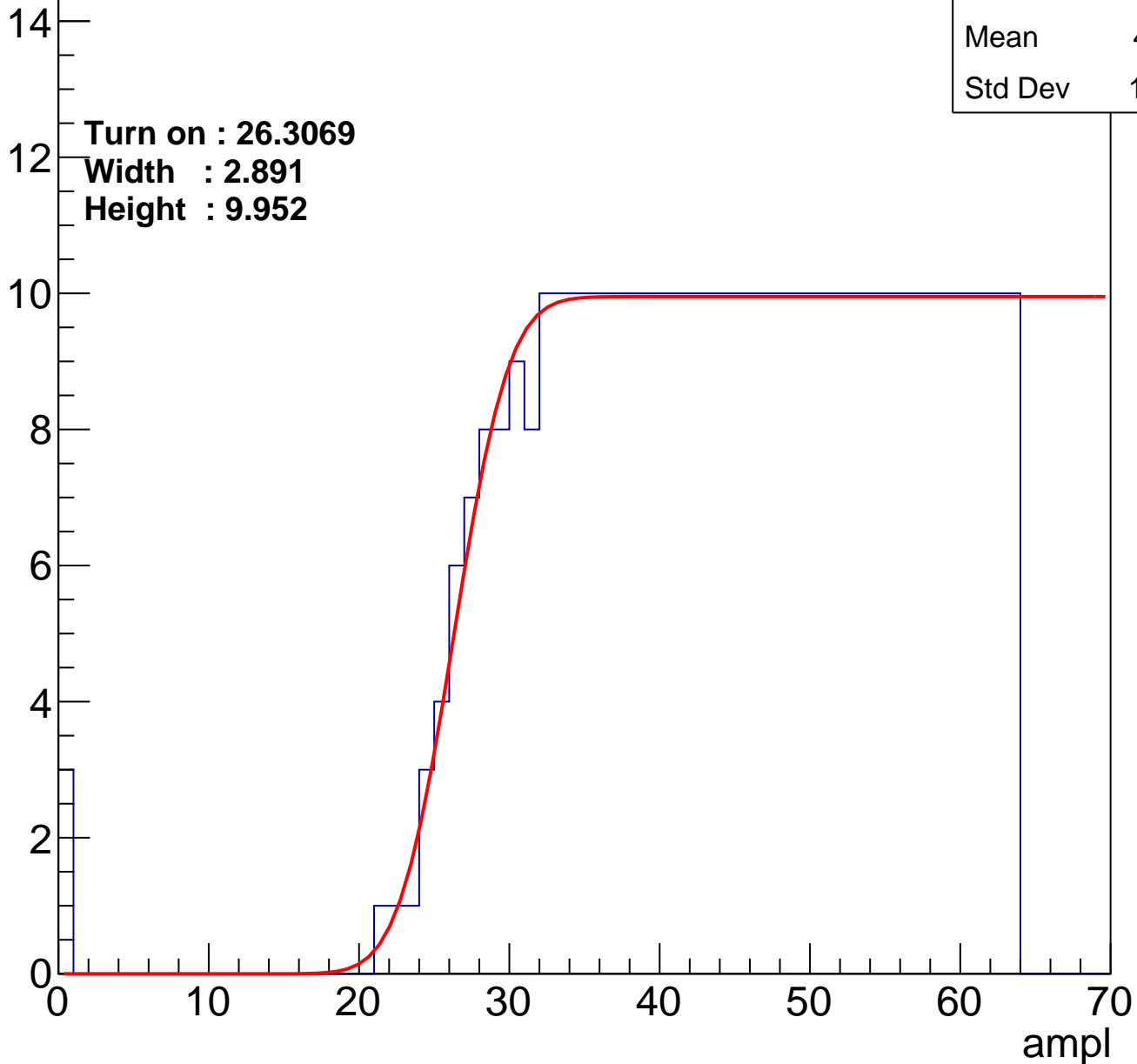
Entries	379
Mean	44.21
Std Dev	11.72

Turn on : 26.3069

Width : 2.891

Height : 9.952

Entry



B1L000S, U20-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.1840

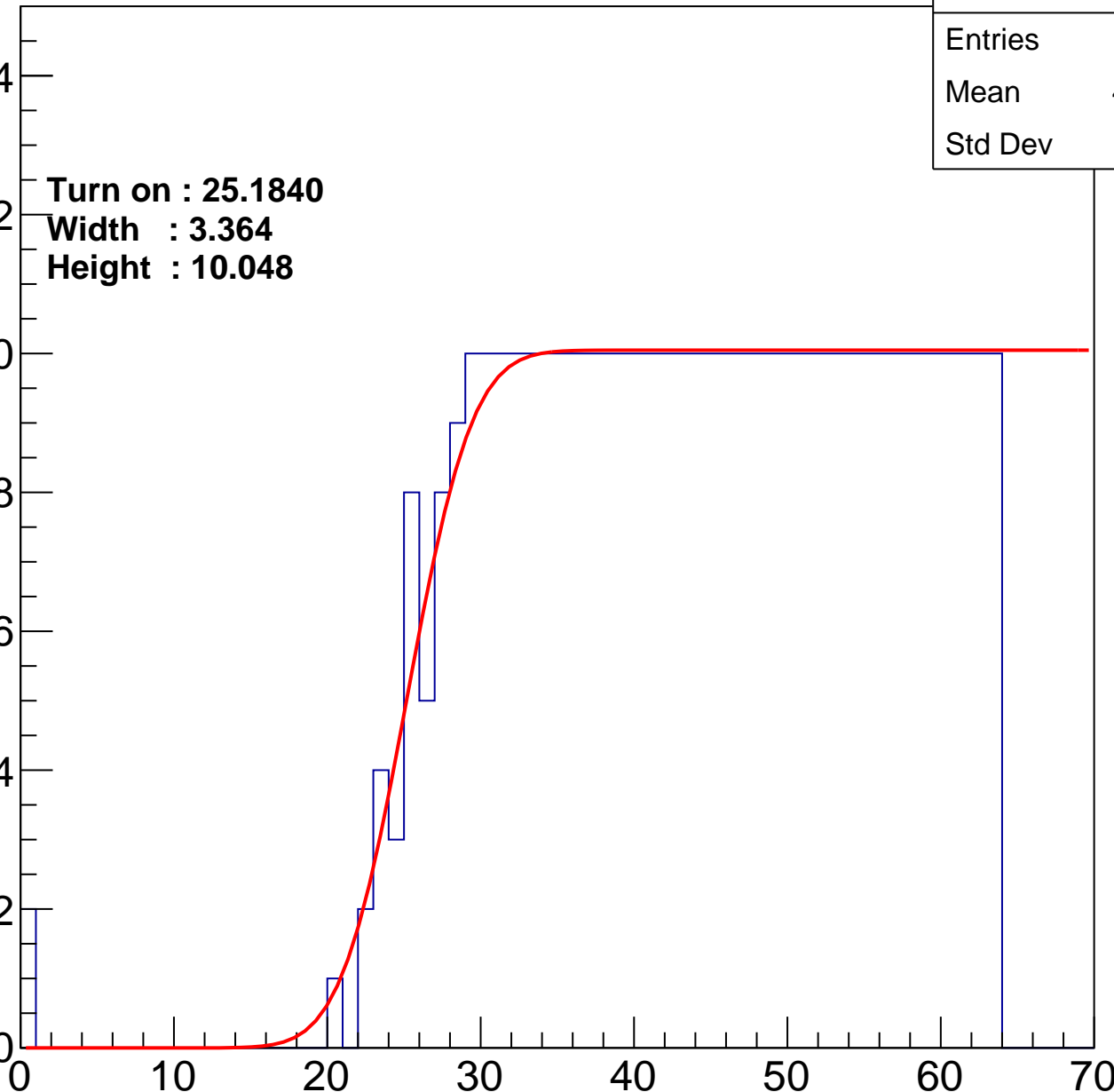
Width : 3.364

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.06
Std Dev	11.04

Turn on : 27.9563

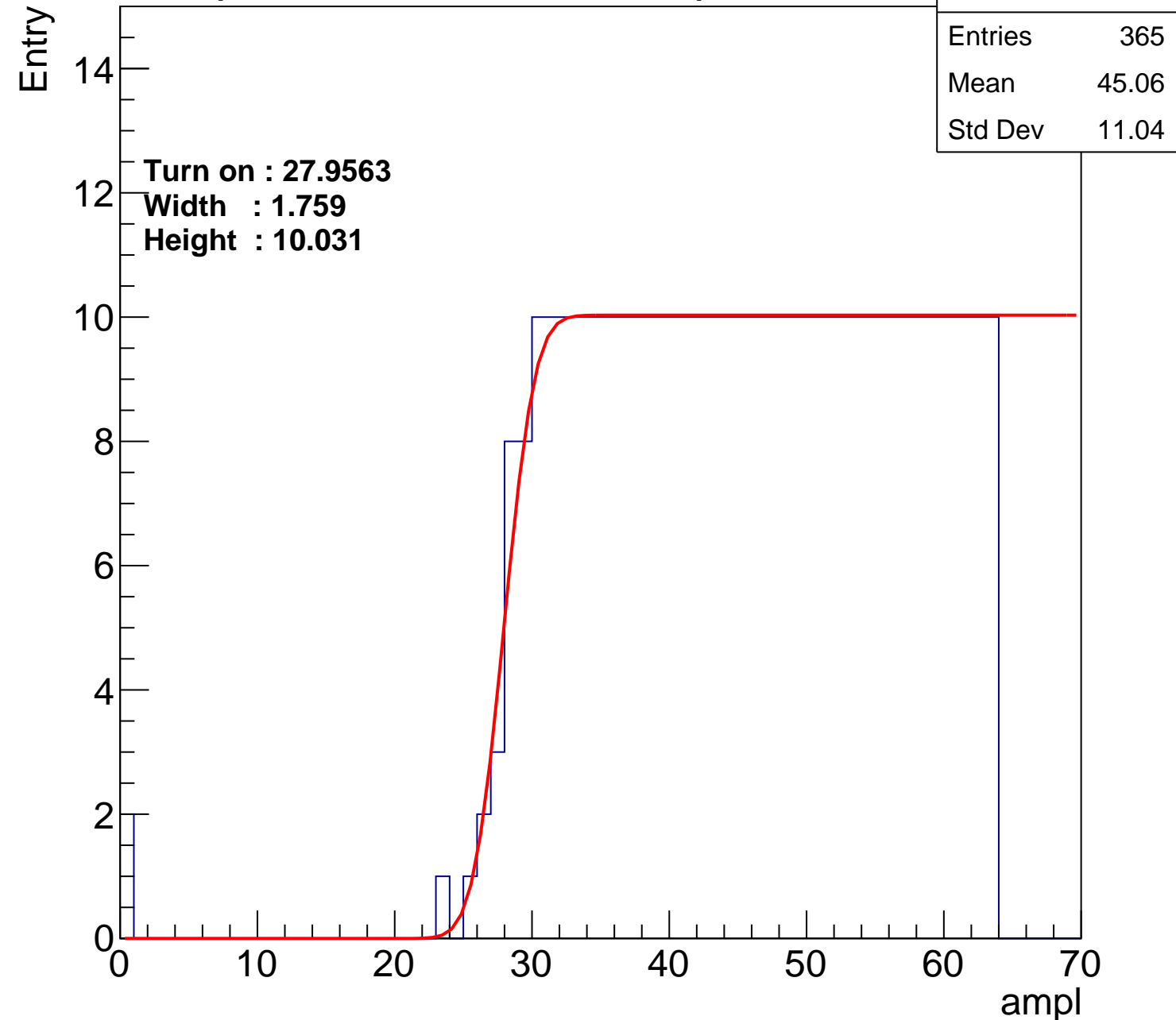
Width : 1.759

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.5808

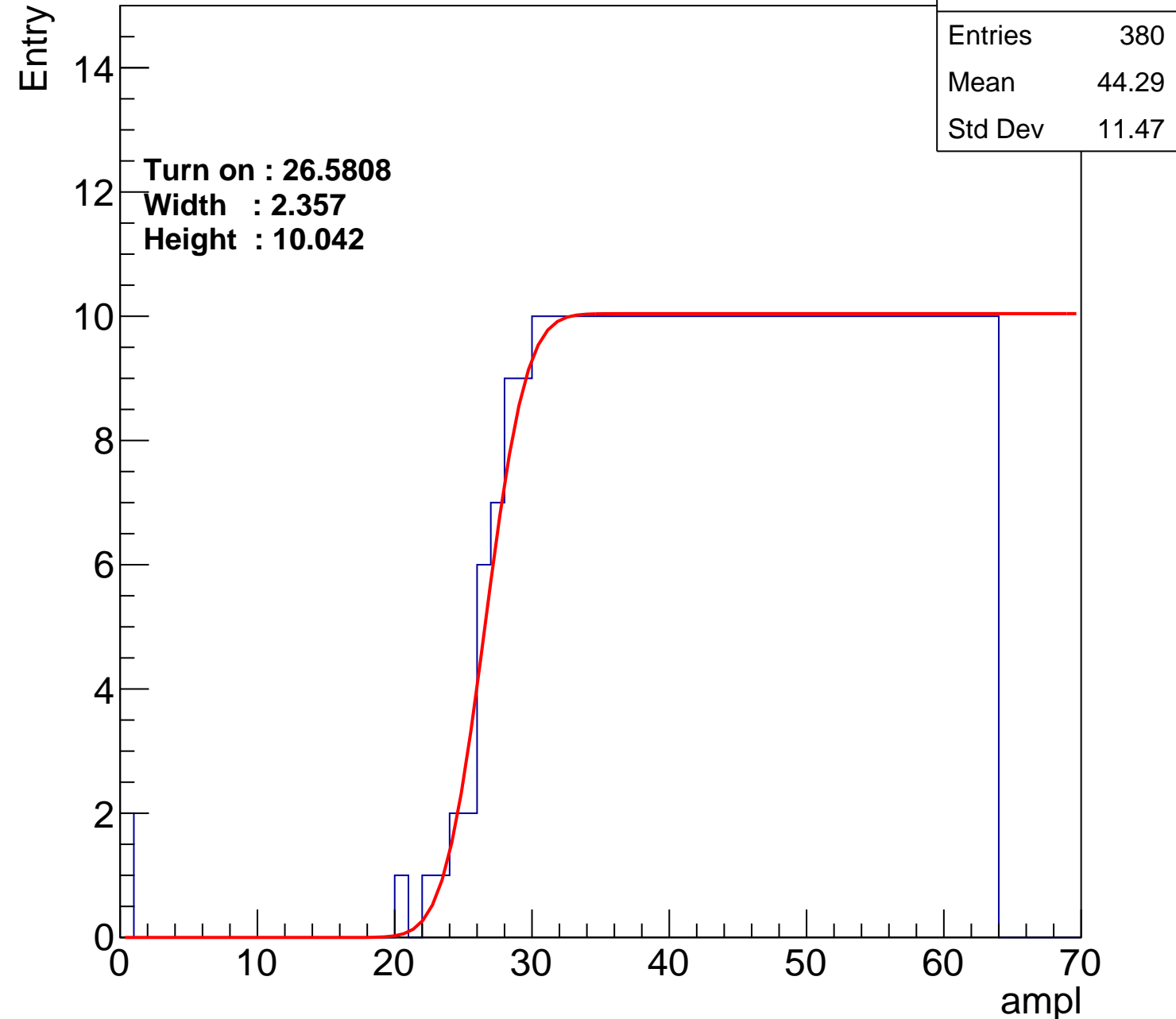
Width : 2.357

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.4201

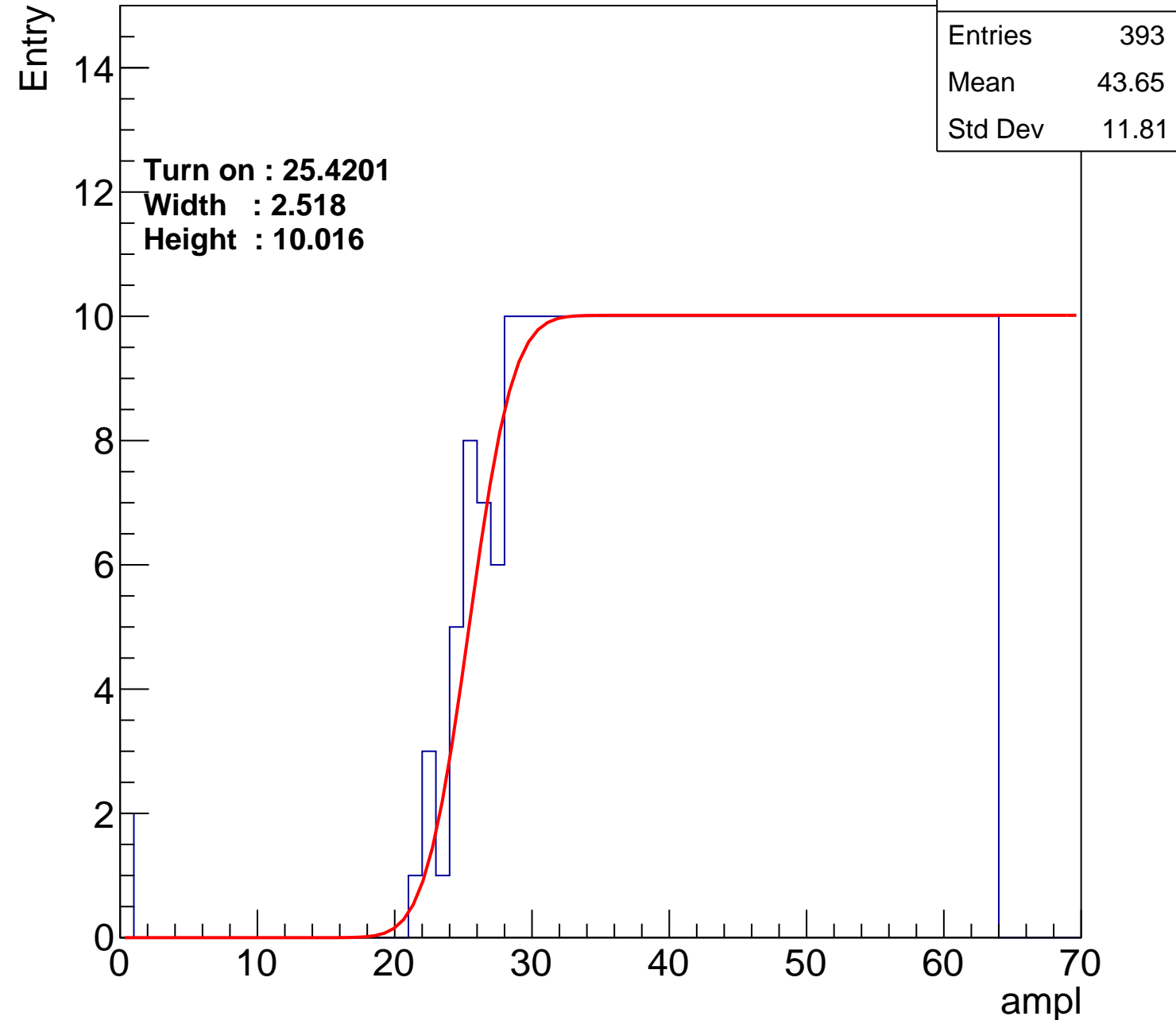
Width : 2.518

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.8864

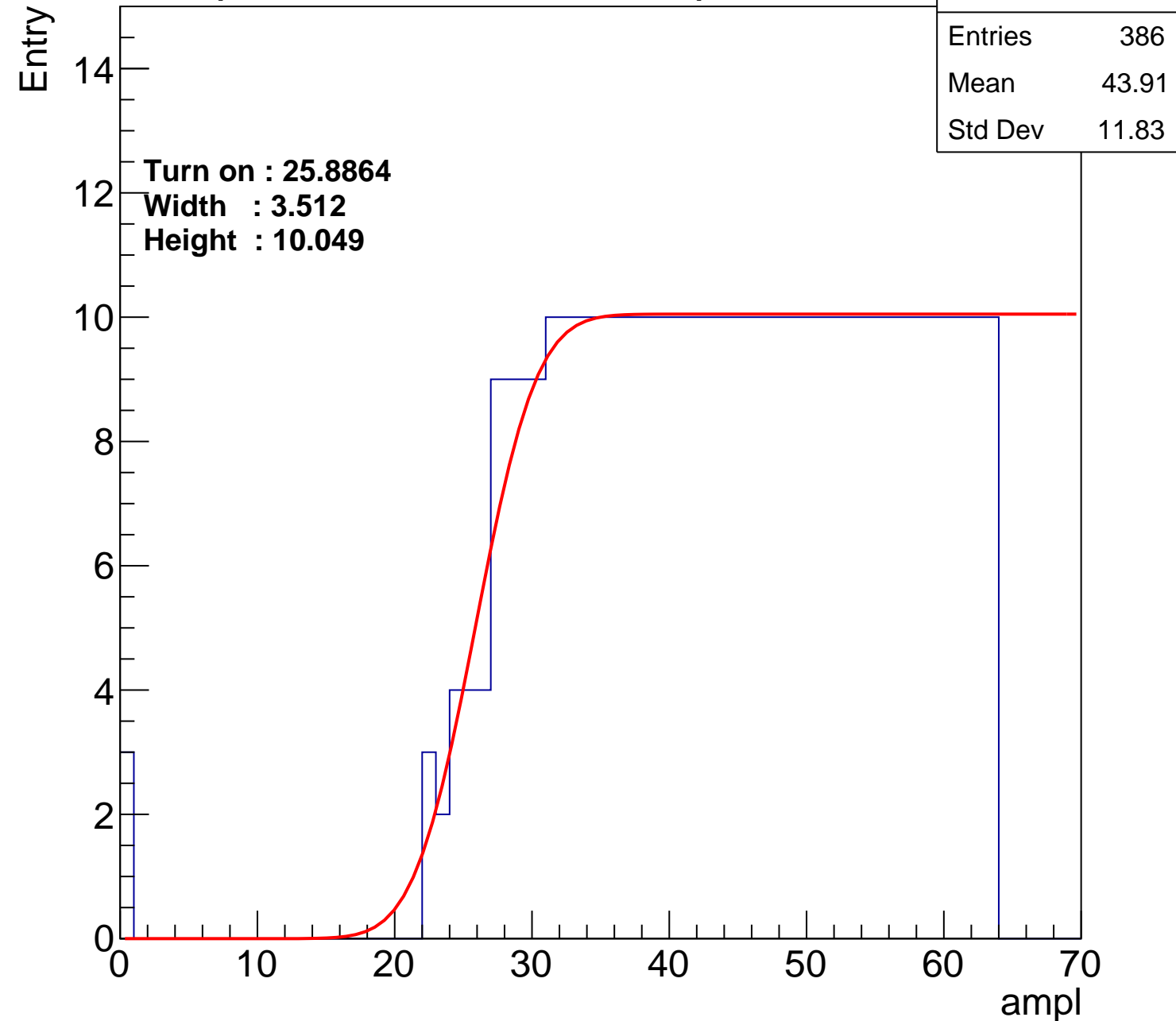
Width : 3.512

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch75

calib_packv5_042523_0143.root, FC#3, port B3

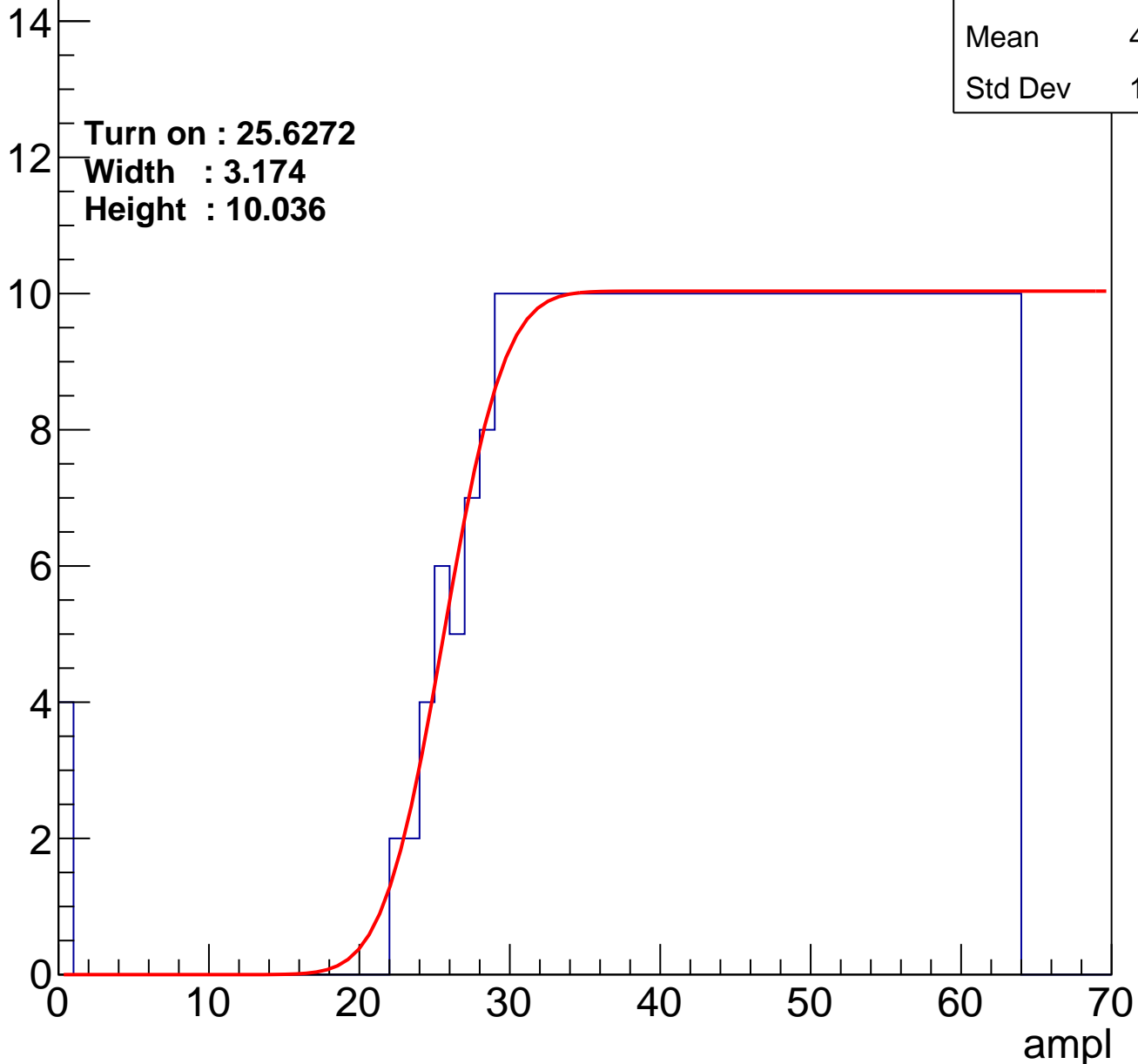
Entries	388
Mean	43.76
Std Dev	12.03

Turn on : 25.6272

Width : 3.174

Height : 10.036

Entry



B1L000S, U20-ch76

calib_packv5_042523_0143.root, FC#3, port B3

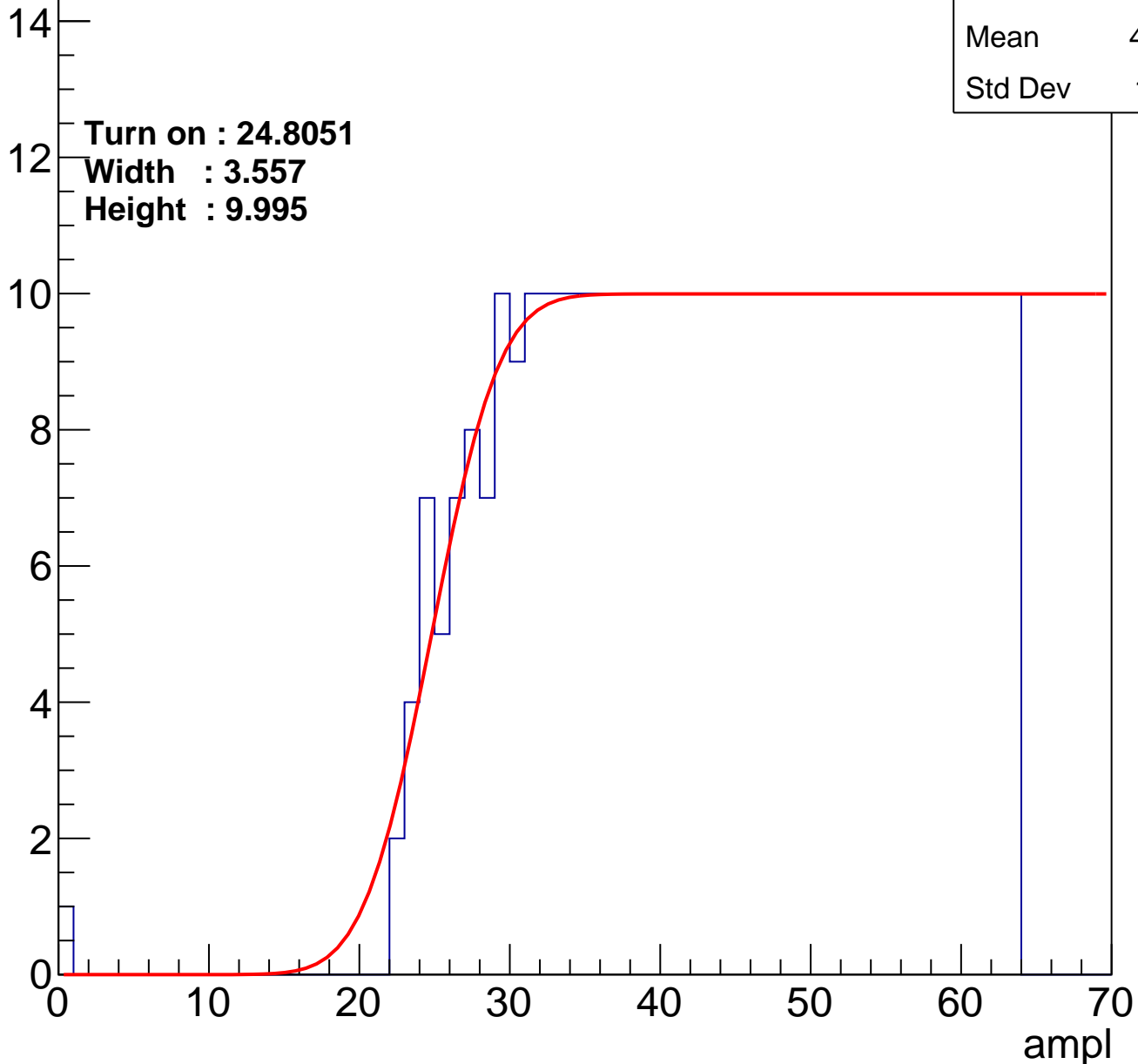
Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 24.8051

Width : 3.557

Height : 9.995

Entry



B1L000S, U20-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.93
Std Dev	11.68

Turn on : 25.6799

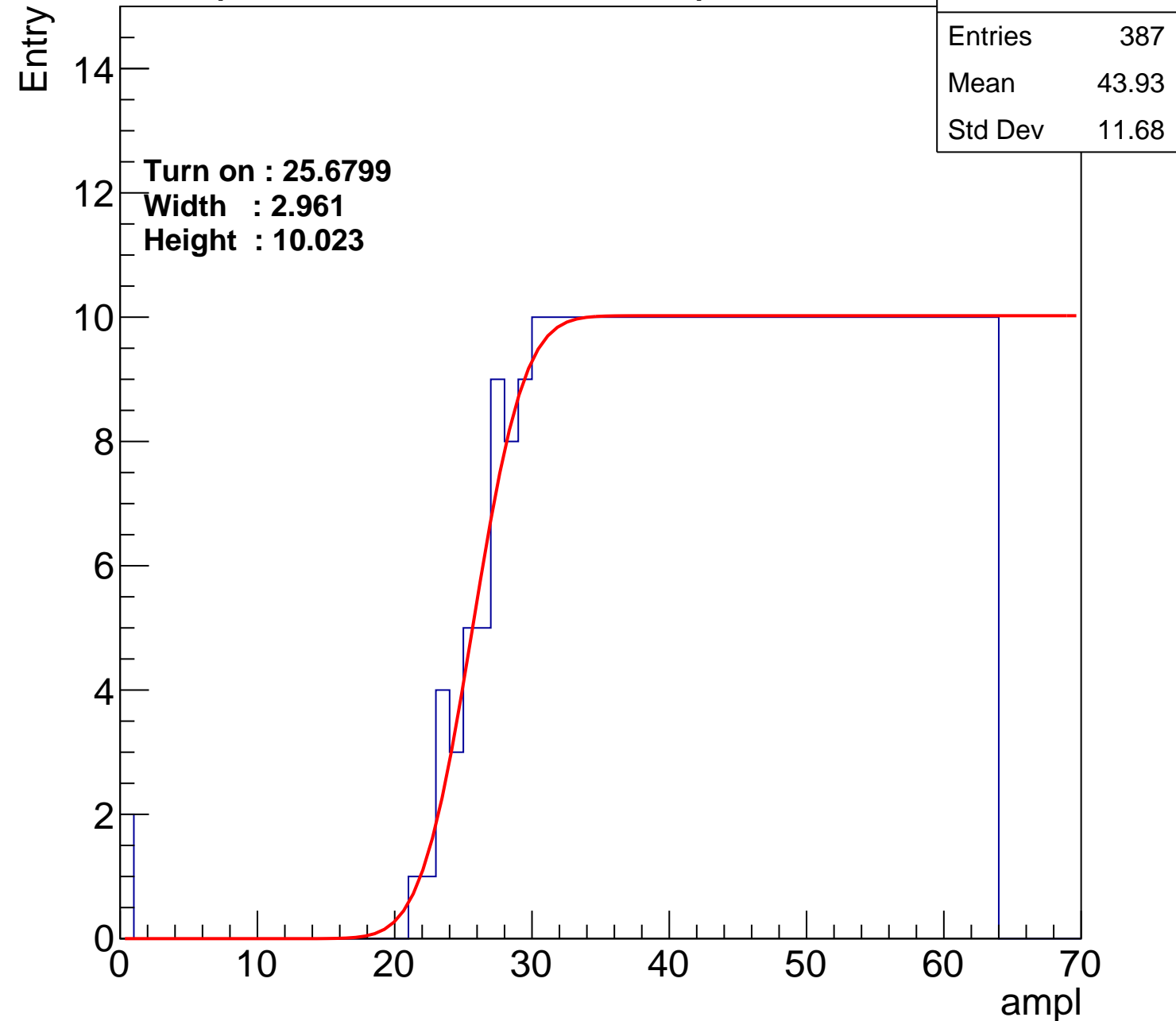
Width : 2.961

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.52
Std Dev	11.86

Turn on : 24.6595

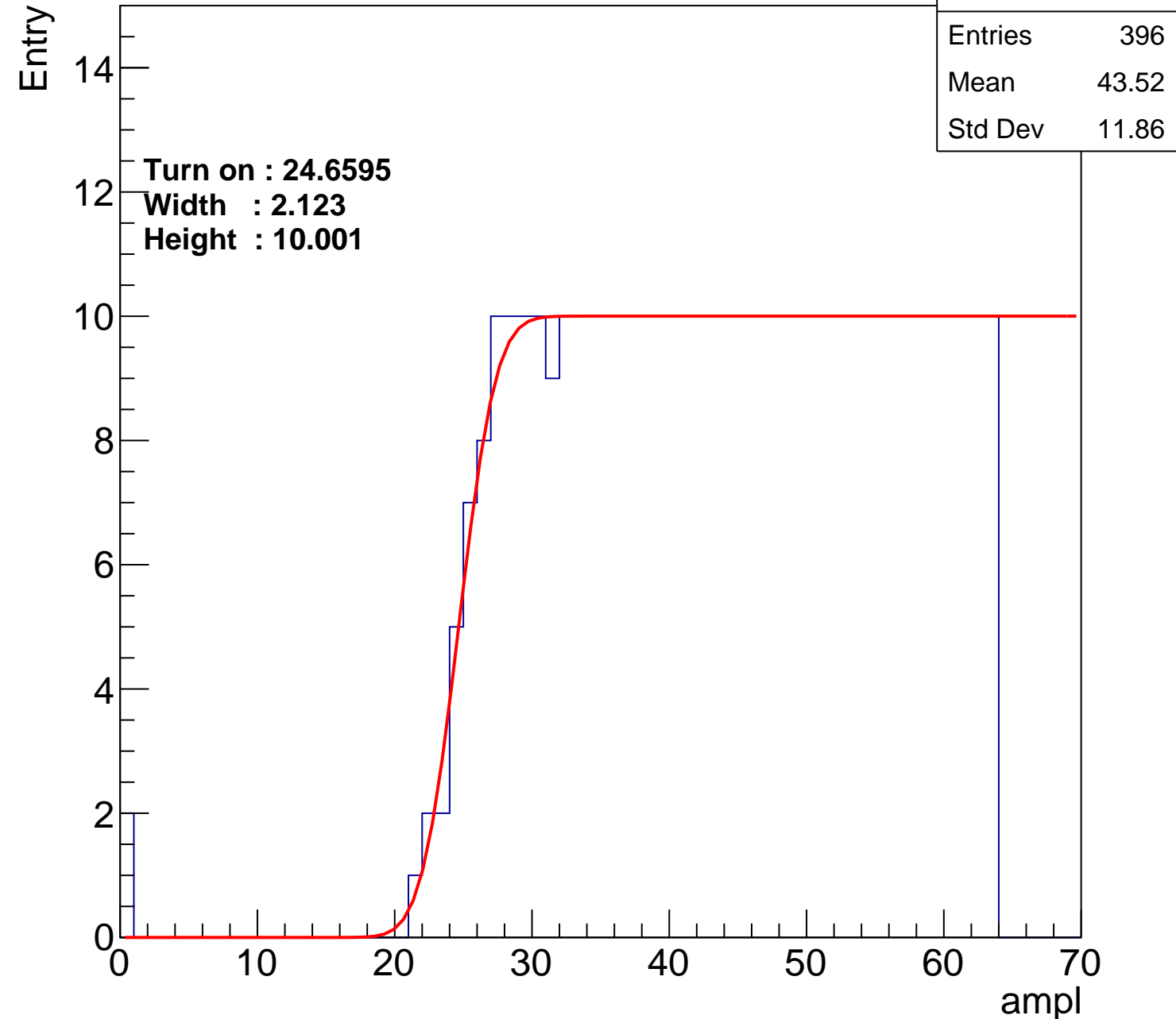
Width : 2.123

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.74
Std Dev	11.75

Turn on : 27.9726

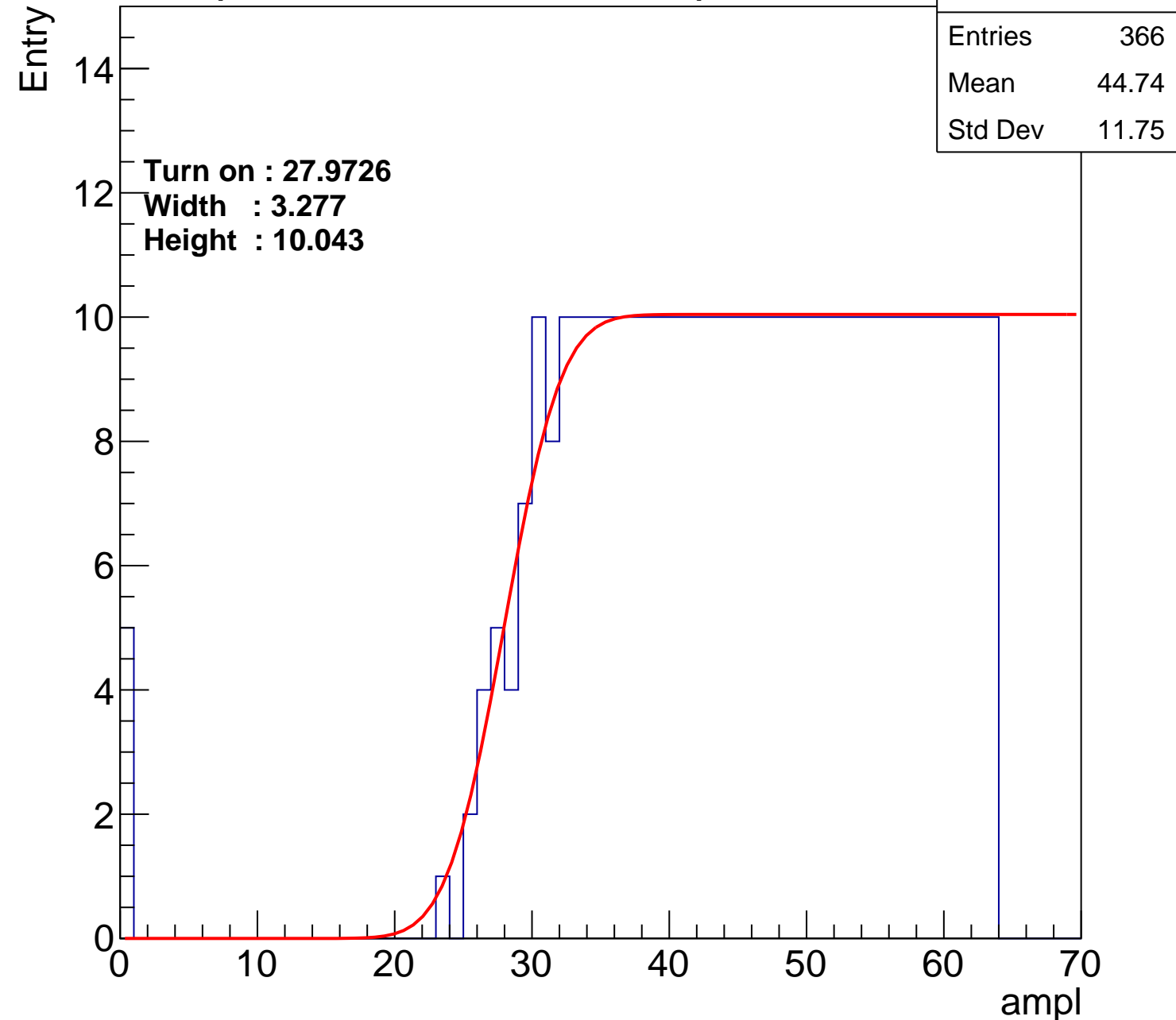
Width : 3.277

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch80

calib_packv5_042523_0143.root, FC#3, port B3

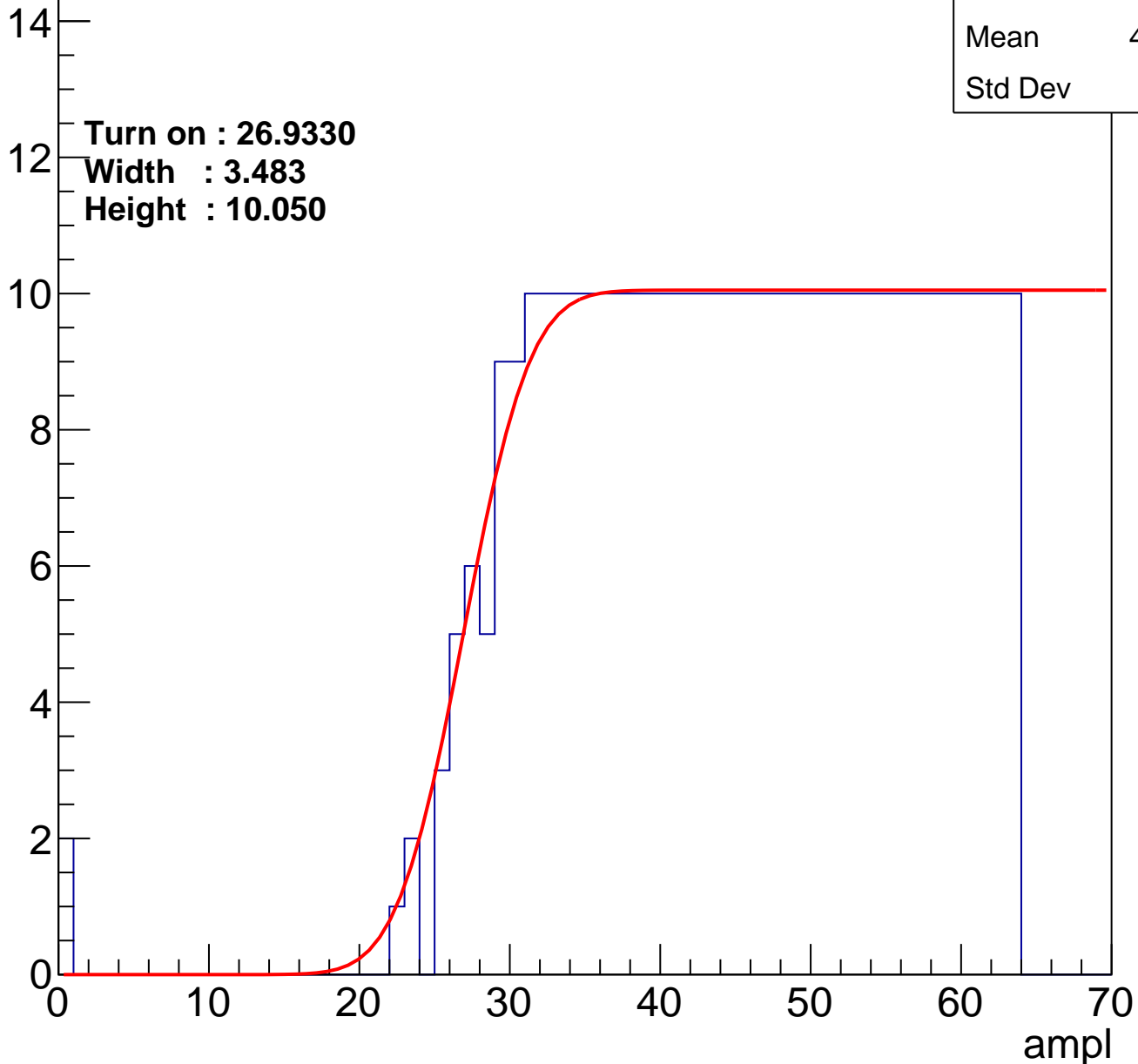
Entries	372
Mean	44.67
Std Dev	11.3

Turn on : 26.9330

Width : 3.483

Height : 10.050

Entry



B1L000S, U20-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.93
Std Dev	11

Turn on : 27.5370

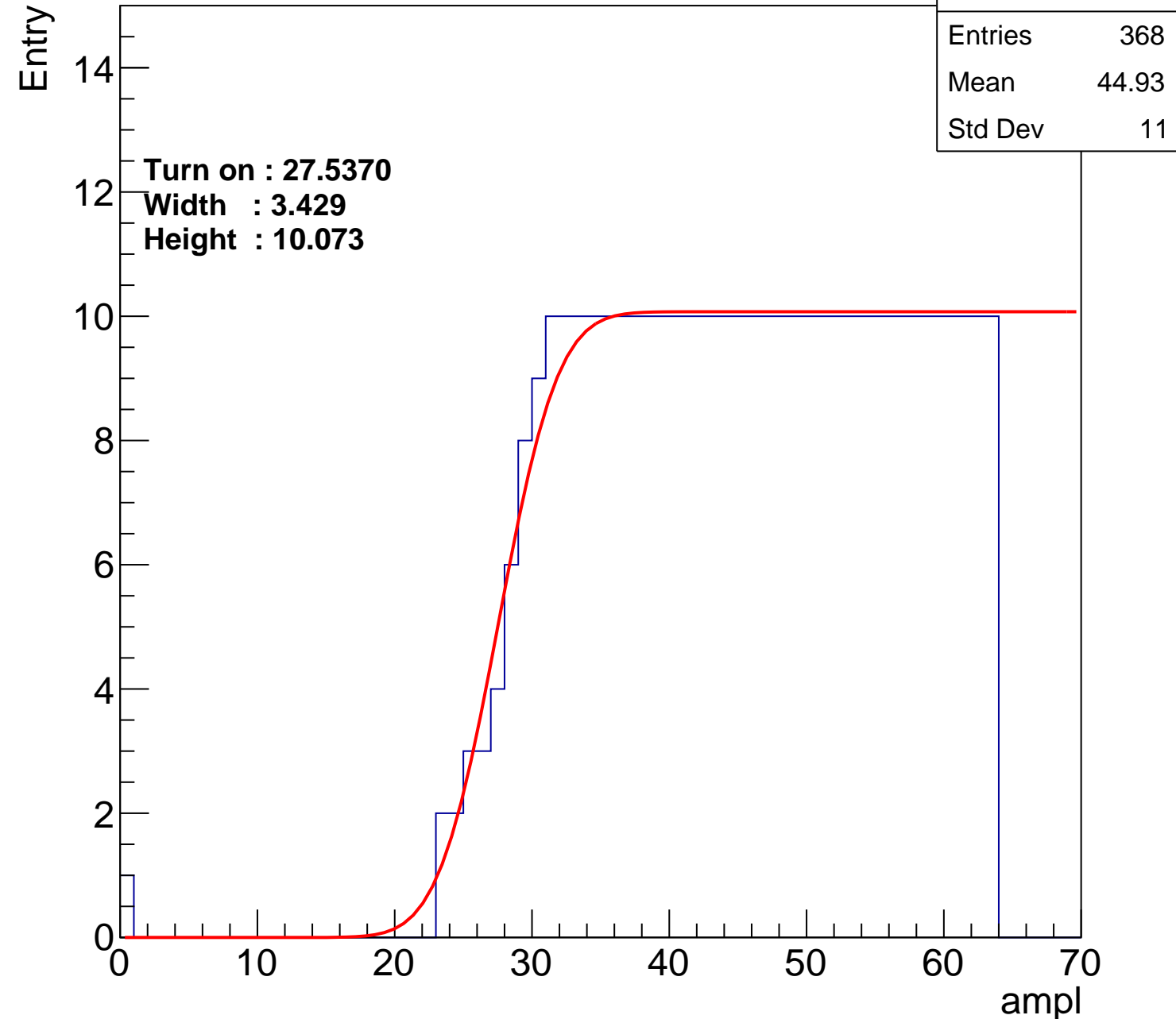
Width : 3.429

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch82

calib_packv5_042523_0143.root, FC#3, port B3

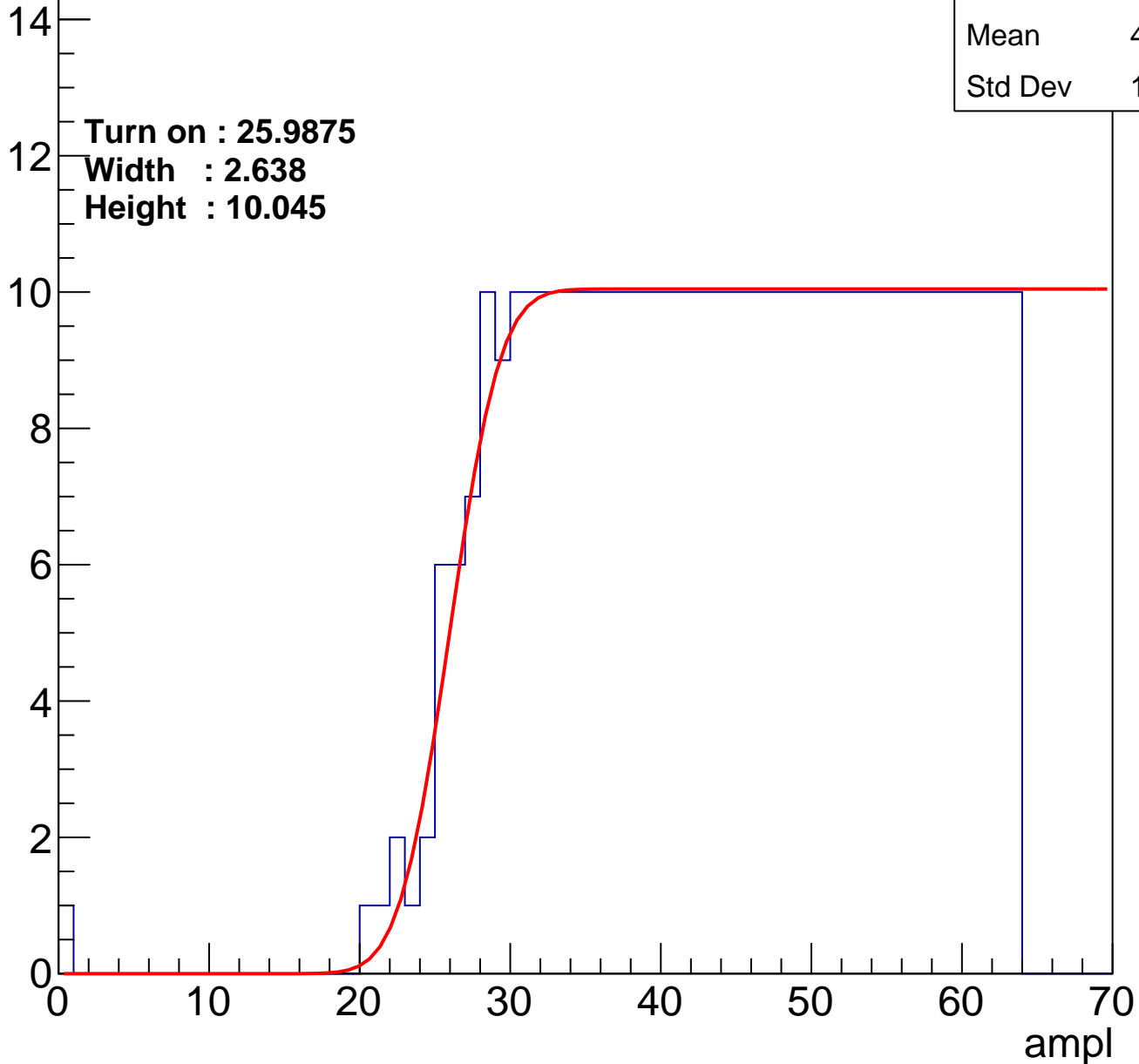
Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.9875

Width : 2.638

Height : 10.045

Entry



B1L000S, U20-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.63
Std Dev	11.63

Turn on : 27.5577

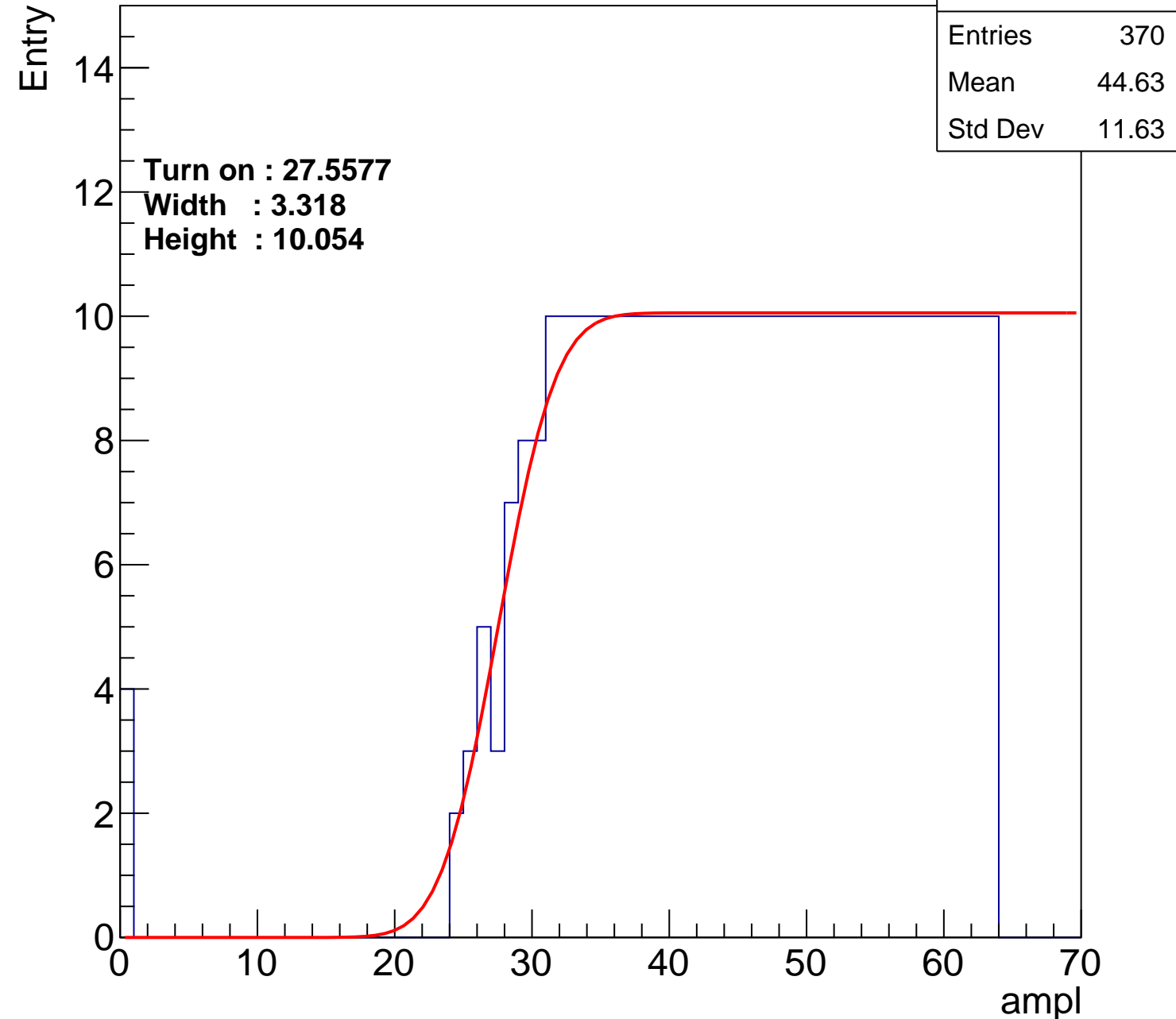
Width : 3.318

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.5150

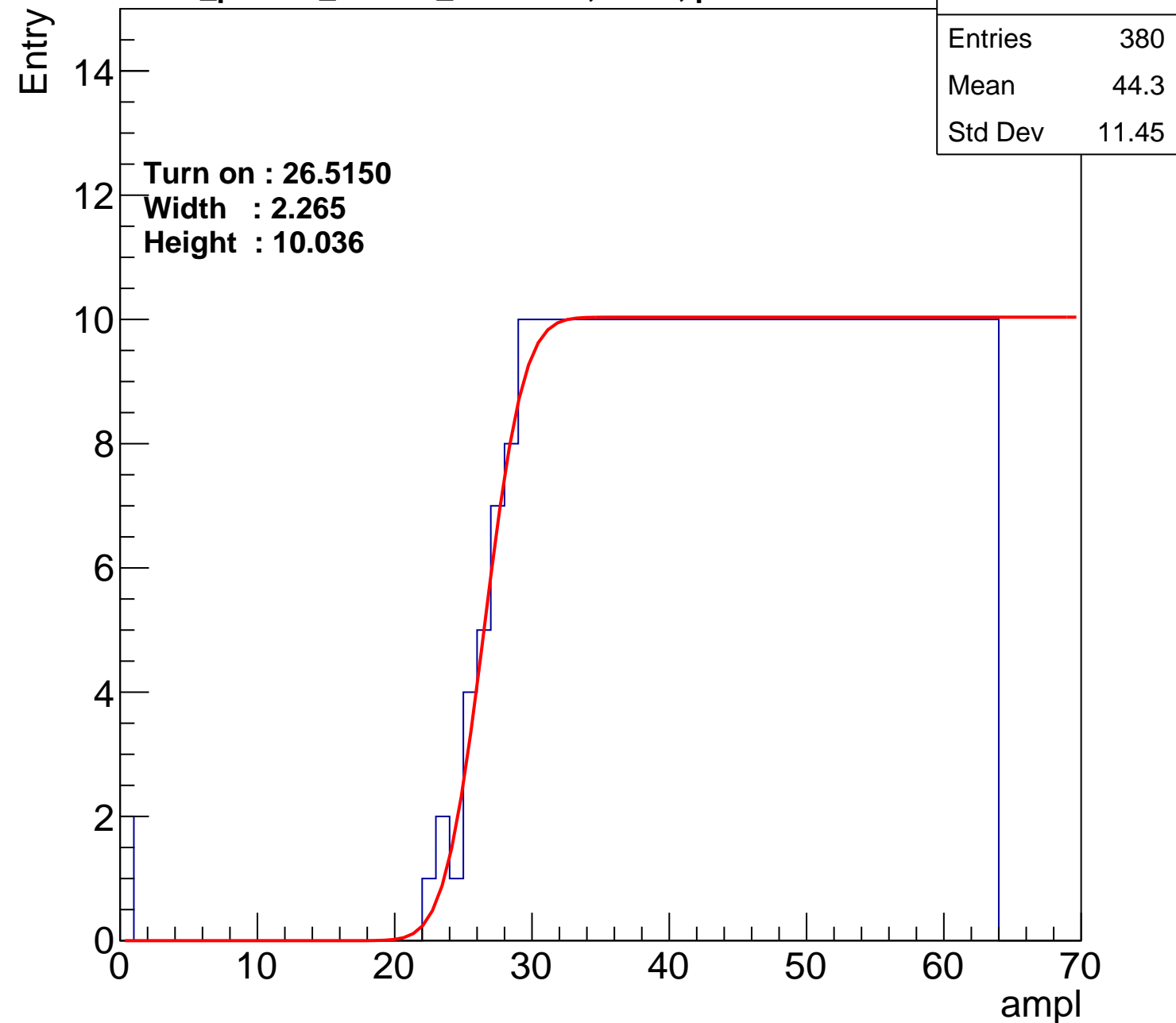
Width : 2.265

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.3443

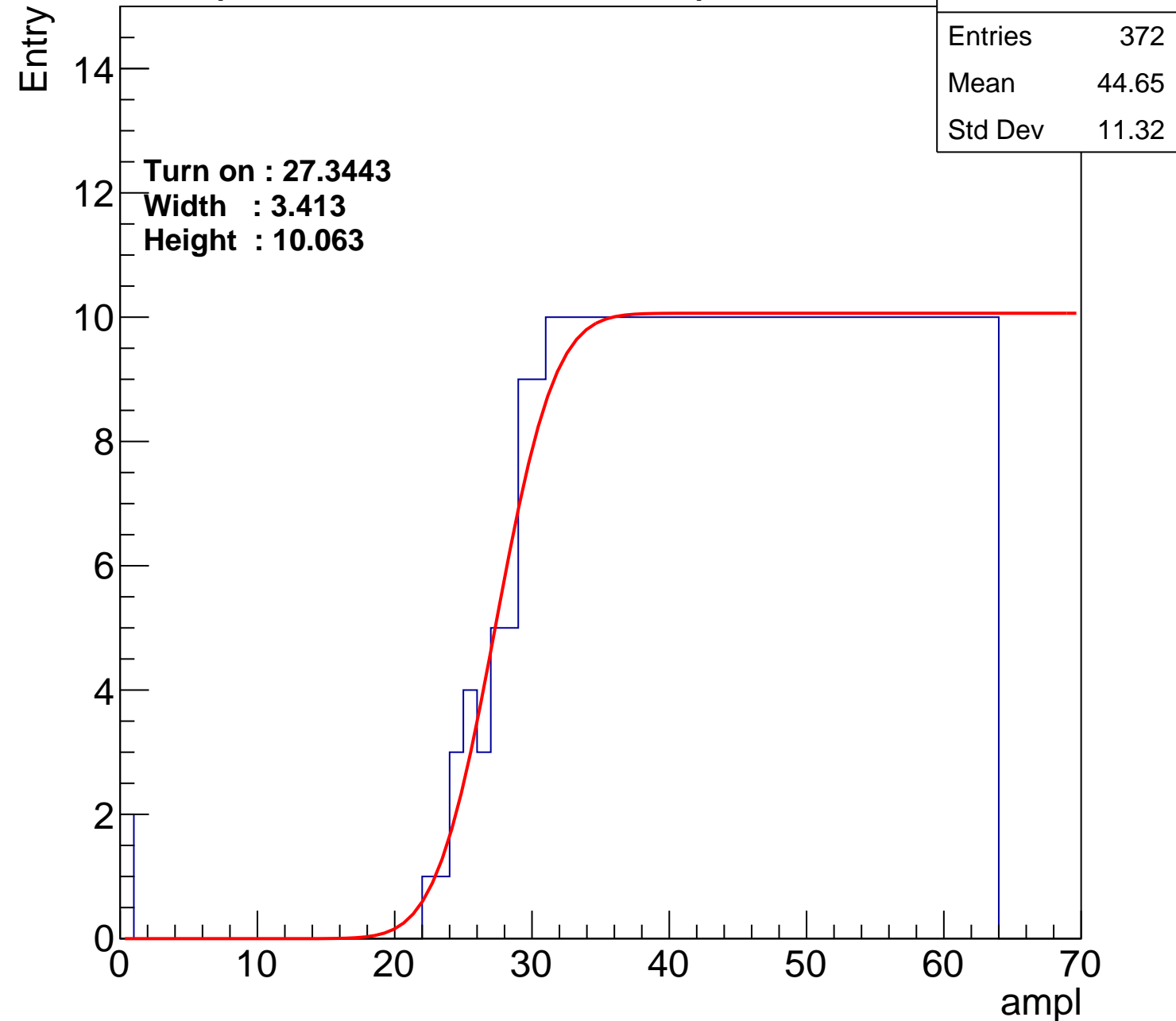
Width : 3.413

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.7361

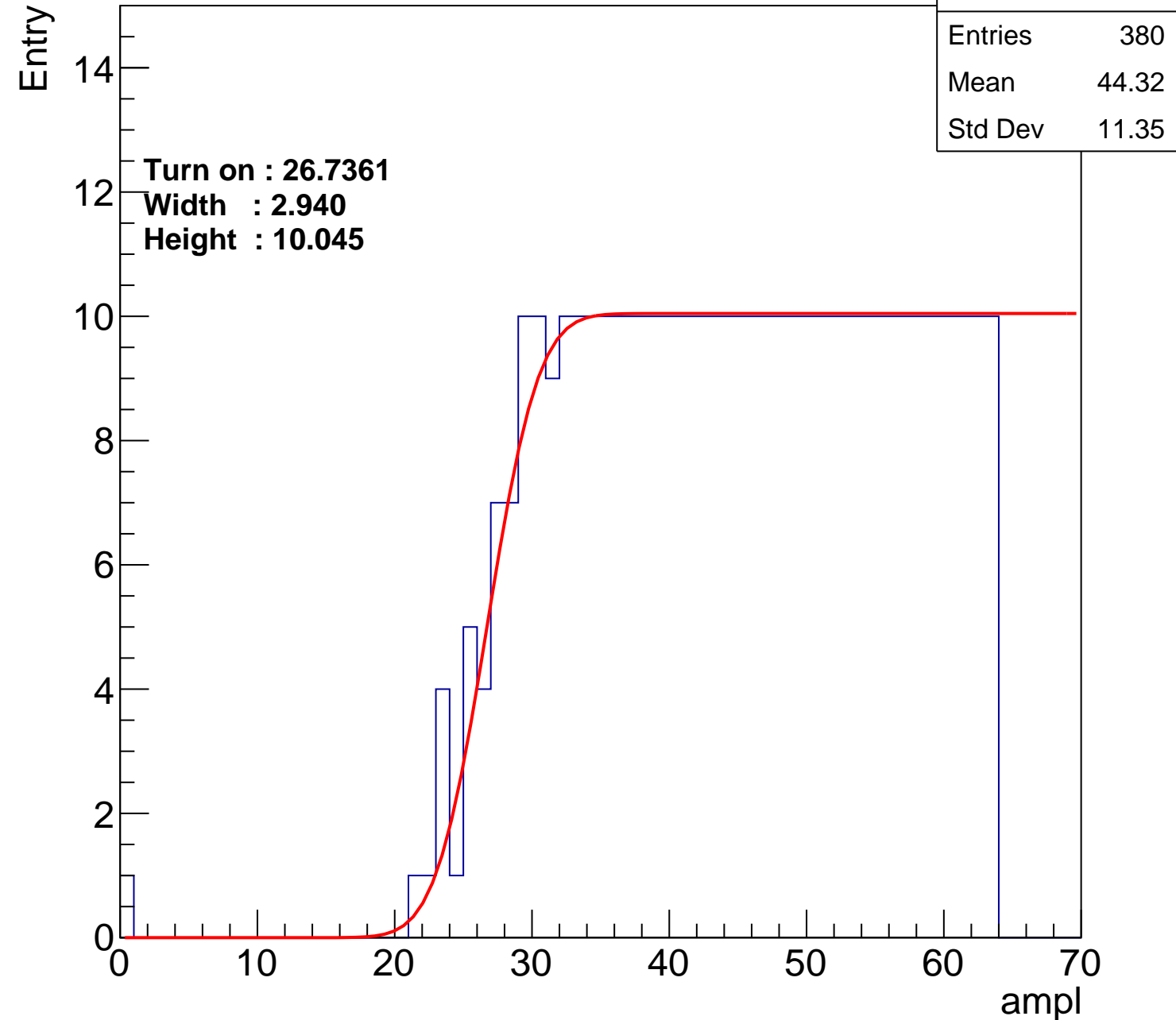
Width : 2.940

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch87

calib_packv5_042523_0143.root, FC#3, port B3

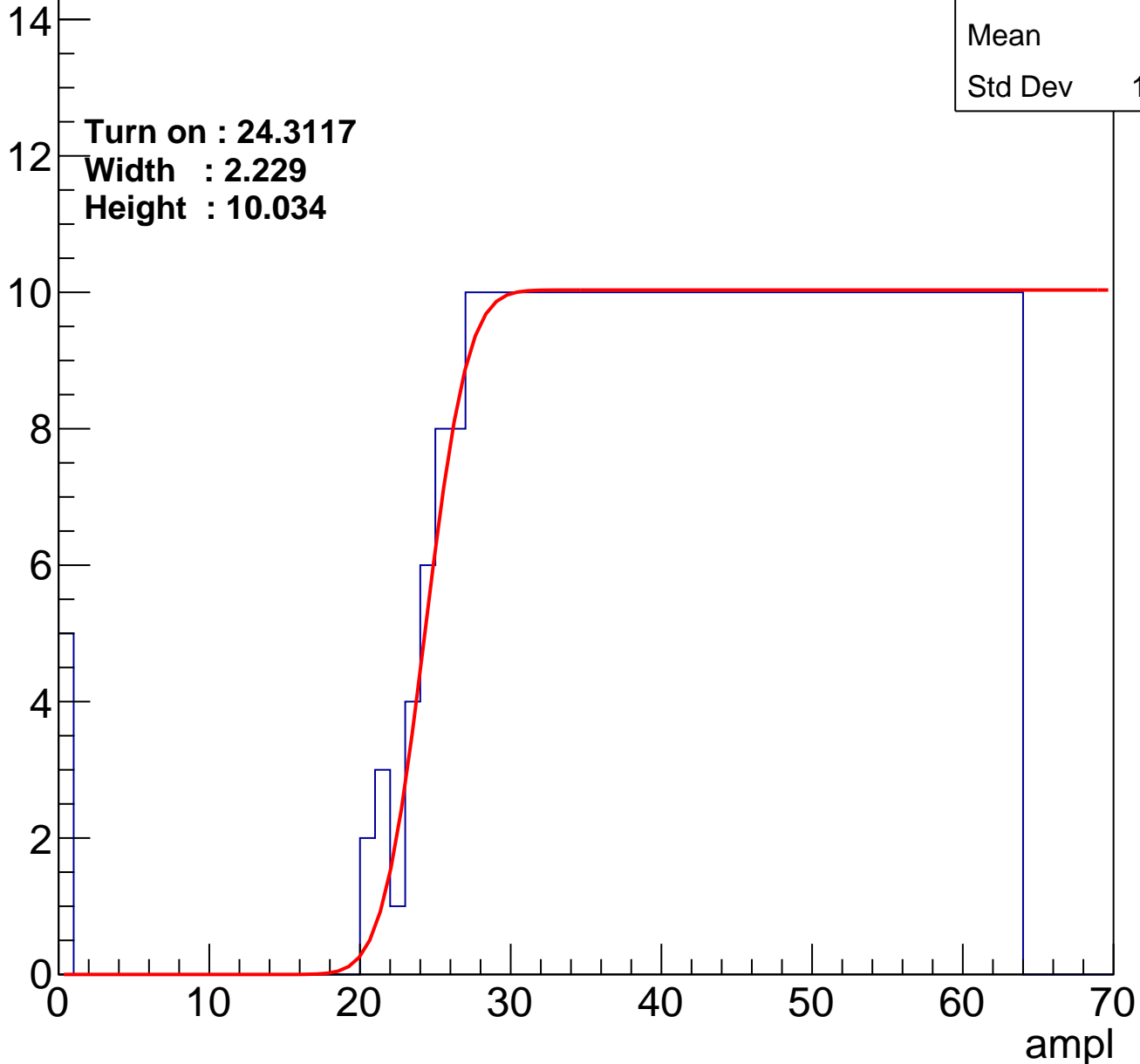
Entries	407
Mean	42.8
Std Dev	12.59

Turn on : 24.3117

Width : 2.229

Height : 10.034

Entry



B1L000S, U20-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 25.8188

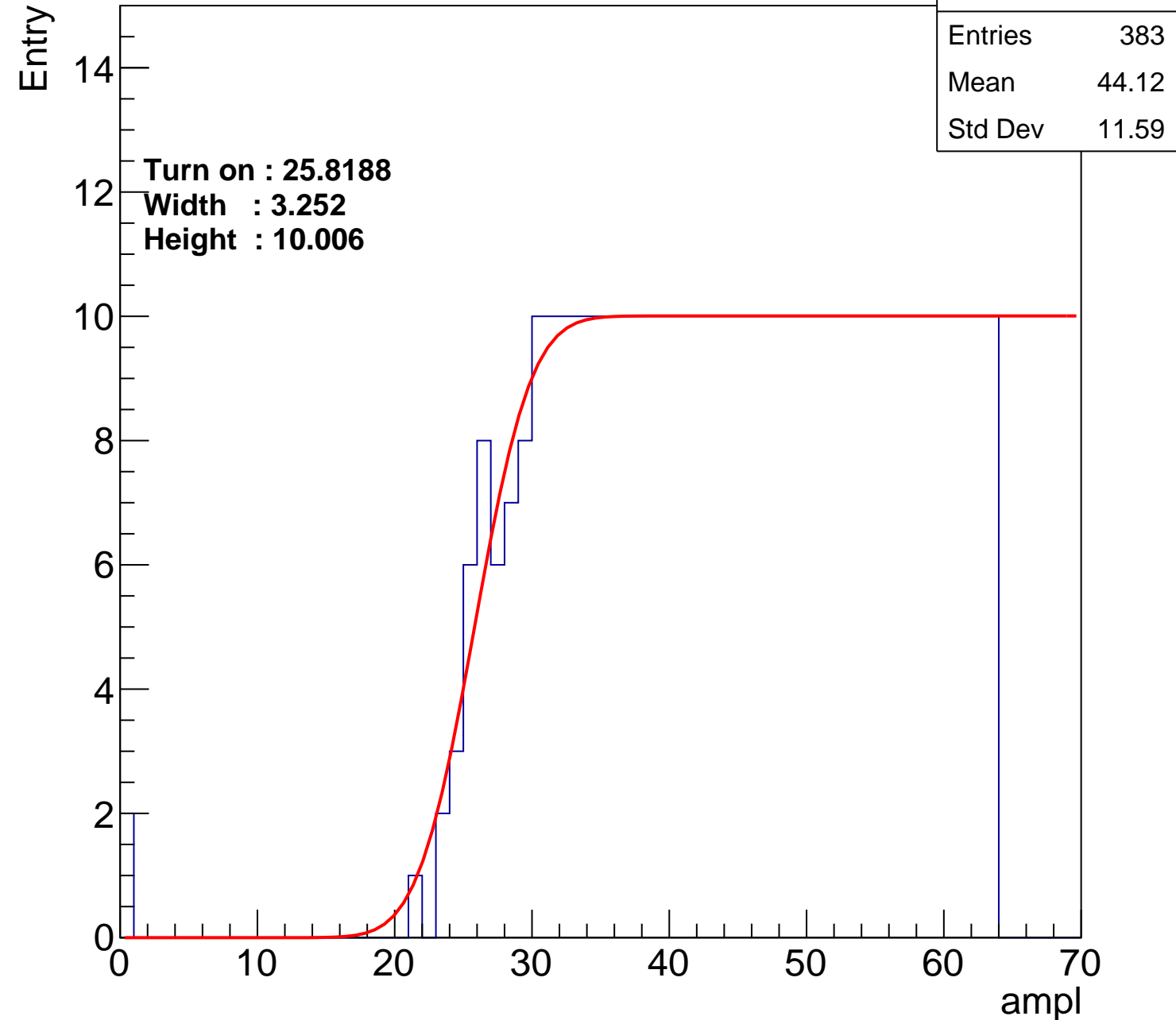
Width : 3.252

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.34
Std Dev	11.57

Turn on : 26.7648

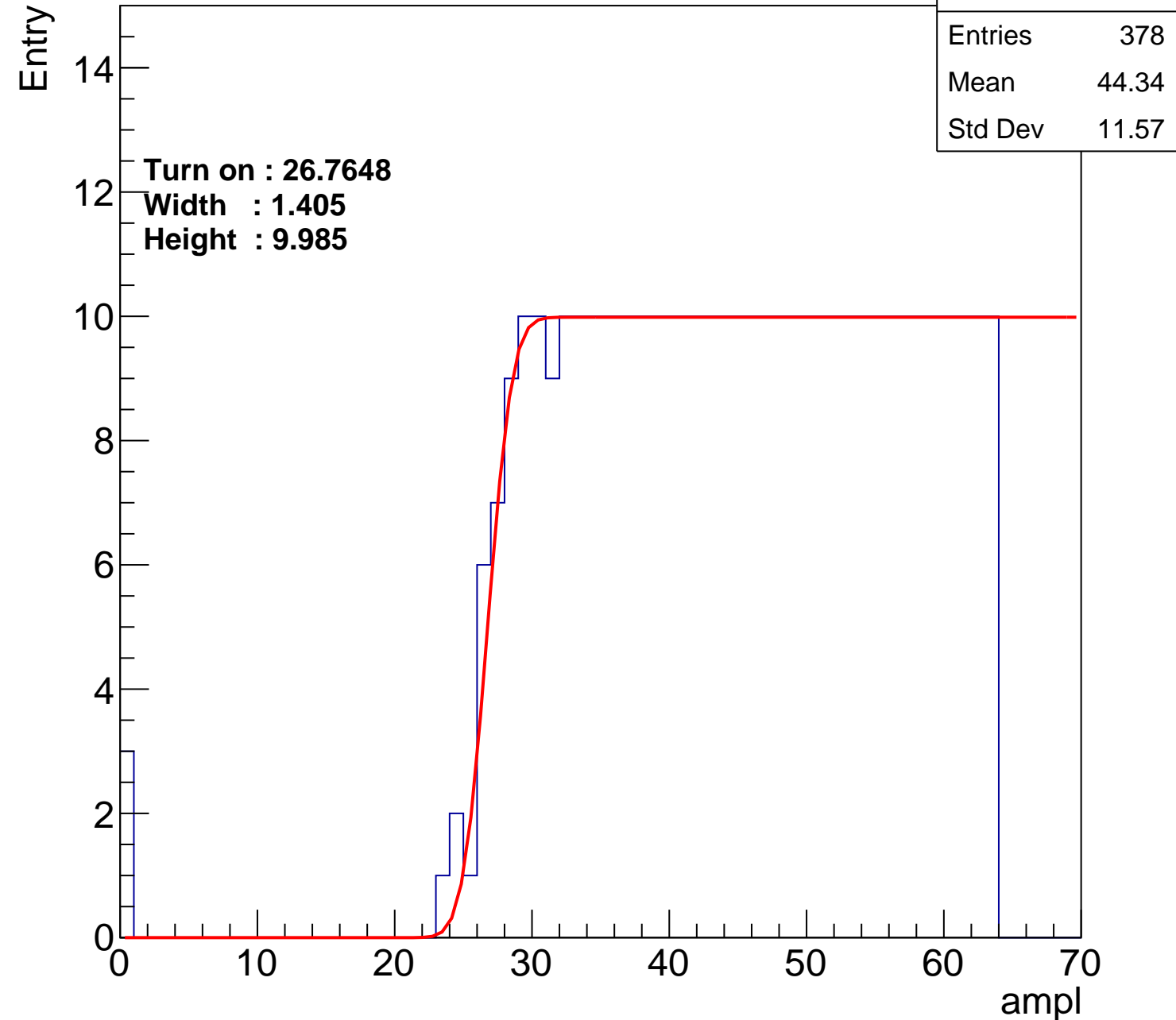
Width : 1.405

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.37
Std Dev	11.83

Turn on : 24.0413

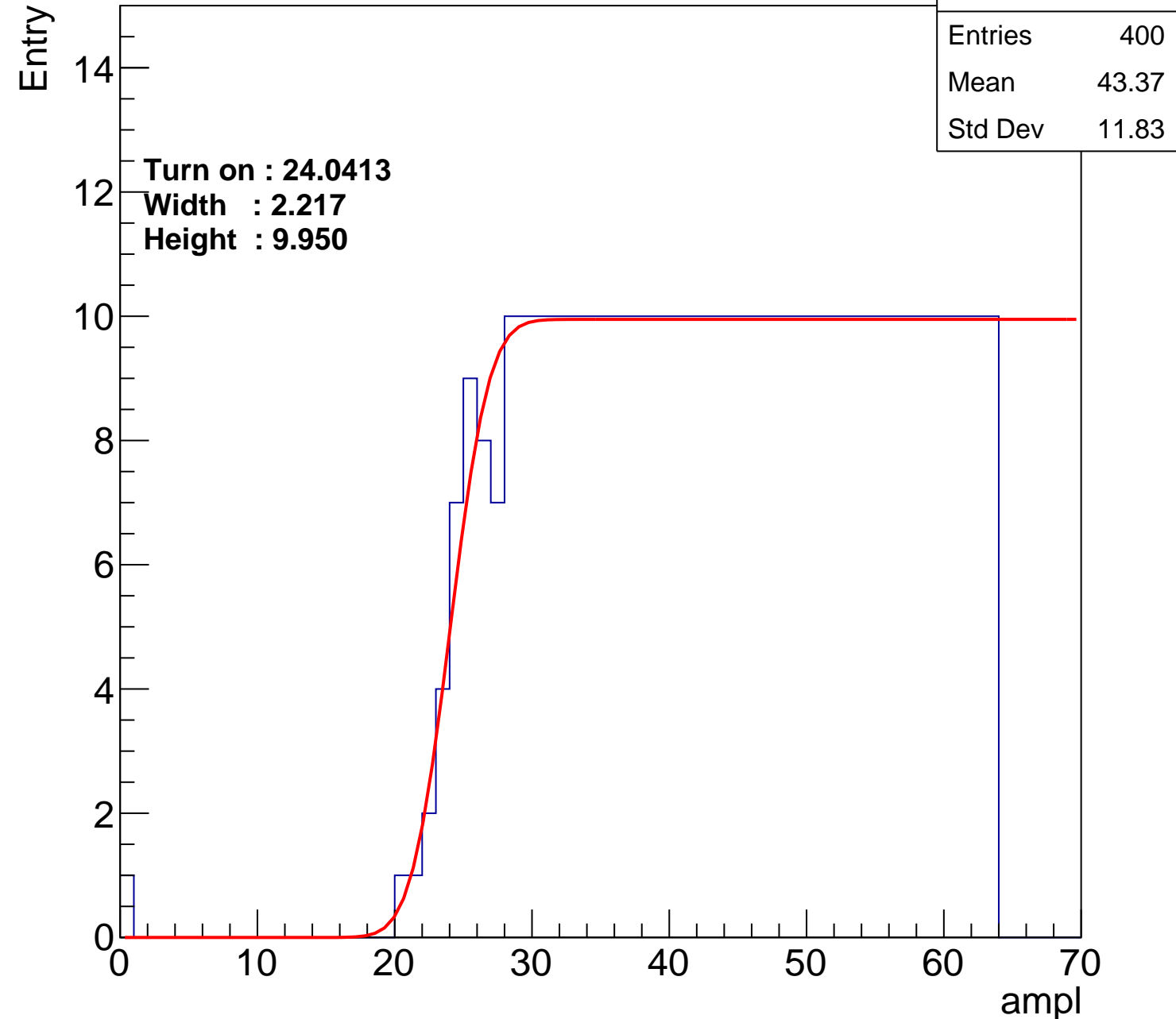
Width : 2.217

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 25.7091

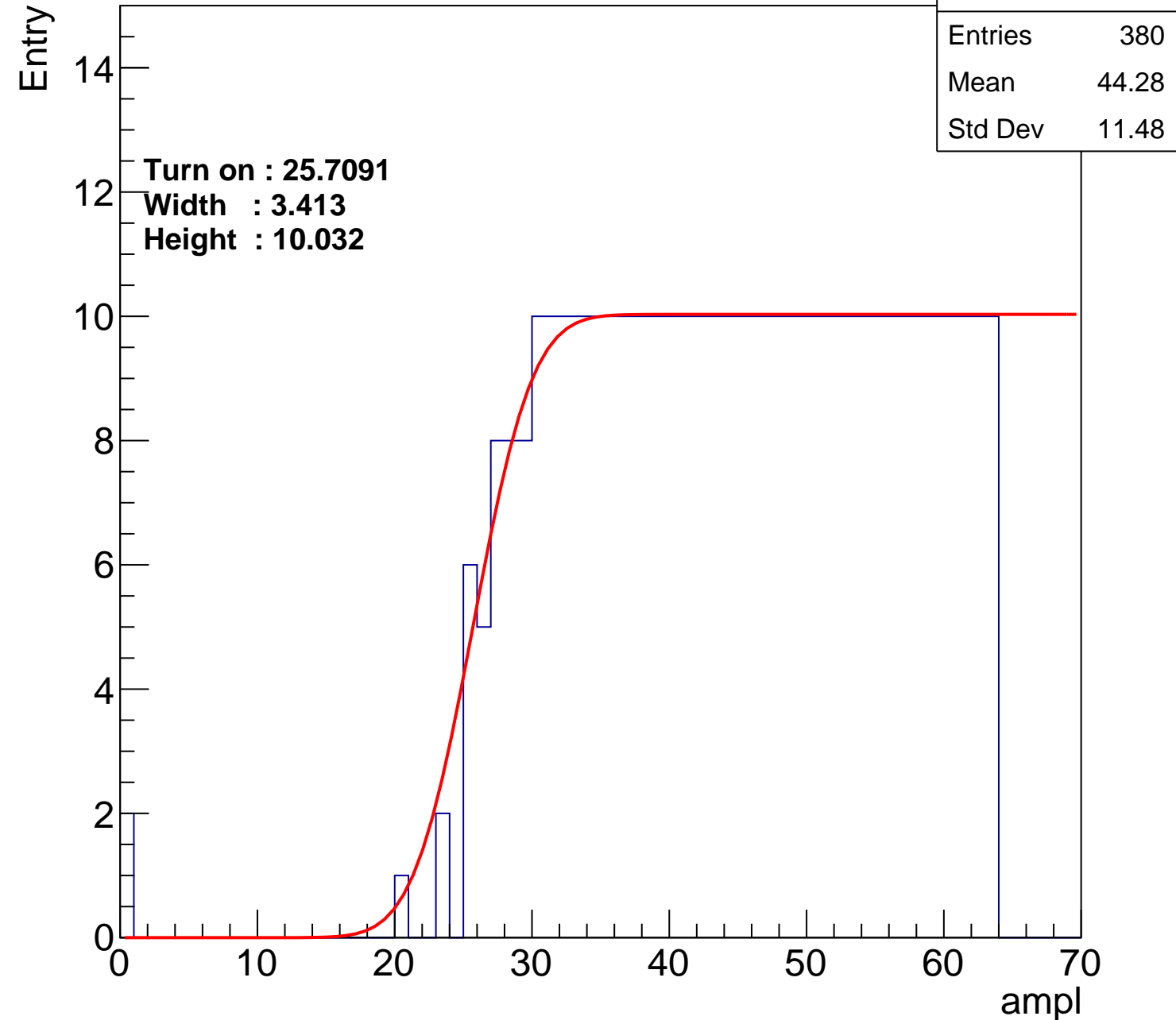
Width : 3.413

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.97
Std Dev	11.54

Turn on : 25.1187

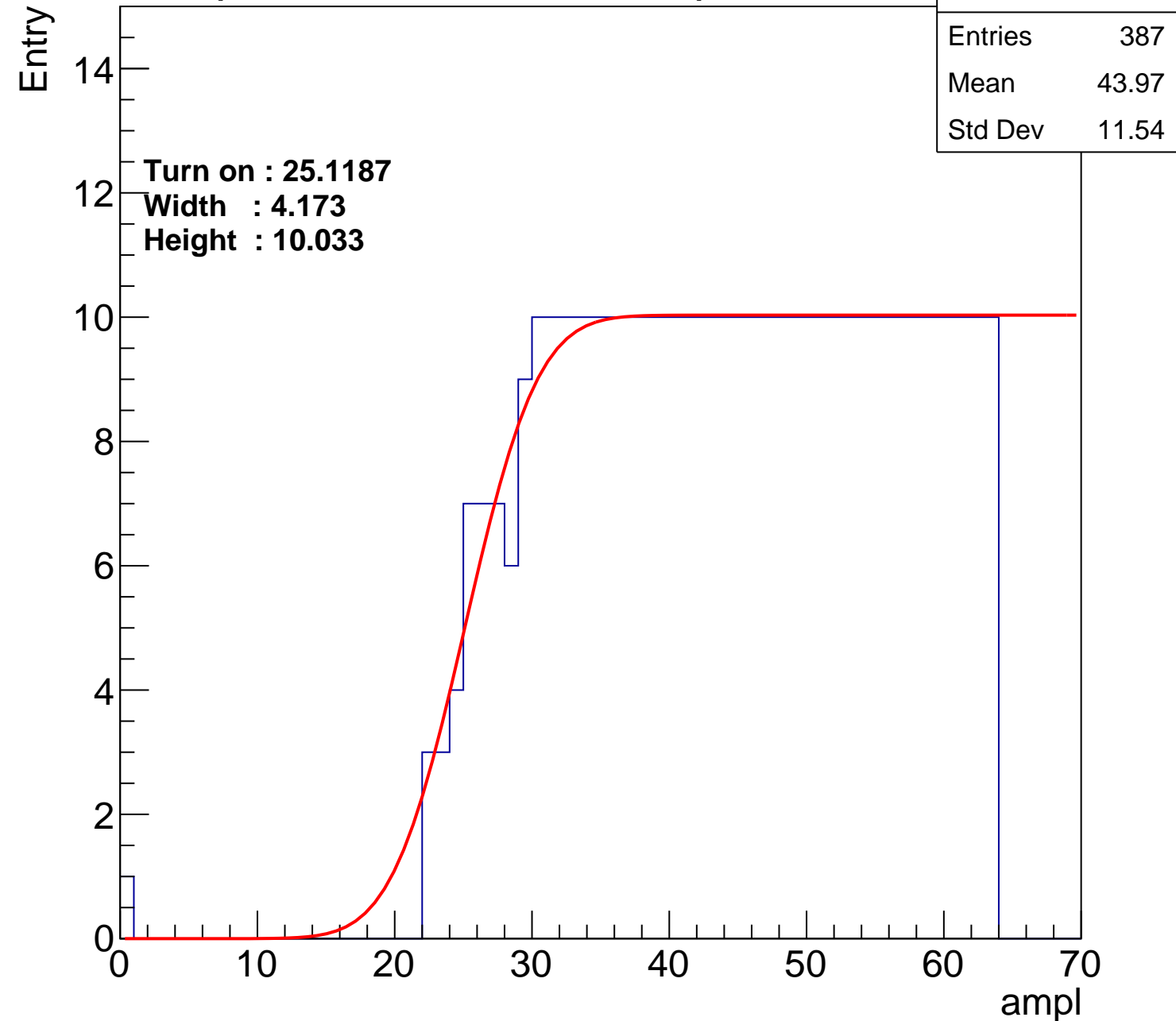
Width : 4.173

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.64
Std Dev	10.68

Turn on : 29.2928

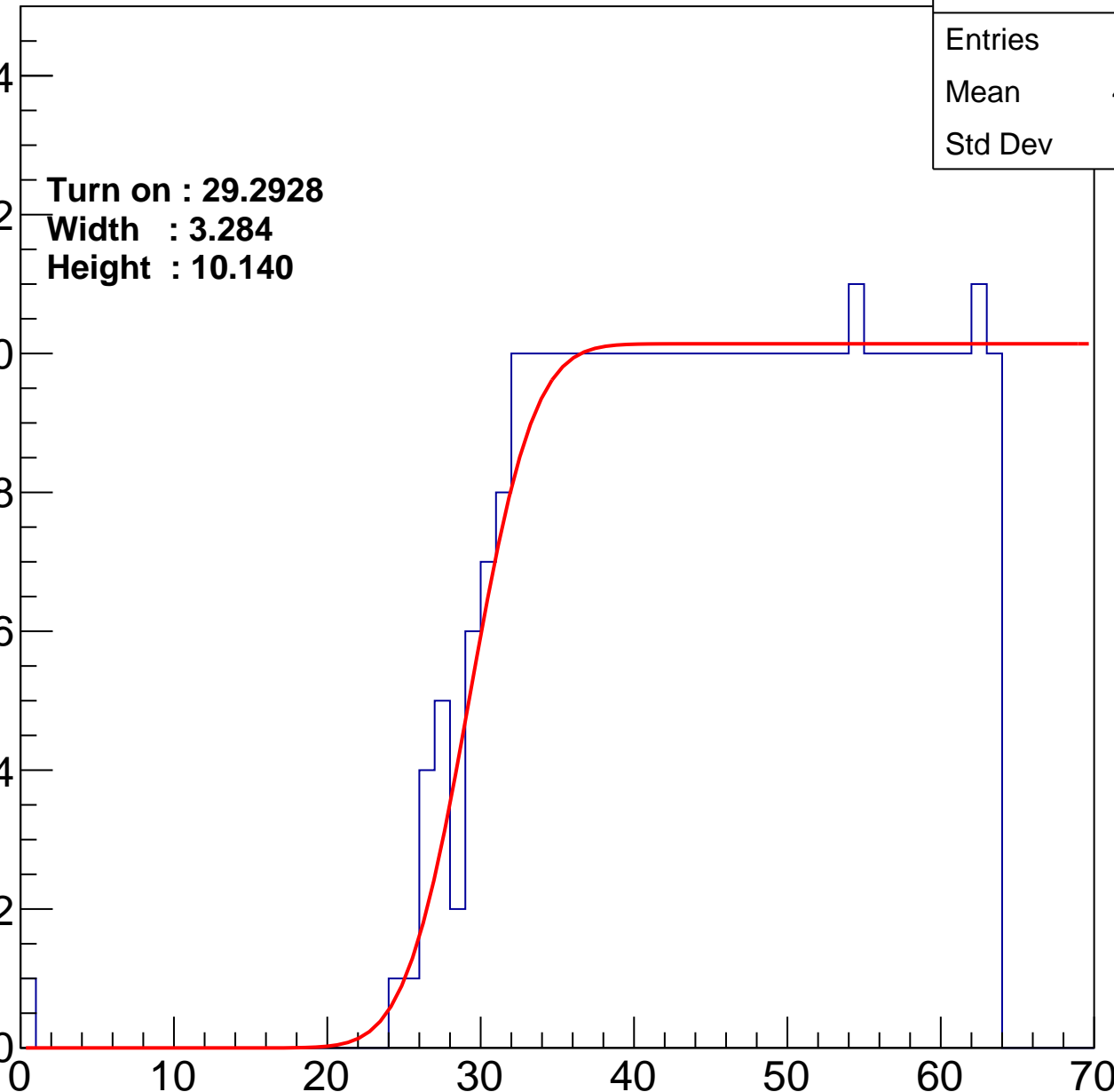
Width : 3.284

Height : 10.140

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch94

calib_packv5_042523_0143.root, FC#3, port B3

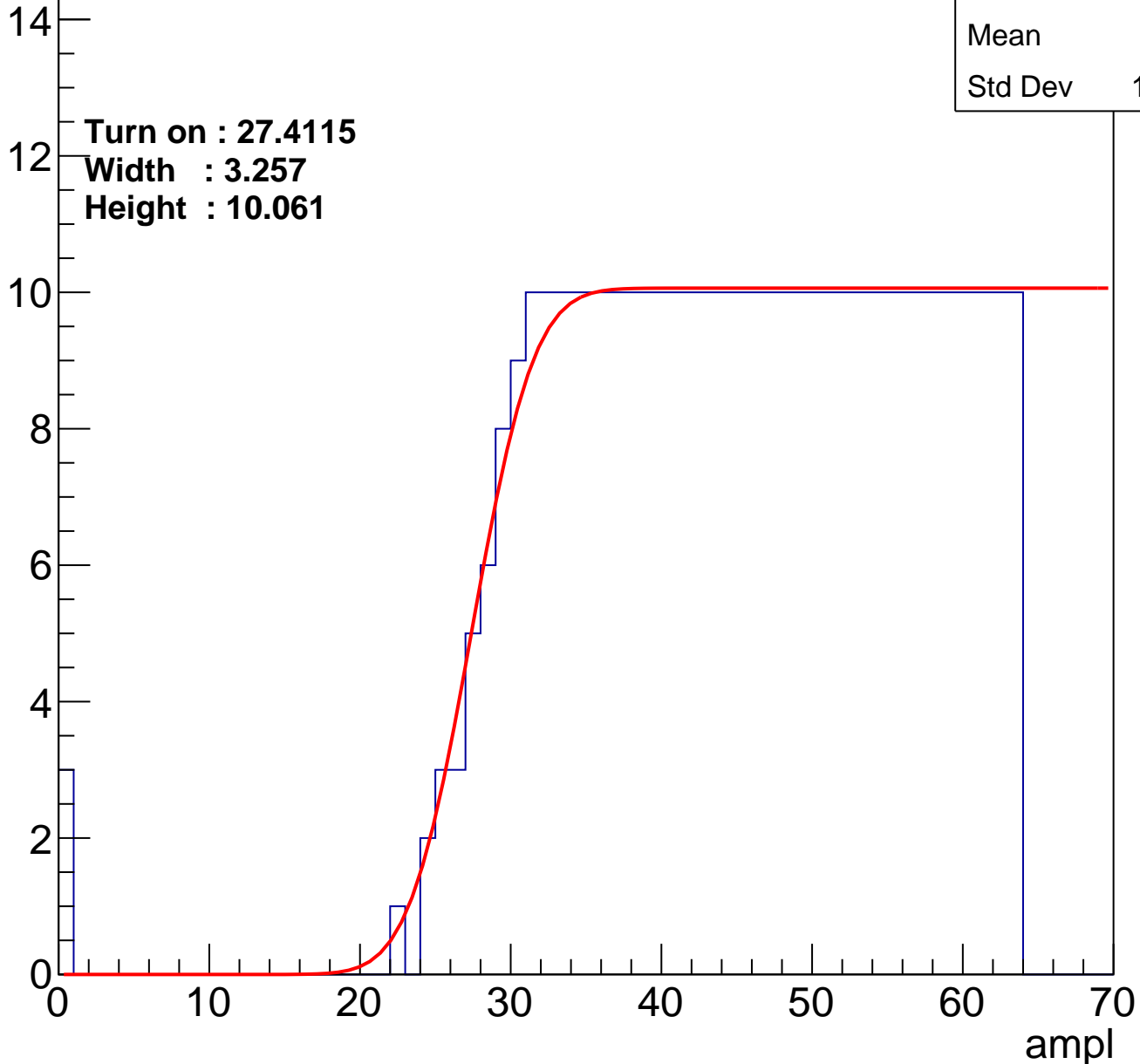
Entries	370
Mean	44.7
Std Dev	11.44

Turn on : 27.4115

Width : 3.257

Height : 10.061

Entry



B1L000S, U20-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.38
Std Dev	11.69

Turn on : 26.9169

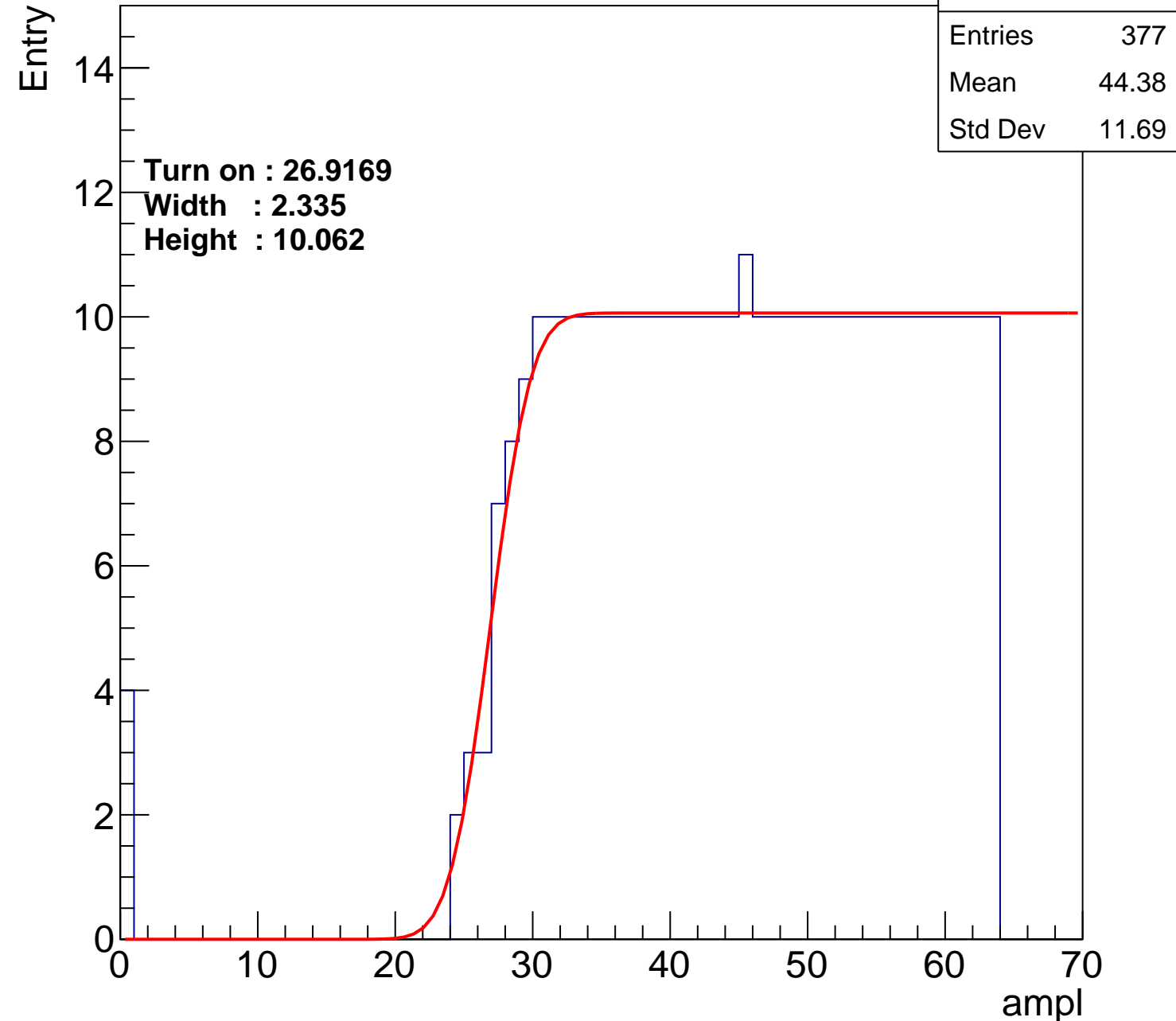
Width : 2.335

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.94
Std Dev	11.12

Turn on : 27.6495

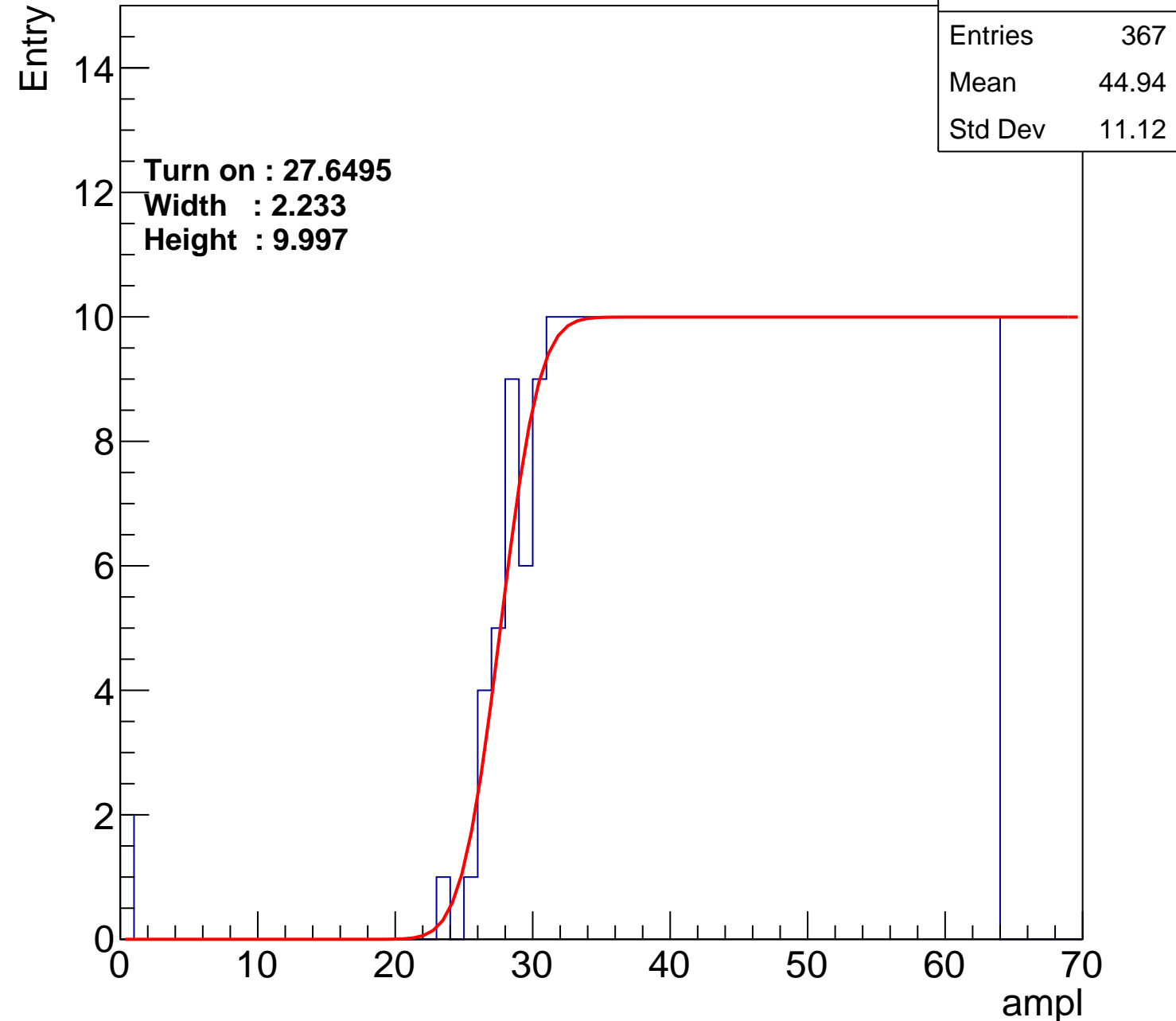
Width : 2.233

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch97

calib_packv5_042523_0143.root, FC#3, port B3

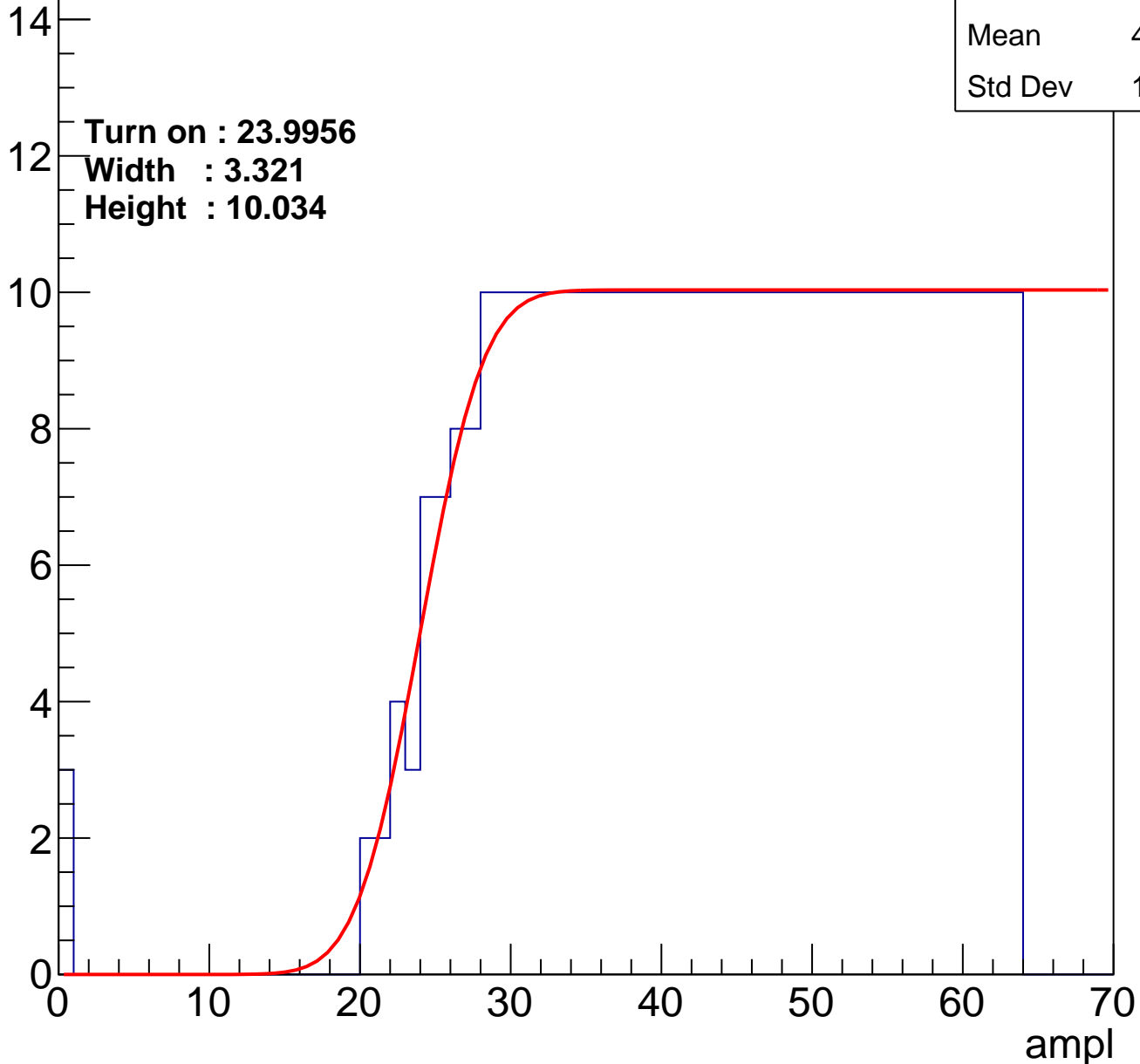
Entries	404
Mean	43.03
Std Dev	12.27

Turn on : 23.9956

Width : 3.321

Height : 10.034

Entry



B1L000S, U20-ch98

calib_packv5_042523_0143.root, FC#3, port B3

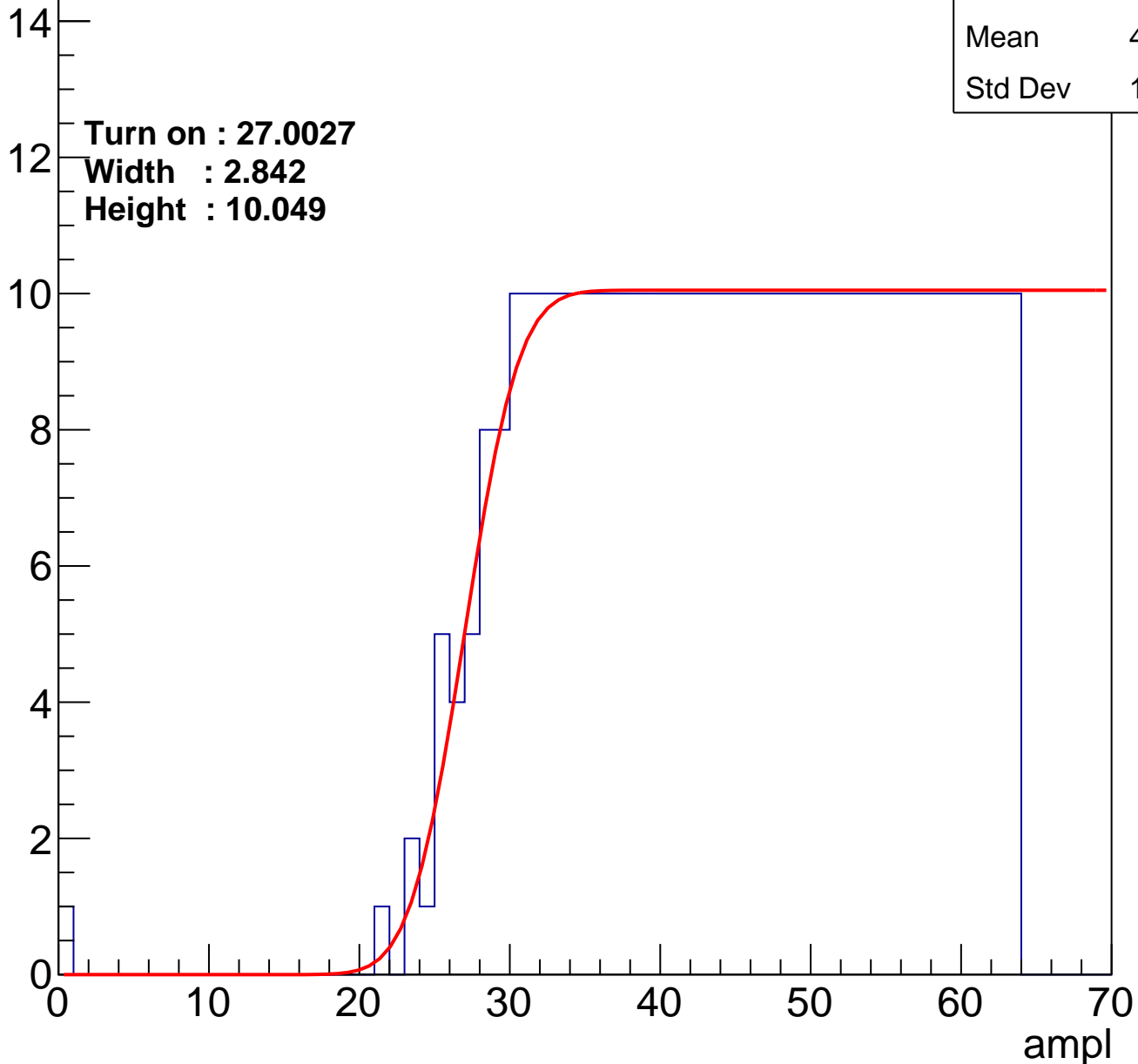
Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 27.0027

Width : 2.842

Height : 10.049

Entry



B1L000S, U20-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.99
Std Dev	11.11

Turn on : 28.0626

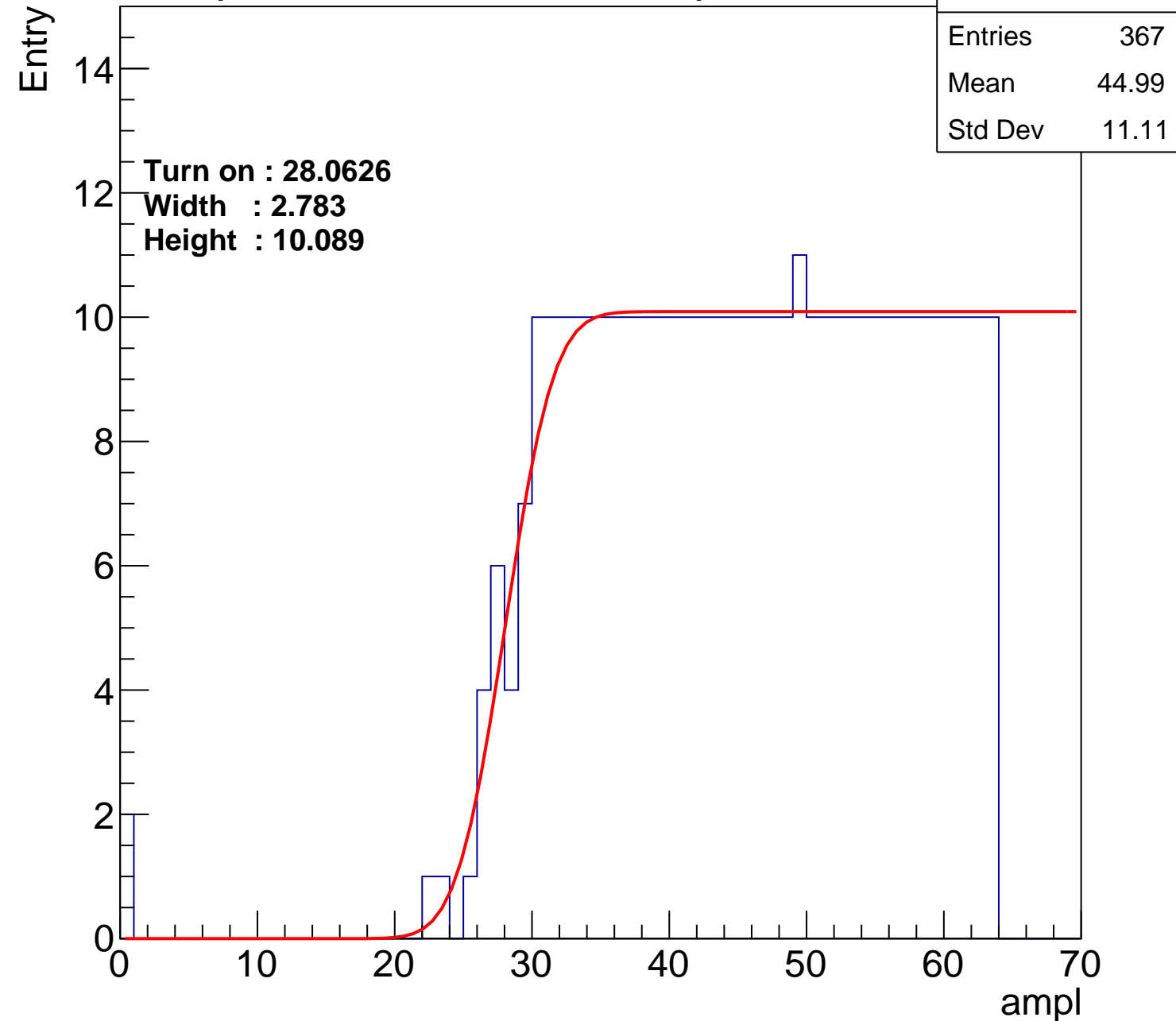
Width : 2.783

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch100

calib_packv5_042523_0143.root, FC#3, port B3

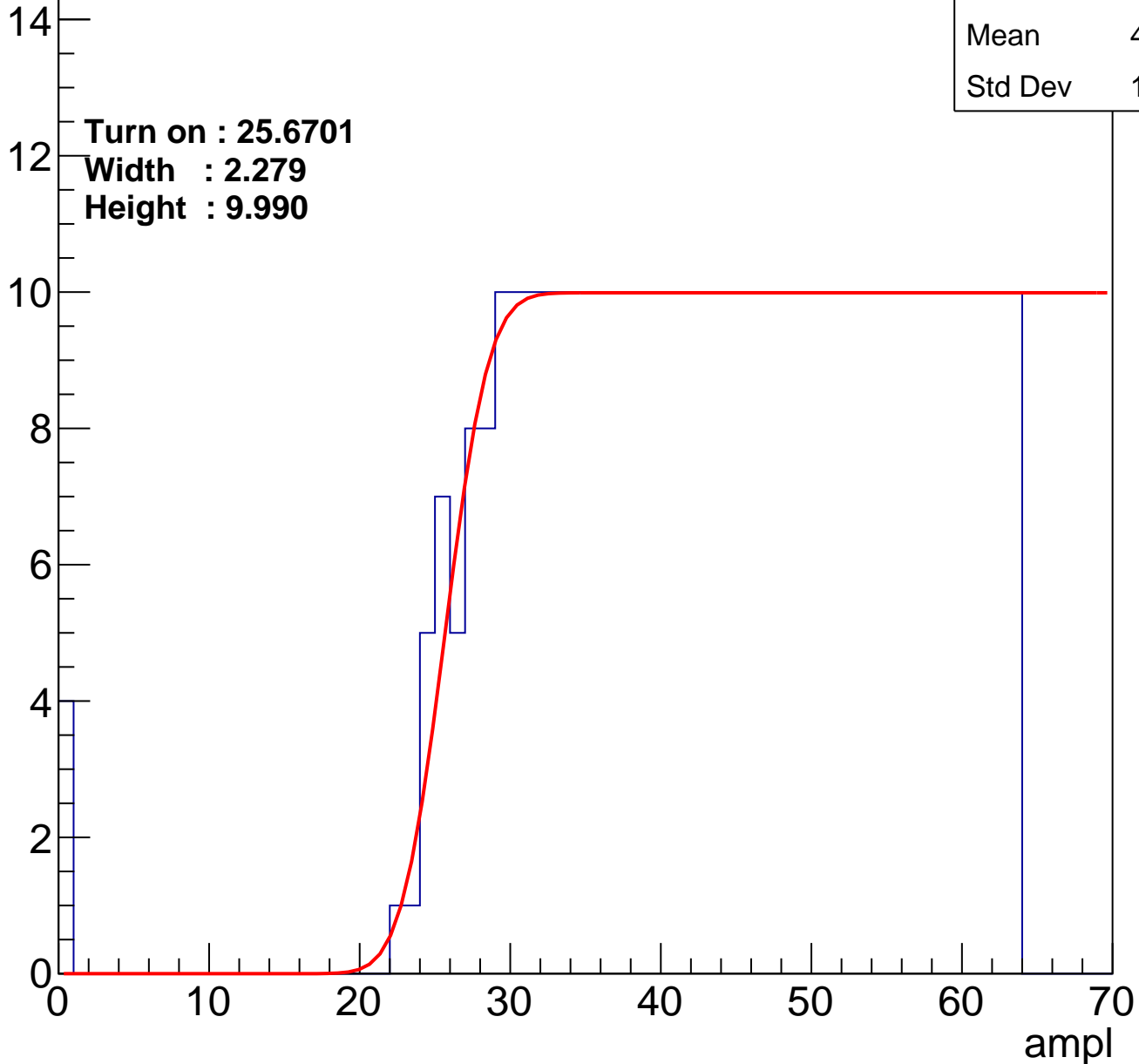
Entries	389
Mean	43.73
Std Dev	12.02

Turn on : 25.6701

Width : 2.279

Height : 9.990

Entry



B1L000S, U20-ch101

calib_packv5_042523_0143.root, FC#3, port B3

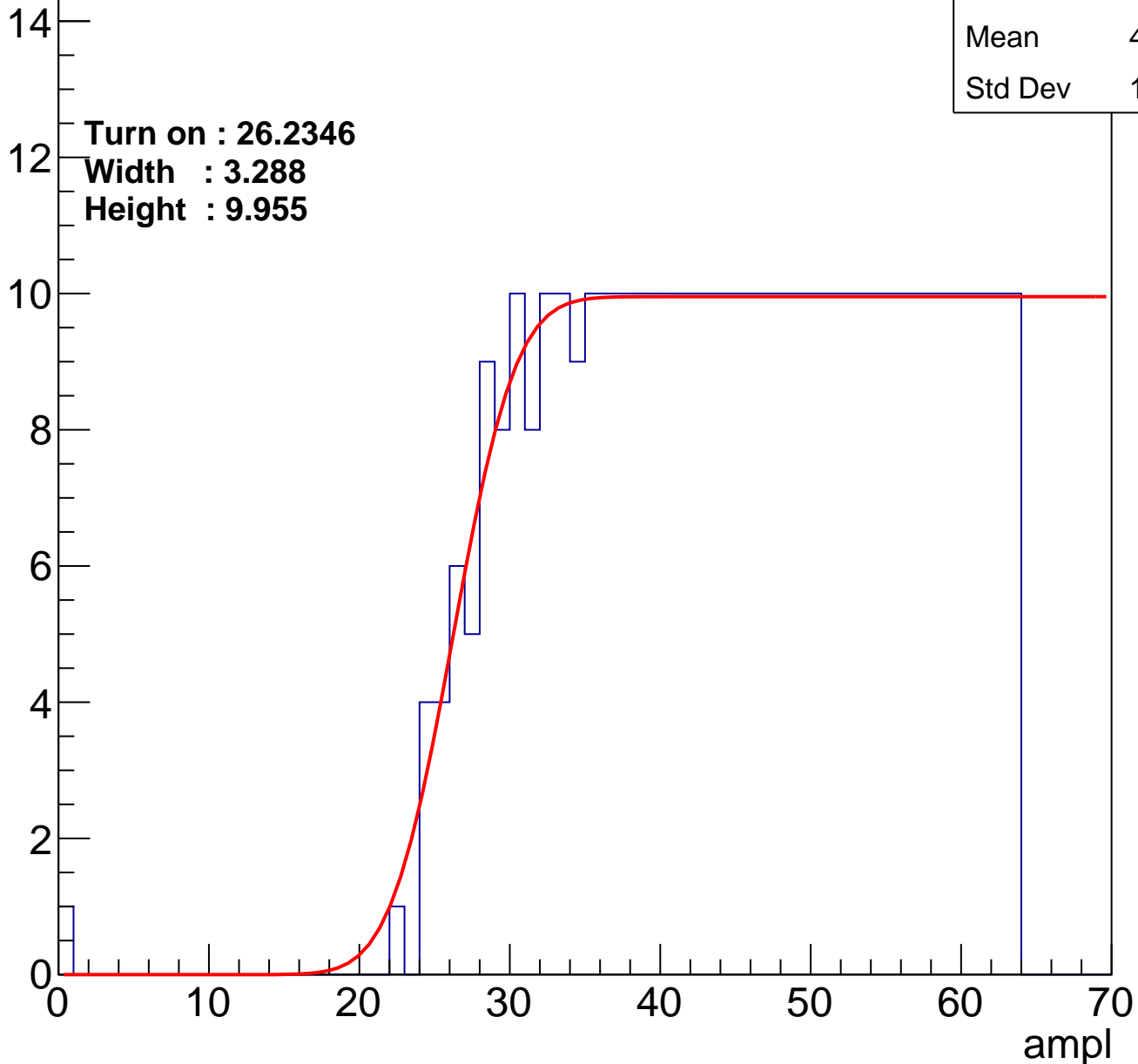
Entries	375
Mean	44.55
Std Dev	11.23

Turn on : 26.2346

Width : 3.288

Height : 9.955

Entry



B1L000S, U20-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.47
Std Dev	11.53

Turn on : 26.9618

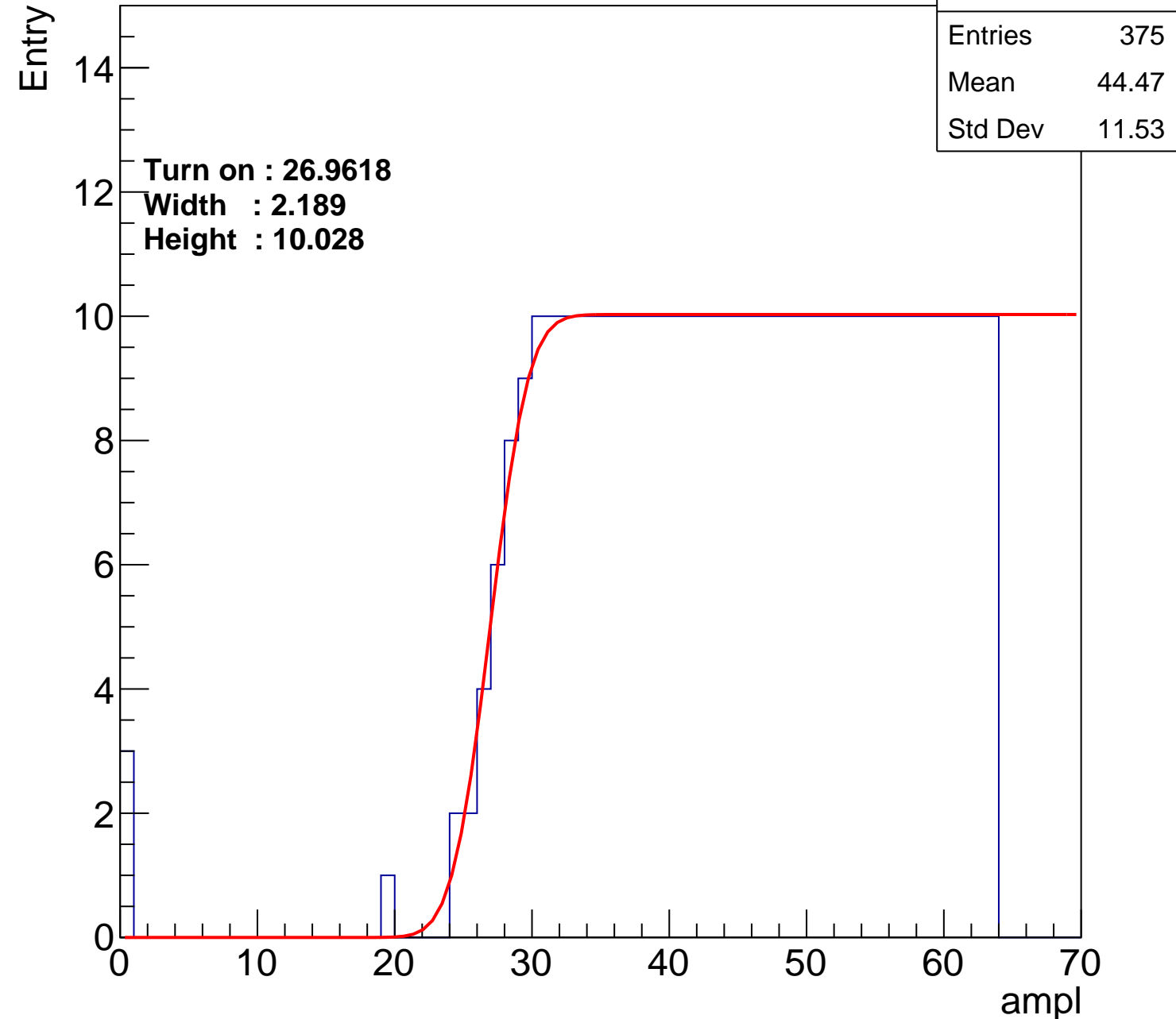
Width : 2.189

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch103

calib_packv5_042523_0143.root, FC#3, port B3

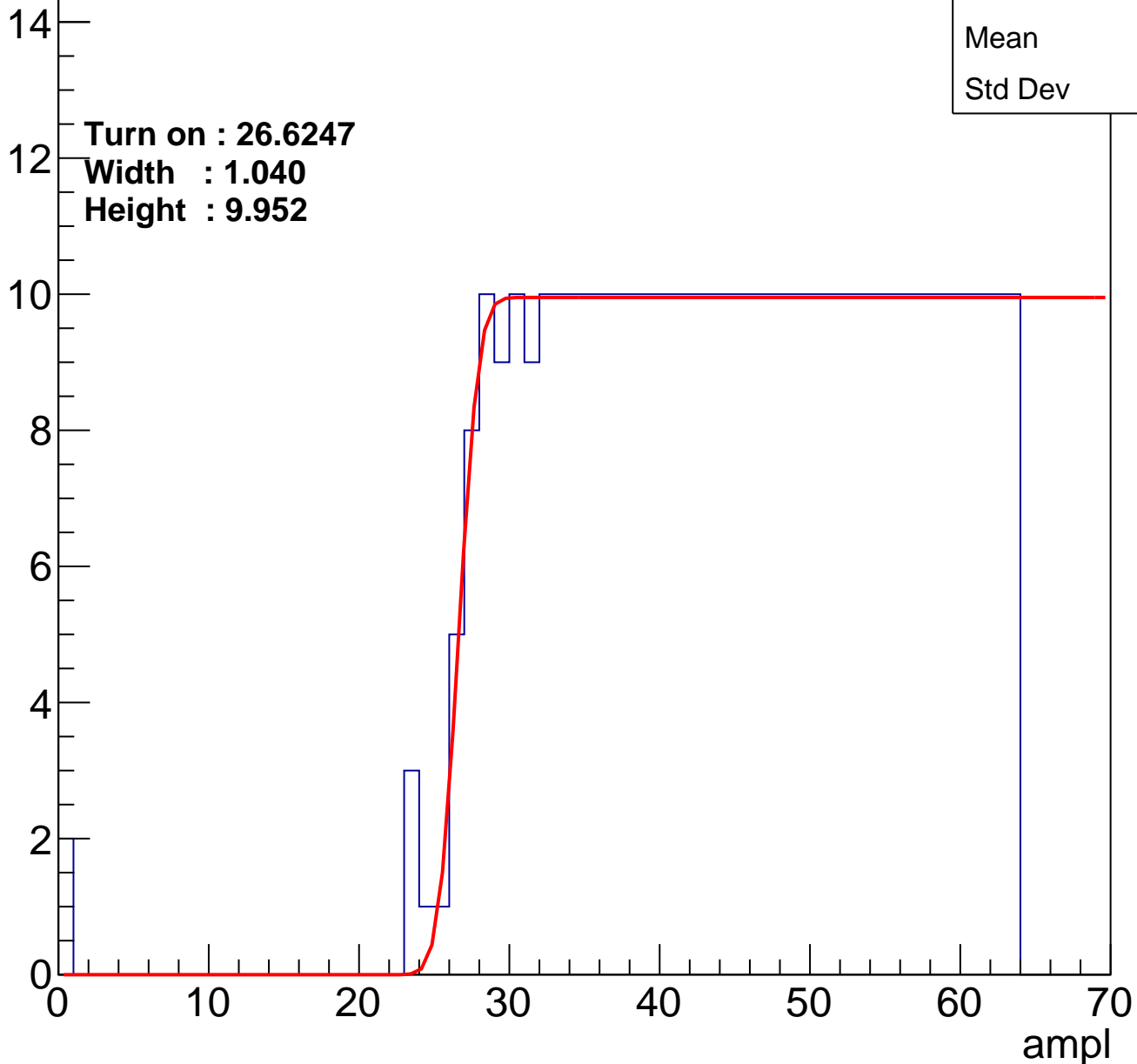
Entries	378
Mean	44.4
Std Dev	11.4

Turn on : 26.6247

Width : 1.040

Height : 9.952

Entry



B1L000S, U20-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.22
Std Dev	11.79

Turn on : 26.3189

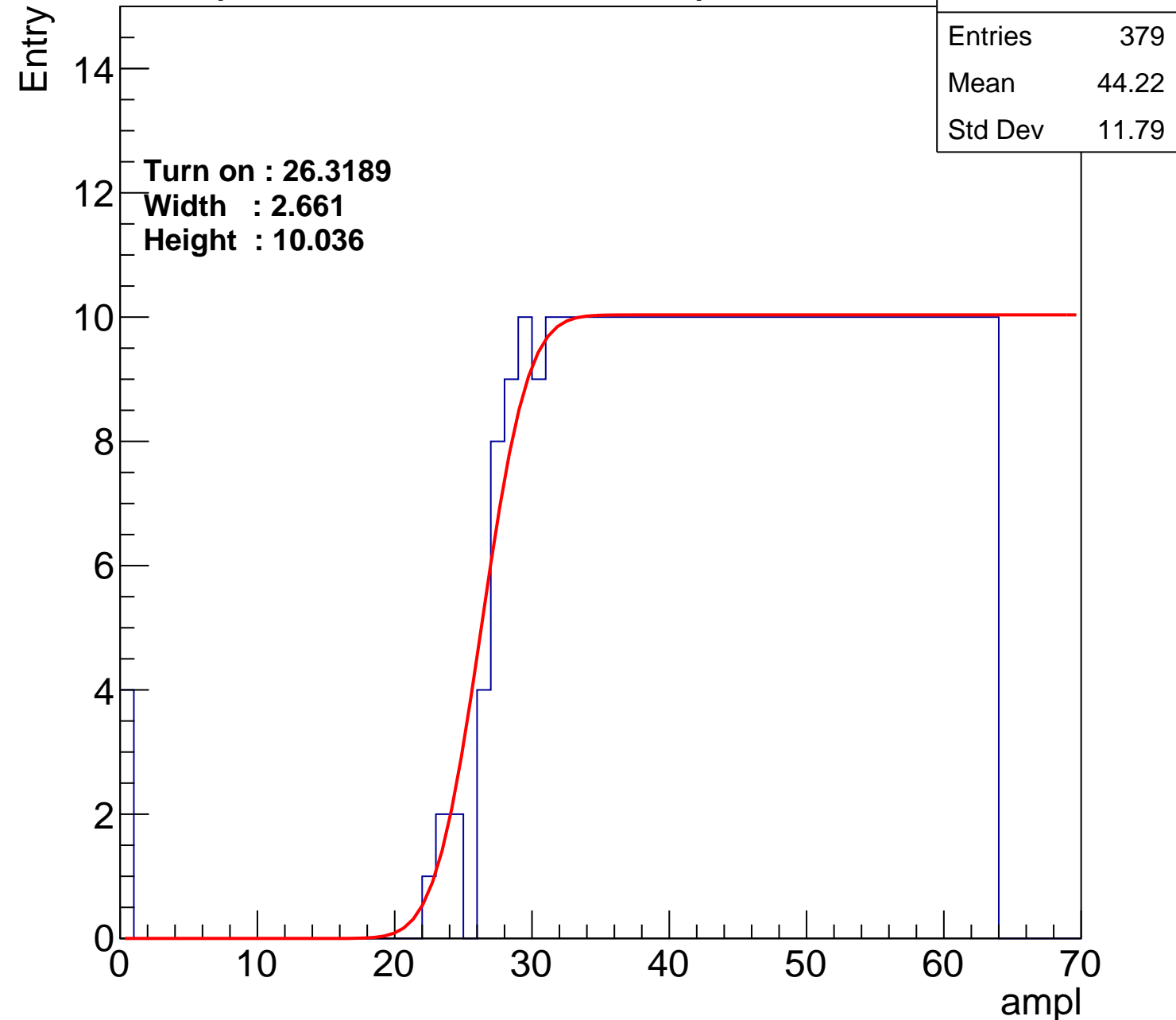
Width : 2.661

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.2642

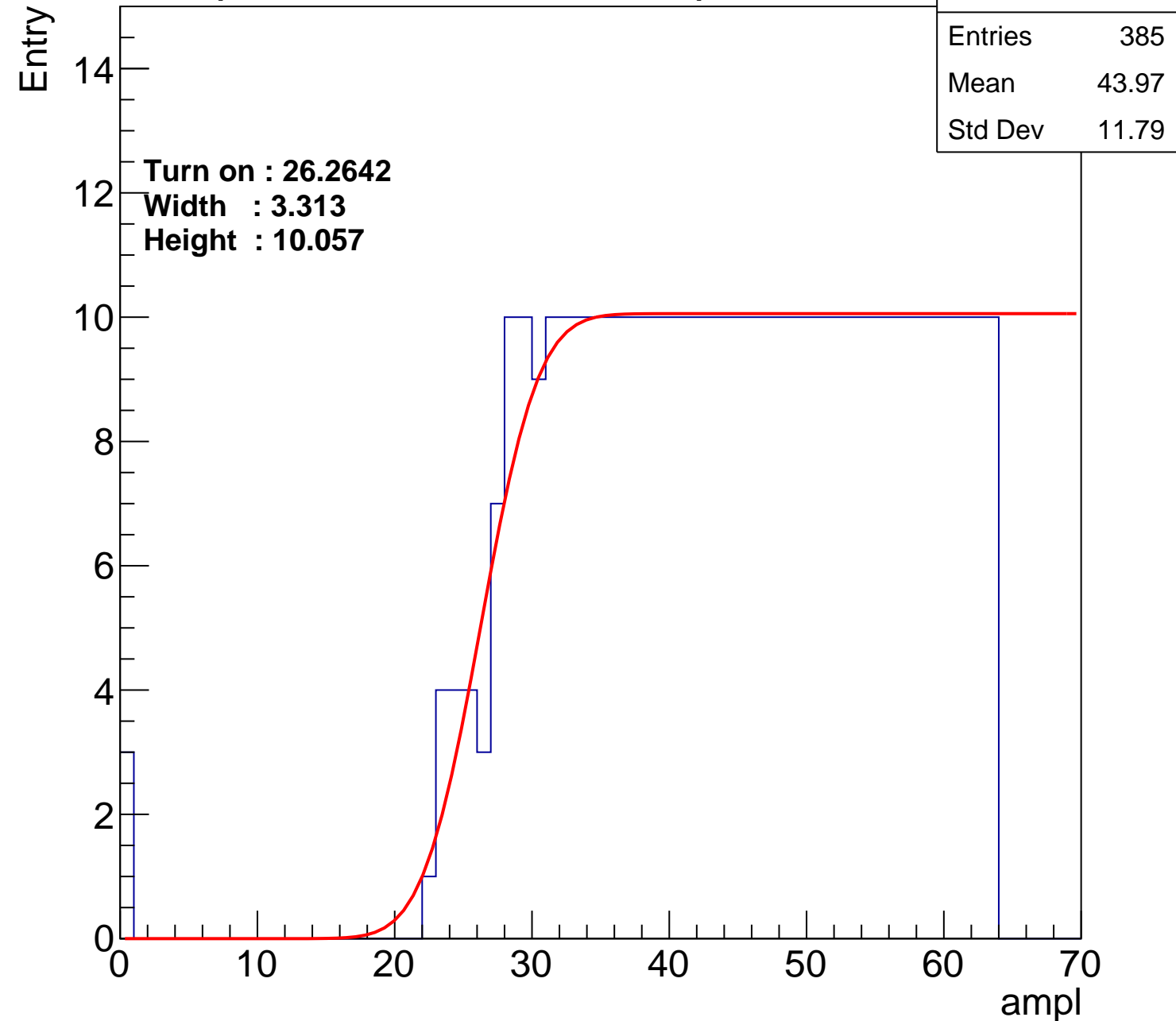
Width : 3.313

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.62
Std Dev	11.46

Turn on : 27.2602

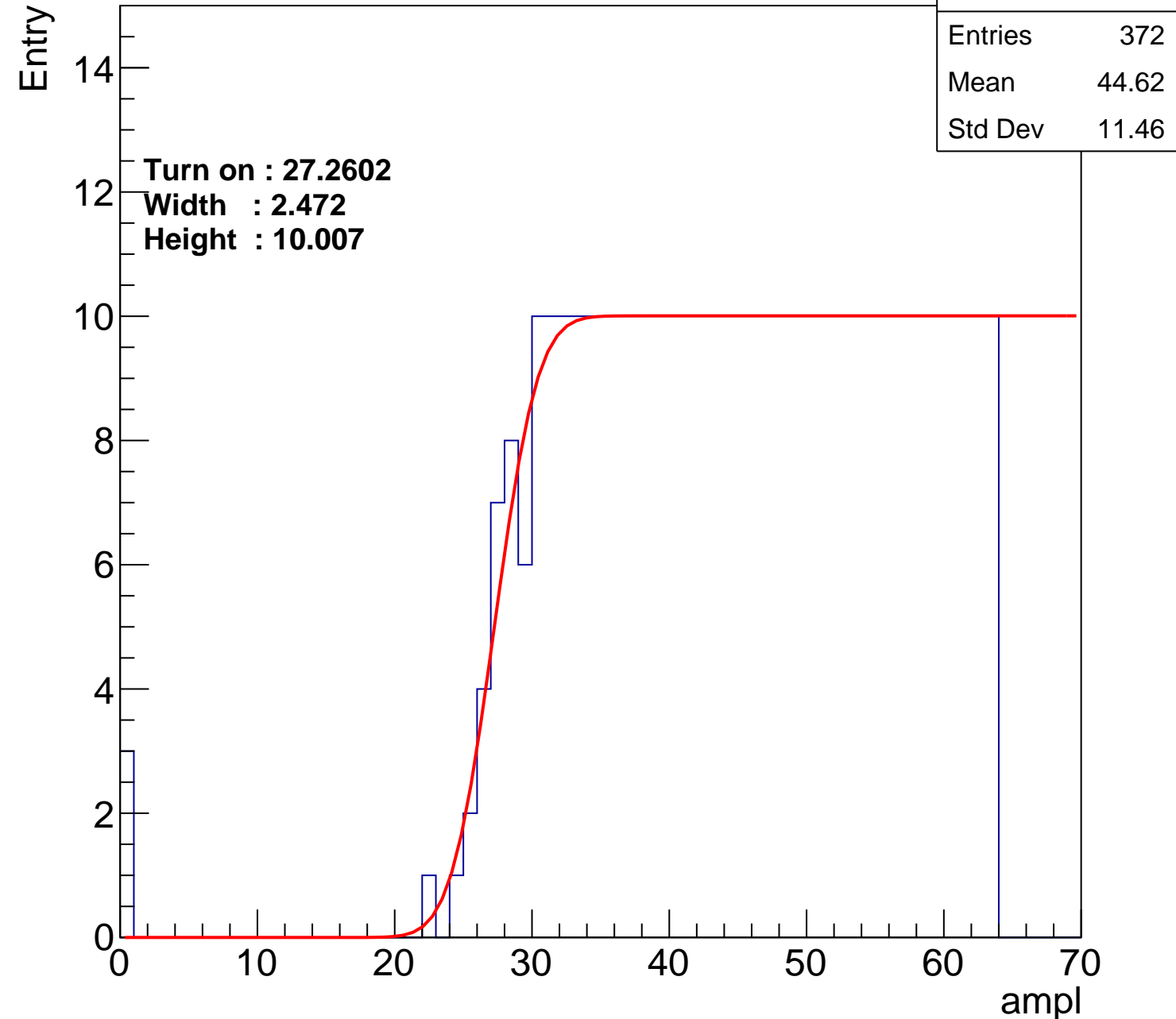
Width : 2.472

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.85
Std Dev	11.55

Turn on : 28.0386

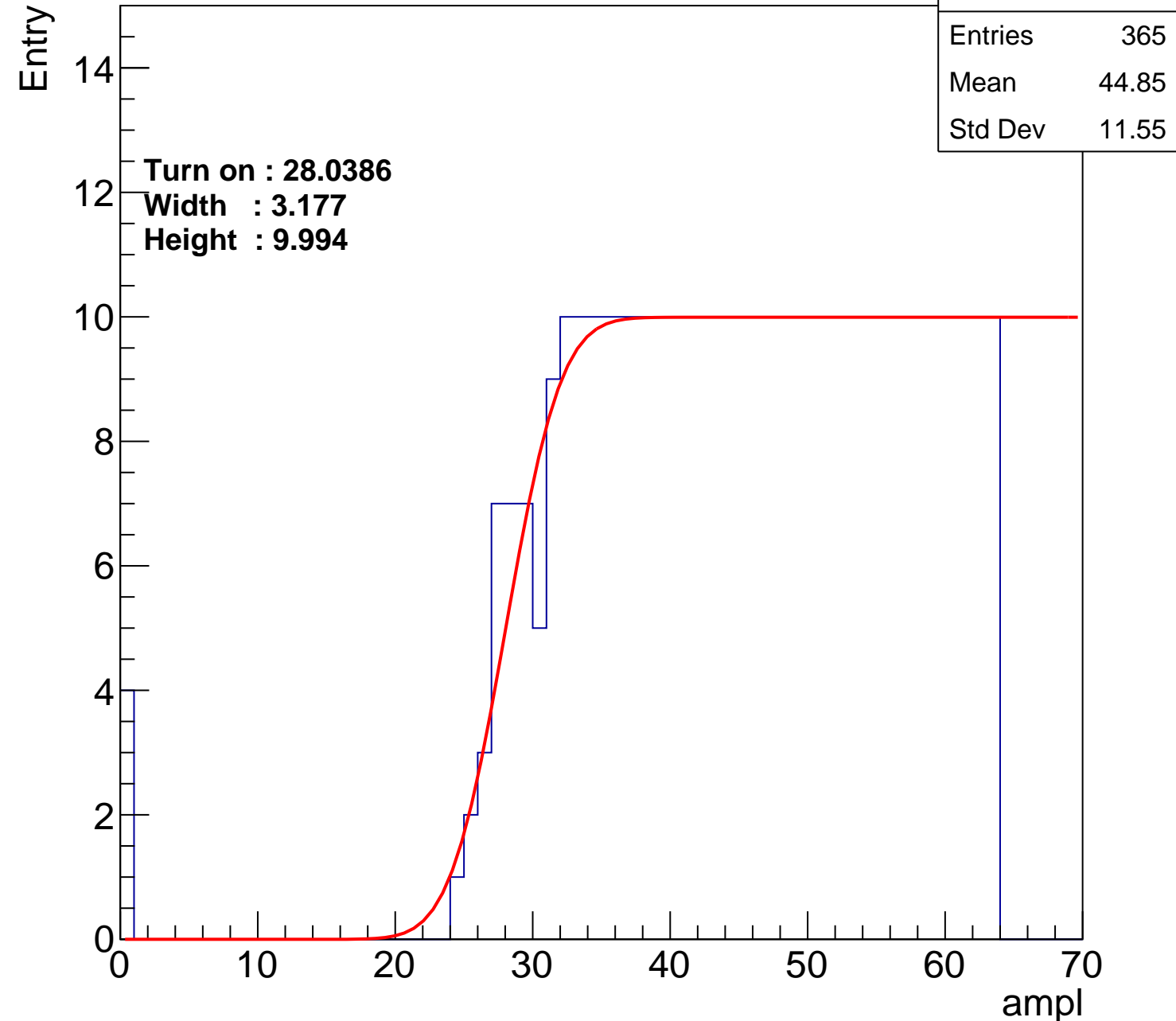
Width : 3.177

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.96
Std Dev	11.75

Turn on : 25.8411

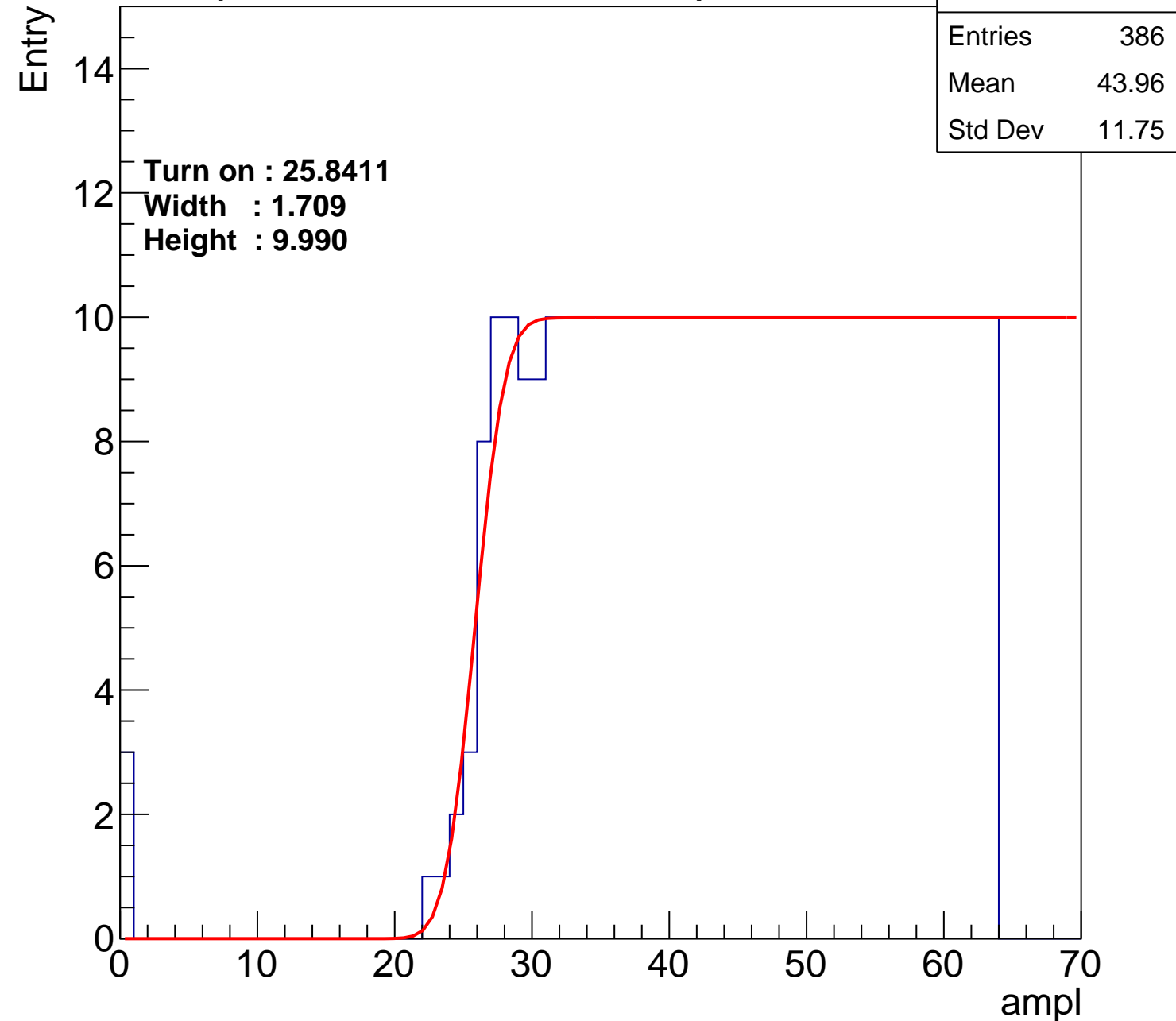
Width : 1.709

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch109

calib_packv5_042523_0143.root, FC#3, port B3

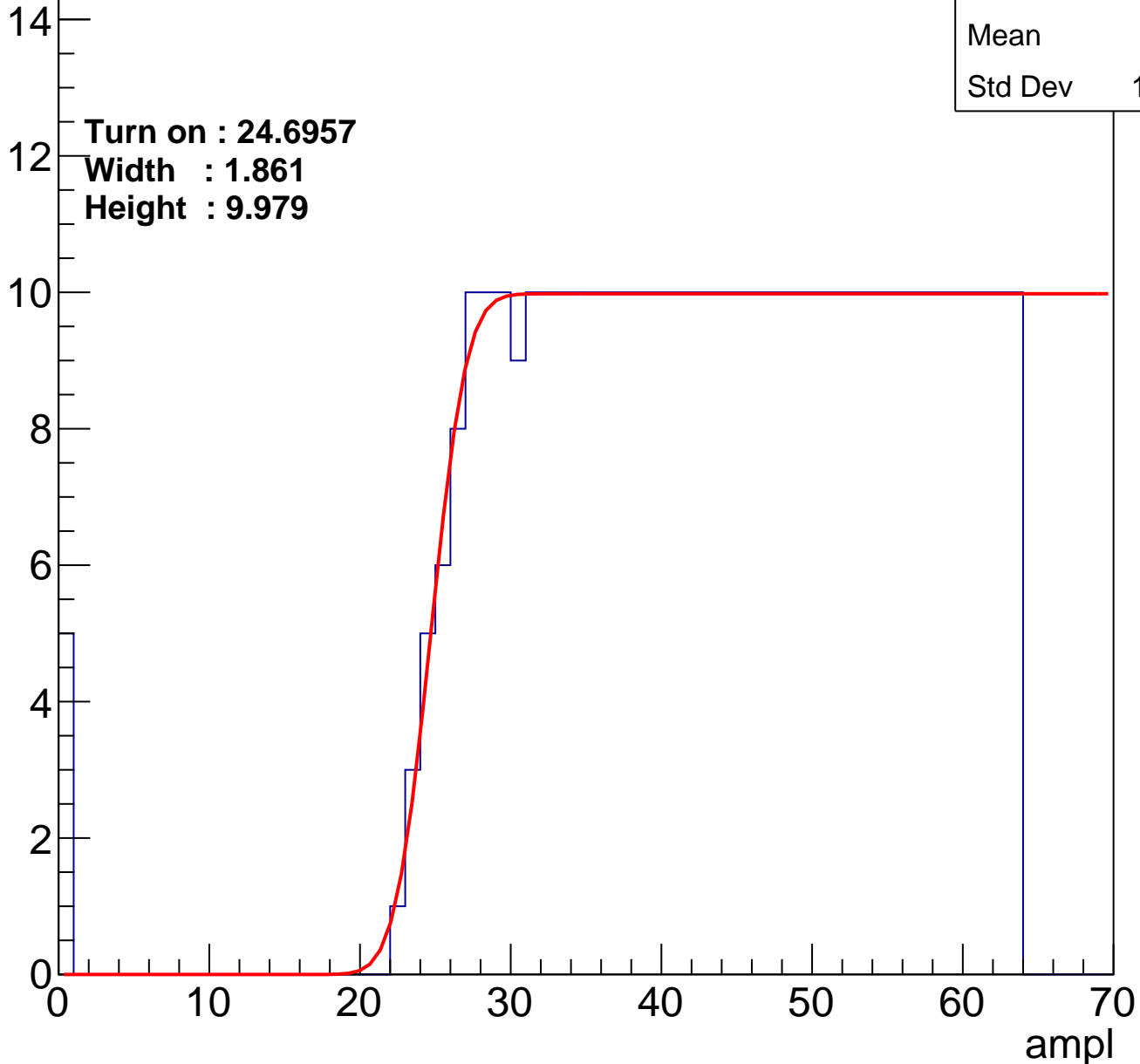
Entries	397
Mean	43.3
Std Dev	12.33

Turn on : 24.6957

Width : 1.861

Height : 9.979

Entry



B1L000S, U20-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	352
Mean	45.64
Std Dev	10.82

Turn on : 29.6210

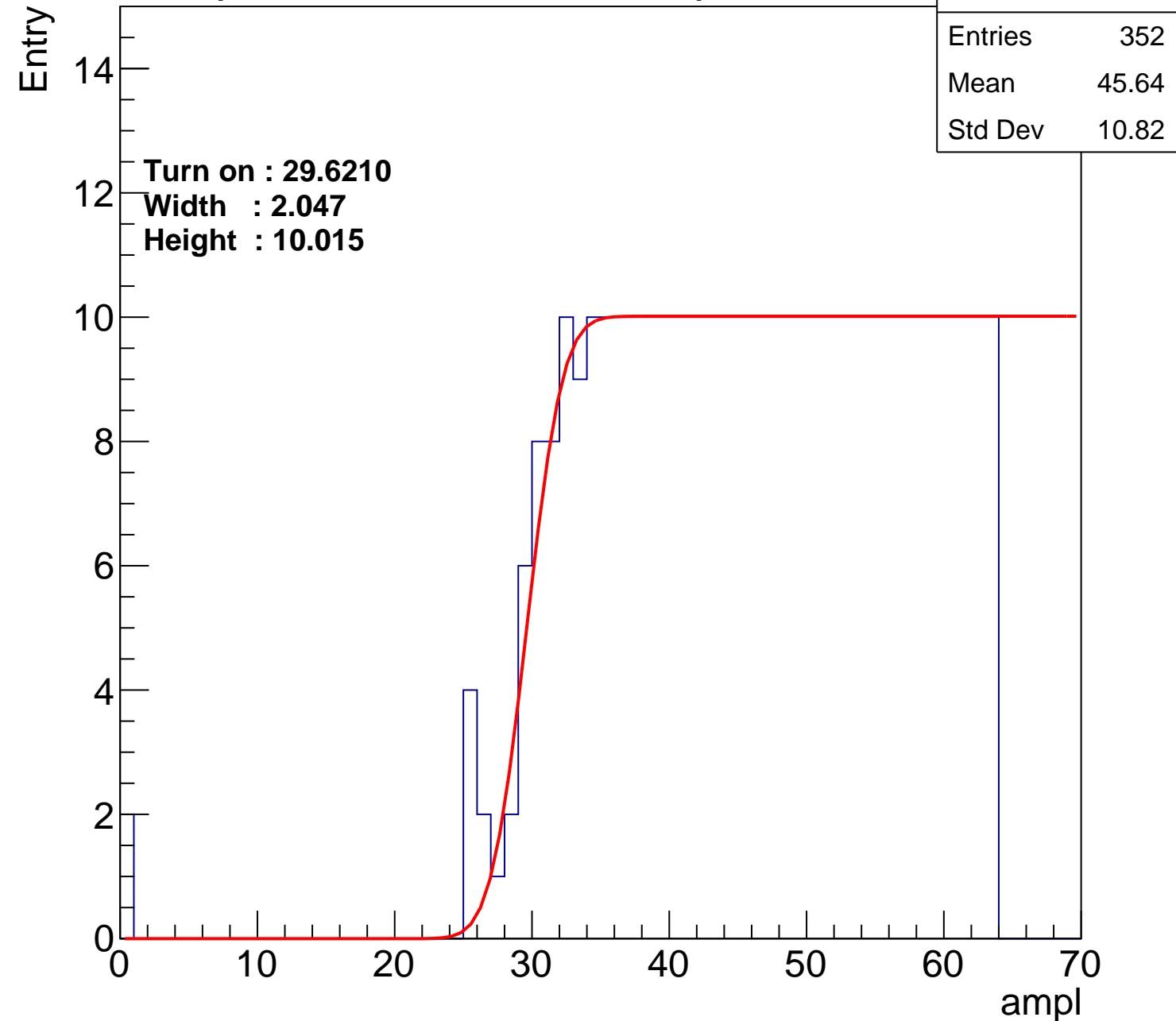
Width : 2.047

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.01
Std Dev	11.32

Turn on : 28.2046

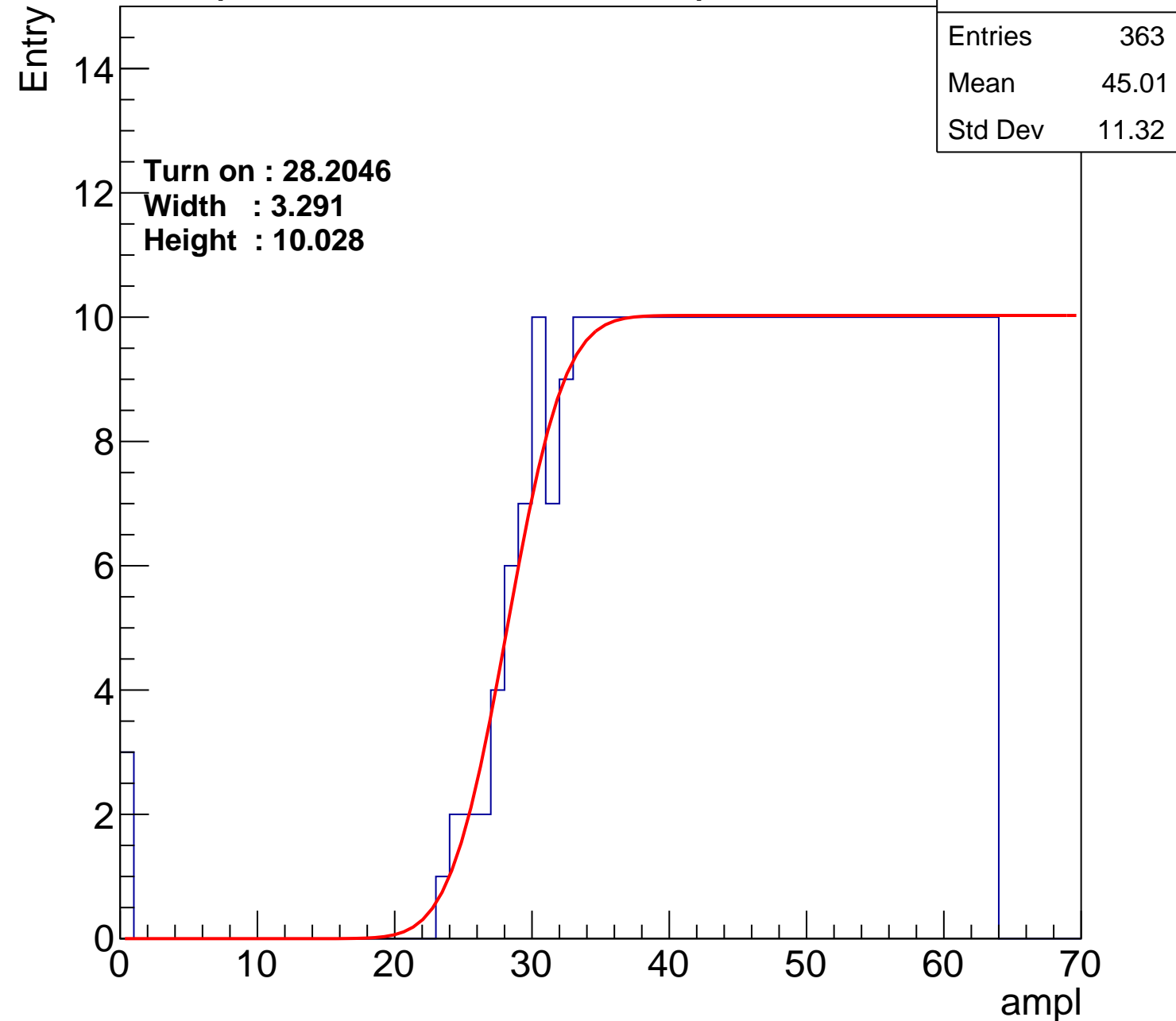
Width : 3.291

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.39
Std Dev	10.94

Turn on : 28.6234

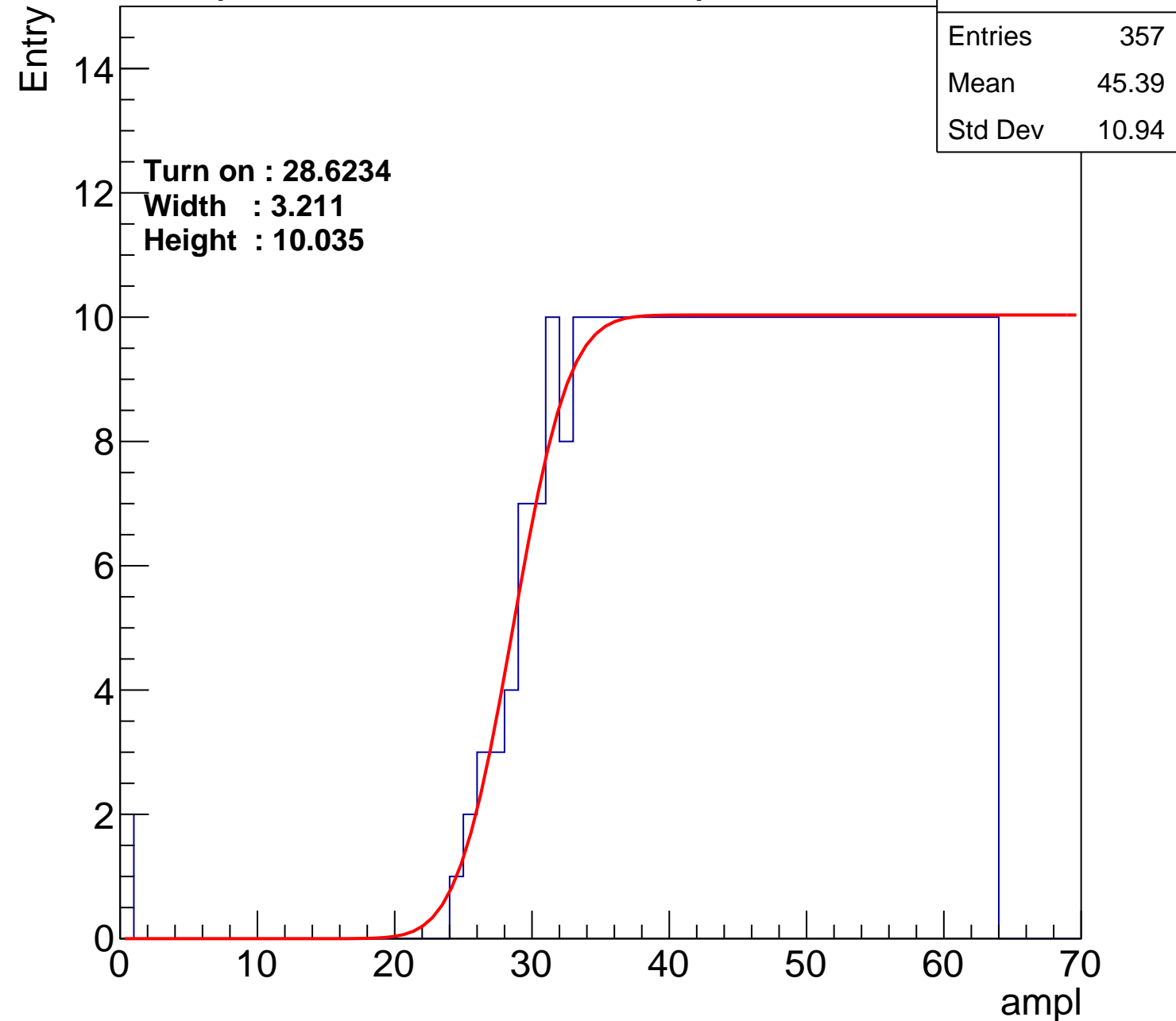
Width : 3.211

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.4283

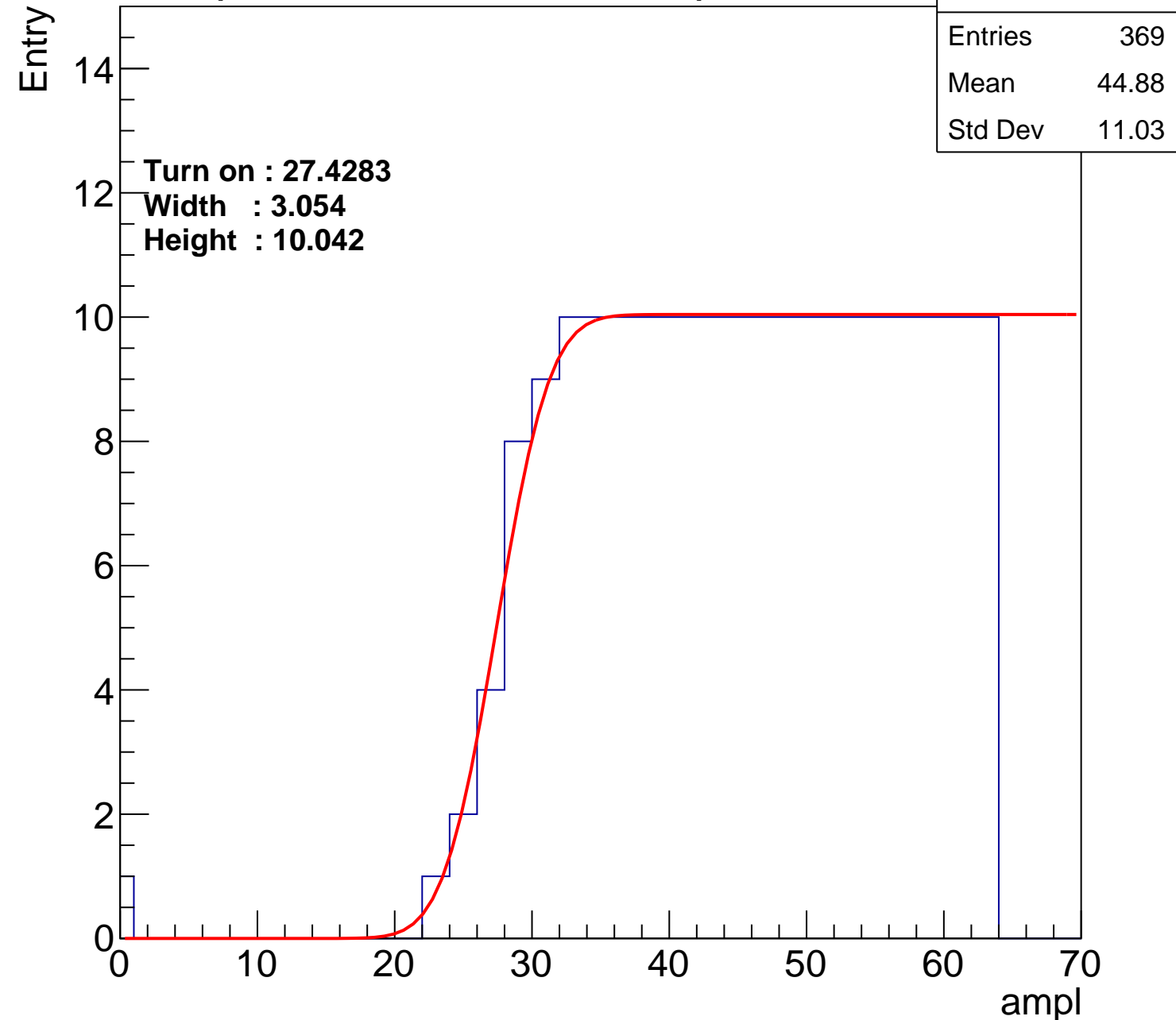
Width : 3.054

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.93
Std Dev	10.94

Turn on : 27.4592

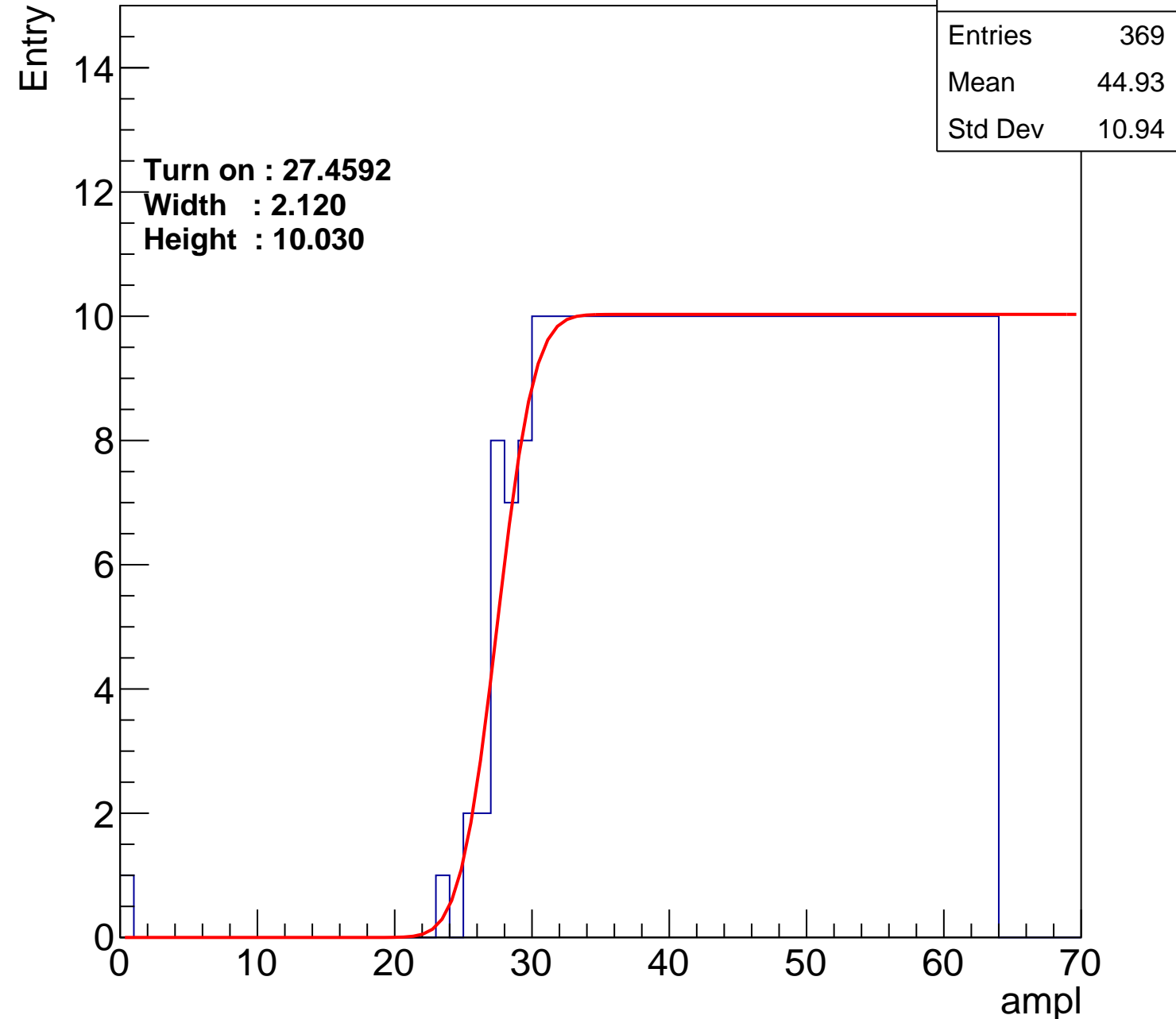
Width : 2.120

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch115

calib_packv5_042523_0143.root, FC#3, port B3

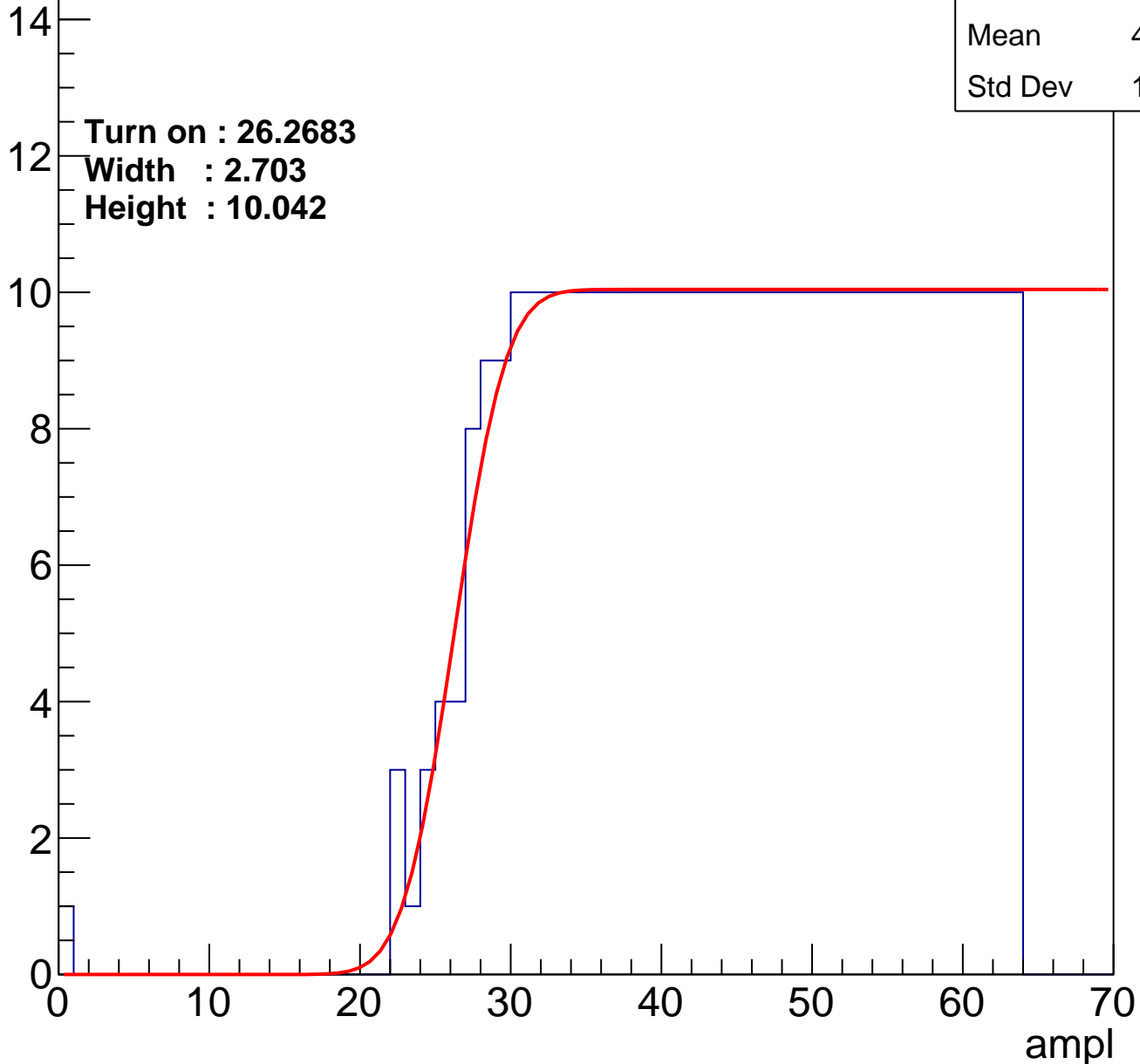
Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.2683

Width : 2.703

Height : 10.042

Entry



B1L000S, U20-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.5047

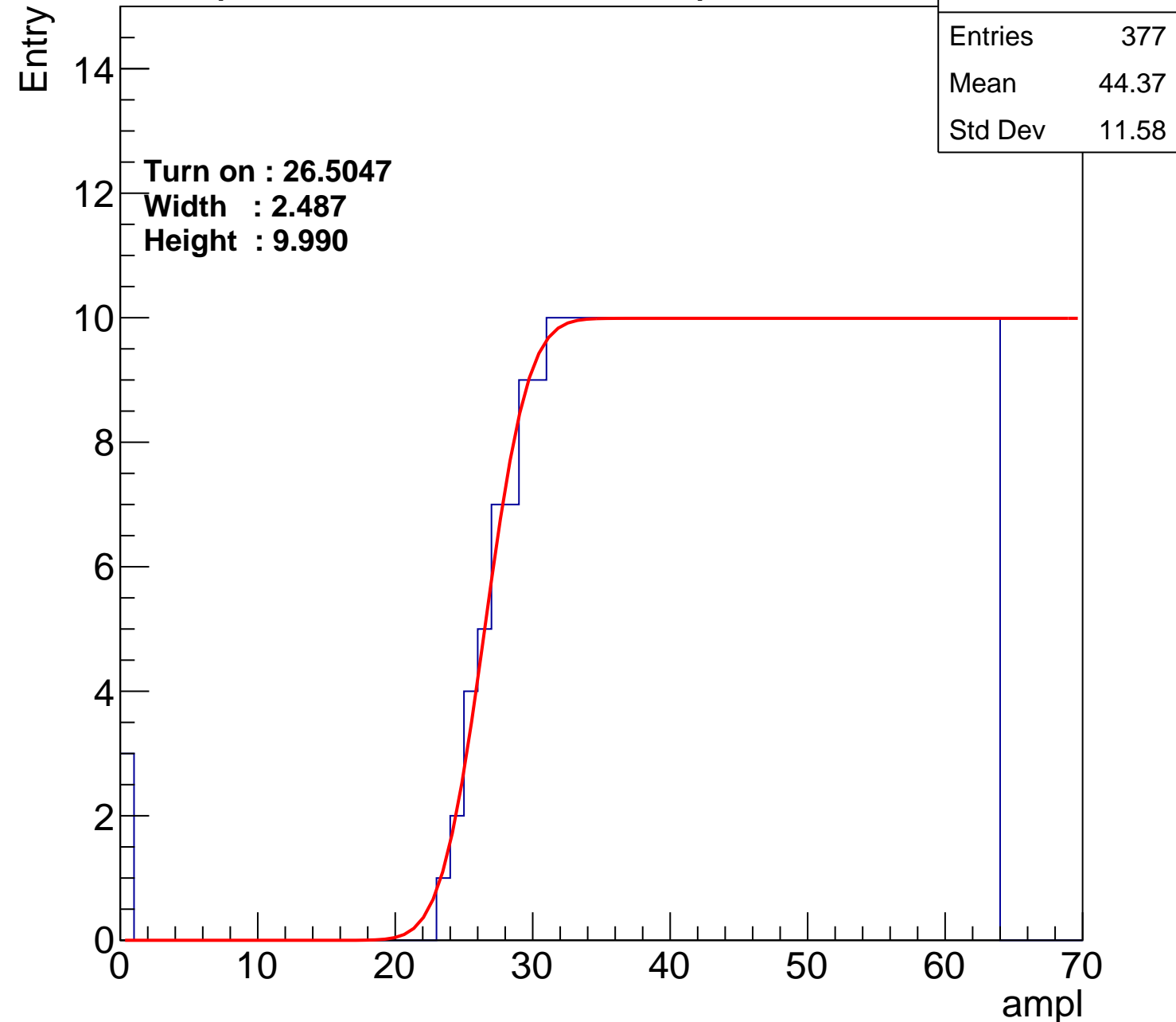
Width : 2.487

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch117

calib_packv5_042523_0143.root, FC#3, port B3

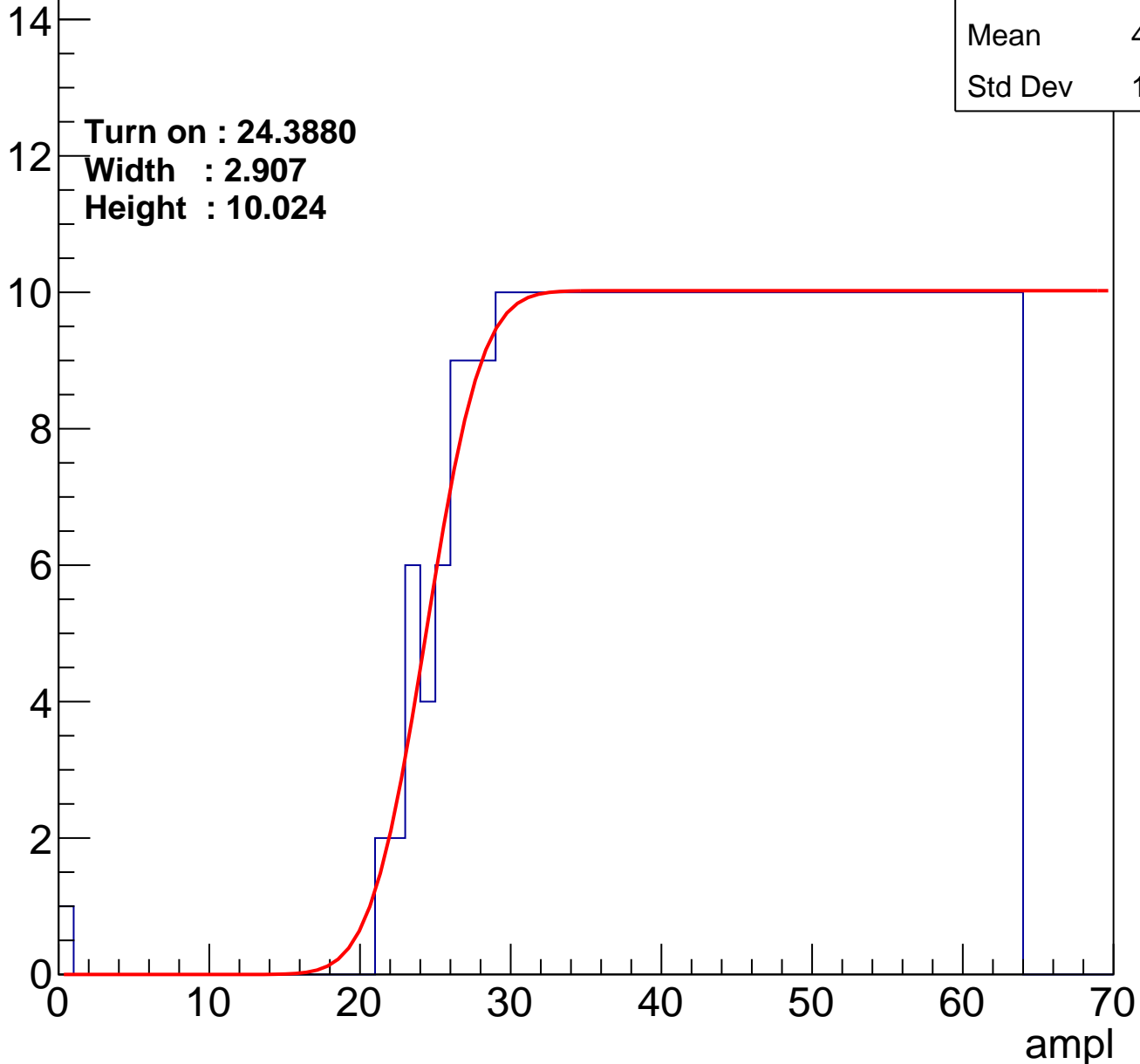
Entries	398
Mean	43.46
Std Dev	11.77

Turn on : 24.3880

Width : 2.907

Height : 10.024

Entry



B1L000S, U20-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	42.95
Std Dev	12.3

Turn on : 24.0922

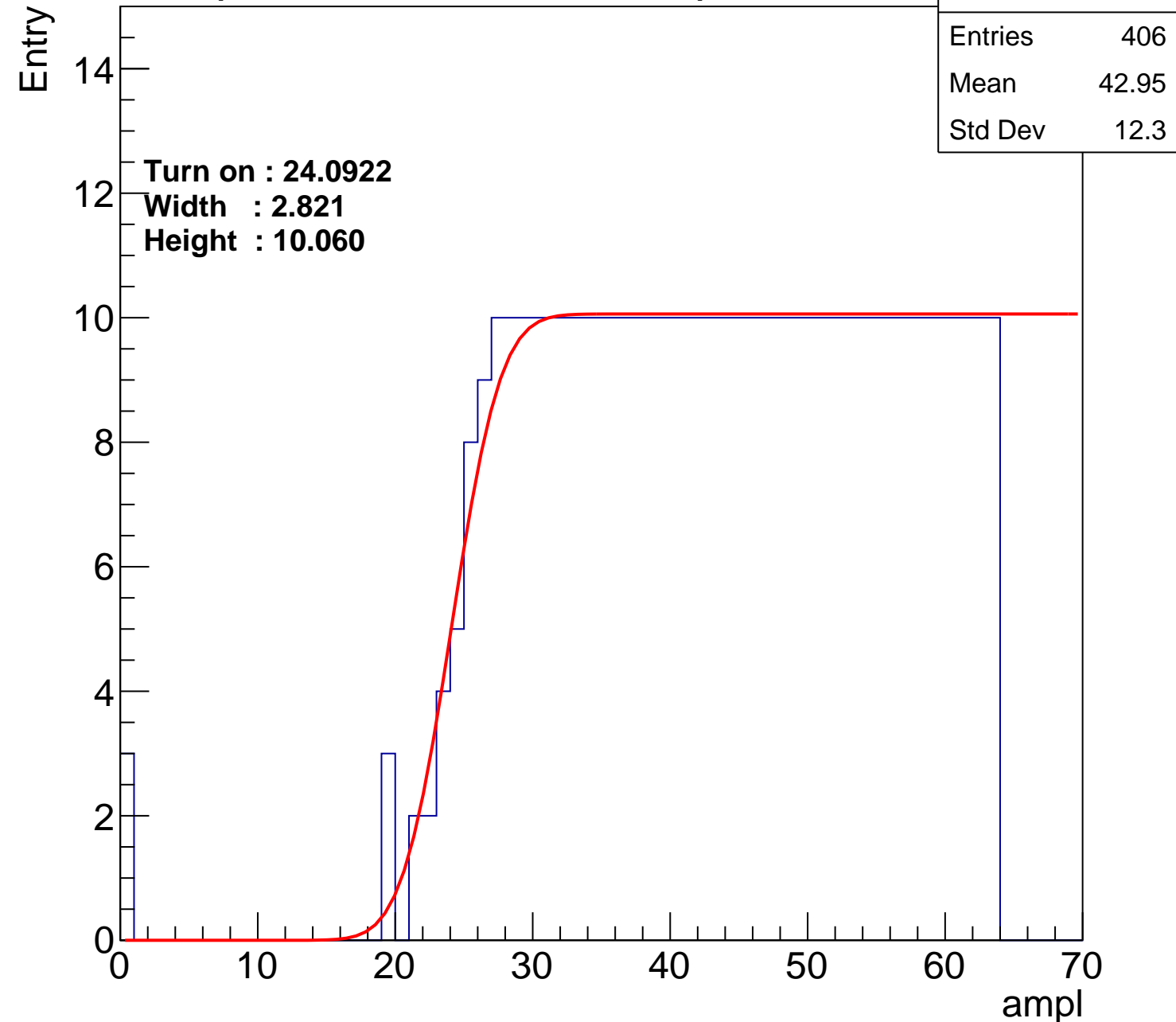
Width : 2.821

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.38
Std Dev	11.74

Turn on : 26.9978

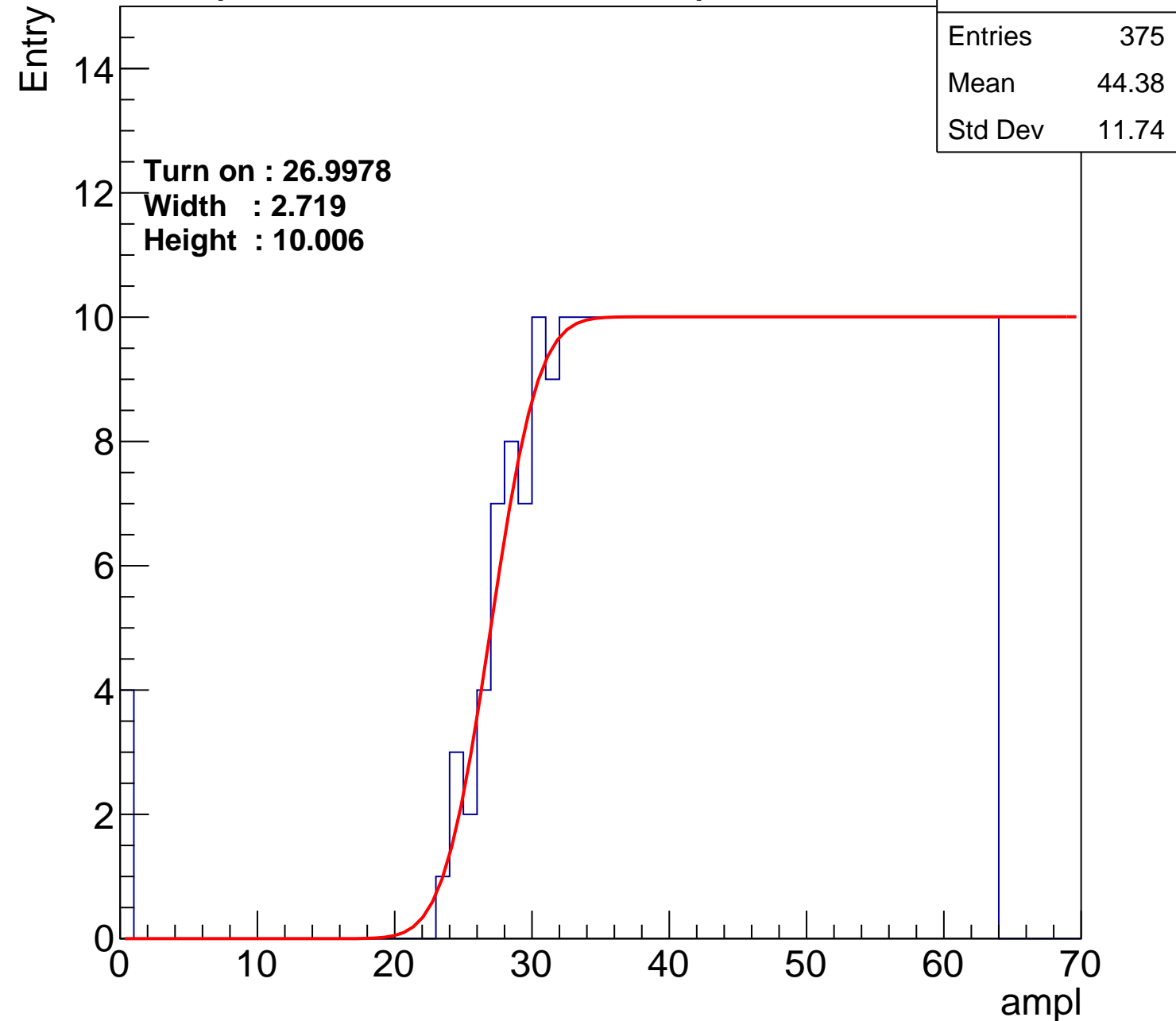
Width : 2.719

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch120

calib_packv5_042523_0143.root, FC#3, port B3

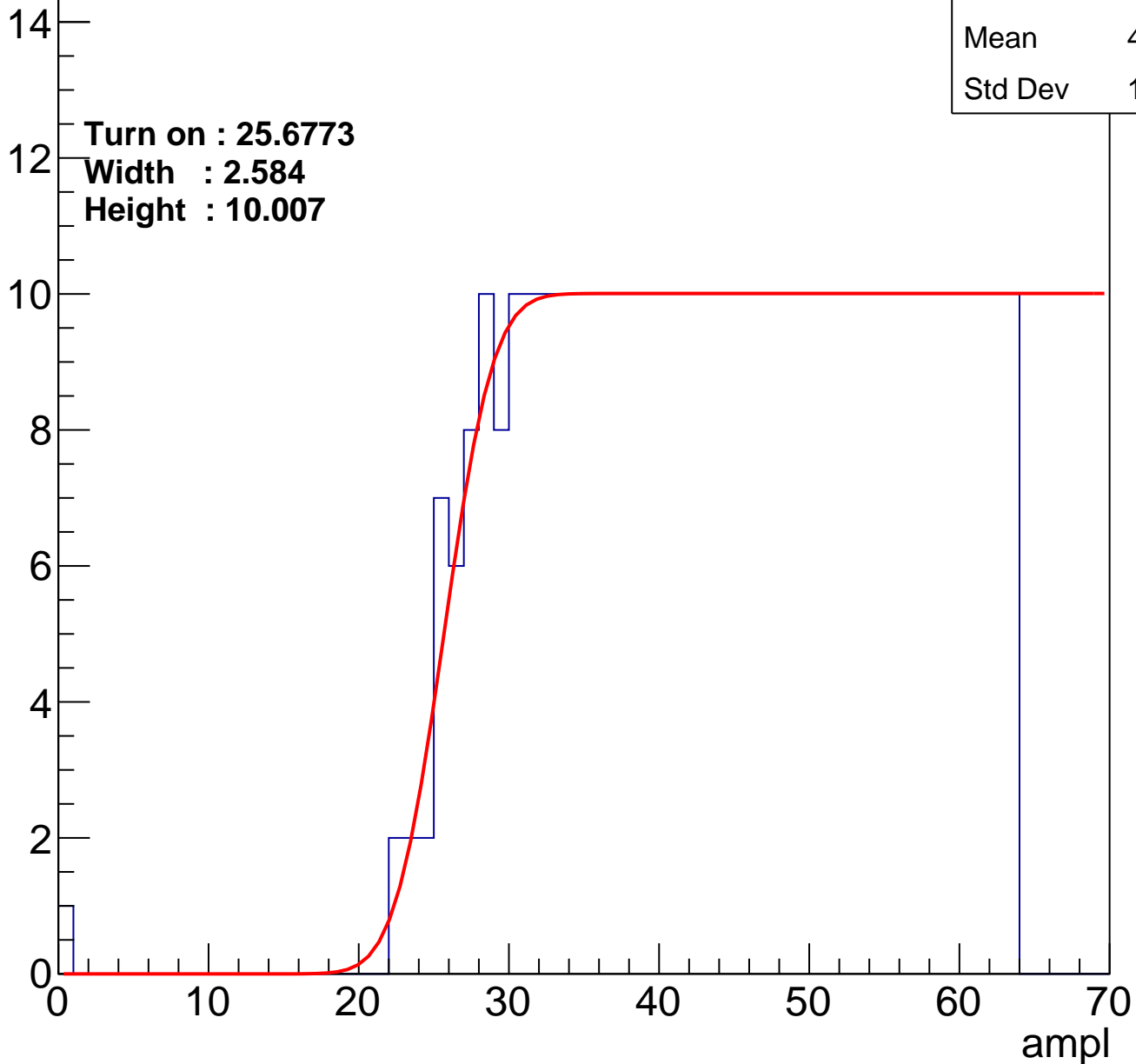
Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.6773

Width : 2.584

Height : 10.007

Entry



B1L000S, U20-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	407
Mean	43.05
Std Dev	11.96

Turn on : 23.4528

Width : 2.378

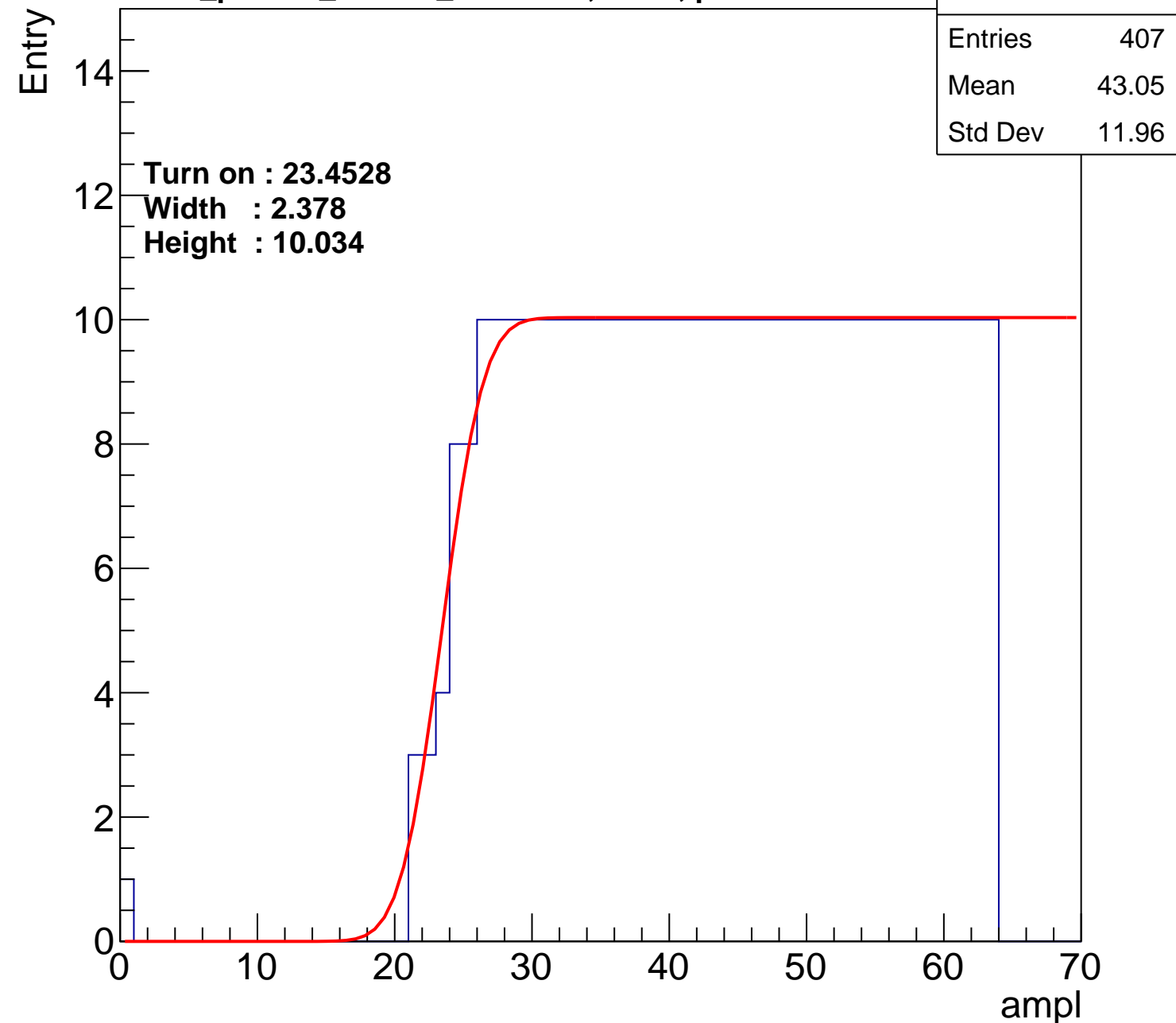
Height : 10.034

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L000S, U20-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.24

Turn on : 27.4493

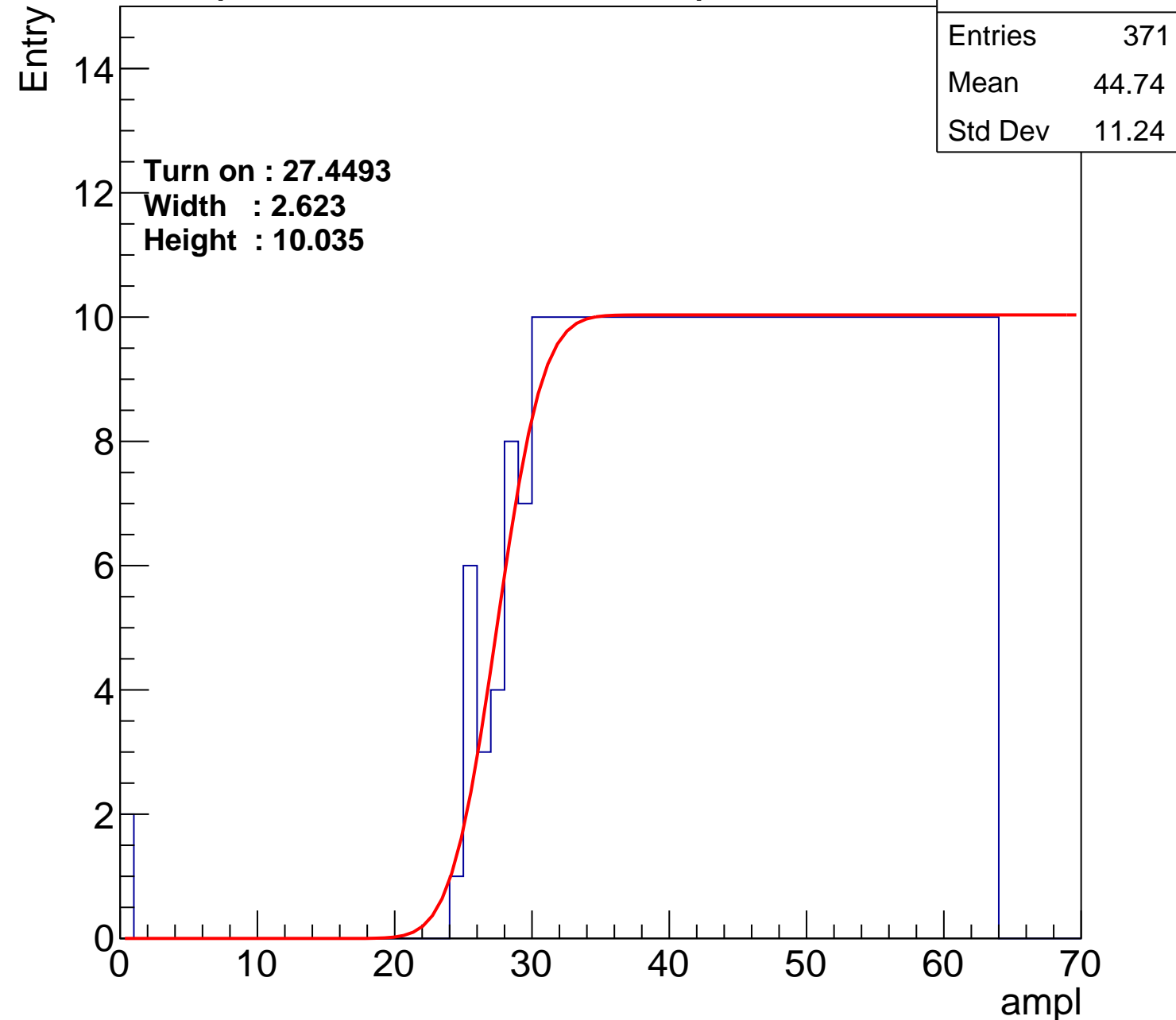
Width : 2.623

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch123

calib_packv5_042523_0143.root, FC#3, port B3

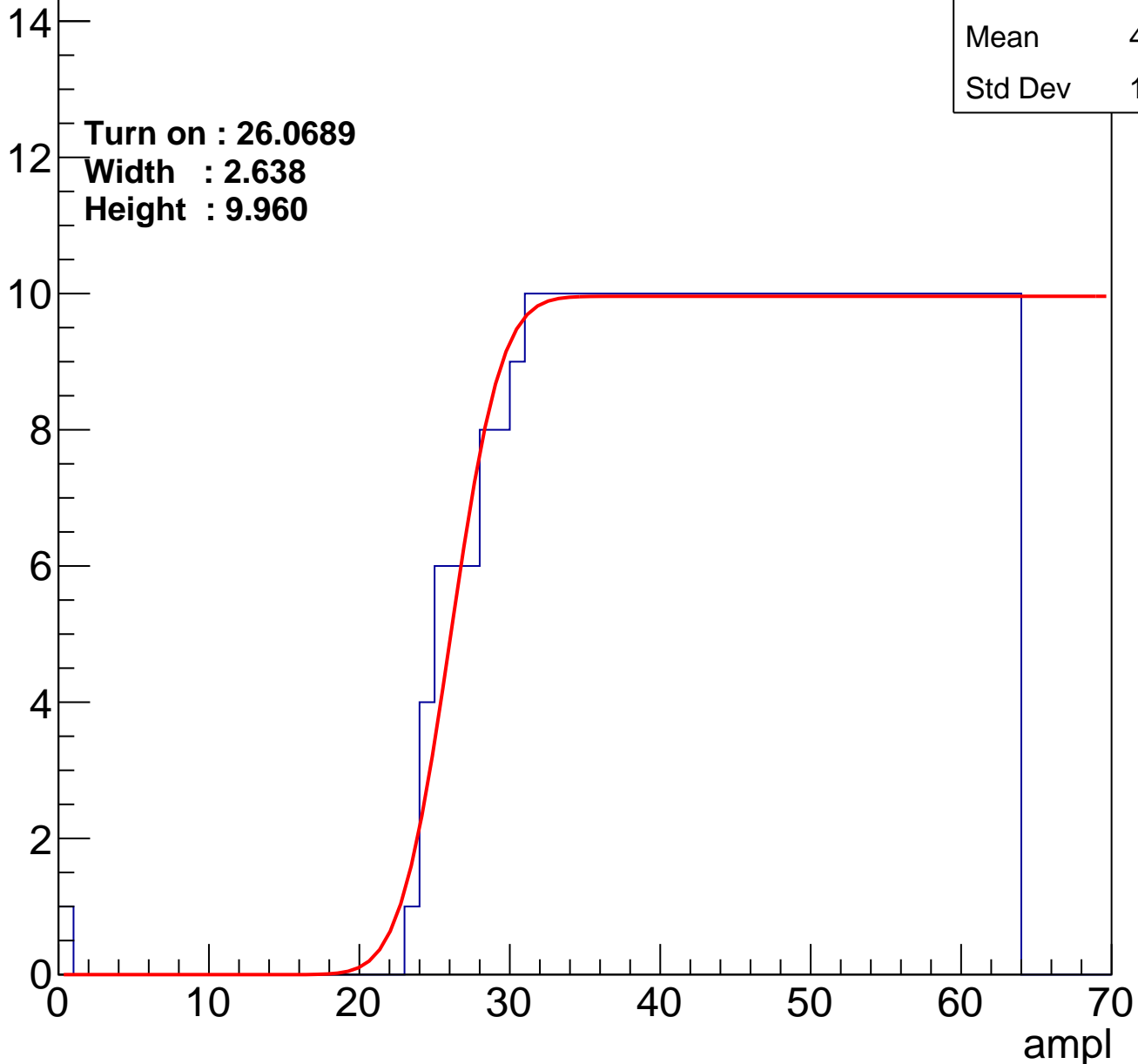
Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.0689

Width : 2.638

Height : 9.960

Entry



B1L000S, U20-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.5
Std Dev	11.78

Turn on : 24.7876

Width : 2.995

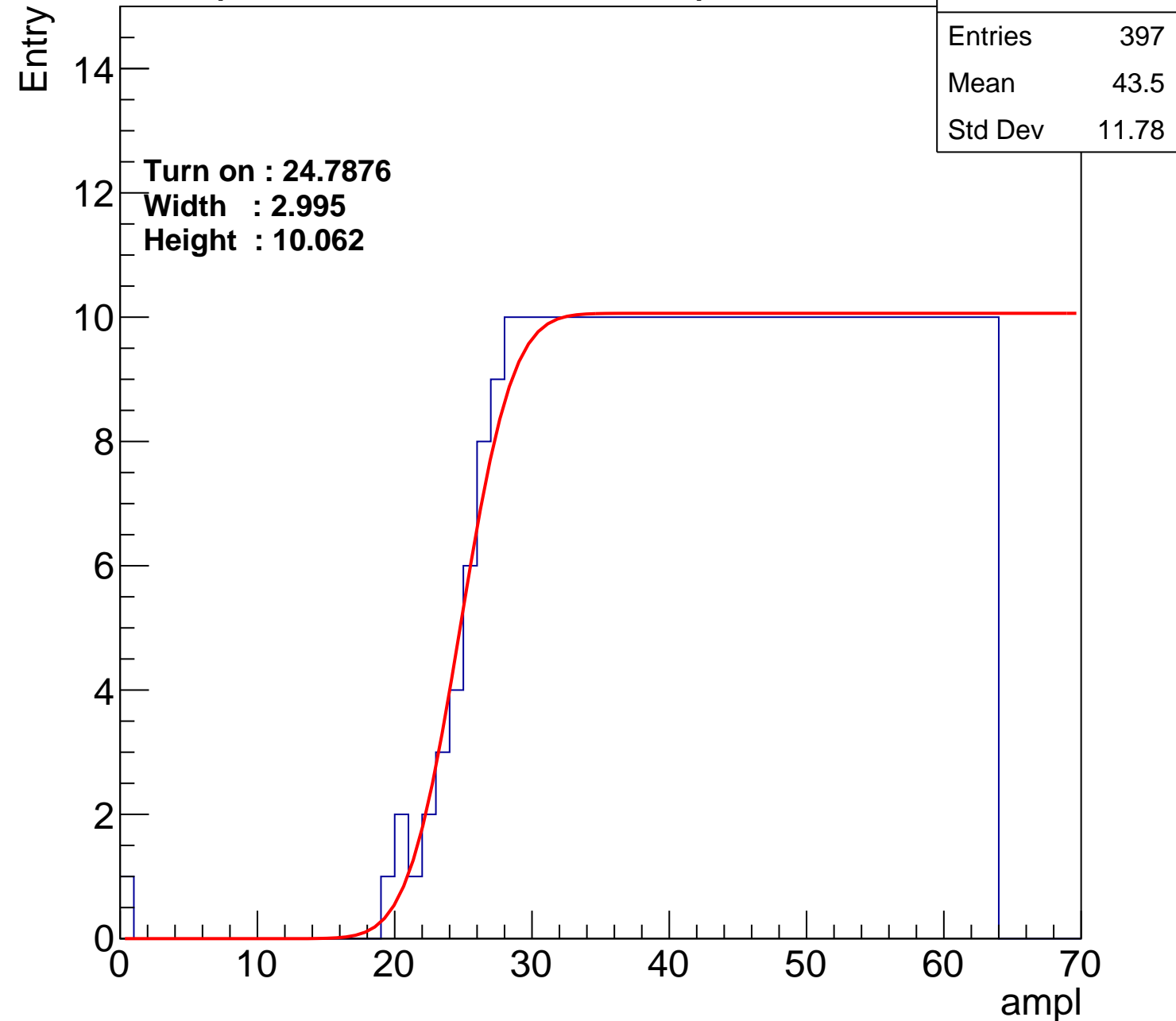
Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U20-ch125

calib_packv5_042523_0143.root, FC#3, port B3

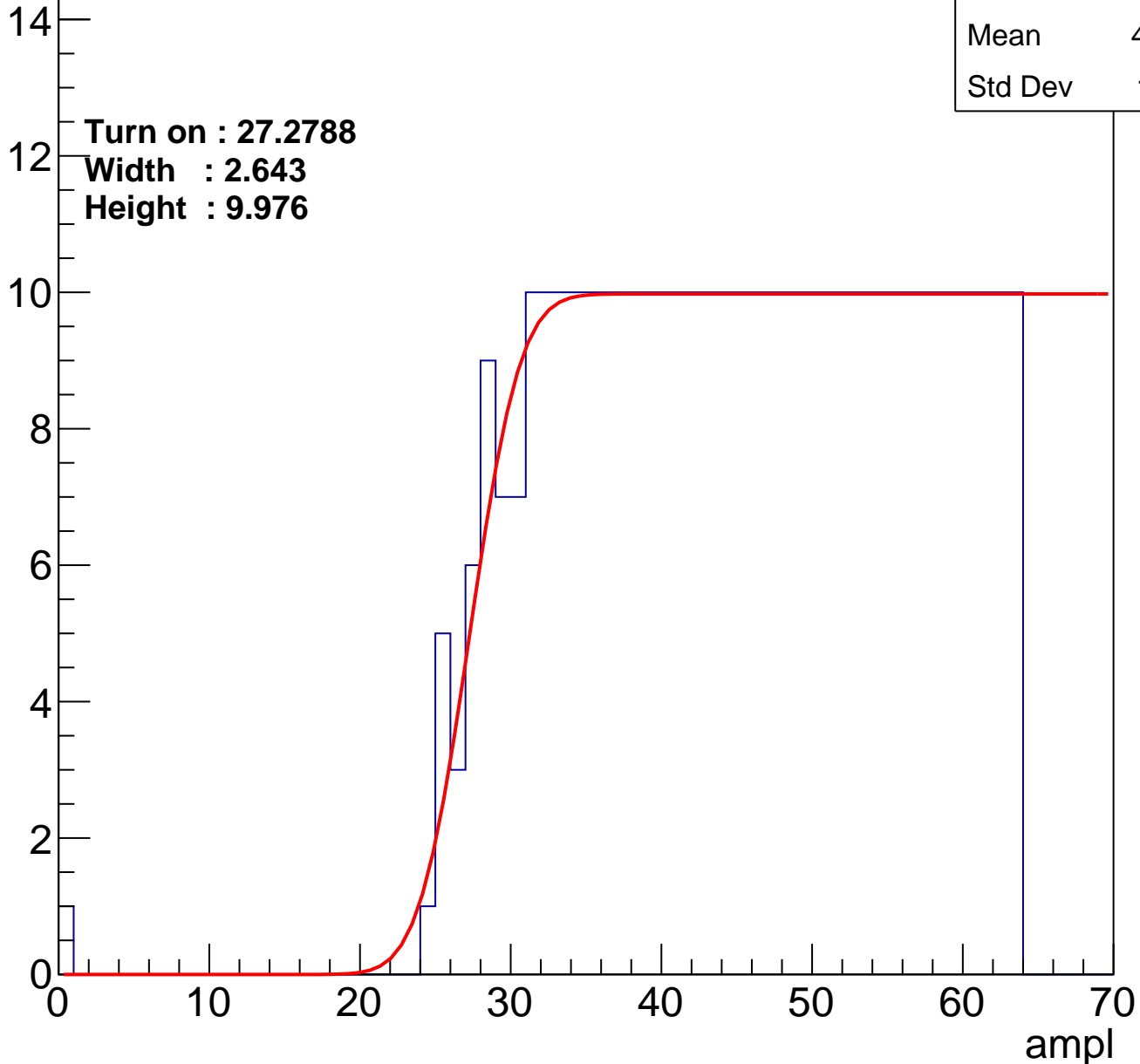
Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.2788

Width : 2.643

Height : 9.976

Entry



B1L000S, U20-ch126

calib_packv5_042523_0143.root, FC#3, port B3

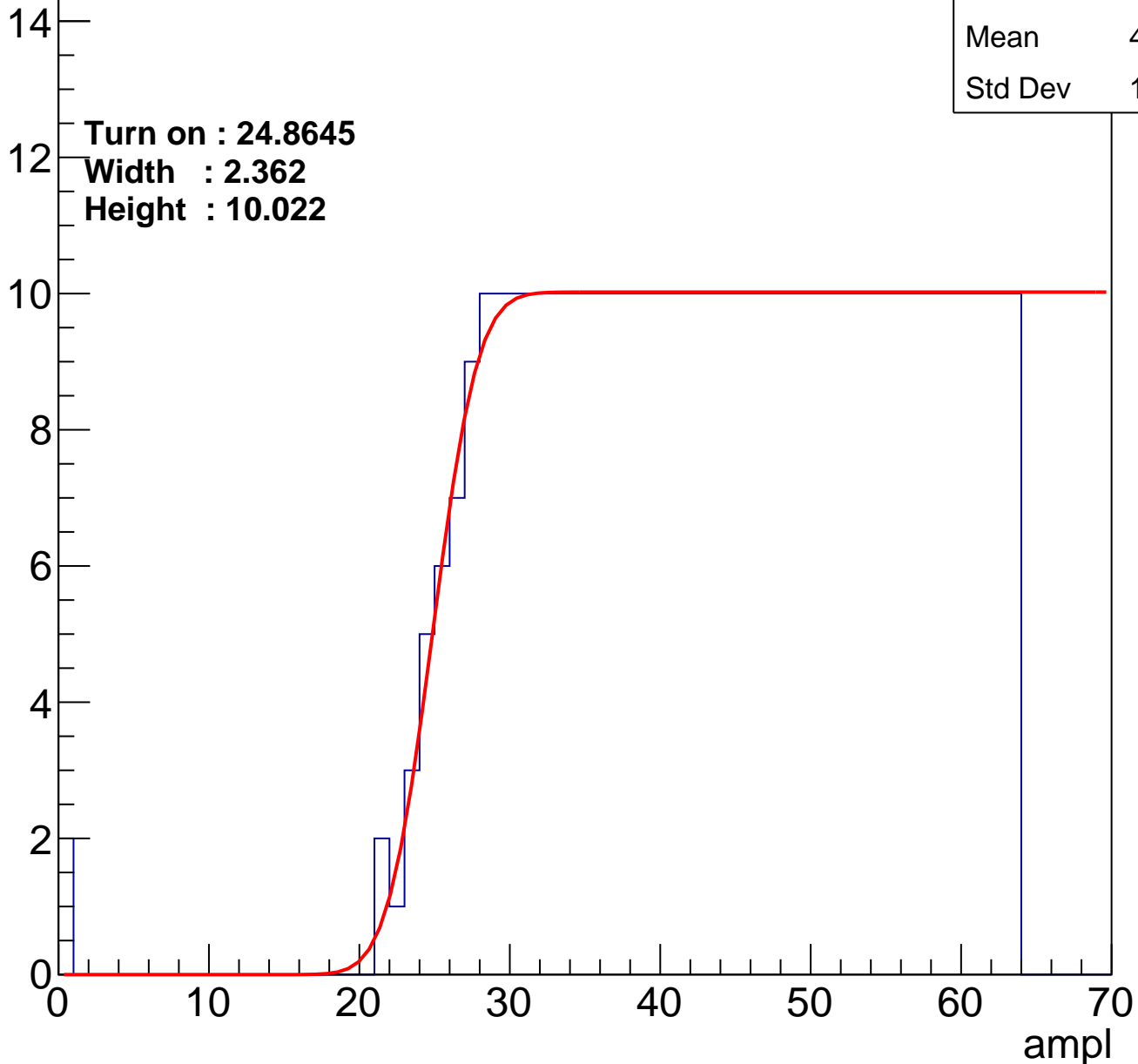
Entries	395
Mean	43.56
Std Dev	11.84

Turn on : 24.8645

Width : 2.362

Height : 10.022

Entry



B1L000S, U20-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.6
Std Dev	11.62

Turn on : 27.6654

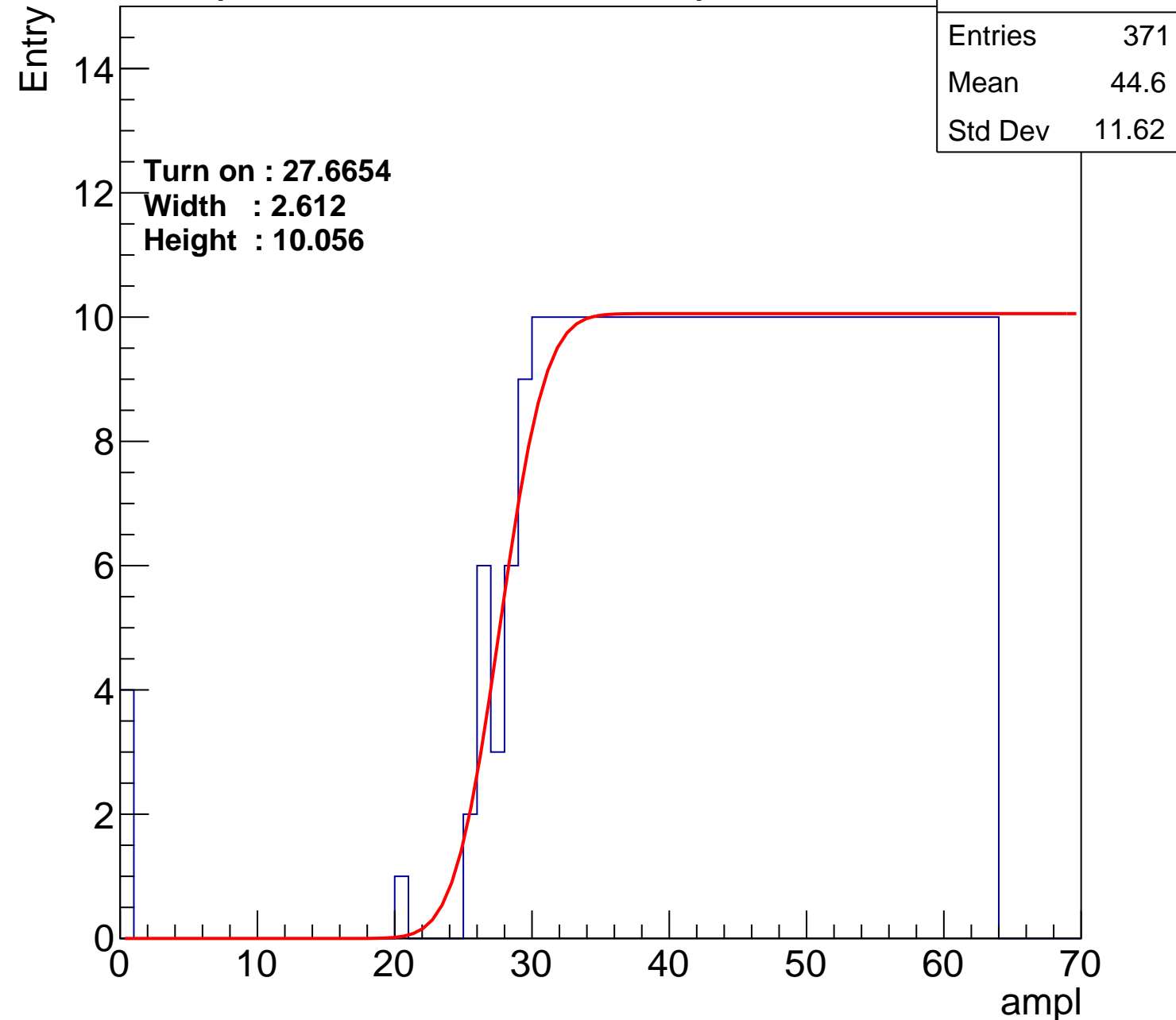
Width : 2.612

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U20-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.6
Std Dev	11.62

Turn on : 27.6654

Width : 2.612

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

