



# B0L000S, U14-ch0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	403
Mean	42.88
Std Dev	12.72

Turn on : 24.4665

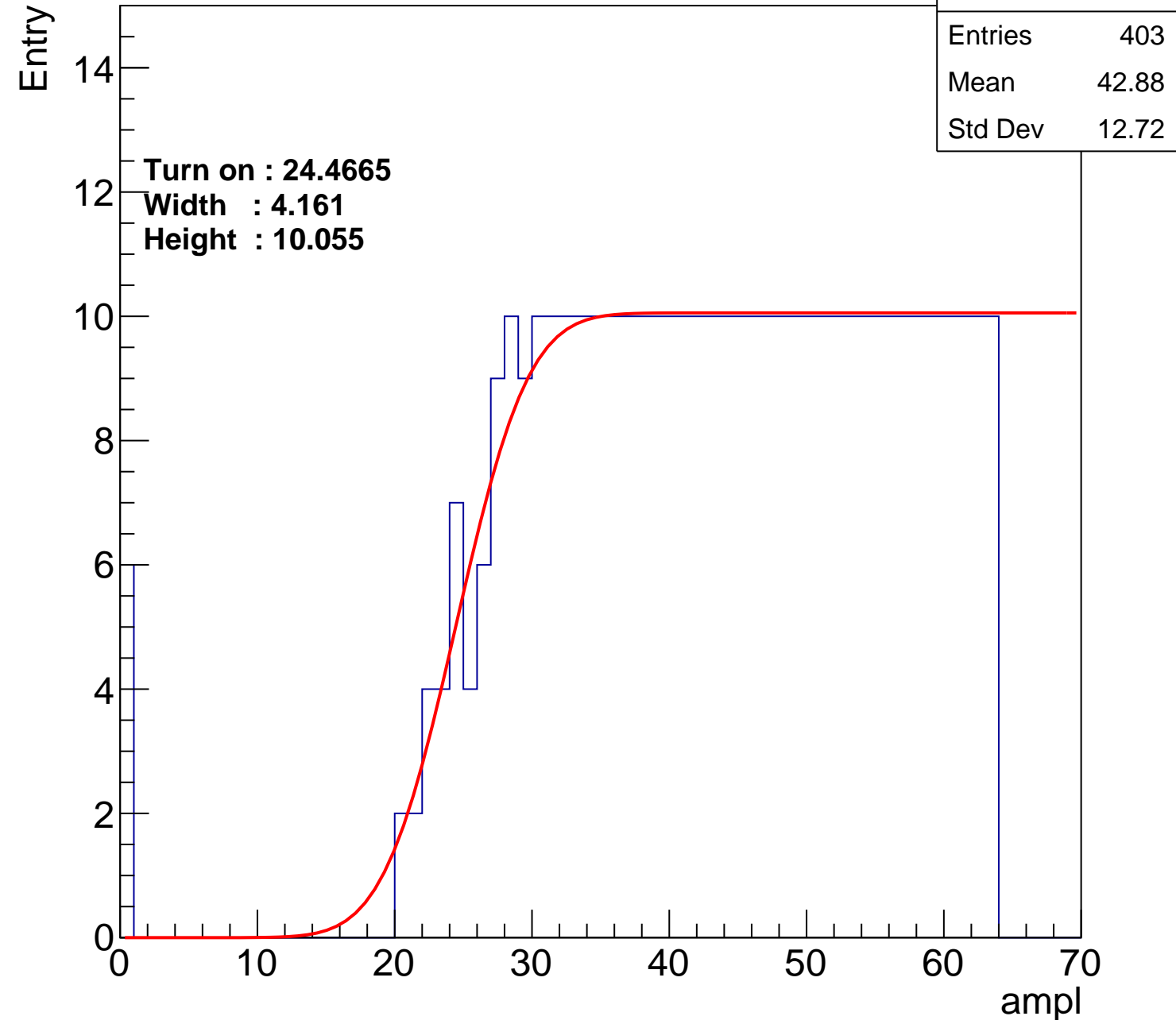
Width : 4.161

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	363
Mean	45.07
Std Dev	11.12

Turn on : 28.1058

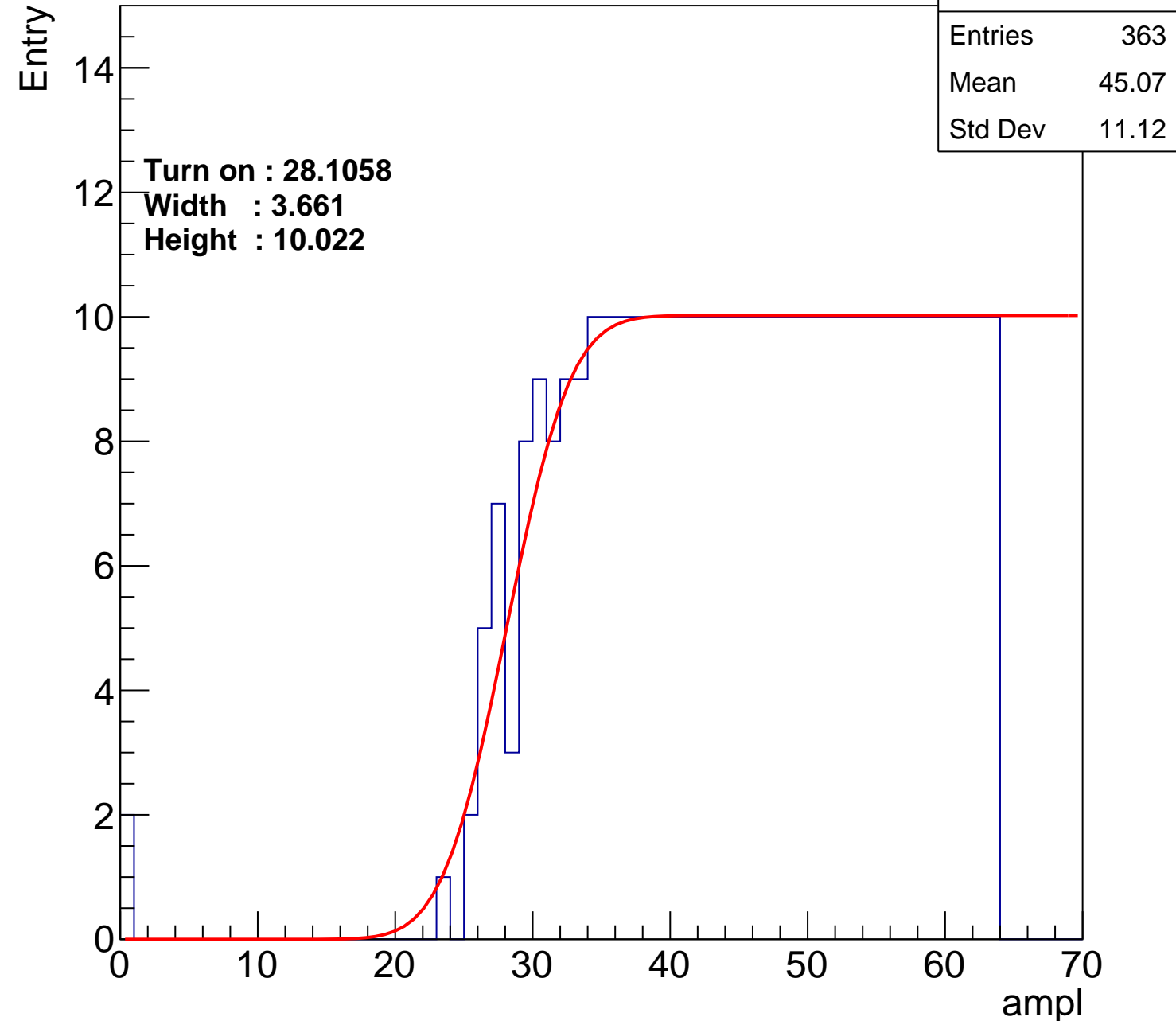
Width : 3.661

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	395
Mean	43.47
Std Dev	12.06

**Turn on : 25.3083**

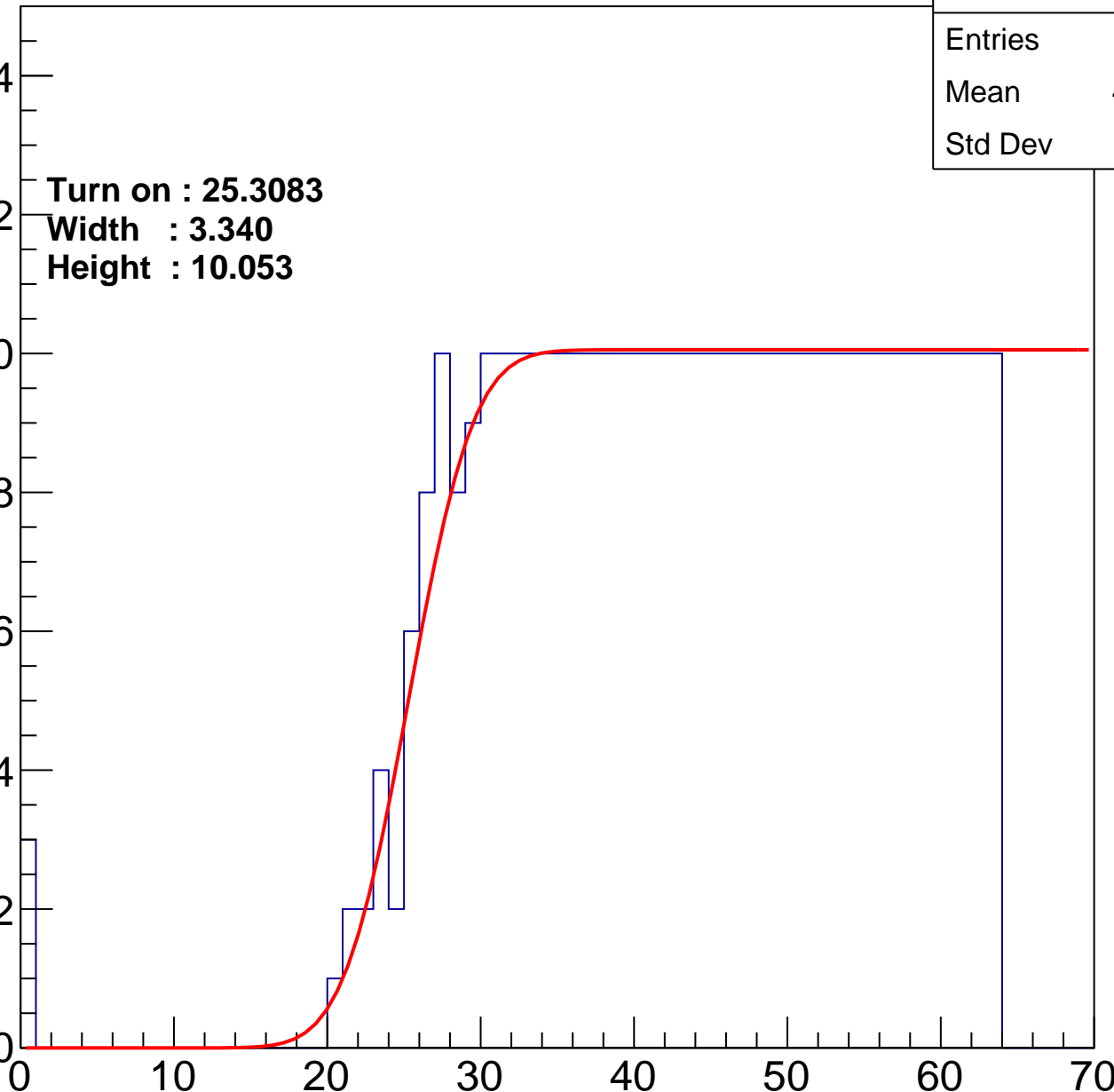
**Width : 3.340**

**Height : 10.053**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.38
Std Dev	11.44

**Turn on : 26.3798**

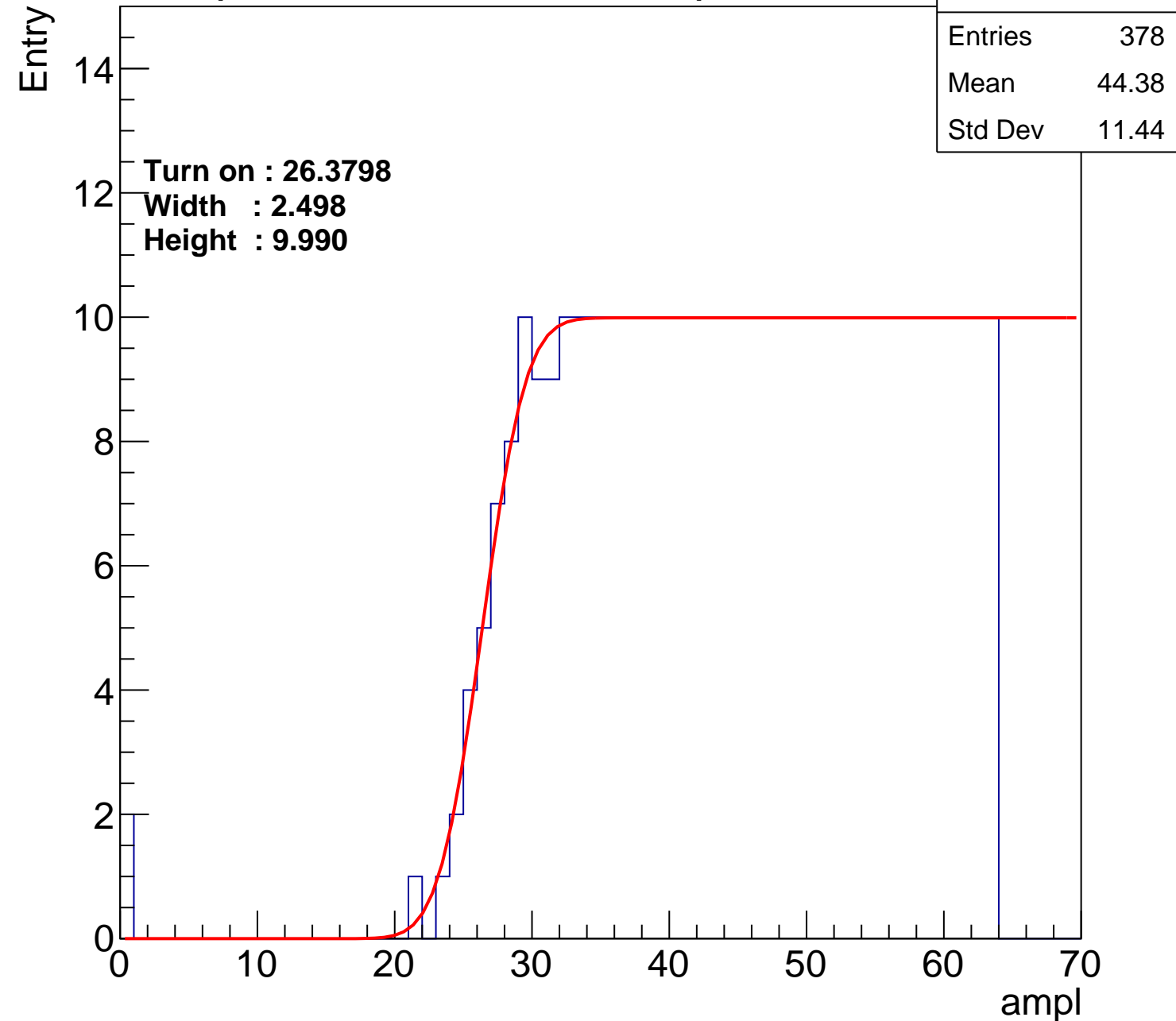
**Width : 2.498**

**Height : 9.990**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch4

calib\_packv5\_042523\_0143.root, FC#5, port B1

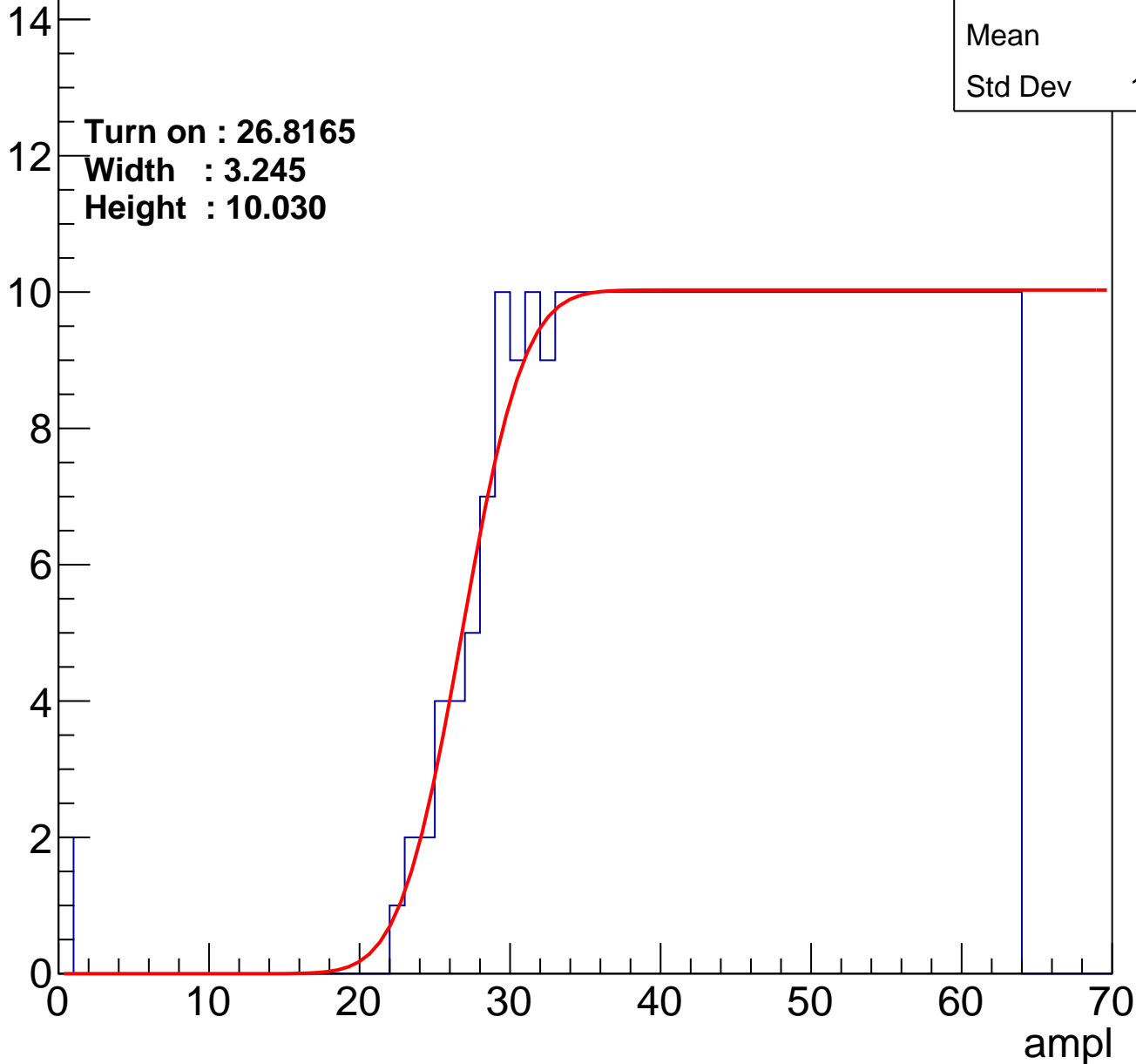
Entry

Entries	375
Mean	44.5
Std Dev	11.39

Turn on : 26.8165

Width : 3.245

Height : 10.030



# B0L000S, U14-ch5

calib\_packv5\_042523\_0143.root, FC#5, port B1

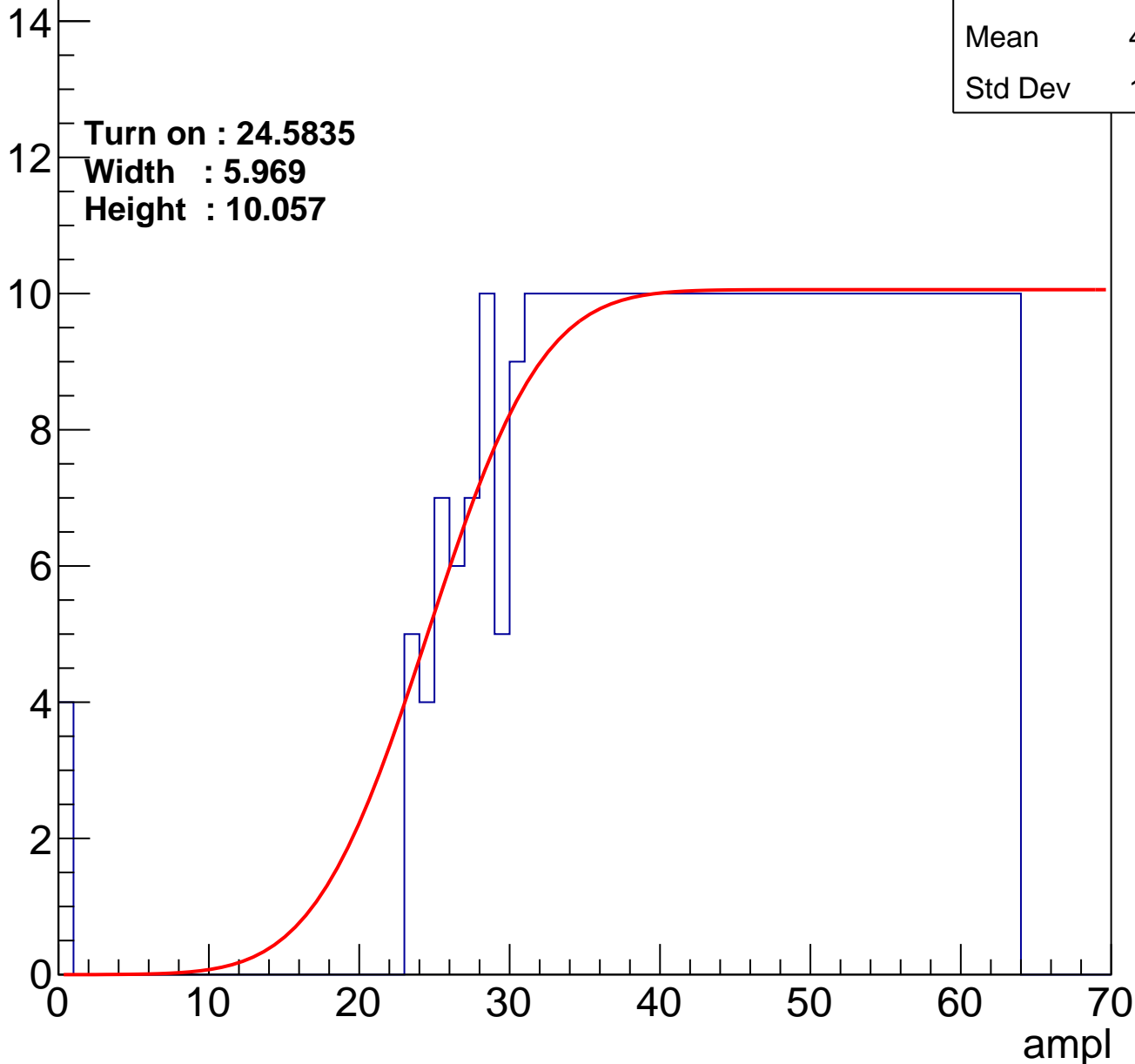
Entry

Entries	387
Mean	43.76
Std Dev	12.07

Turn on : 24.5835

Width : 5.969

Height : 10.057



# B0L000S, U14-ch6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 28.0511

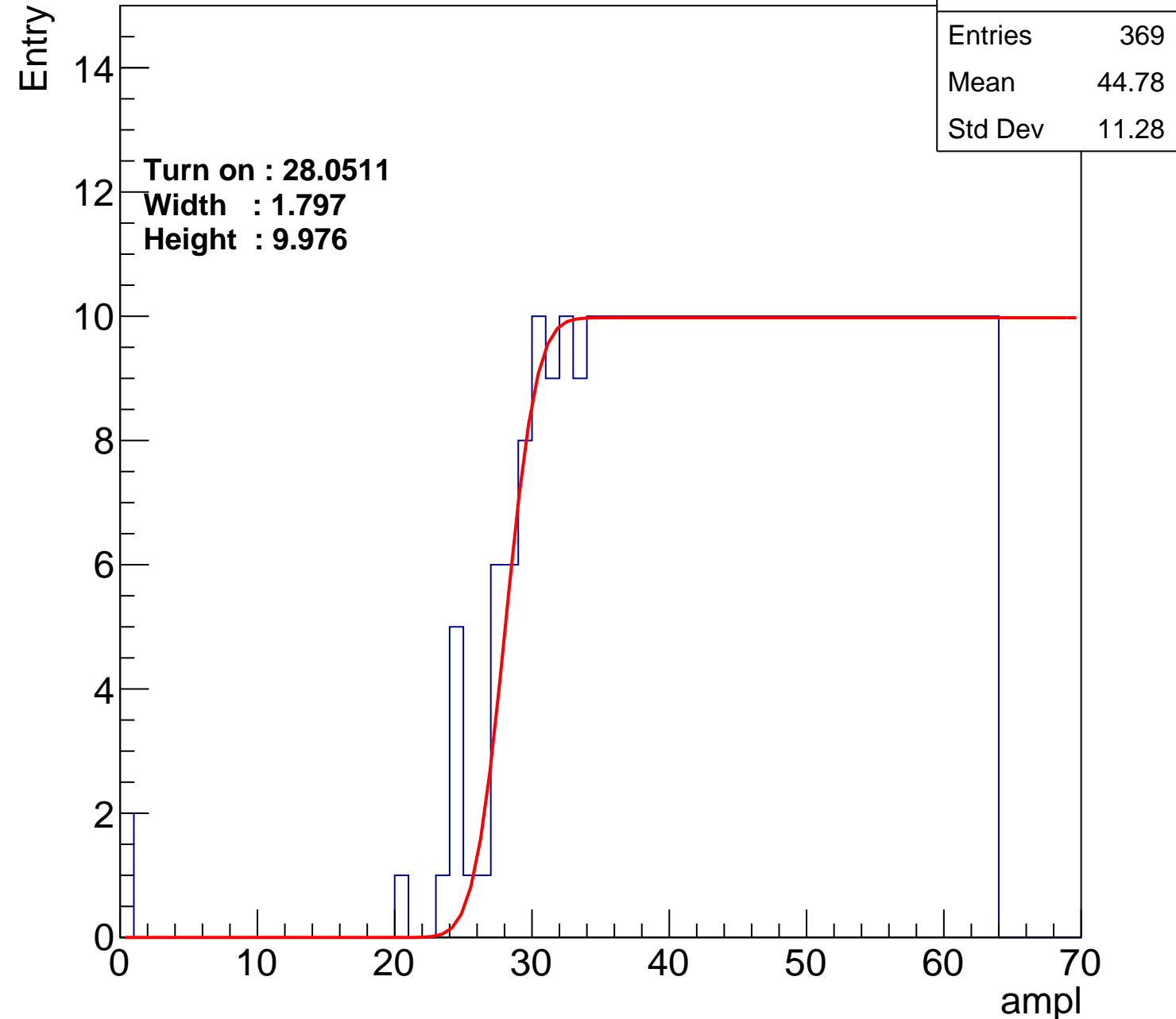
Width : 1.797

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	45.13
Std Dev	10.88

**Turn on : 27.5725**

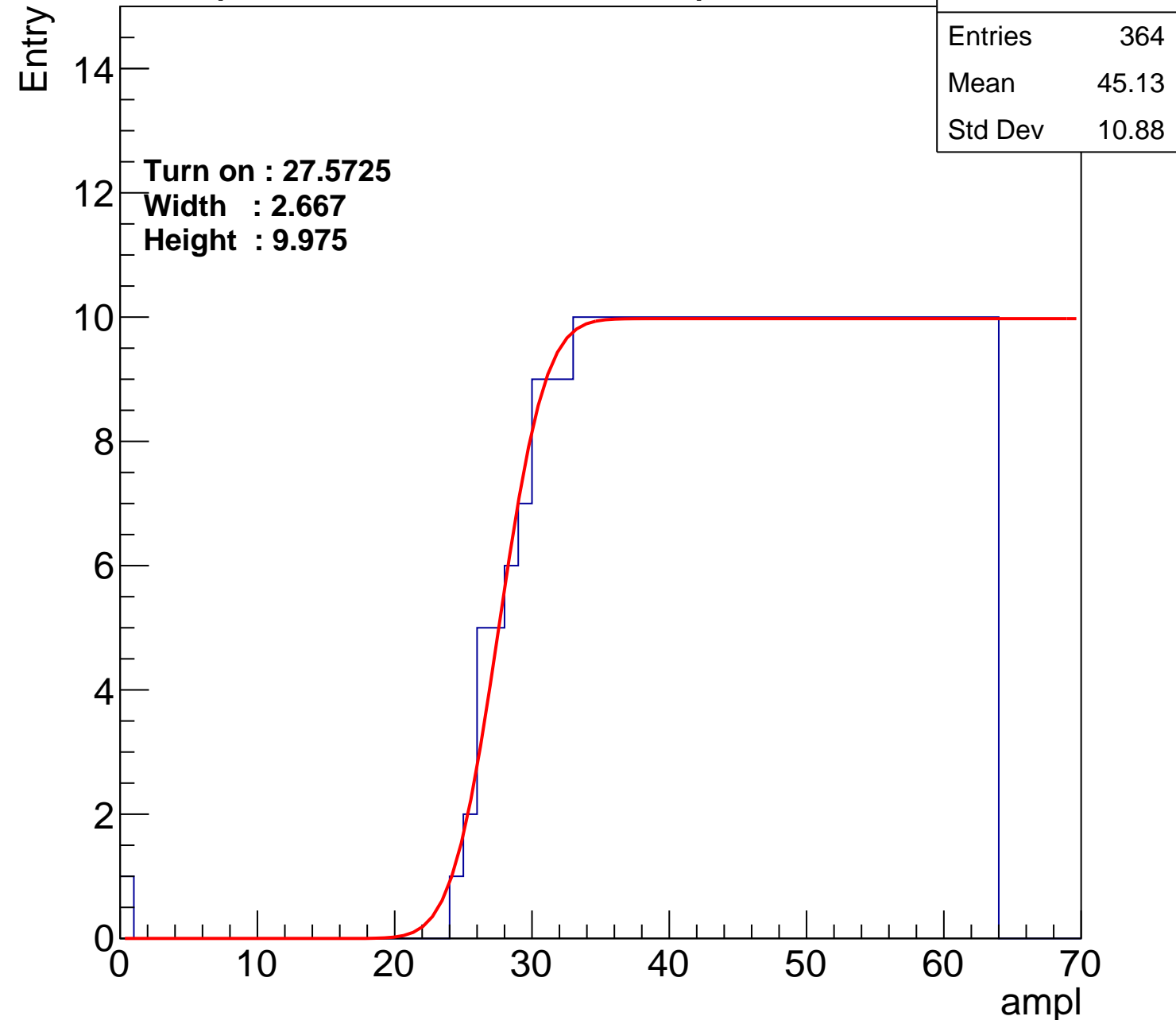
**Width : 2.667**

**Height : 9.975**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch8

calib\_packv5\_042523\_0143.root, FC#5, port B1

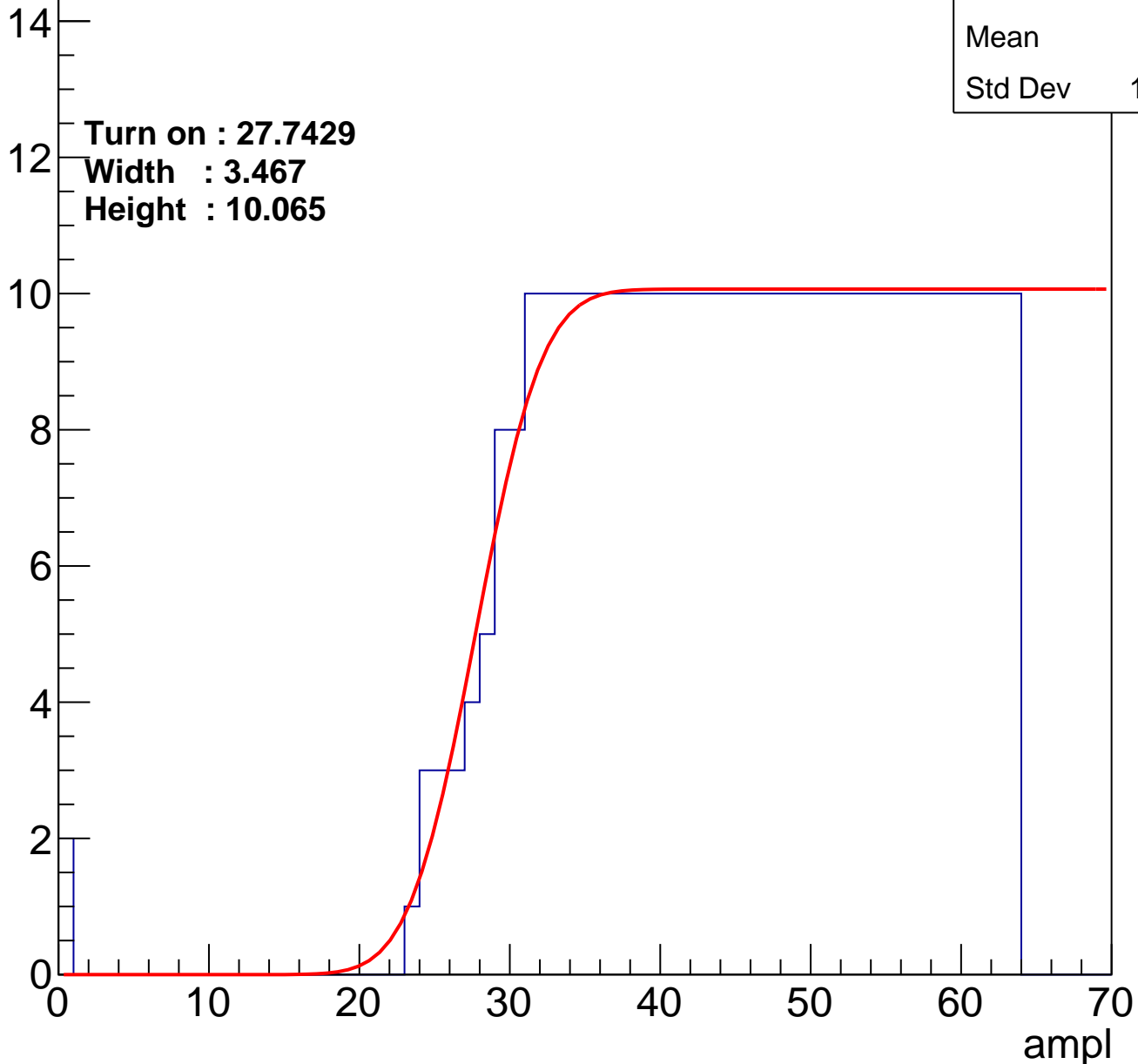
Entries	367
Mean	44.9
Std Dev	11.19

**Turn on : 27.7429**

**Width : 3.467**

**Height : 10.065**

Entry



# B0L000S, U14-ch9

calib\_packv5\_042523\_0143.root, FC#5, port B1

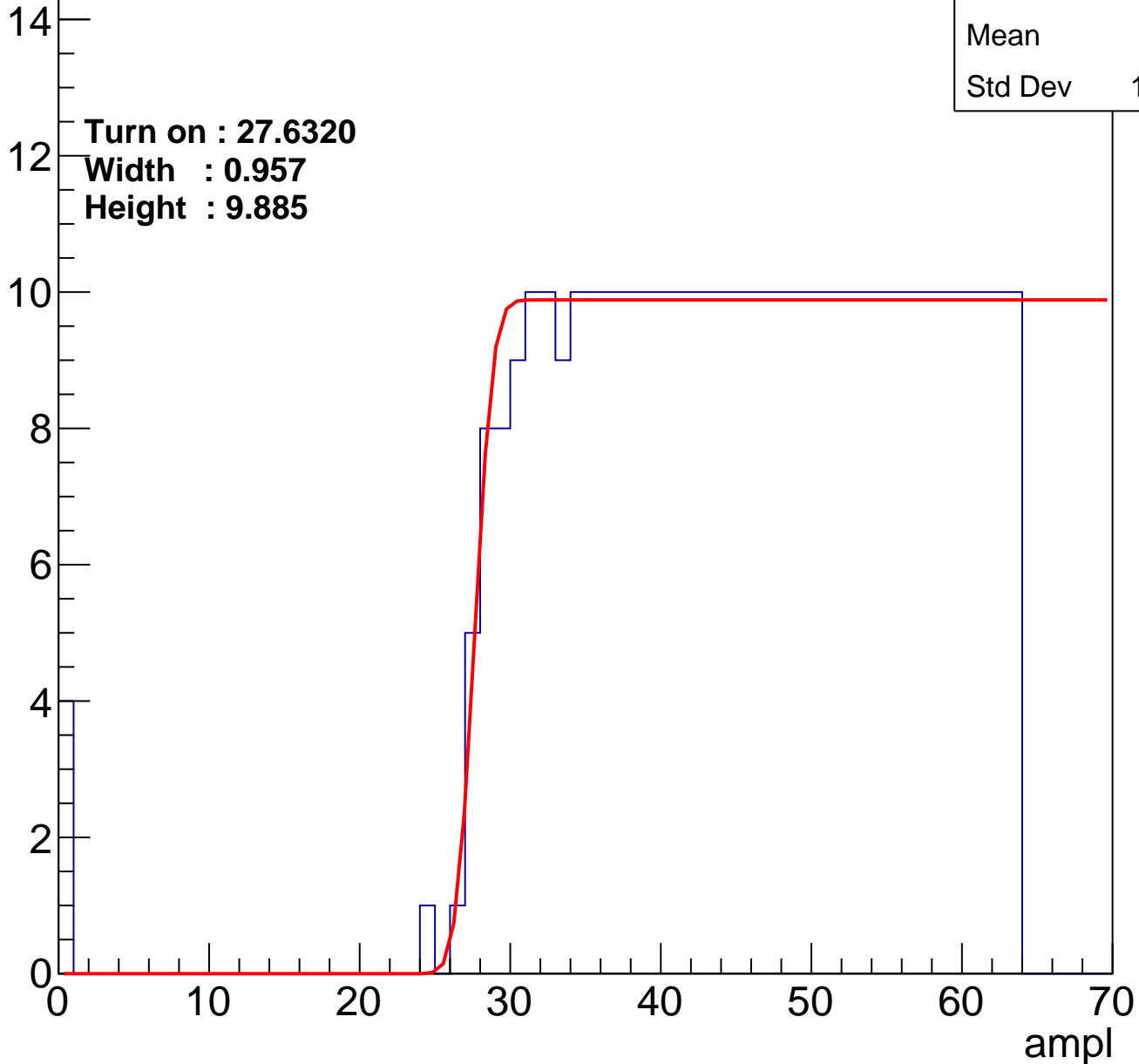
Entry

Entries	365
Mean	44.9
Std Dev	11.47

Turn on : 27.6320

Width : 0.957

Height : 9.885



# B0L000S, U14-ch10

calib\_packv5\_042523\_0143.root, FC#5, port B1

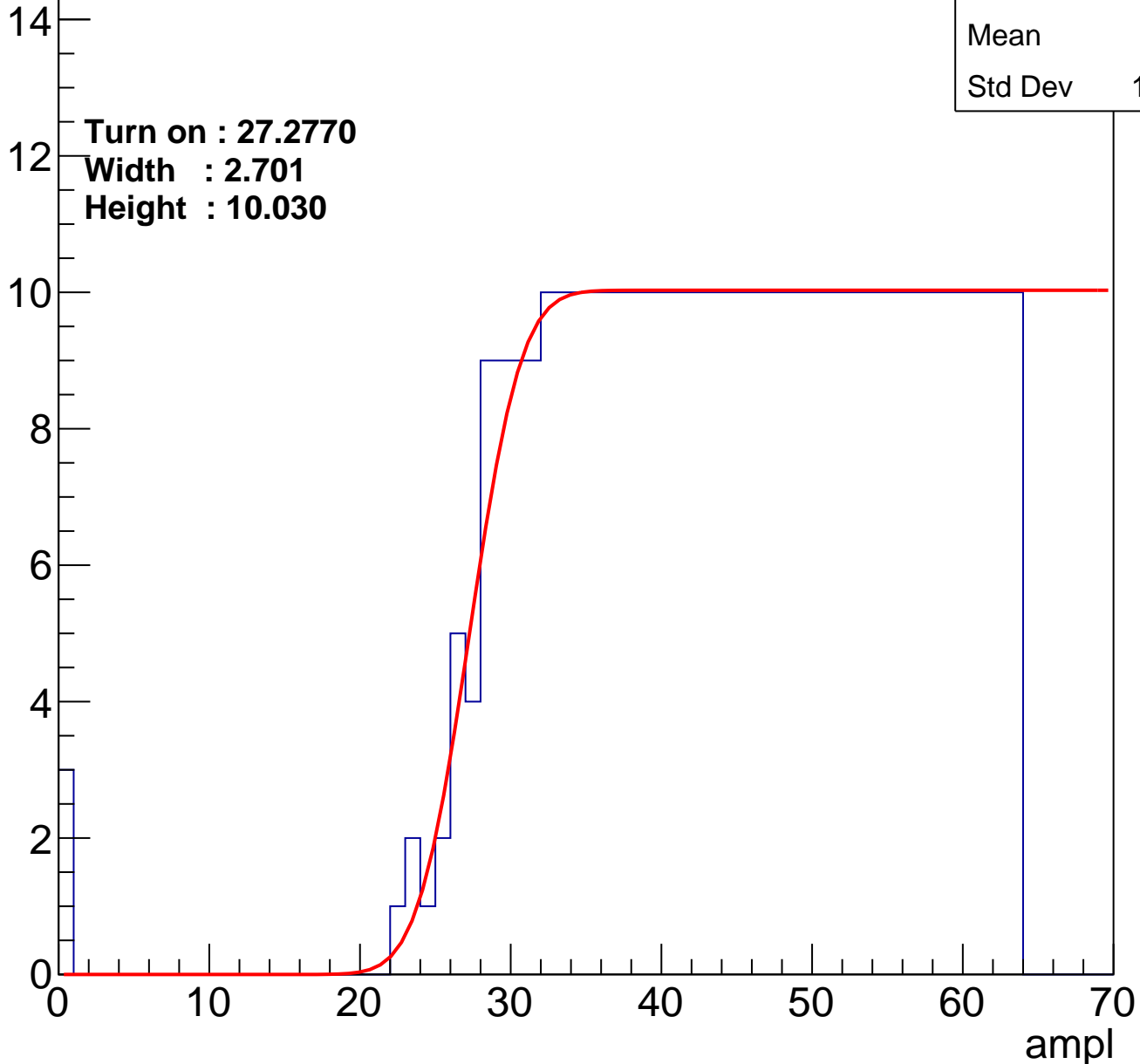
Entries	374
Mean	44.5
Std Dev	11.54

**Turn on : 27.2770**

**Width : 2.701**

**Height : 10.030**

Entry



# B0L000S, U14-ch11

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 25.8965

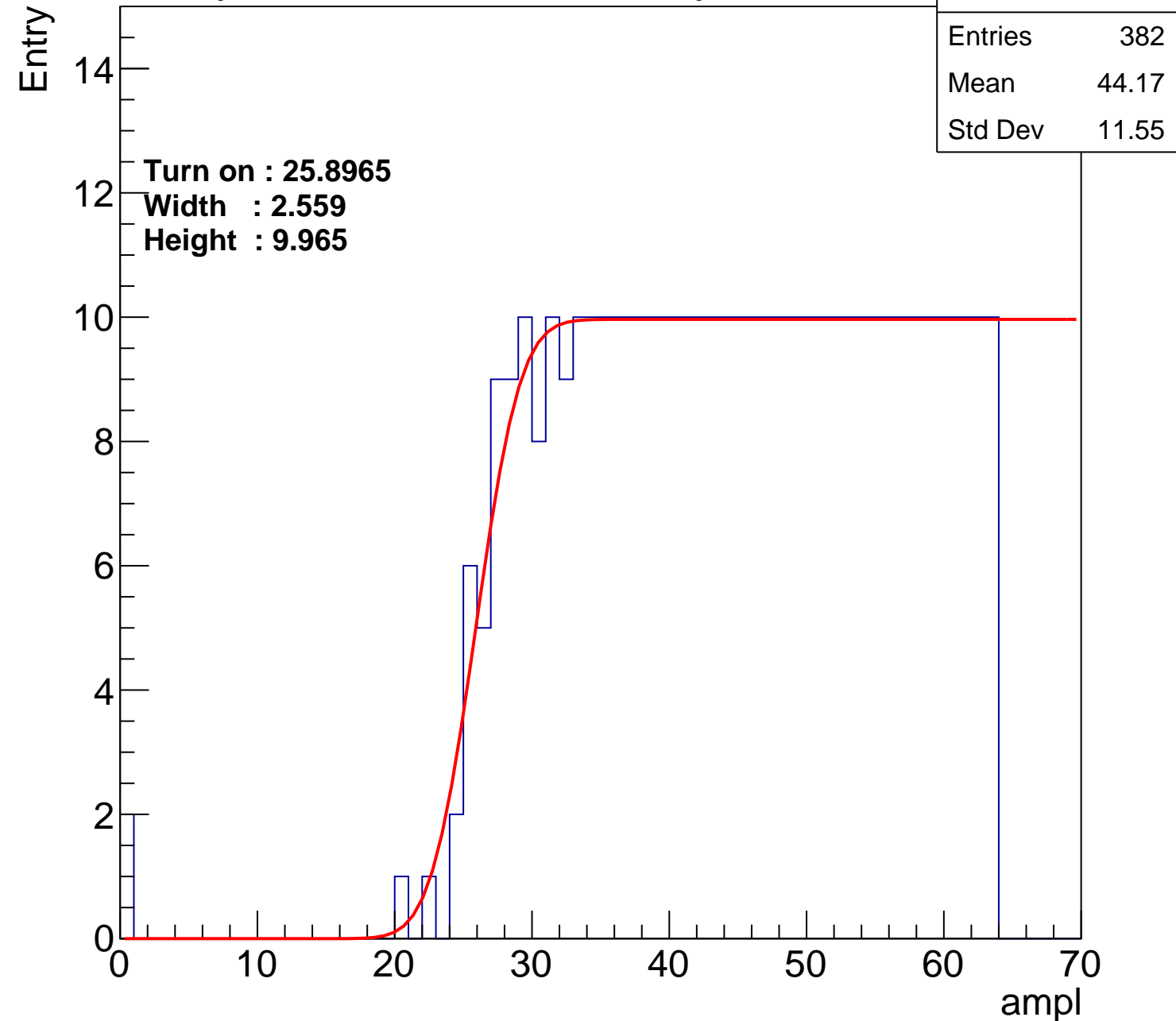
Width : 2.559

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch12

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.4
Std Dev	11.89

**Turn on : 26.9846**

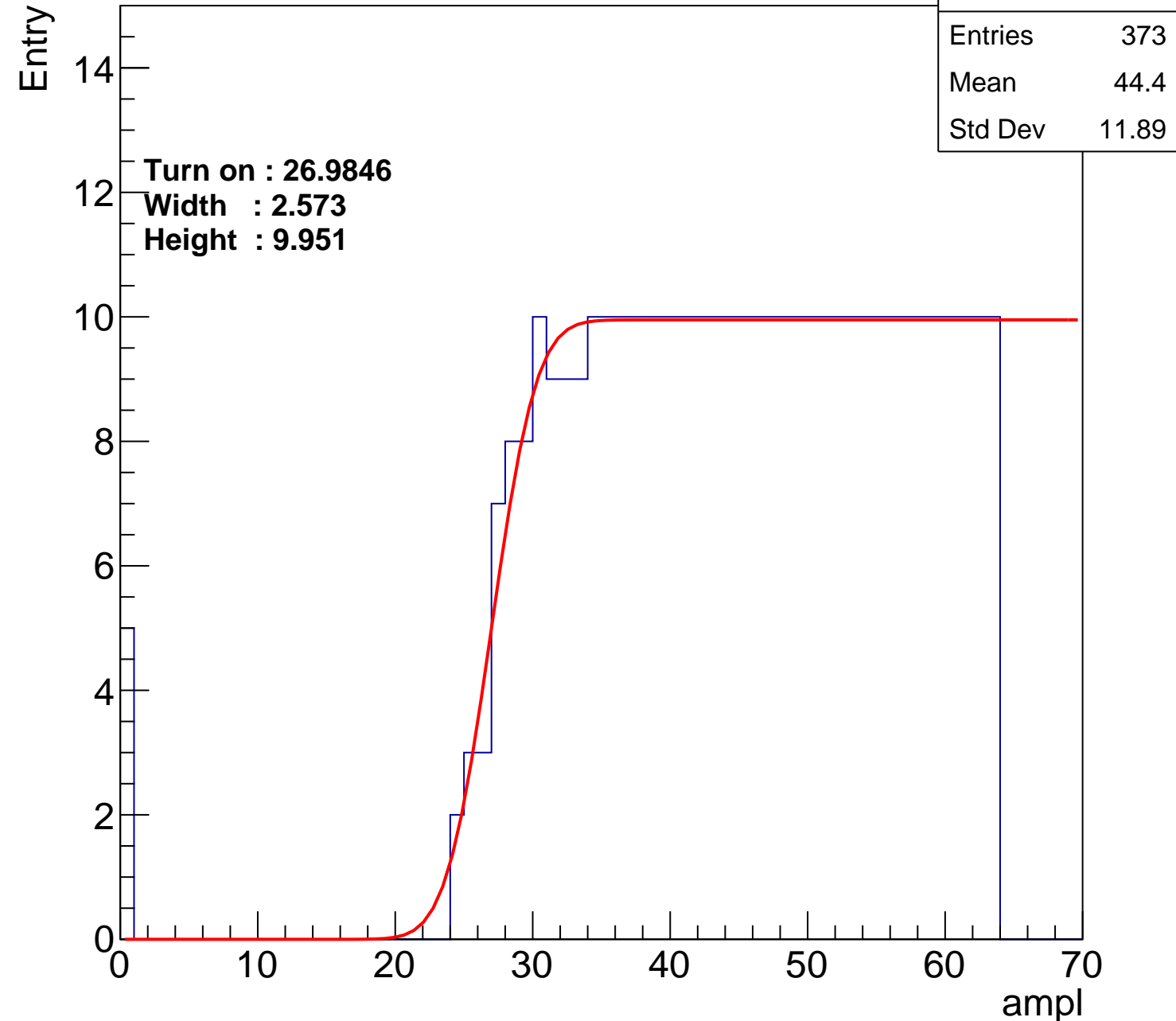
**Width : 2.573**

**Height : 9.951**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch13

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 25.8617

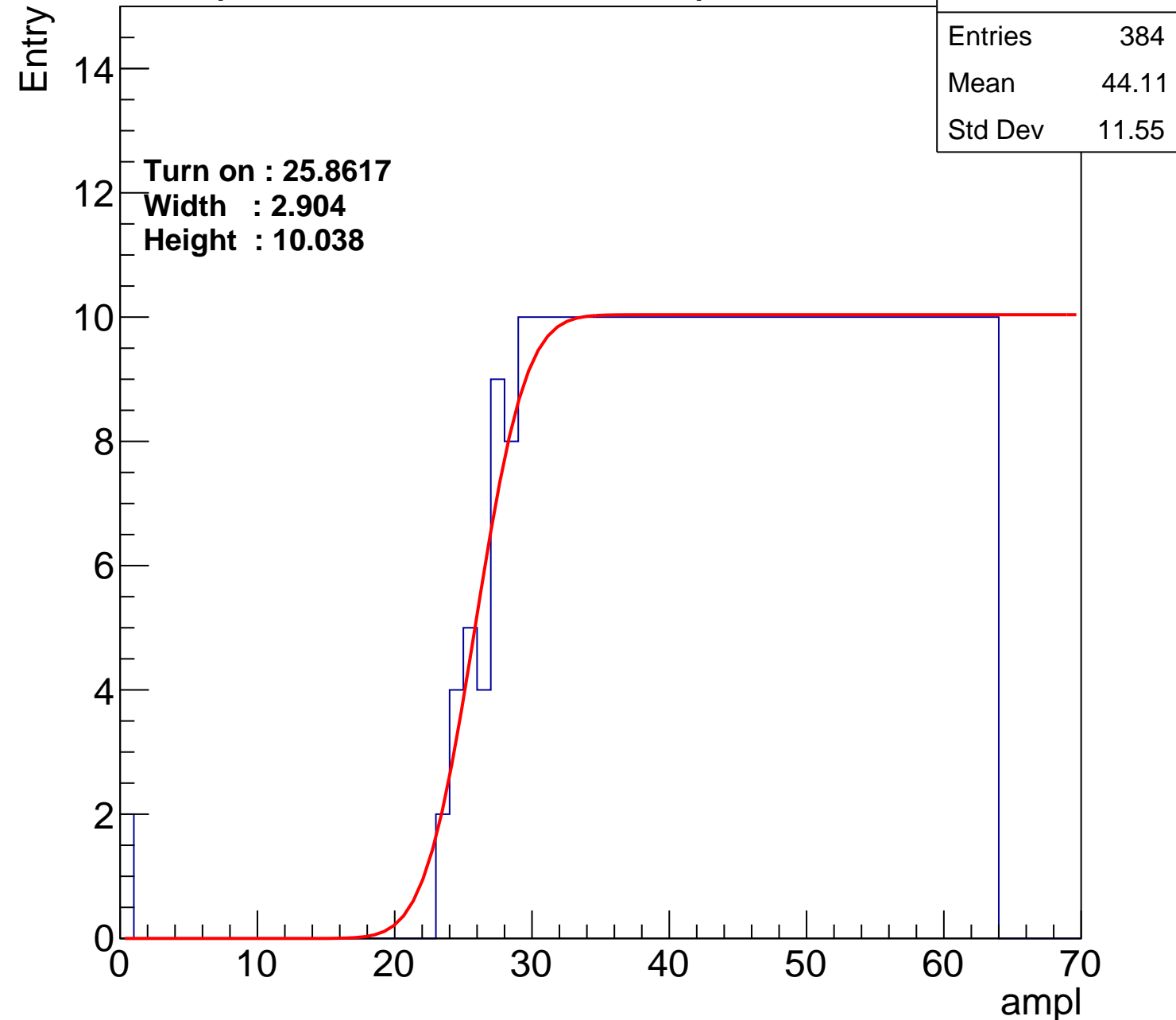
Width : 2.904

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch14

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.9

Turn on : 25.6854

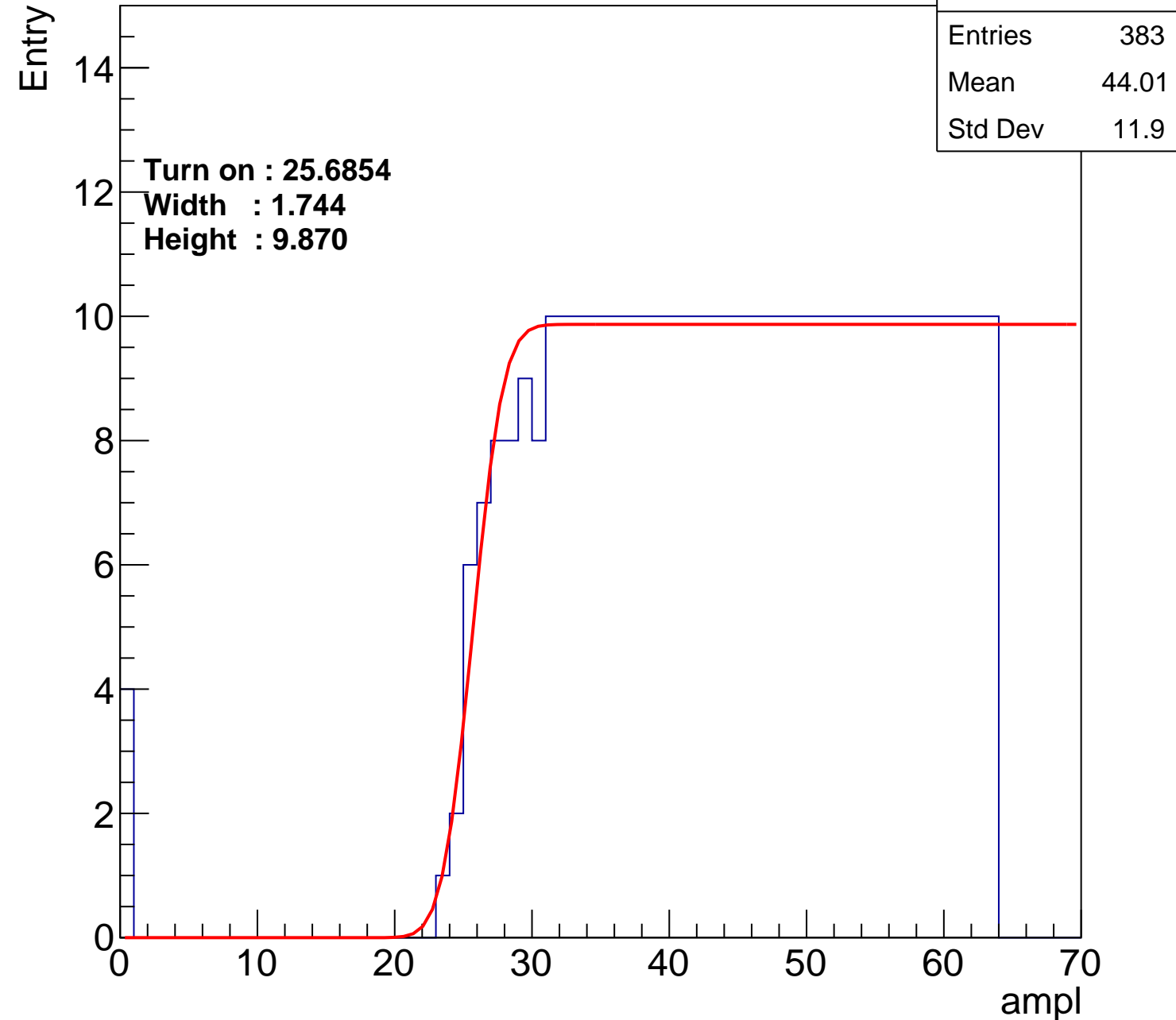
Width : 1.744

Height : 9.870

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch15

calib\_packv5\_042523\_0143.root, FC#5, port B1

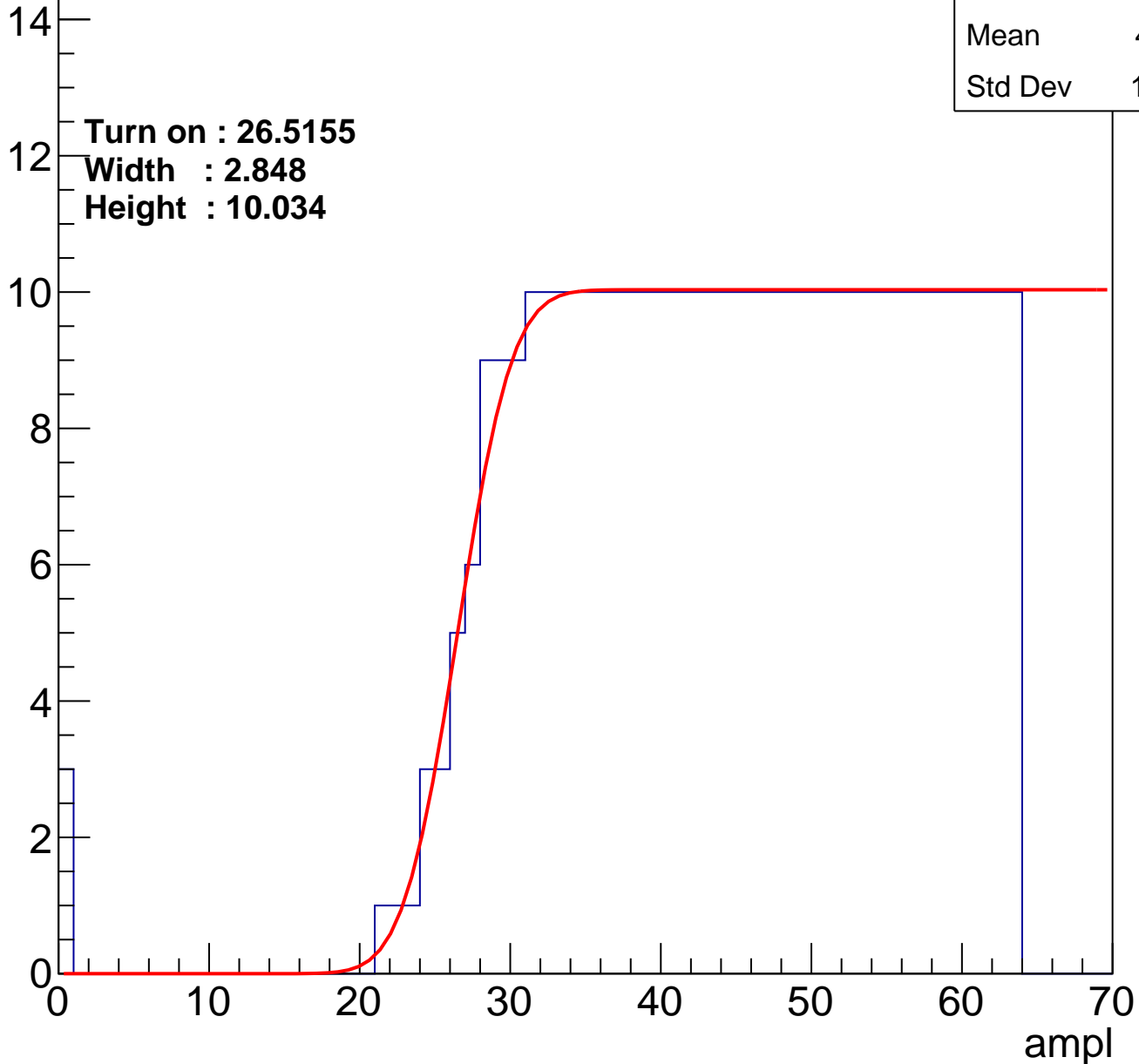
Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.5155

Width : 2.848

Height : 10.034

Entry



# B0L000S, U14-ch16

calib\_packv5\_042523\_0143.root, FC#5, port B1

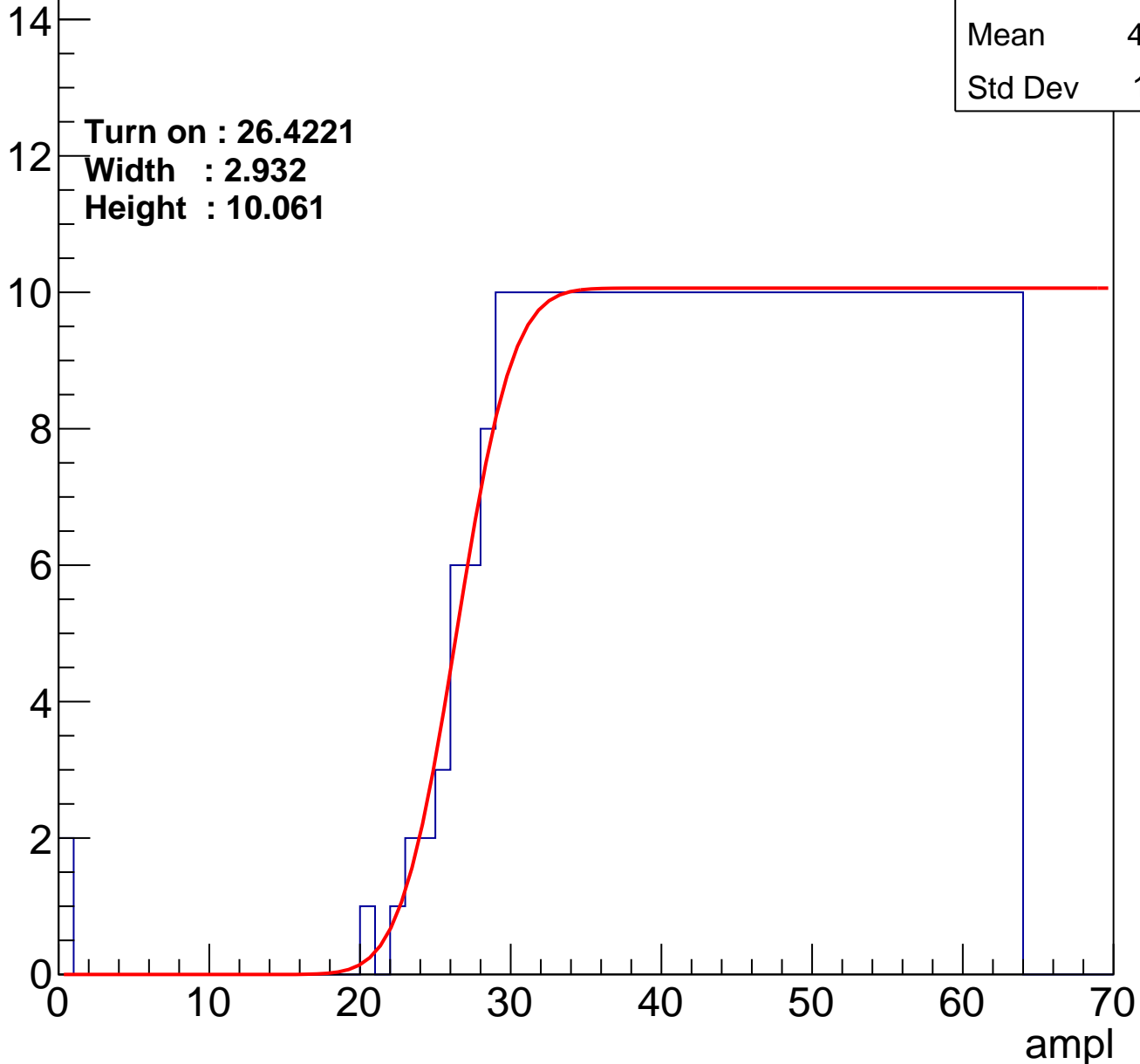
Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.4221

Width : 2.932

Height : 10.061

Entry



# B0L000S, U14-ch17

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.49
Std Dev	11.24

**Turn on : 26.6675**

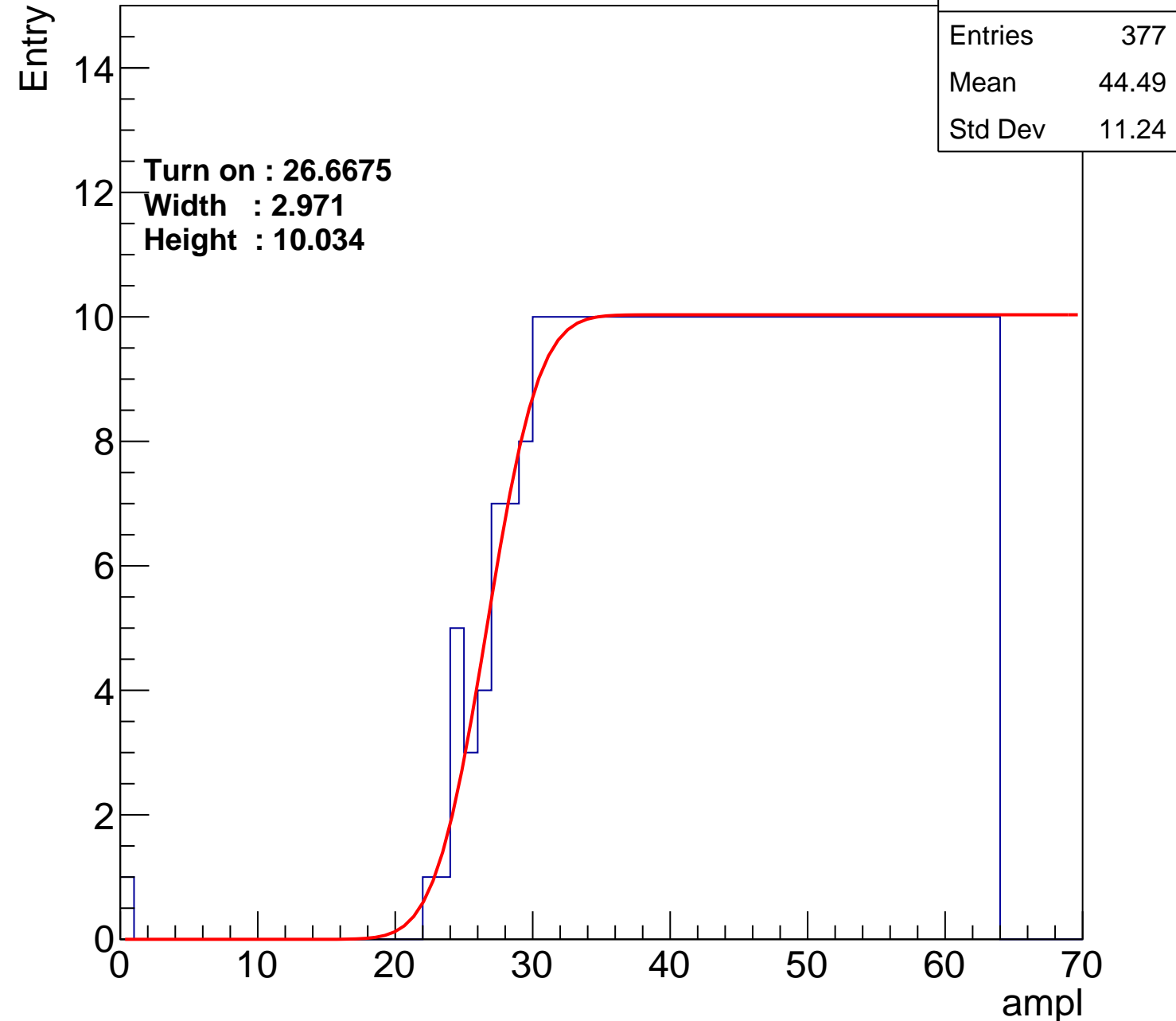
**Width : 2.971**

**Height : 10.034**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch18

calib\_packv5\_042523\_0143.root, FC#5, port B1

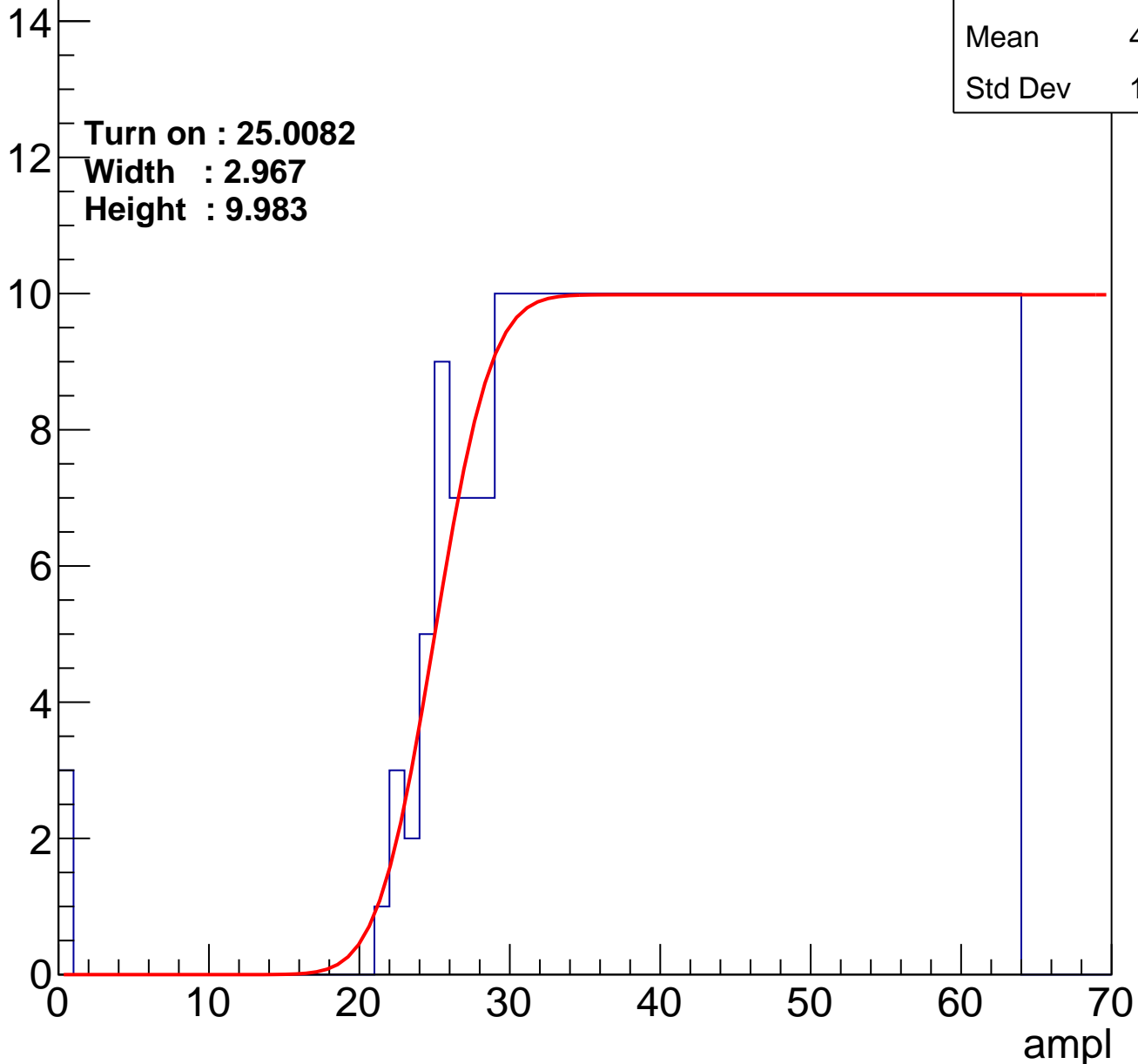
Entries	394
Mean	43.52
Std Dev	12.03

Turn on : 25.0082

Width : 2.967

Height : 9.983

Entry



# B0L000S, U14-ch19

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.52
Std Dev	11.49

Turn on : 26.7076

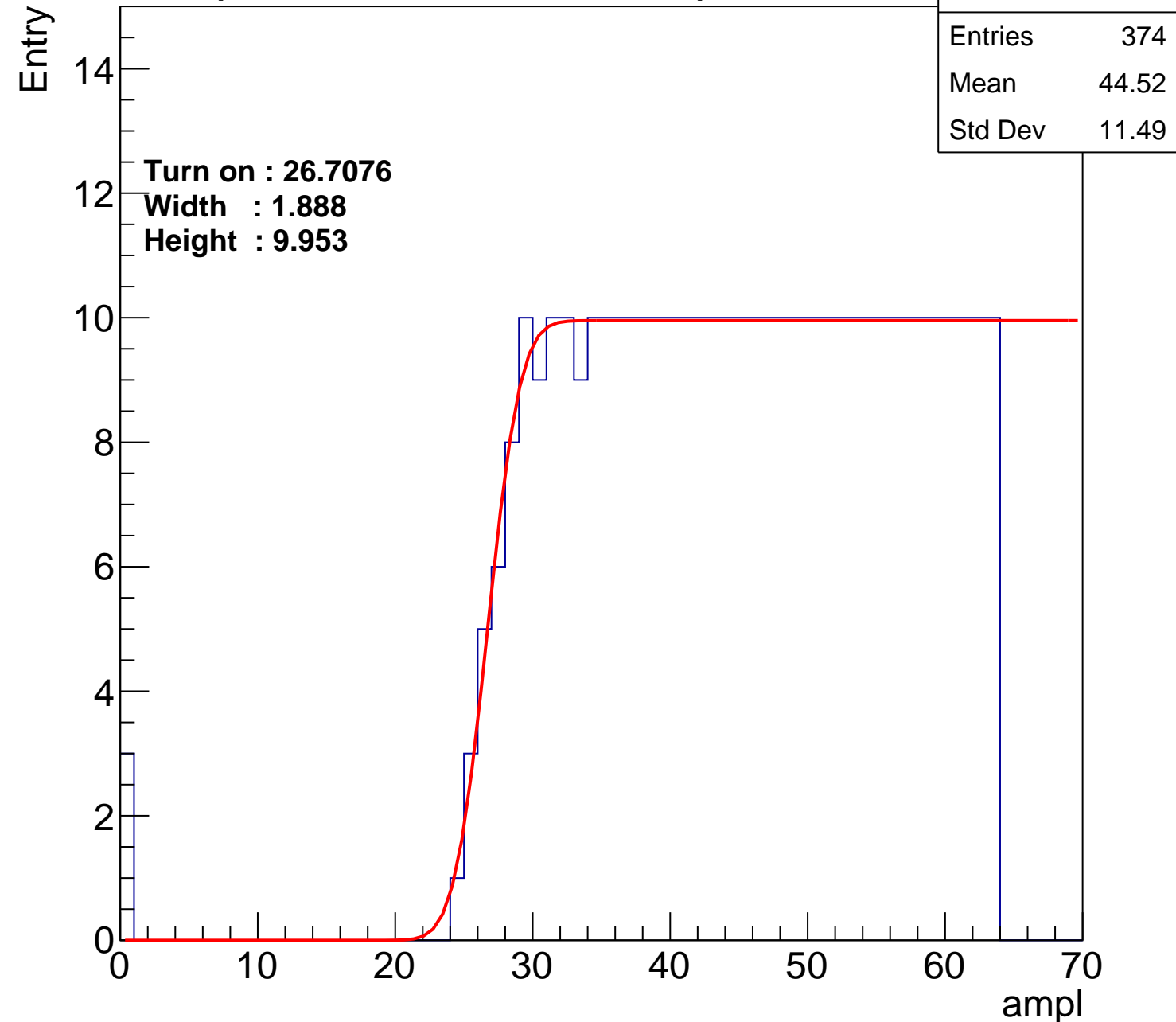
Width : 1.888

Height : 9.953

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch20

calib\_packv5\_042523\_0143.root, FC#5, port B1

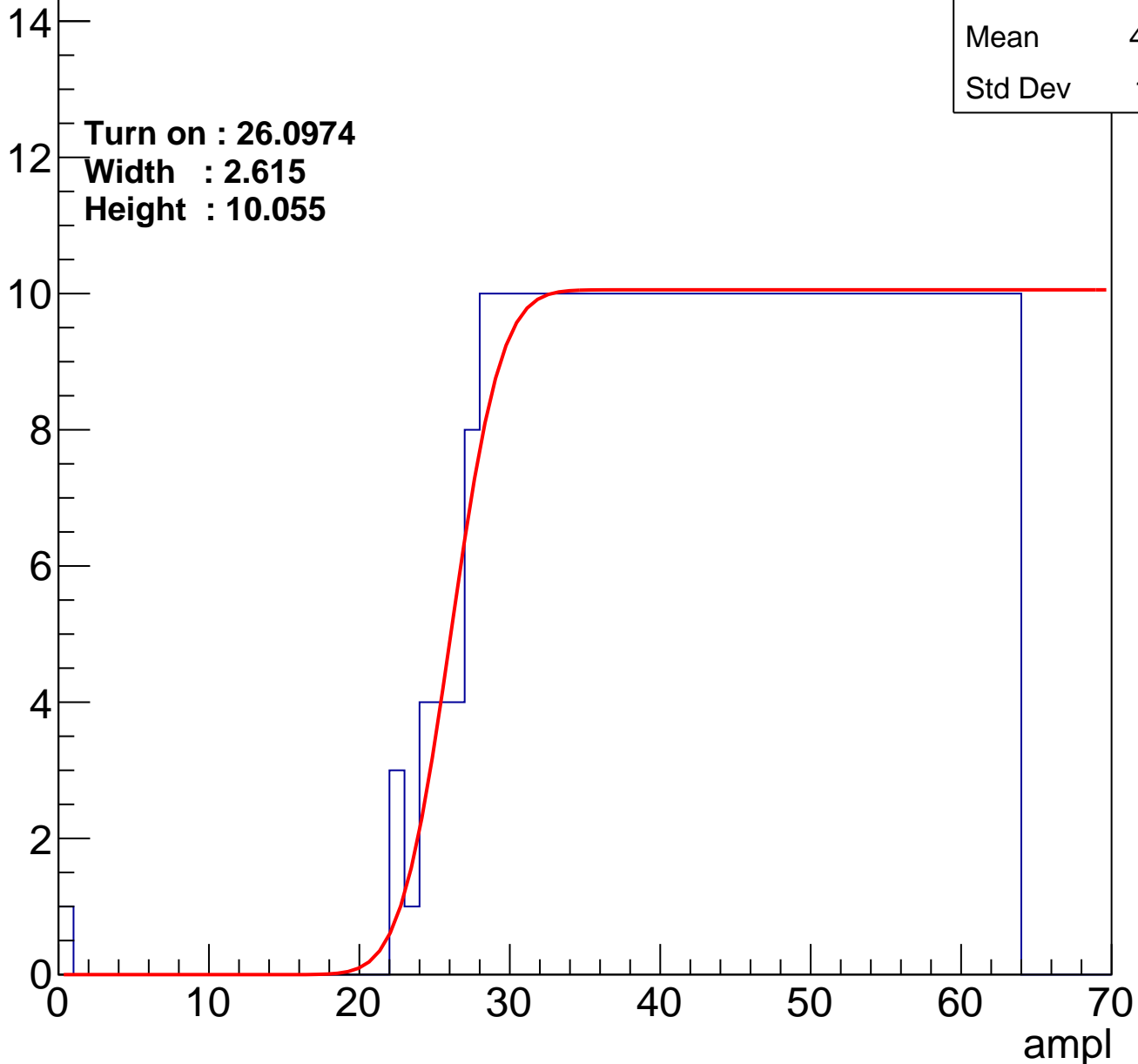
Entries	385
Mean	44.12
Std Dev	11.41

Turn on : 26.0974

Width : 2.615

Height : 10.055

Entry



# B0L000S, U14-ch21

calib\_packv5\_042523\_0143.root, FC#5, port B1

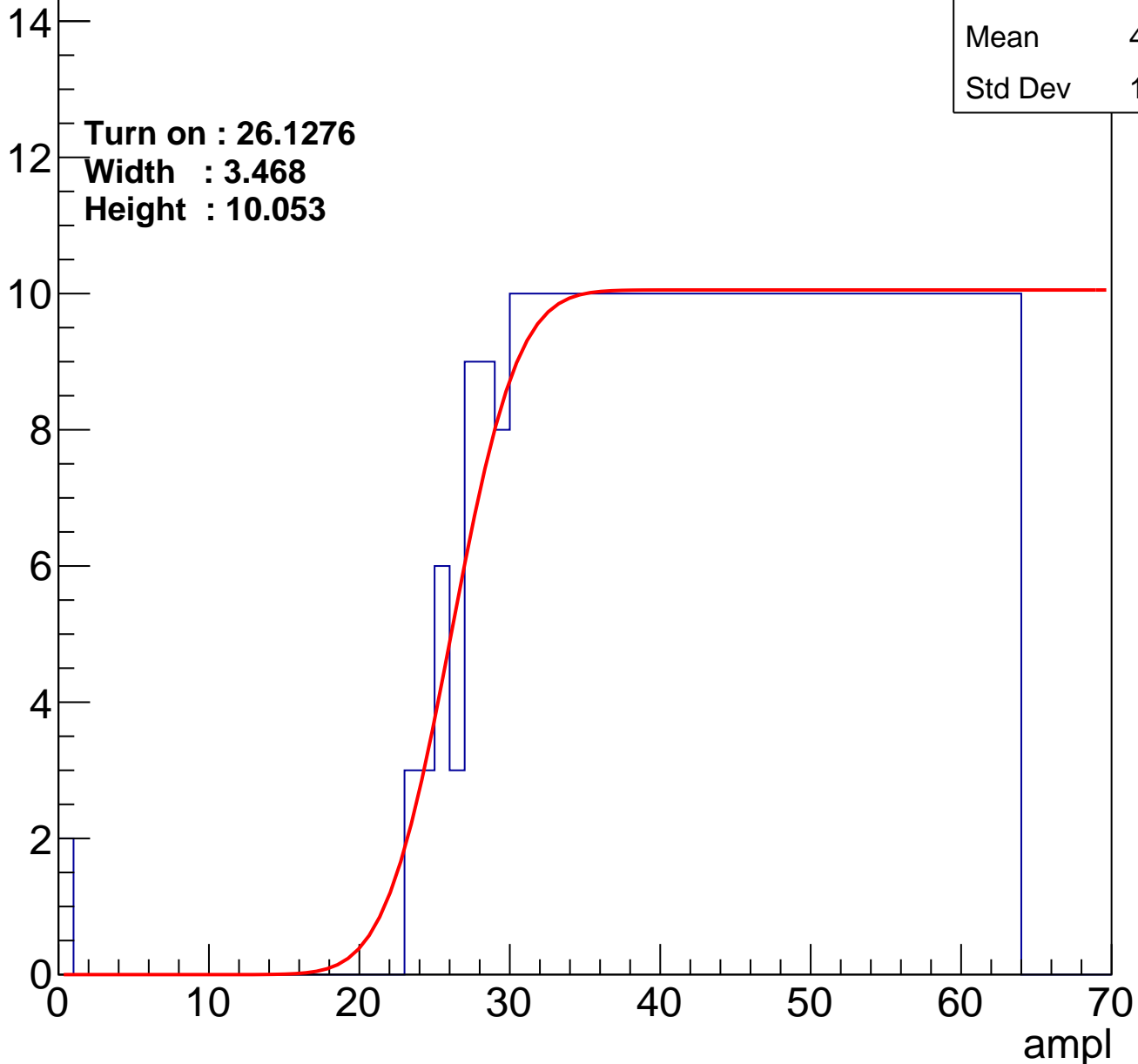
Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.1276

Width : 3.468

Height : 10.053

Entry



# B0L000S, U14-ch22

calib\_packv5\_042523\_0143.root, FC#5, port B1

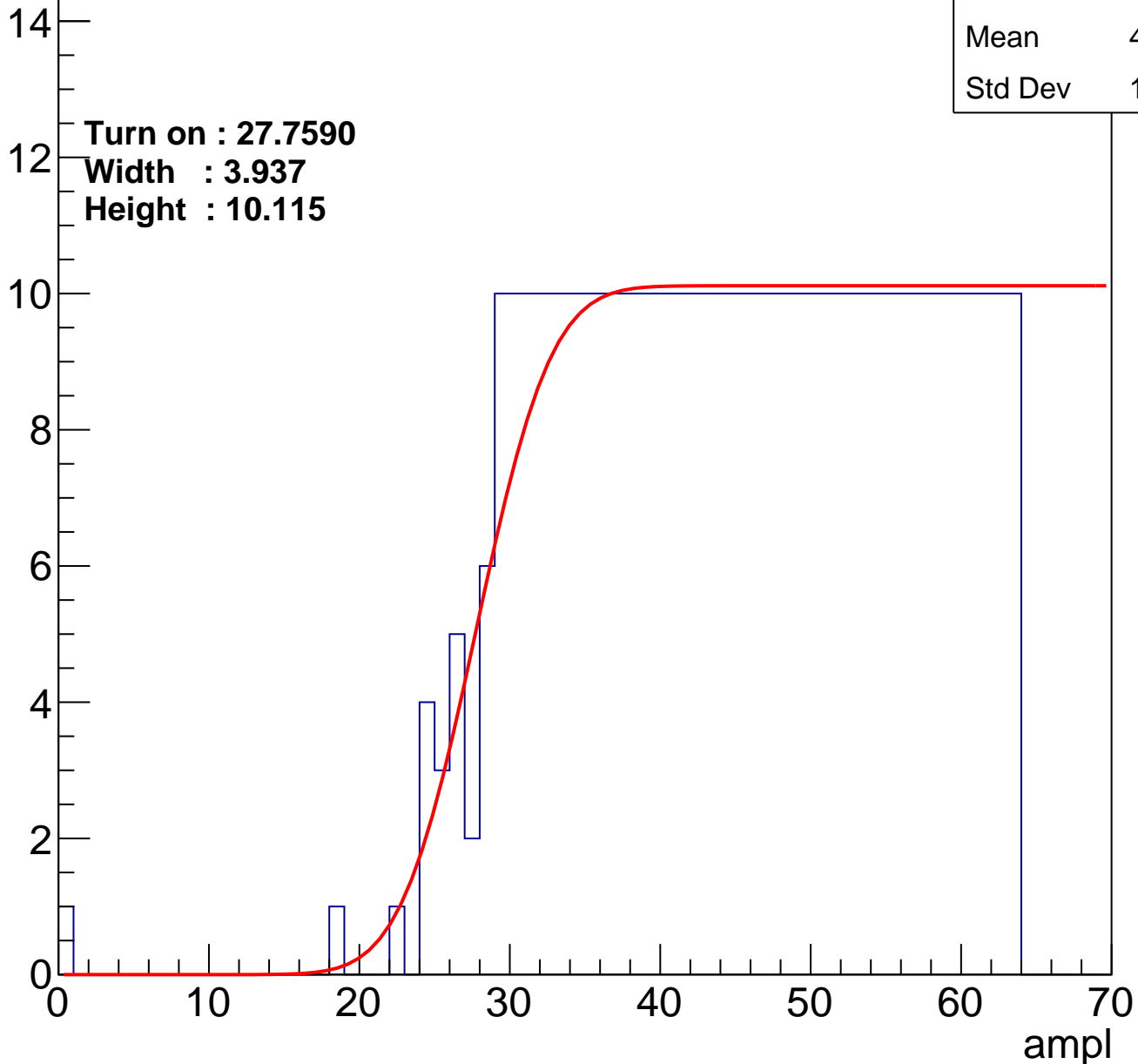
Entries	373
Mean	44.67
Std Dev	11.16

**Turn on : 27.7590**

**Width : 3.937**

**Height : 10.115**

Entry





# B0L000S, U14-ch23

calib\_packv5\_042523\_0143.root, FC#5, port B1

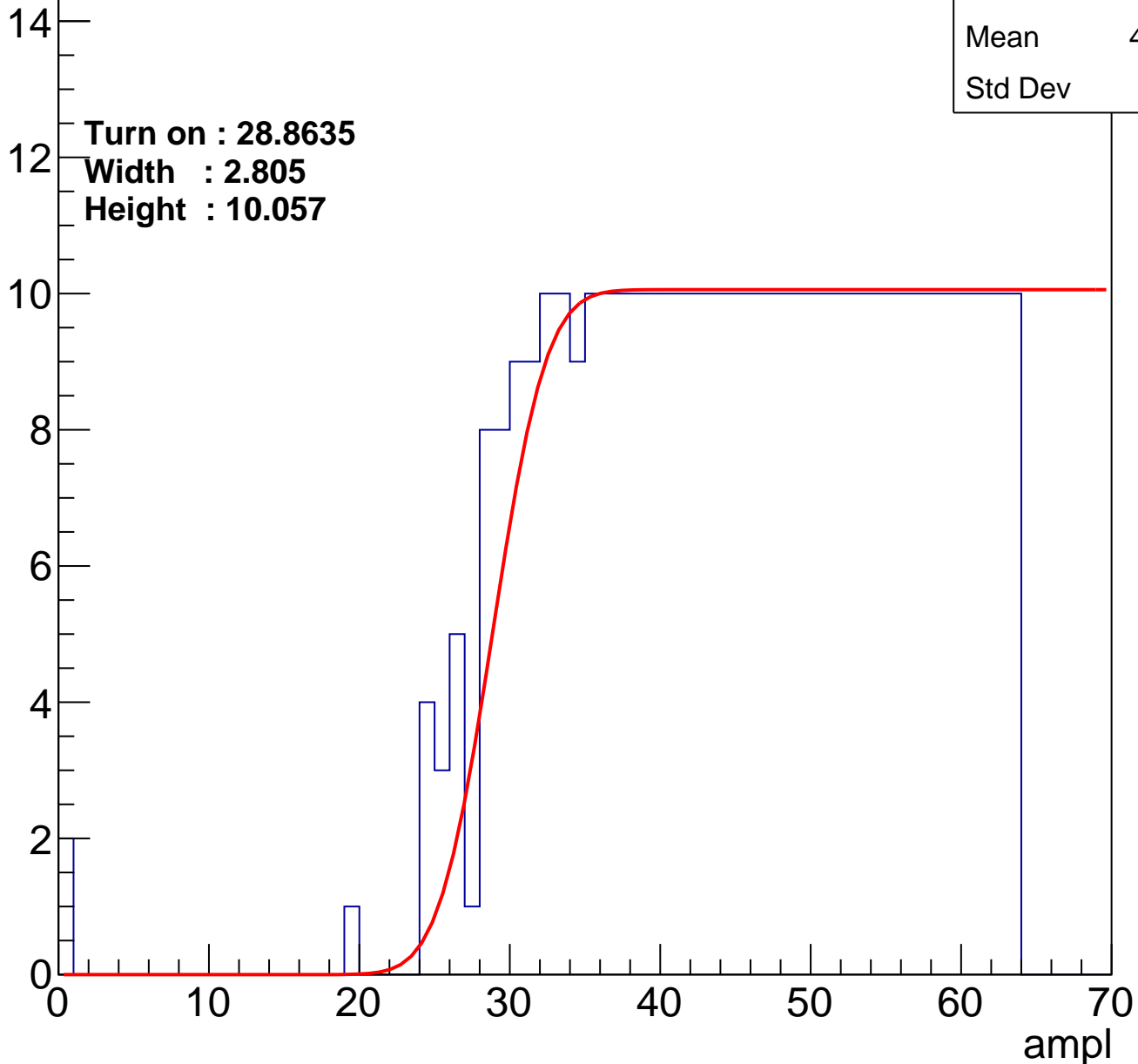
Entries	369
Mean	44.76
Std Dev	11.3

**Turn on : 28.8635**

**Width : 2.805**

**Height : 10.057**

Entry



# B0L000S, U14-ch24

calib\_packv5\_042523\_0143.root, FC#5, port B1

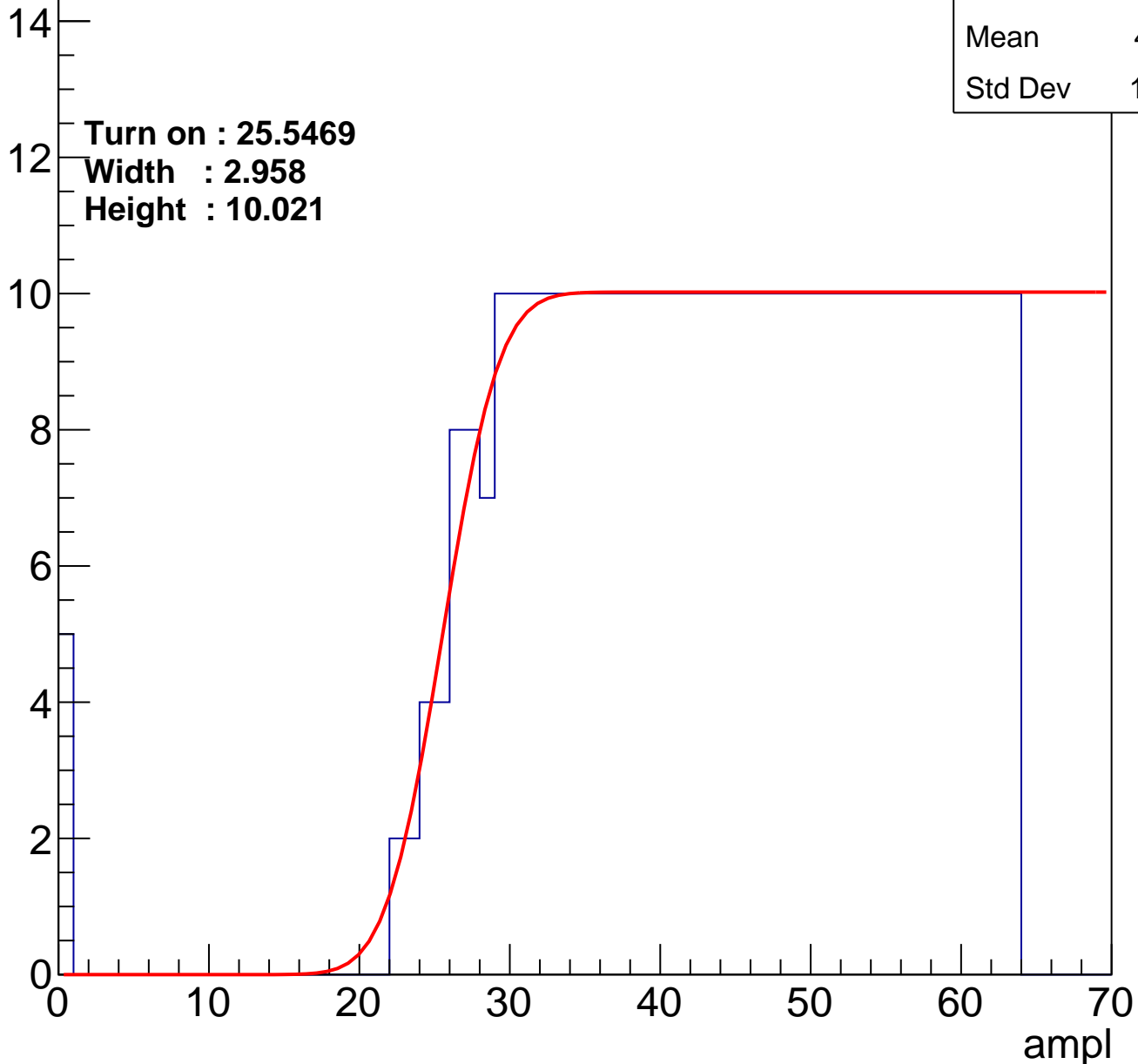
Entries	390
Mean	43.61
Std Dev	12.23

Turn on : 25.5469

Width : 2.958

Height : 10.021

Entry



# B0L000S, U14-ch25

calib\_packv5\_042523\_0143.root, FC#5, port B1

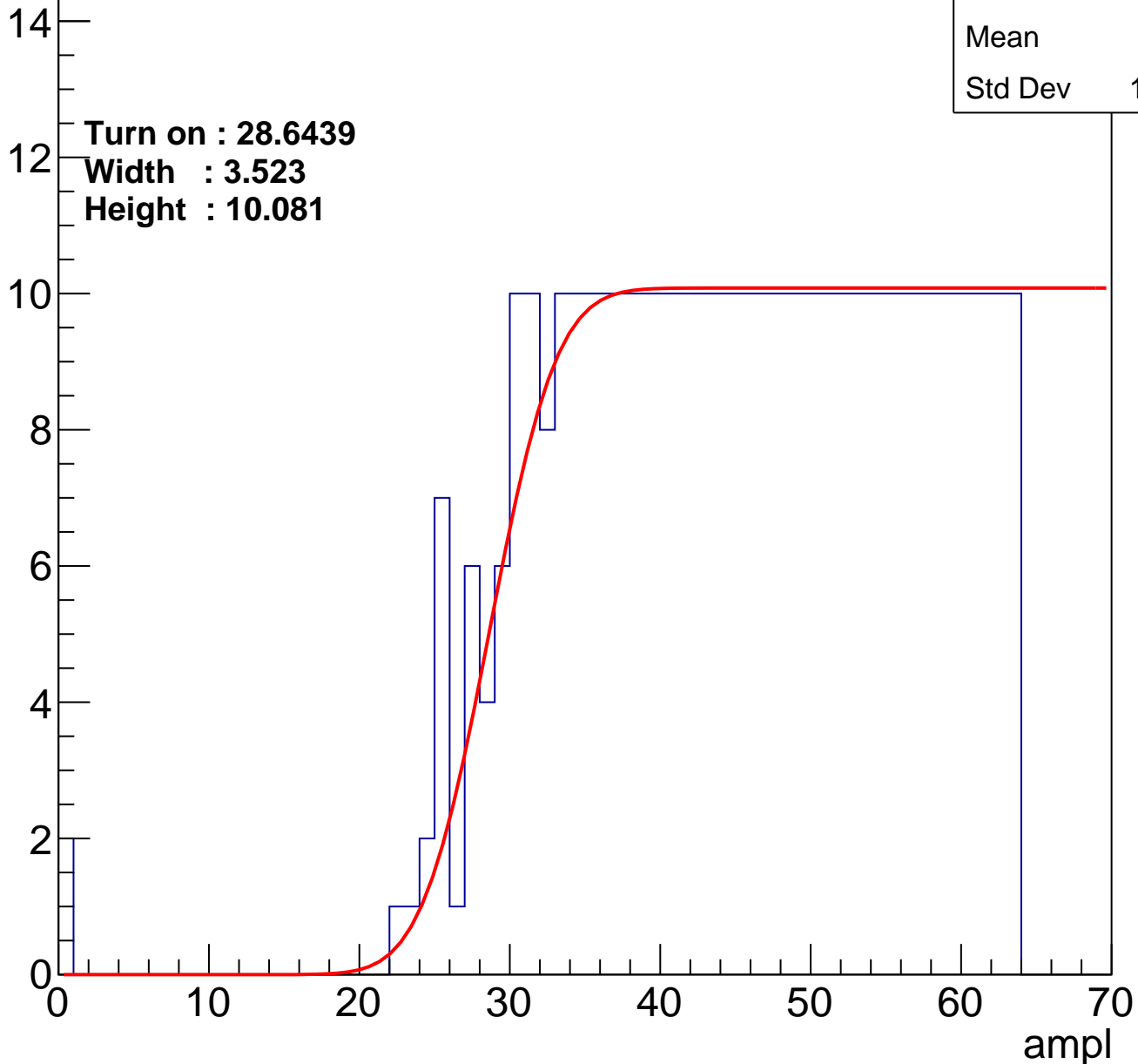
Entries	368
Mean	44.8
Std Dev	11.29

Turn on : 28.6439

Width : 3.523

Height : 10.081

Entry



# B0L000S, U14-ch26

calib\_packv5\_042523\_0143.root, FC#5, port B1

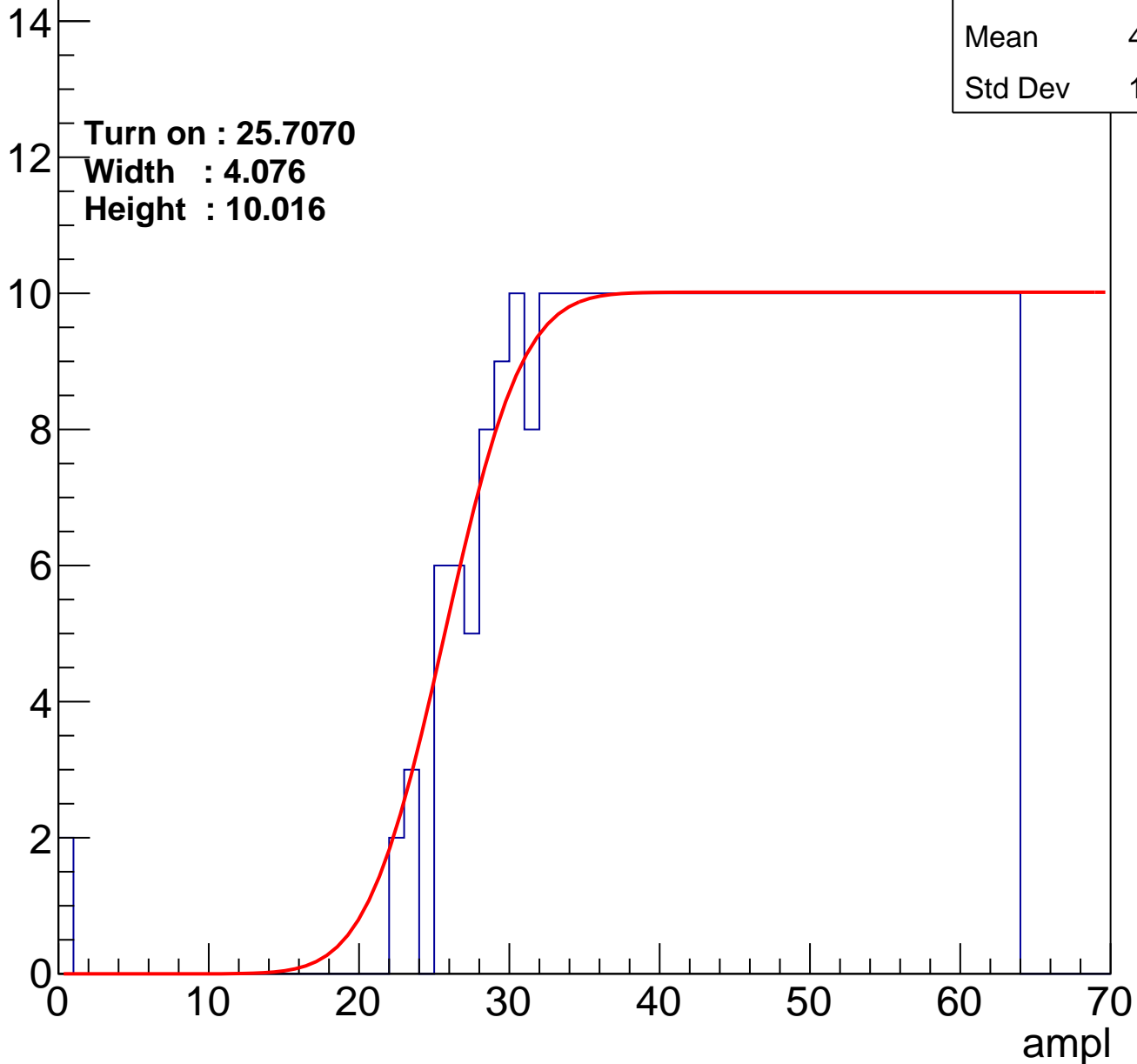
Entries	379
Mean	44.29
Std Dev	11.52

**Turn on : 25.7070**

**Width : 4.076**

**Height : 10.016**

Entry



# B0L000S, U14-ch27

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.35
Std Dev	11.79

Turn on : 27.1042

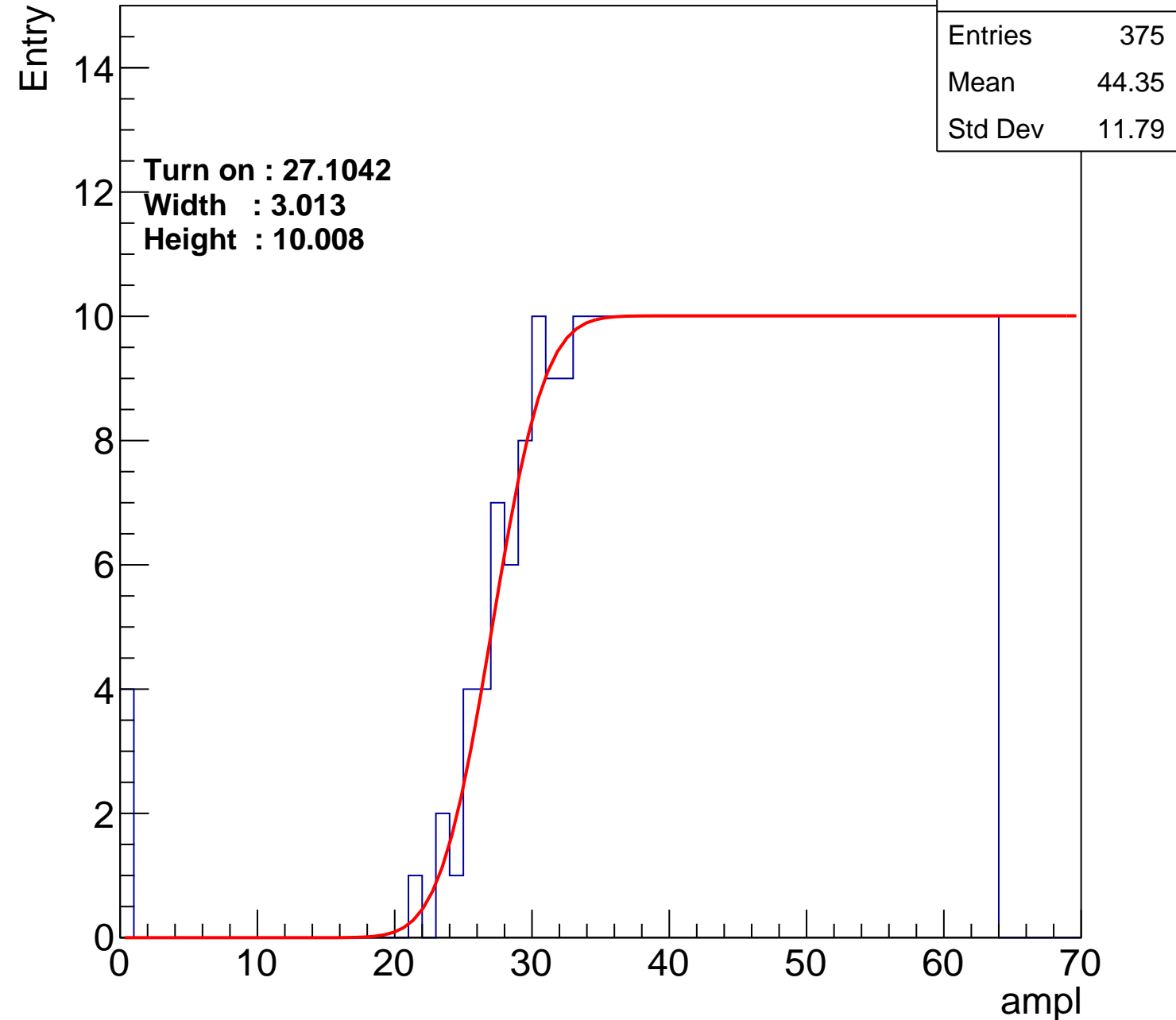
Width : 3.013

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch28

calib\_packv5\_042523\_0143.root, FC#5, port B1

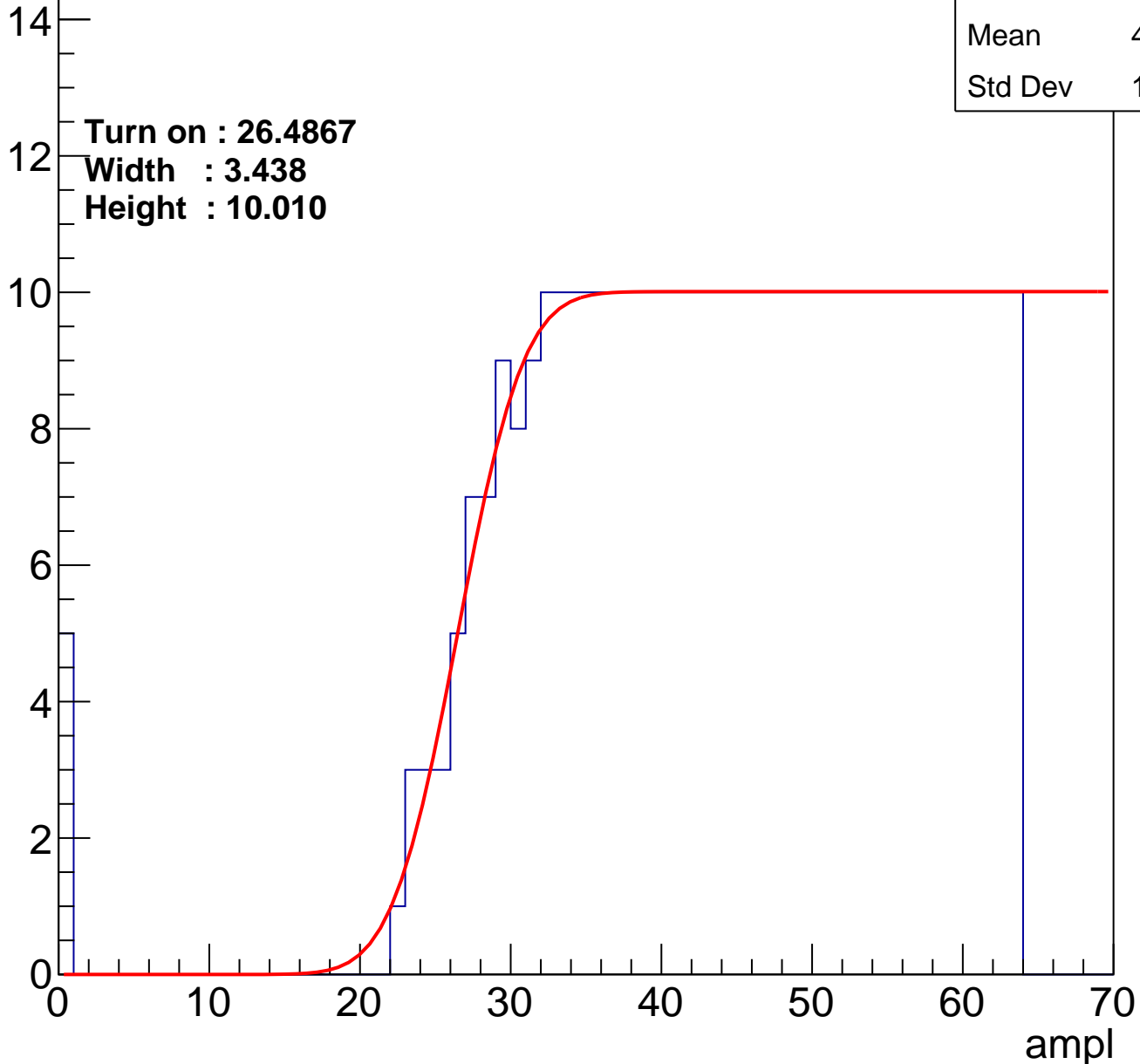
Entries	380
Mean	44.03
Std Dev	12.09

**Turn on : 26.4867**

**Width : 3.438**

**Height : 10.010**

Entry



# B0L000S, U14-ch29

calib\_packv5\_042523\_0143.root, FC#5, port B1

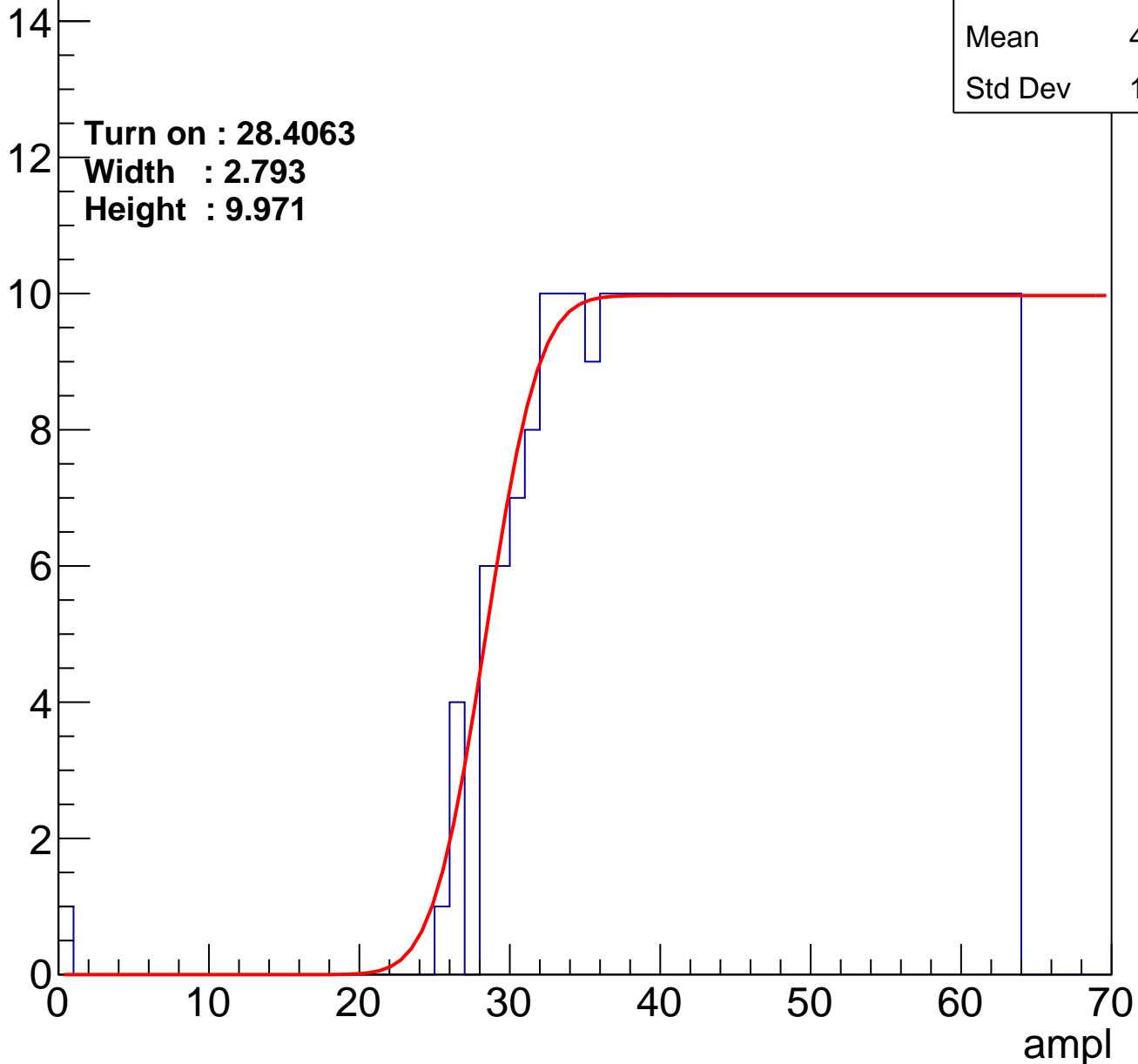
Entries	352
Mean	45.72
Std Dev	10.56

Turn on : 28.4063

Width : 2.793

Height : 9.971

Entry



# B0L000S, U14-ch30

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 25.2458

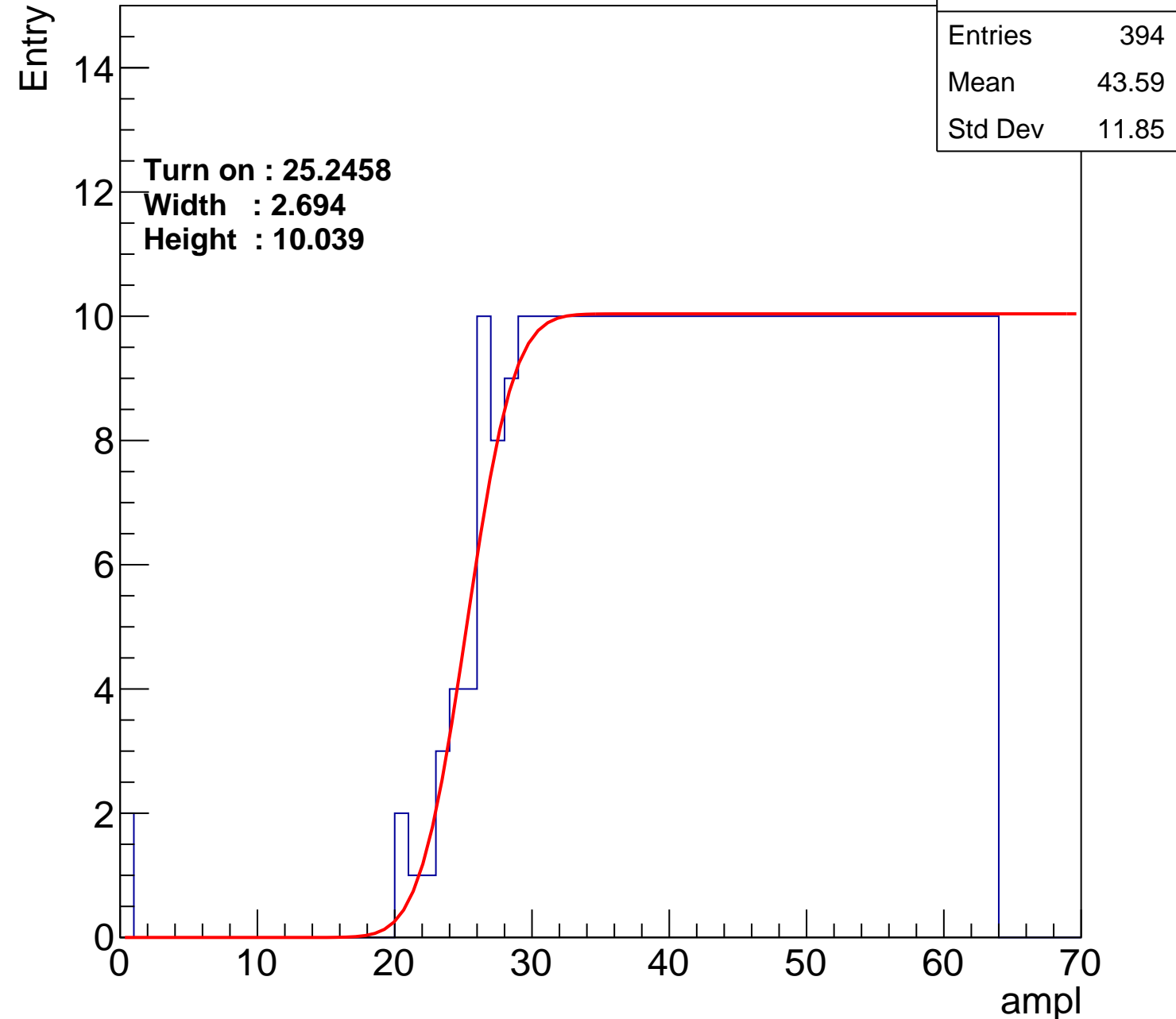
Width : 2.694

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch31

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	397
Mean	43.36
Std Dev	12.12

Turn on : 25.1152

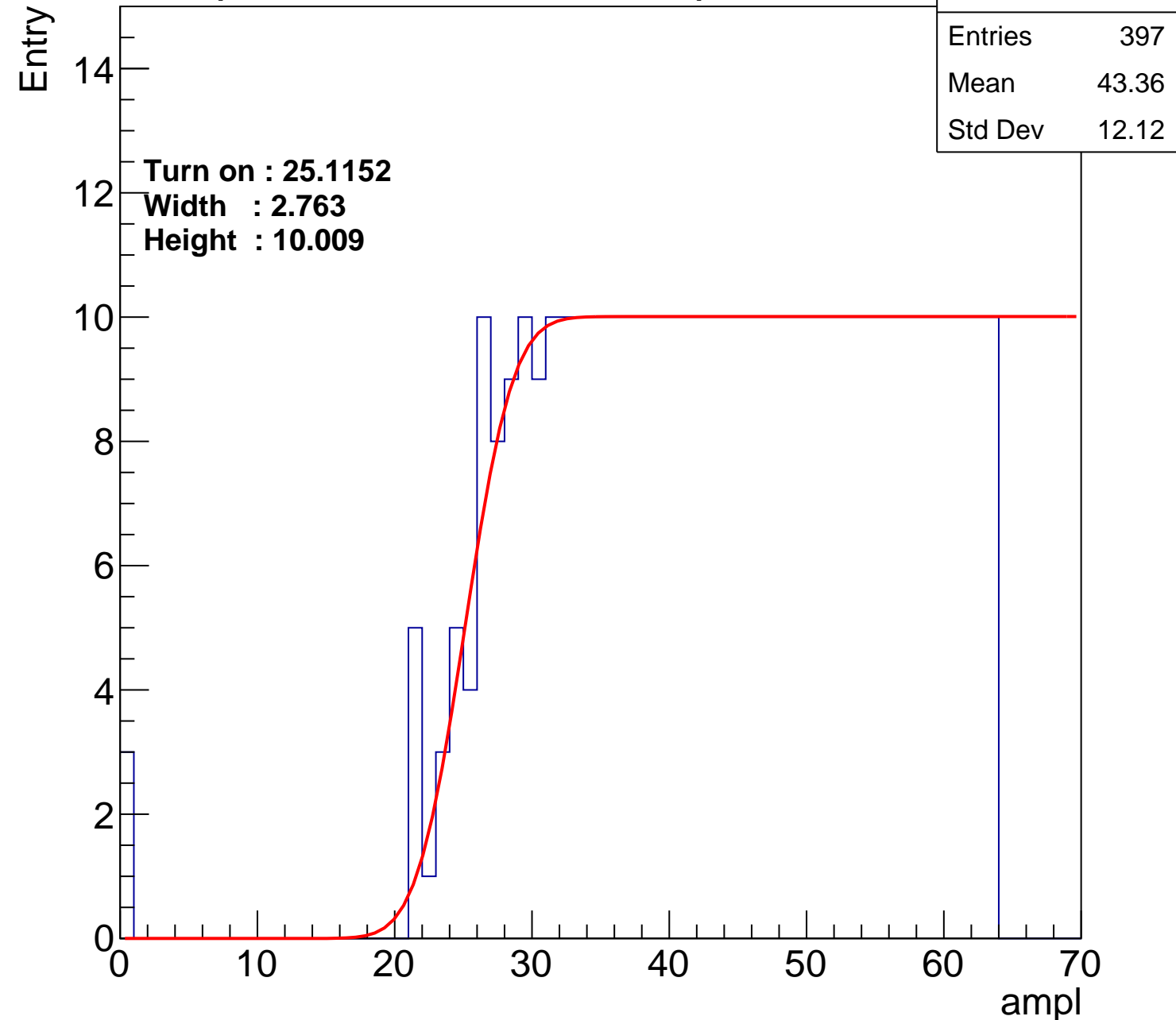
Width : 2.763

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch32

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	405
Mean	42.89
Std Dev	12.55

Turn on : 24.2514

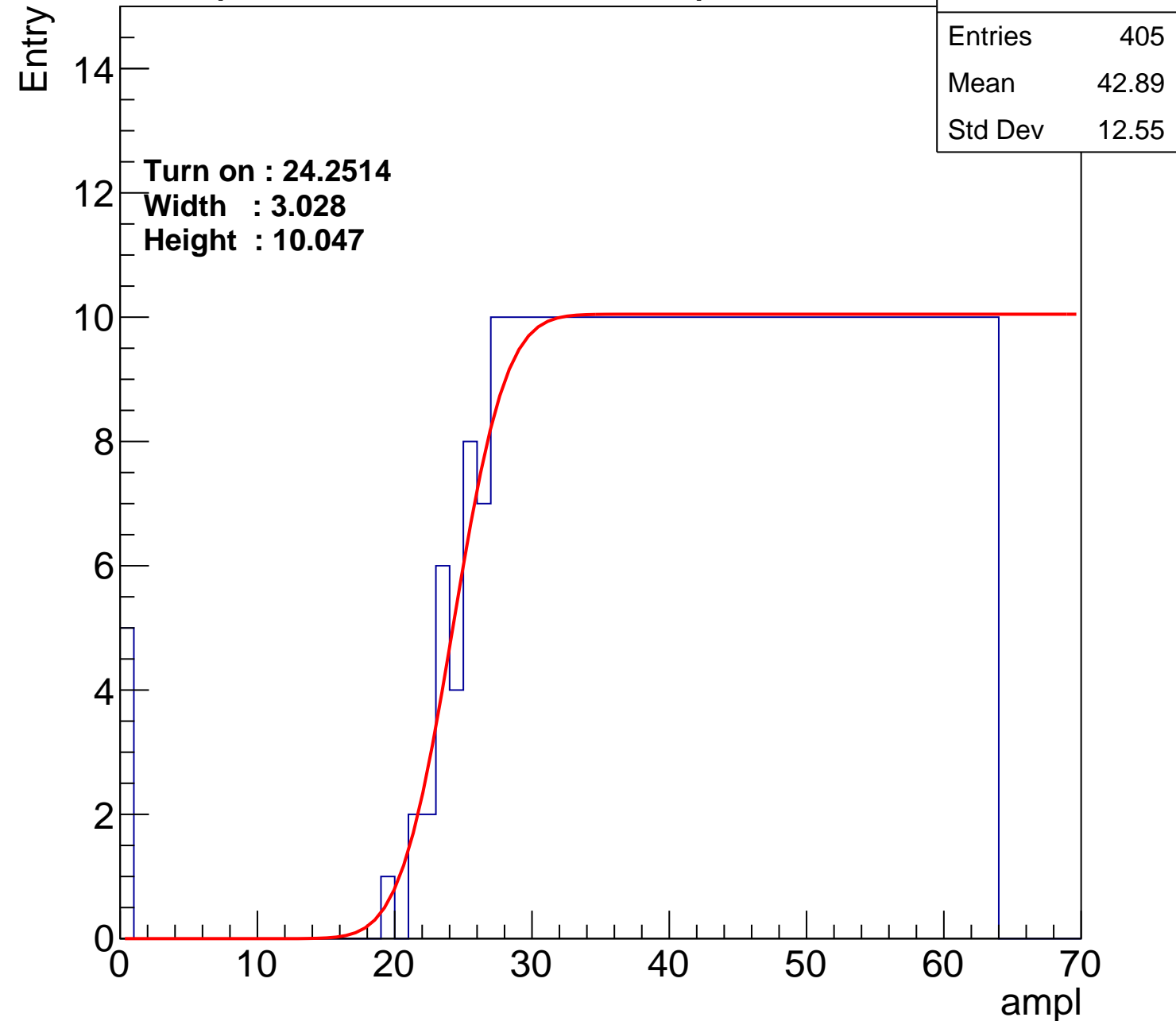
Width : 3.028

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch33

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.88
Std Dev	11.76

Turn on : 25.7361

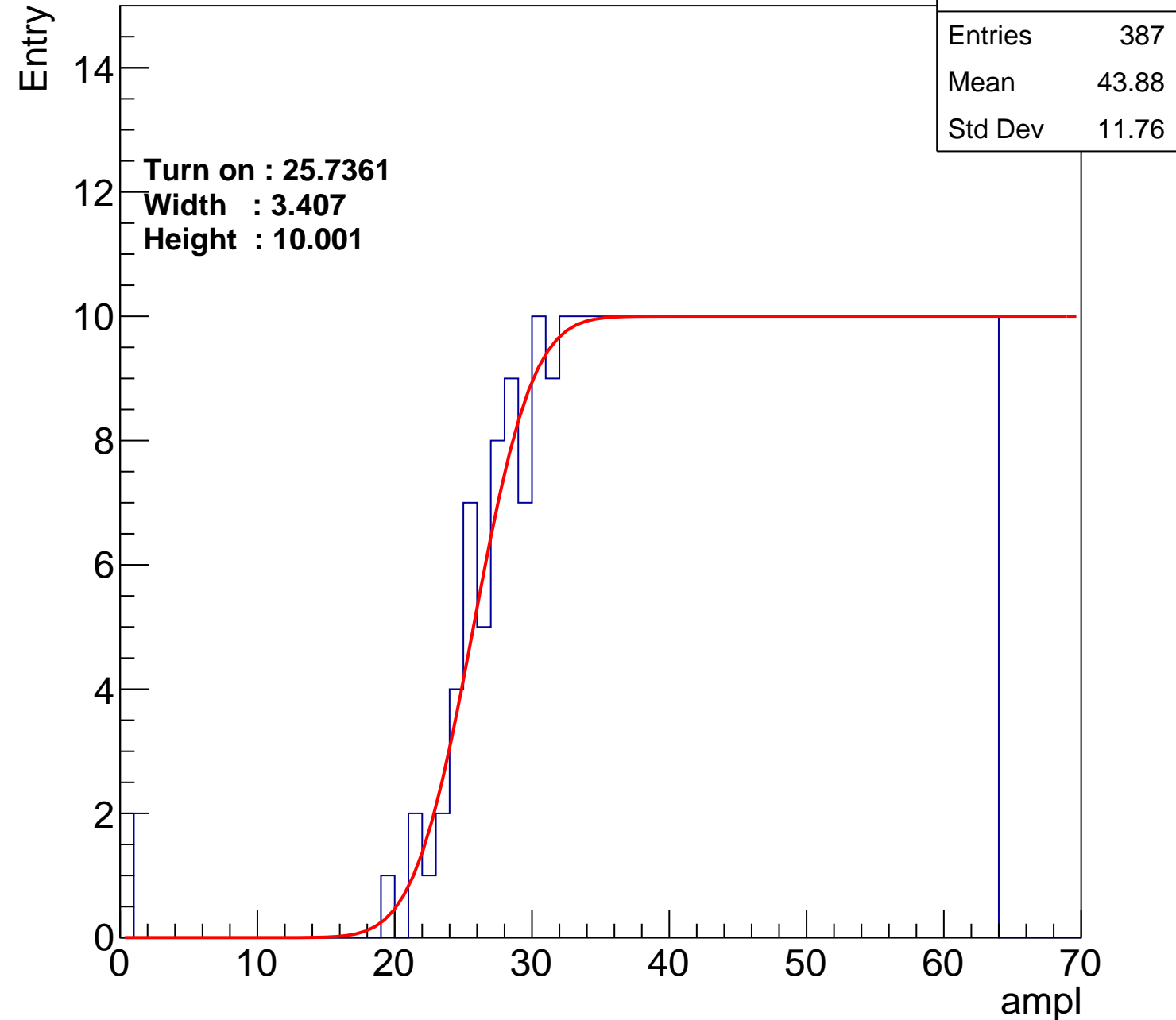
Width : 3.407

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch34

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.3374

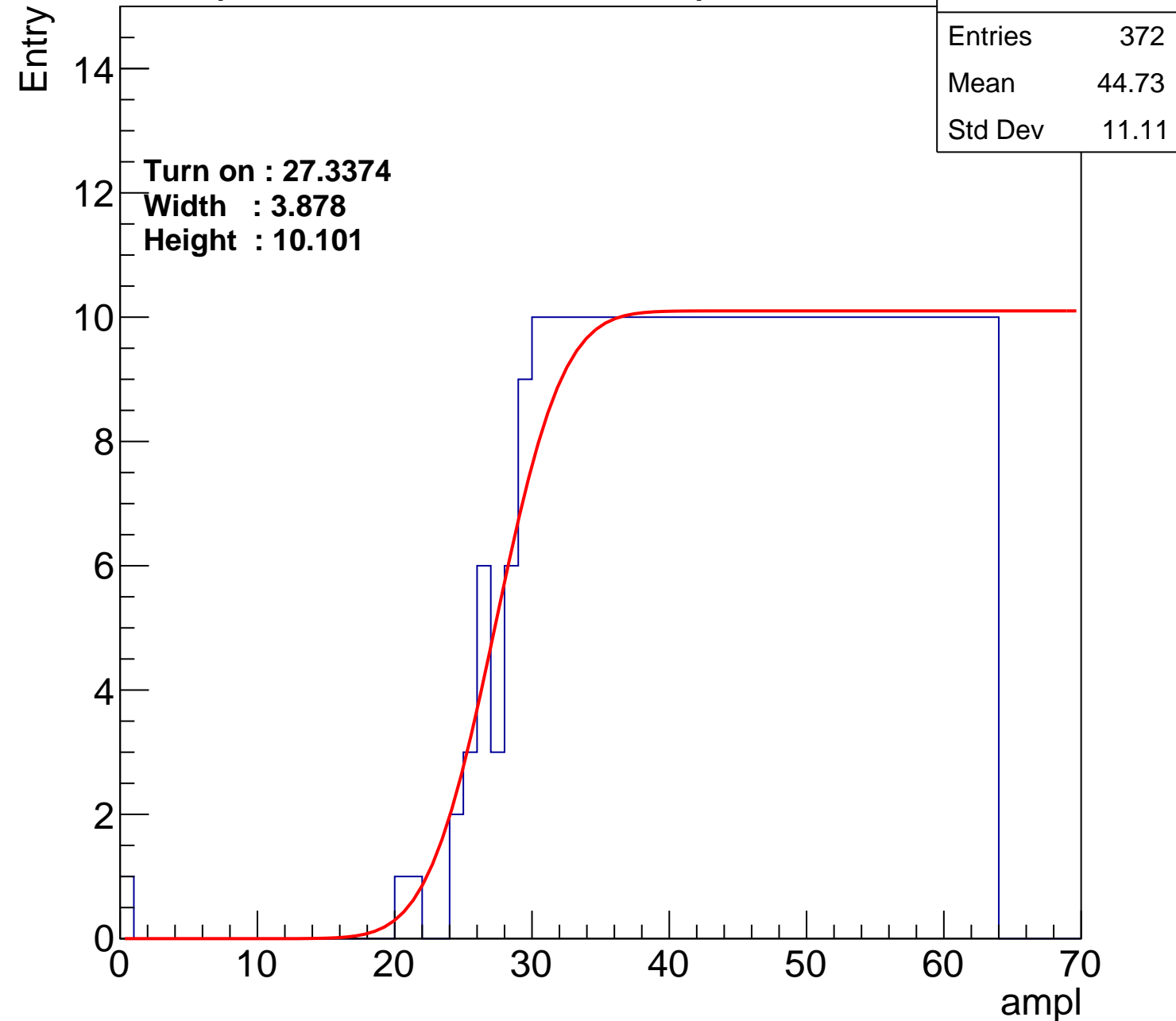
Width : 3.878

Height : 10.101

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch35

calib\_packv5\_042523\_0143.root, FC#5, port B1

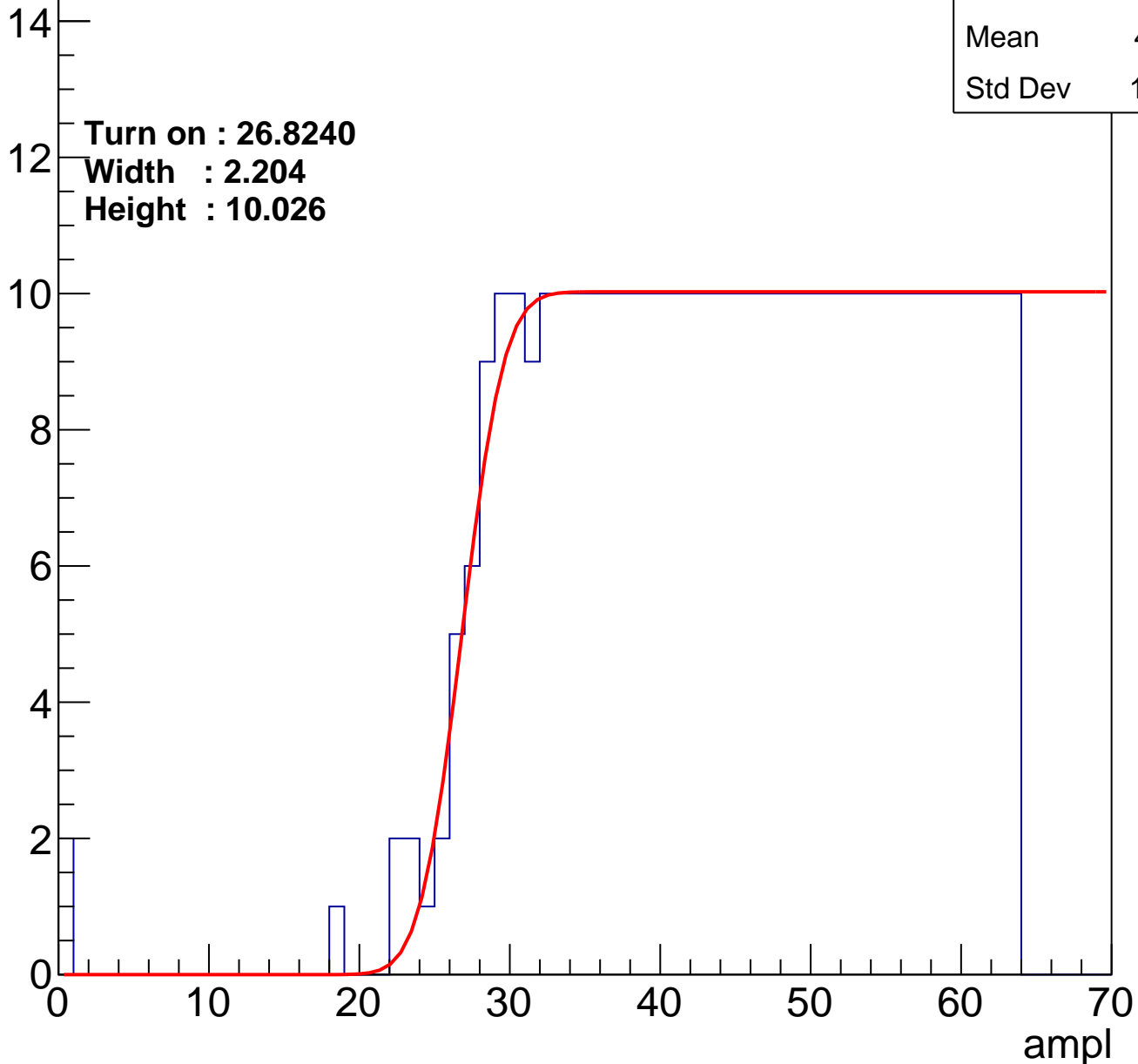
Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.8240

Width : 2.204

Height : 10.026

Entry



# B0L000S, U14-ch36

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.15
Std Dev	11.47

Turn on : 25.9934

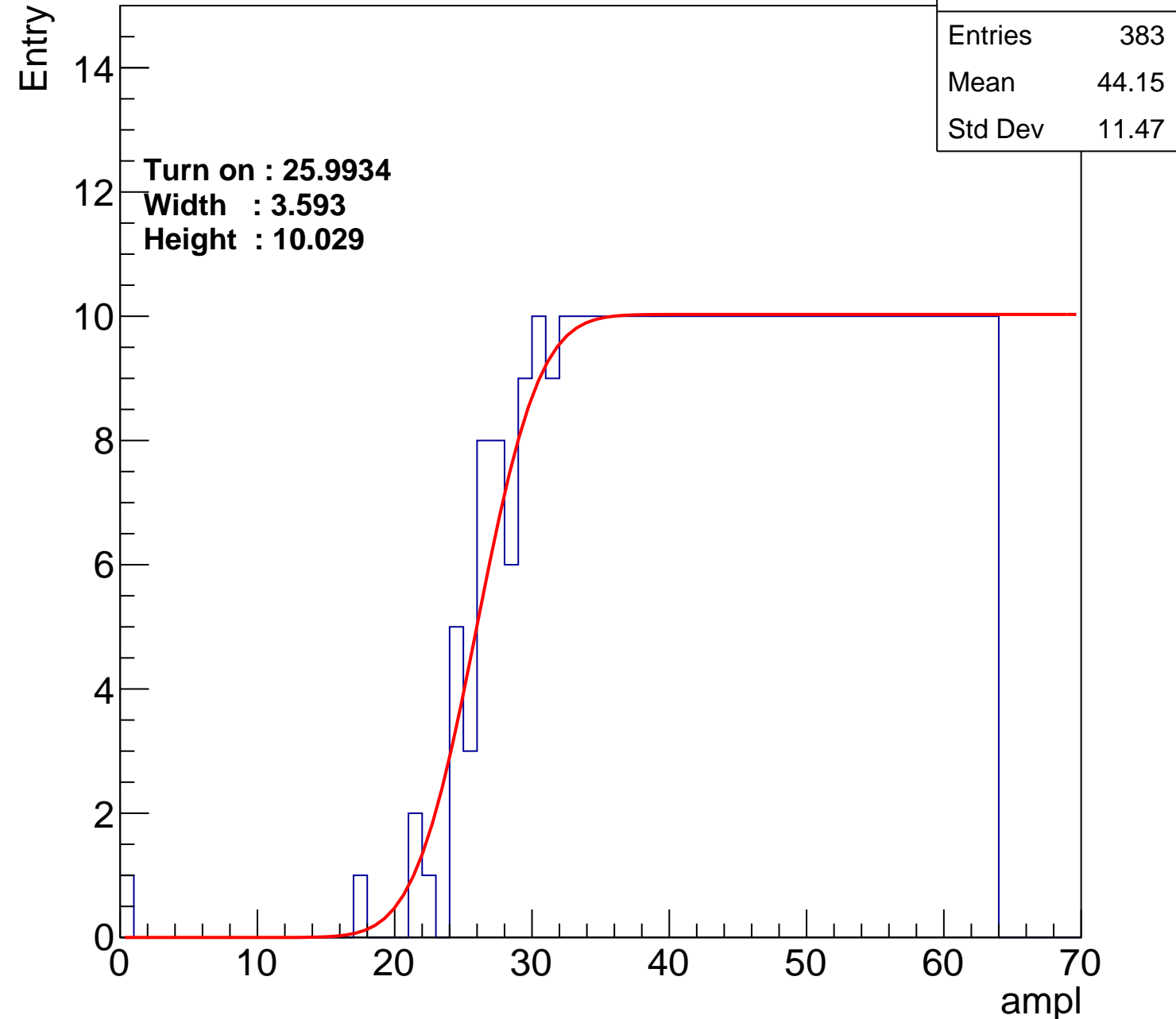
Width : 3.593

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch37

calib\_packv5\_042523\_0143.root, FC#5, port B1

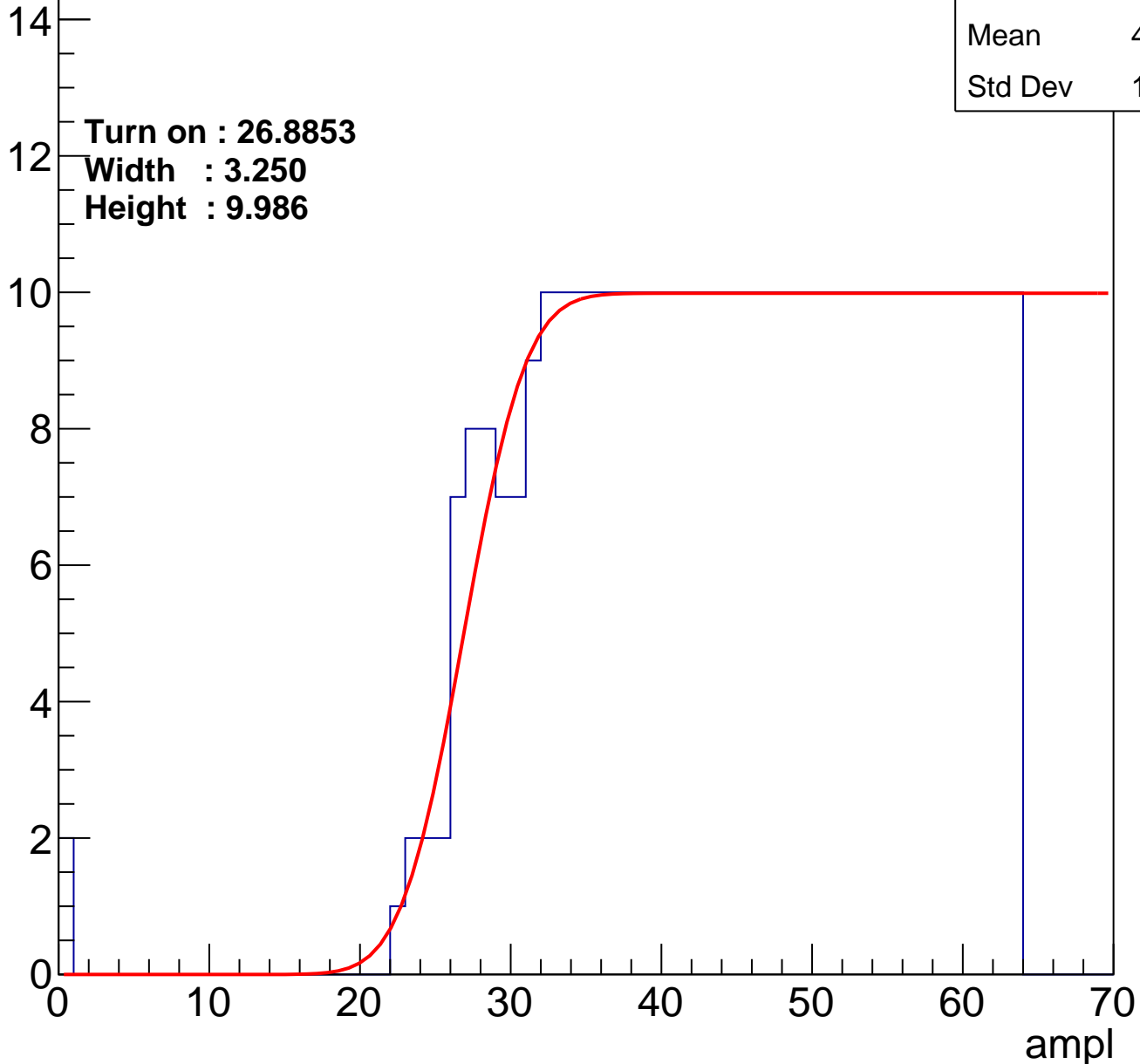
Entries	375
Mean	44.48
Std Dev	11.43

**Turn on : 26.8853**

**Width : 3.250**

**Height : 9.986**

Entry



# B0L000S, U14-ch38

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.08
Std Dev	11.71

**Turn on : 26.3276**

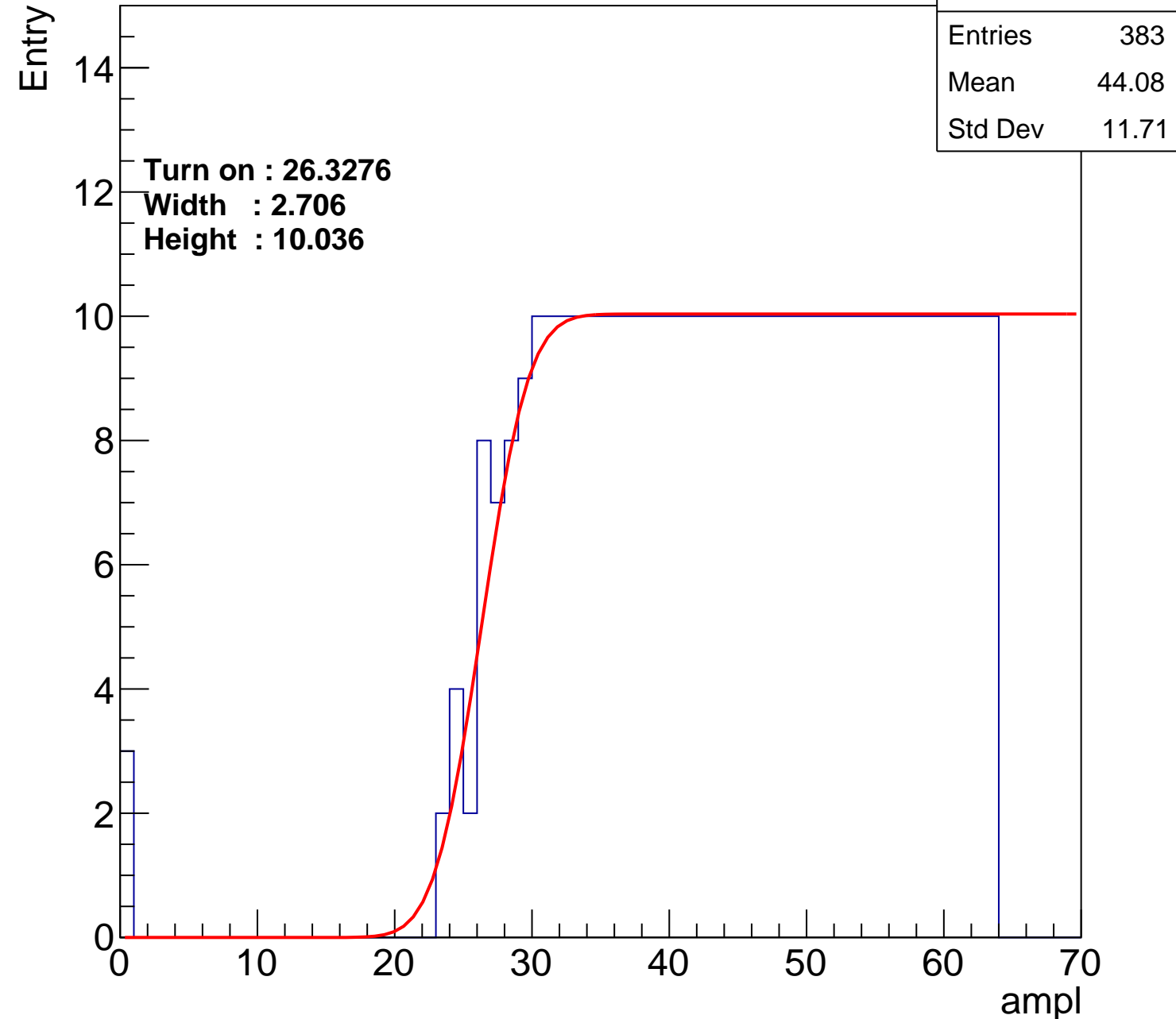
**Width : 2.706**

**Height : 10.036**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch39

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.28
Std Dev	11.8

**Turn on : 26.6325**

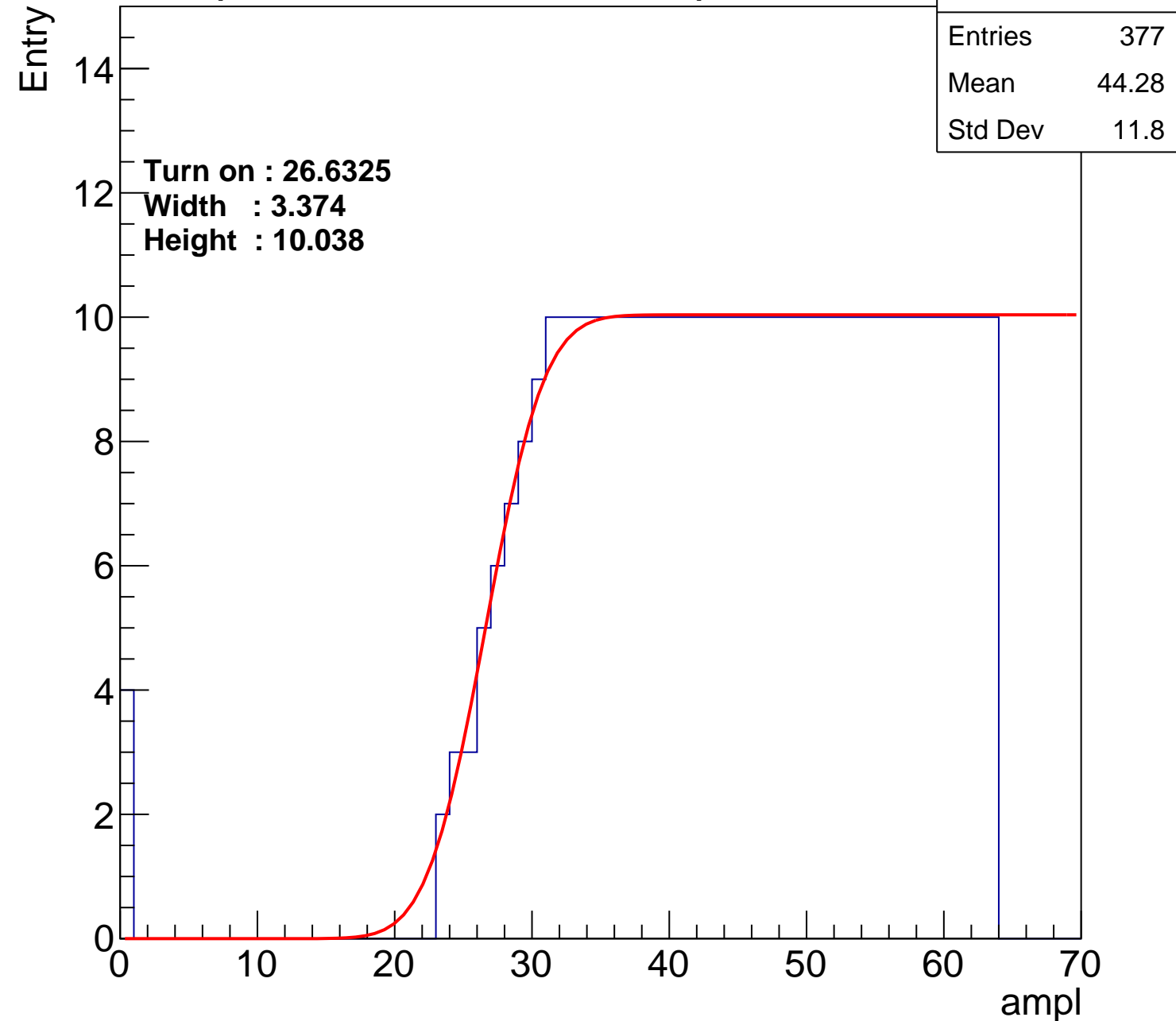
**Width : 3.374**

**Height : 10.038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch40

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.77
Std Dev	11.78

Turn on : 25.4461

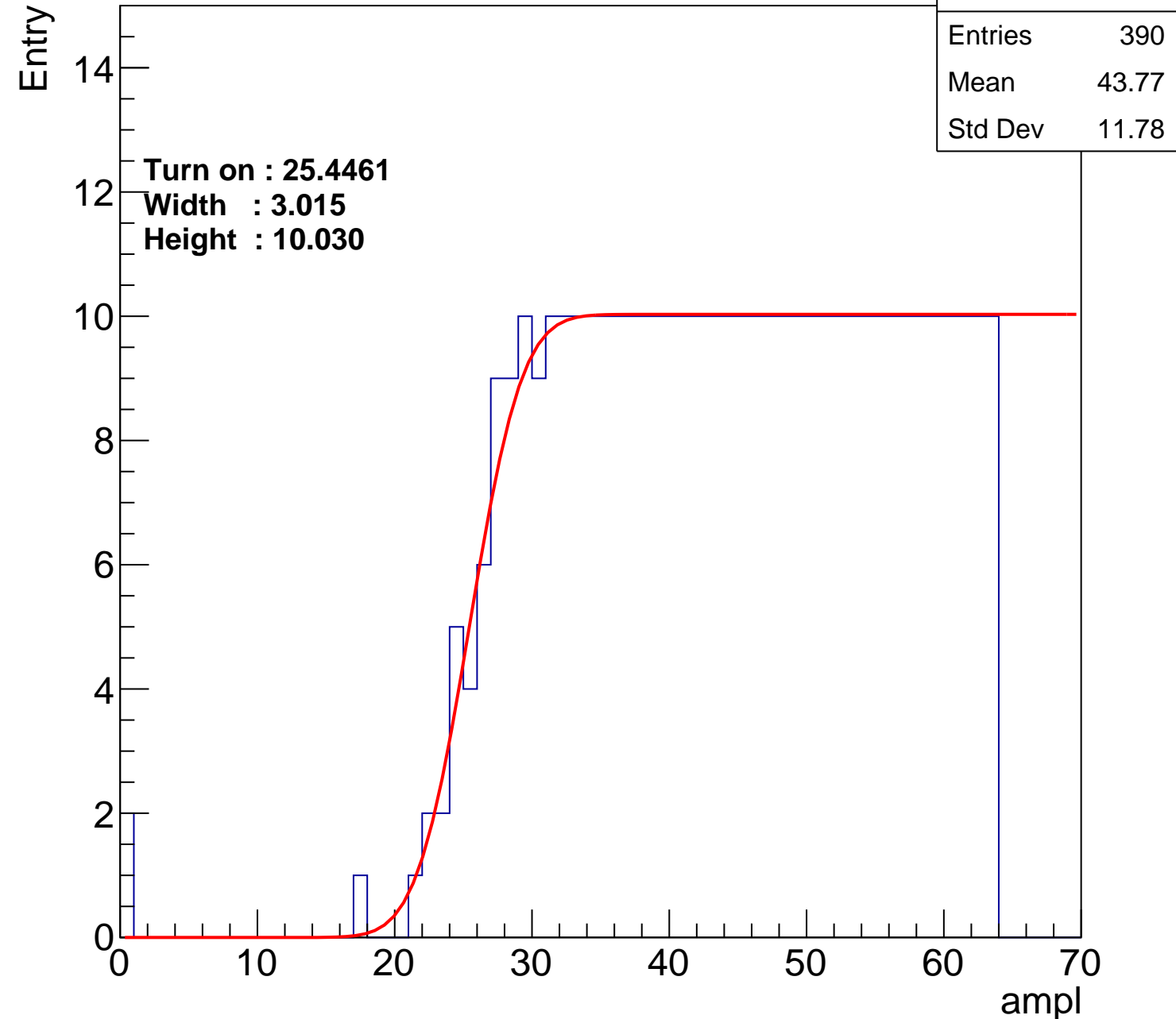
Width : 3.015

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch41

calib\_packv5\_042523\_0143.root, FC#5, port B1

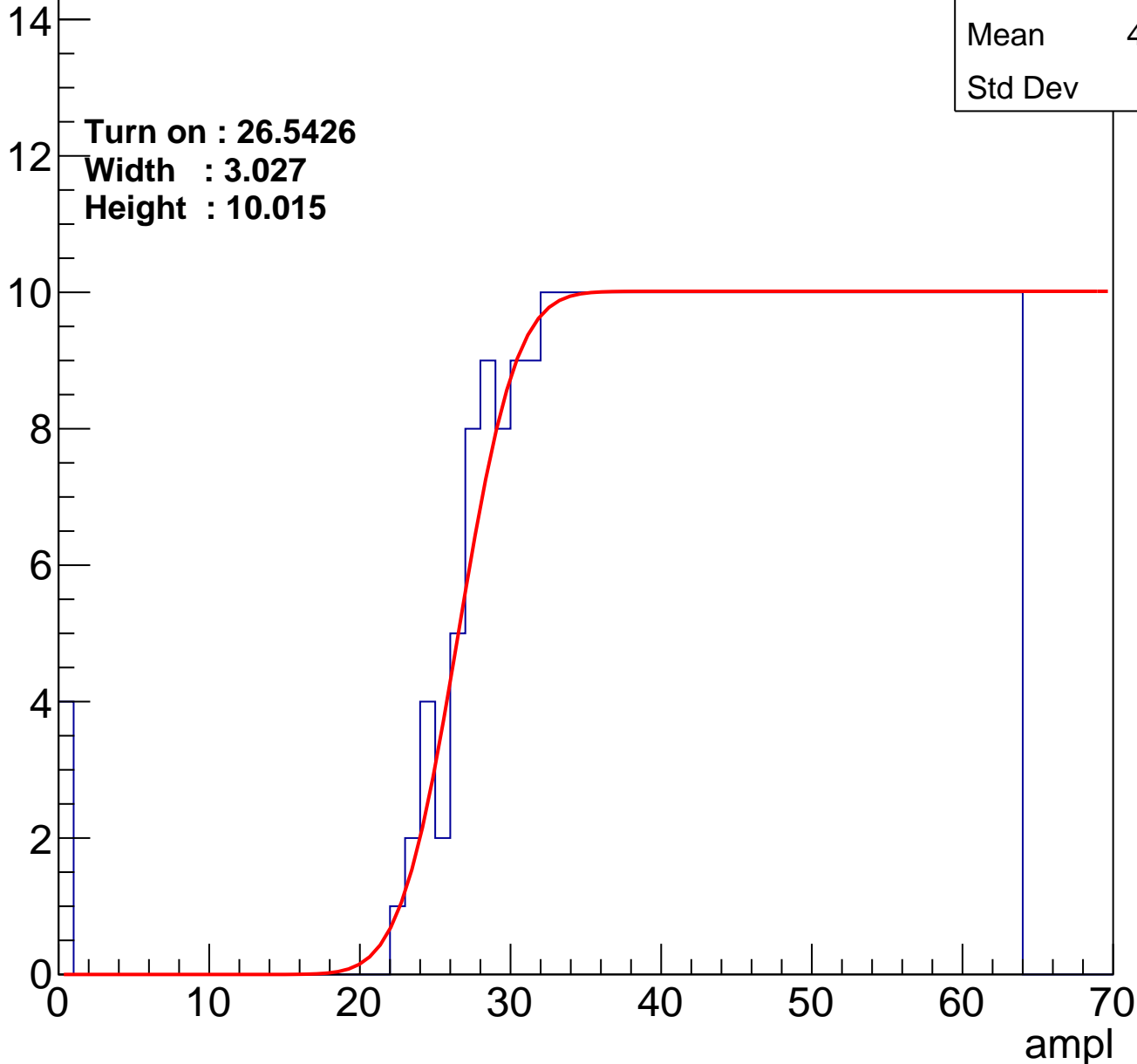
Entries	381
Mean	44.08
Std Dev	11.9

Turn on : 26.5426

Width : 3.027

Height : 10.015

Entry



# B0L000S, U14-ch42

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 26.0724

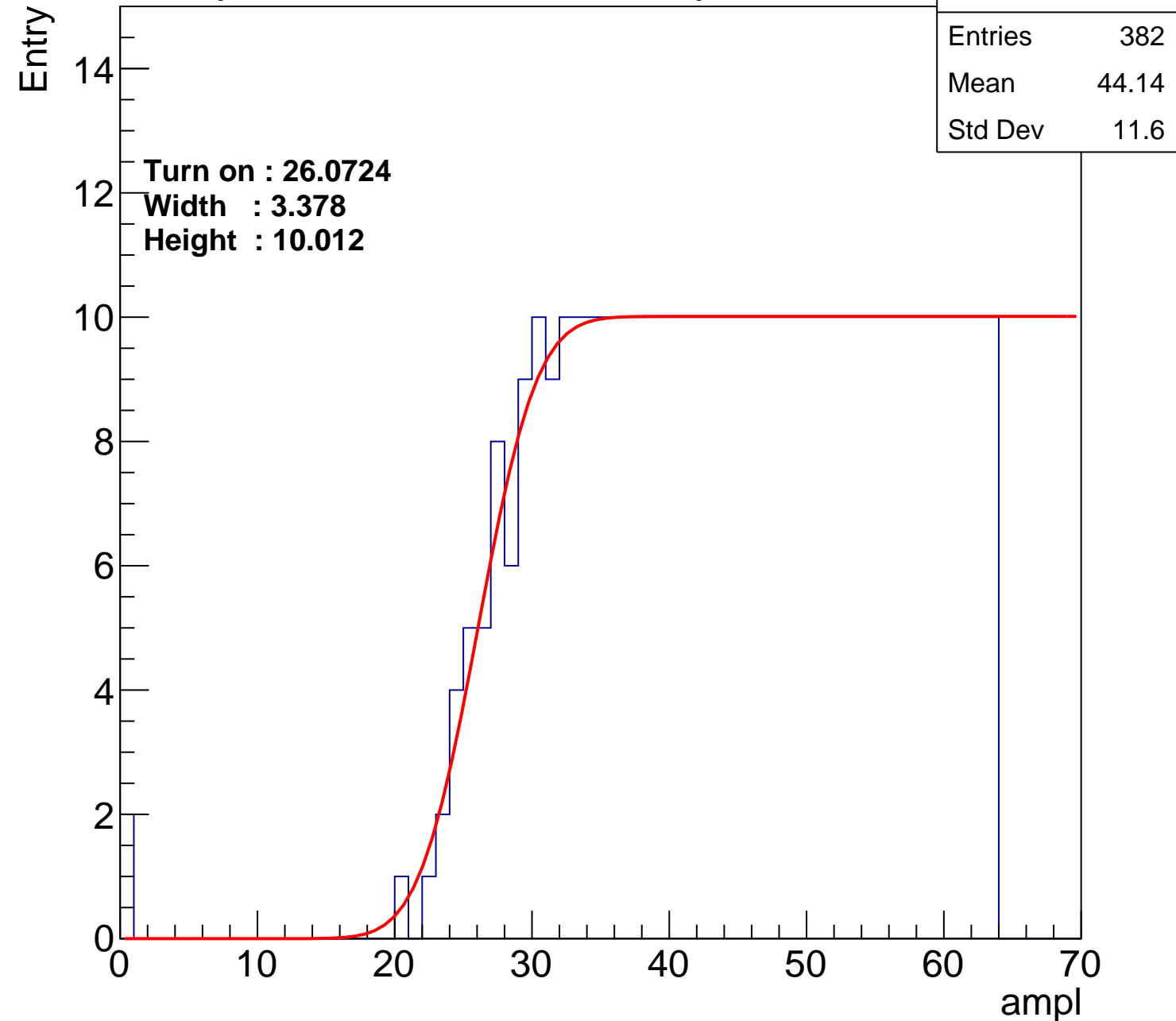
Width : 3.378

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch43

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 25.8318

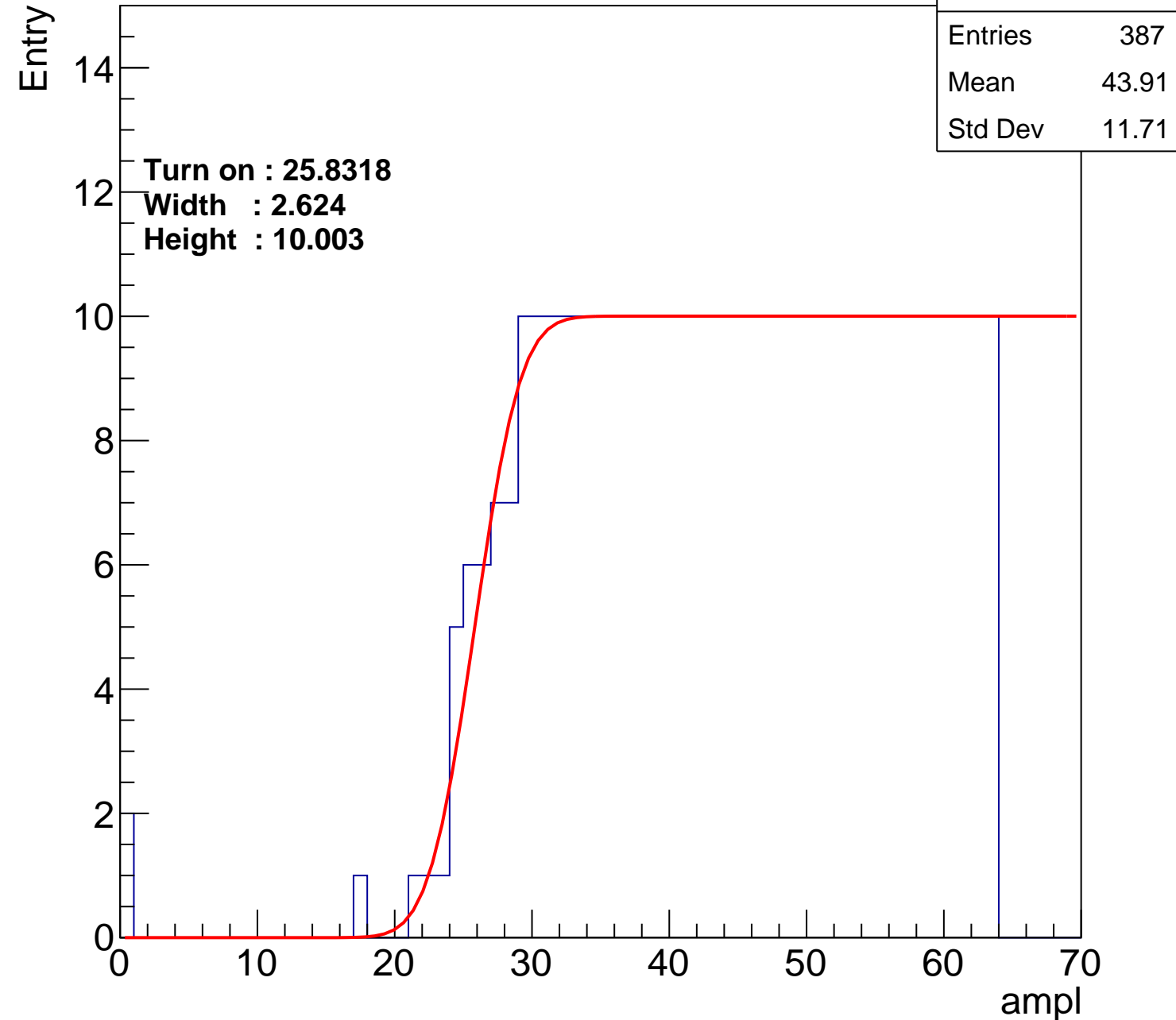
Width : 2.624

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch44

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.94
Std Dev	11.12

Turn on : 26.9914

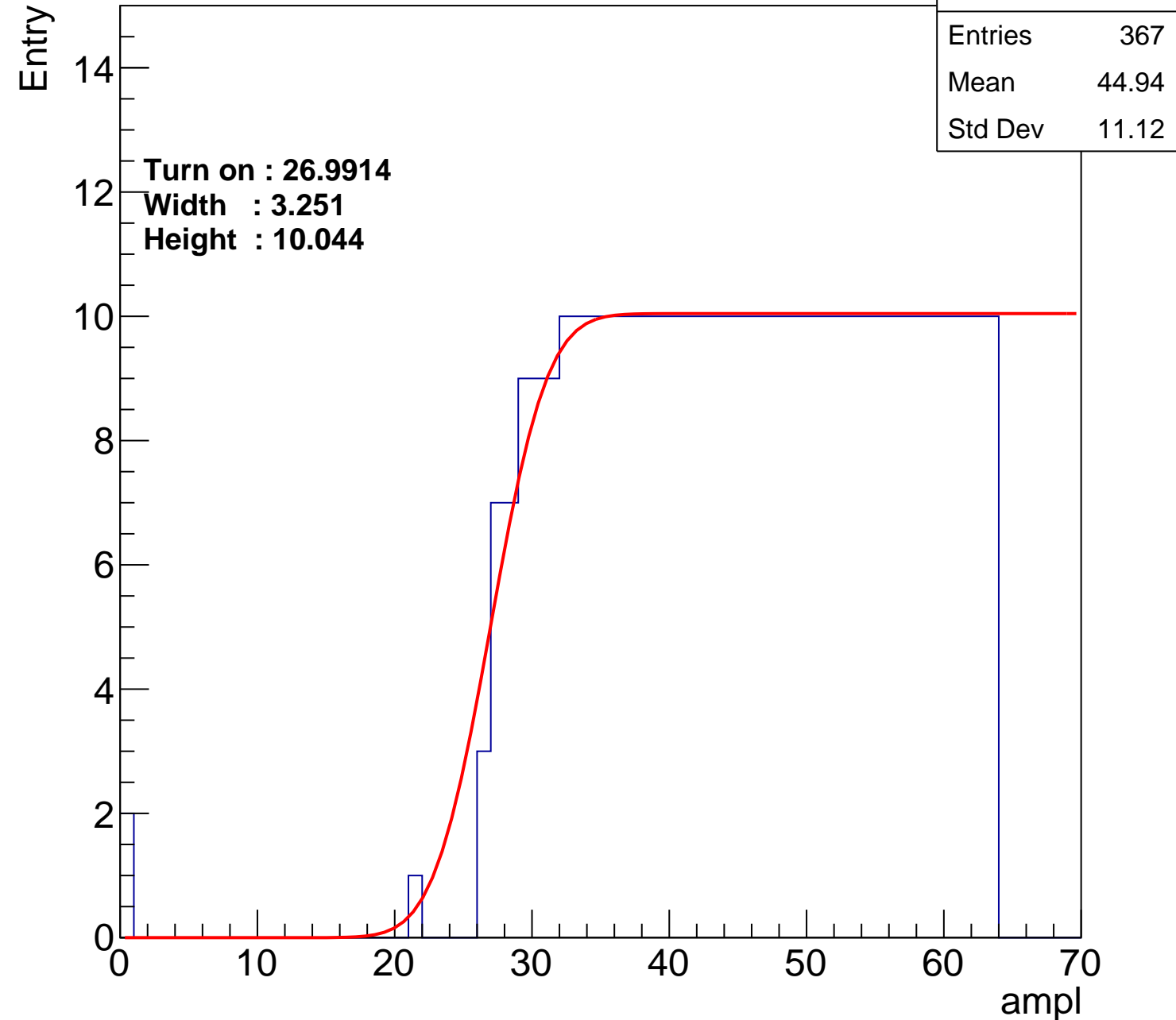
Width : 3.251

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch45

calib\_packv5\_042523\_0143.root, FC#5, port B1

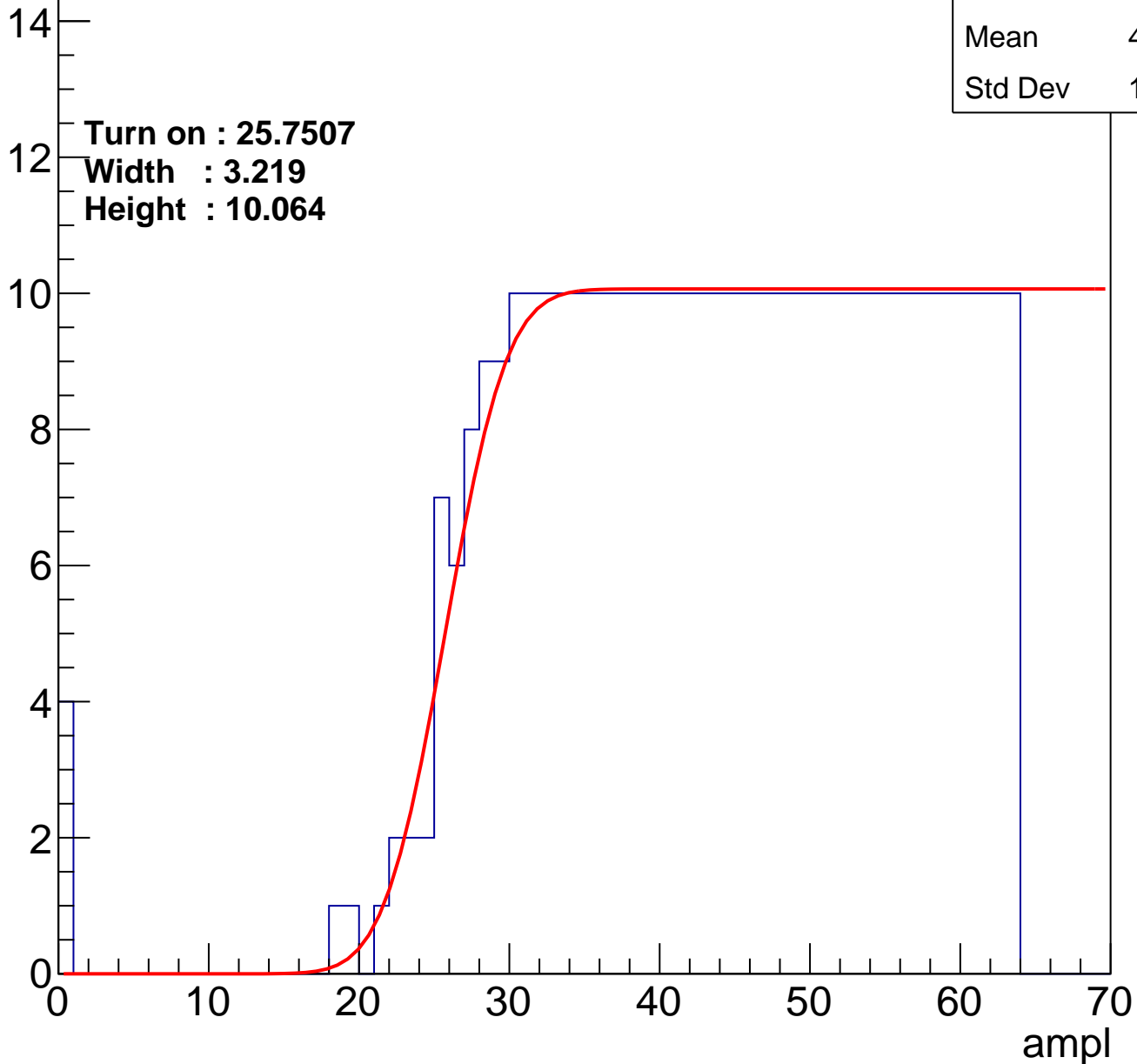
Entries	392
Mean	43.54
Std Dev	12.17

**Turn on : 25.7507**

**Width : 3.219**

**Height : 10.064**

Entry



# B0L000S, U14-ch46

calib\_packv5\_042523\_0143.root, FC#5, port B1

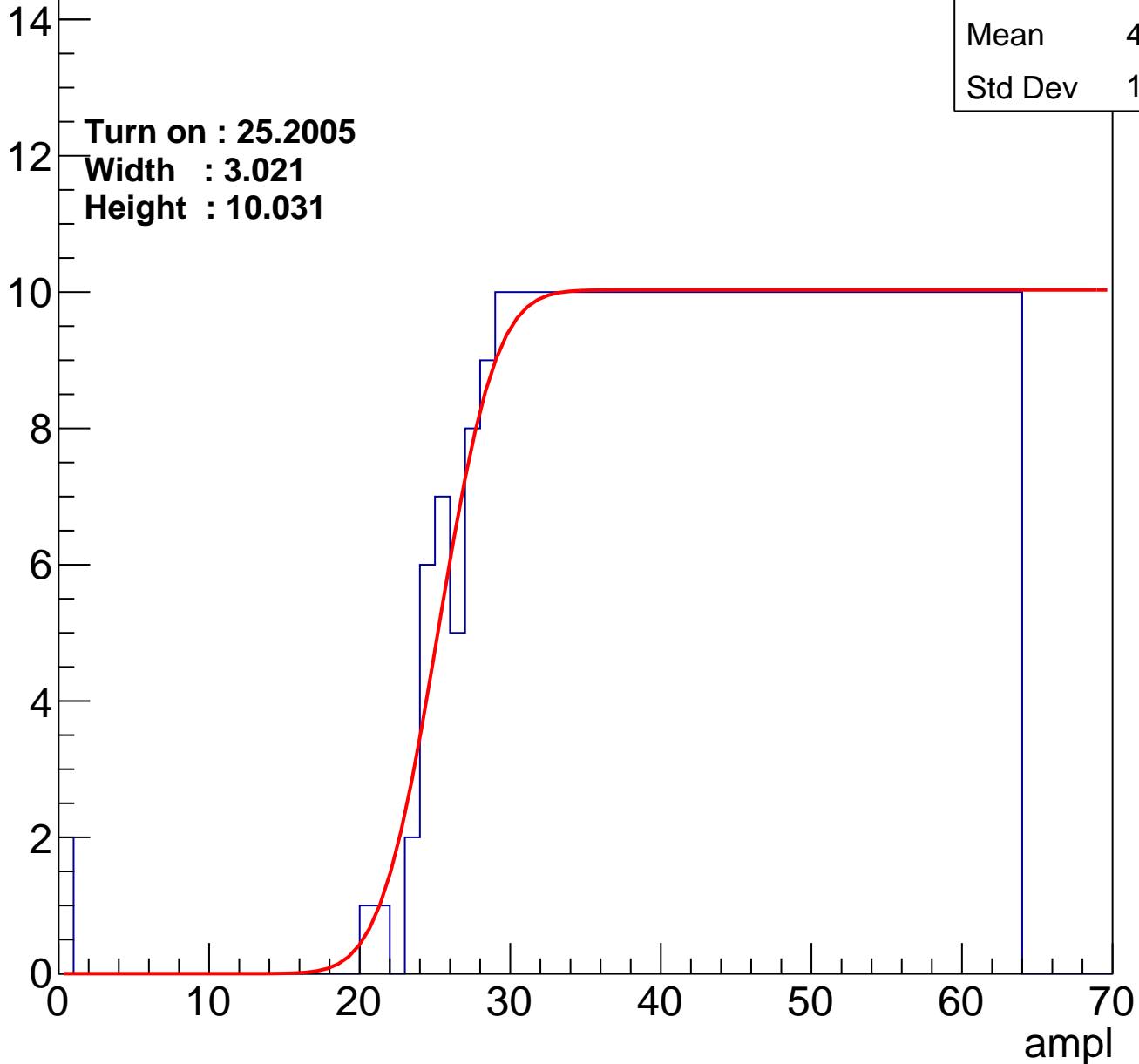
Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 25.2005

Width : 3.021

Height : 10.031

Entry





# B0L000S, U14-ch47

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.07
Std Dev	11.62

**Turn on : 25.8947**

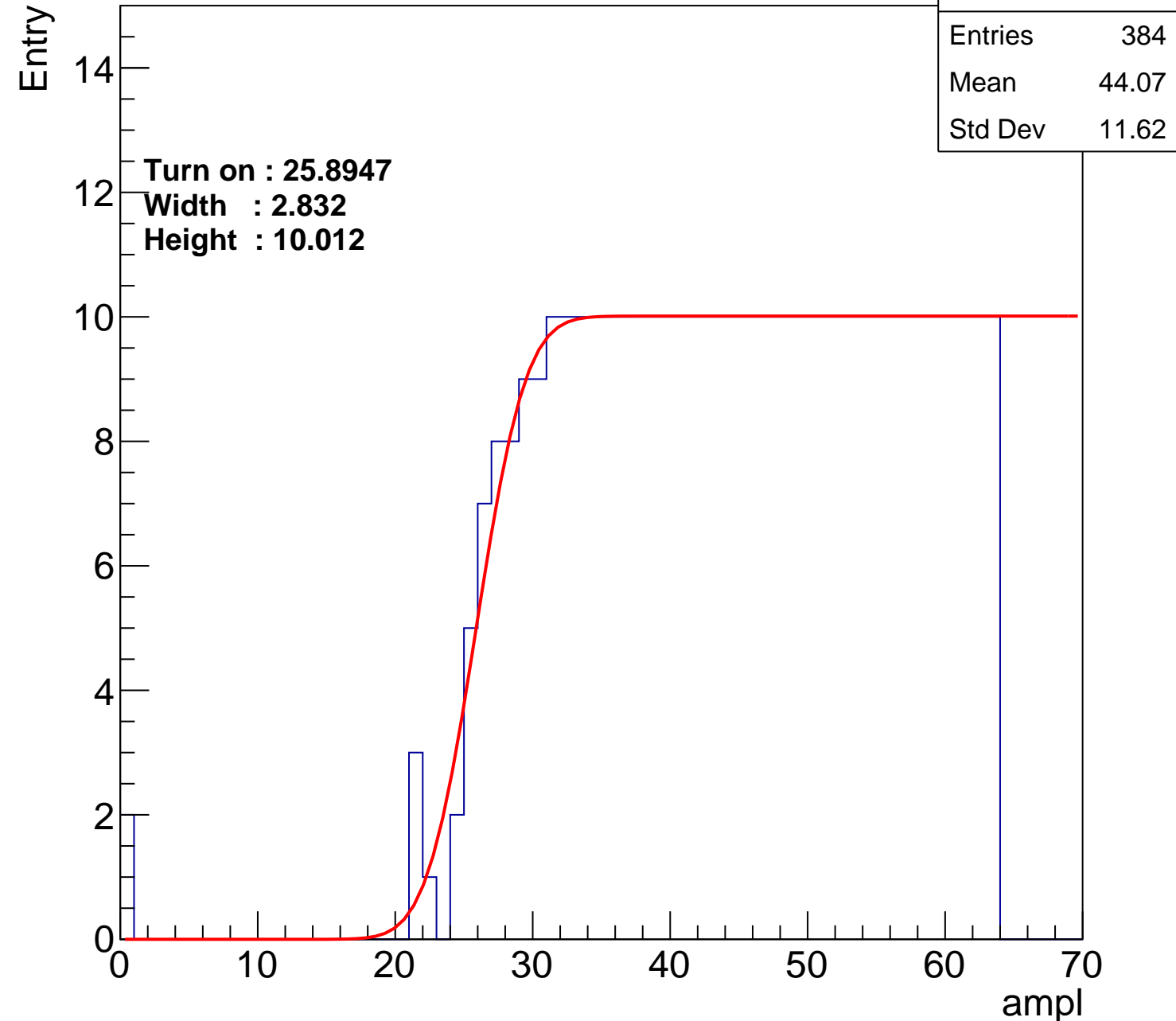
**Width : 2.832**

**Height : 10.012**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch48

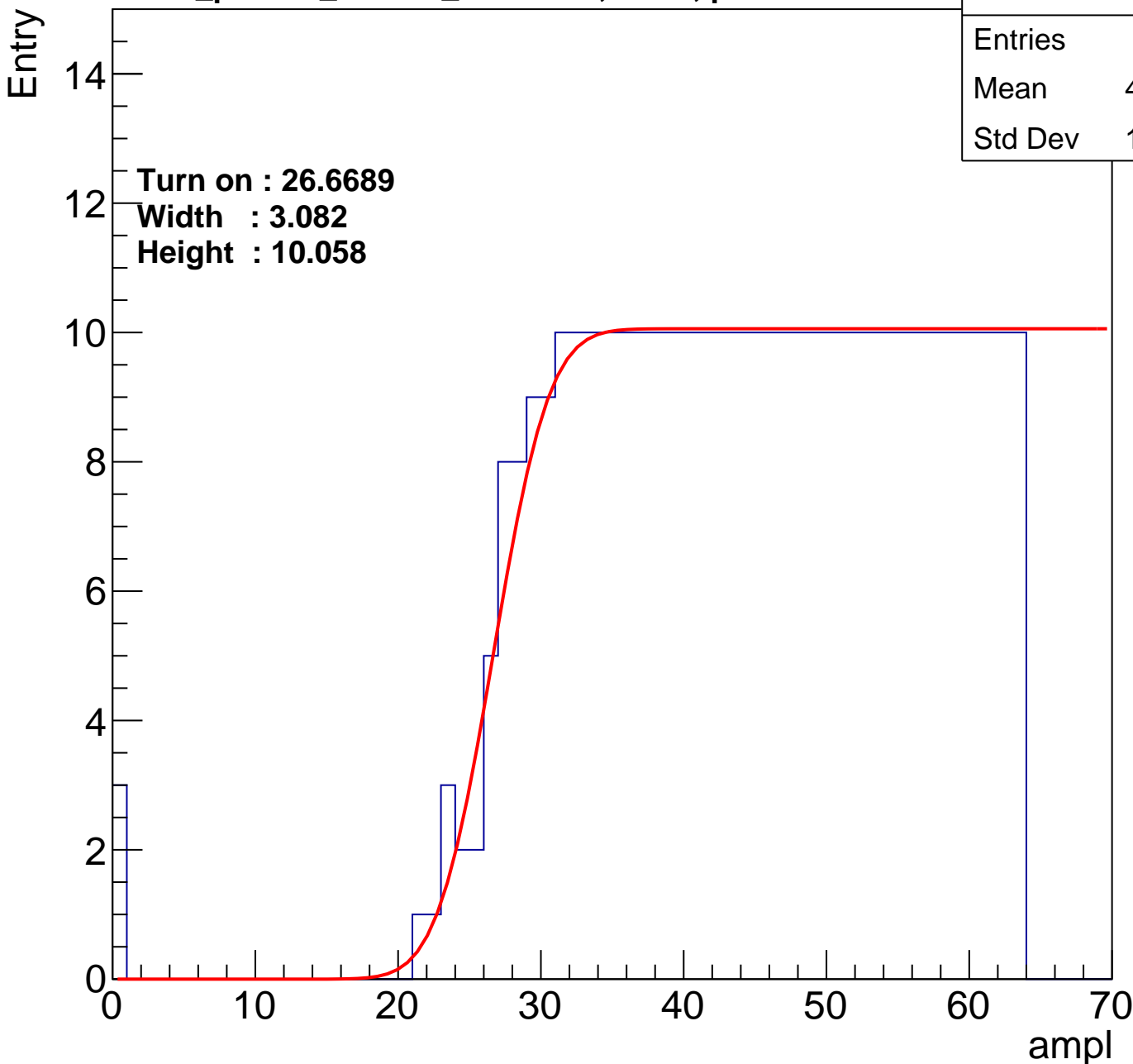
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	381
Mean	44.15
Std Dev	11.72

**Turn on : 26.6689**

**Width : 3.082**

**Height : 10.058**



# B0L000S, U14-ch49

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	43.97
Std Dev	12.14

**Turn on : 26.6665**

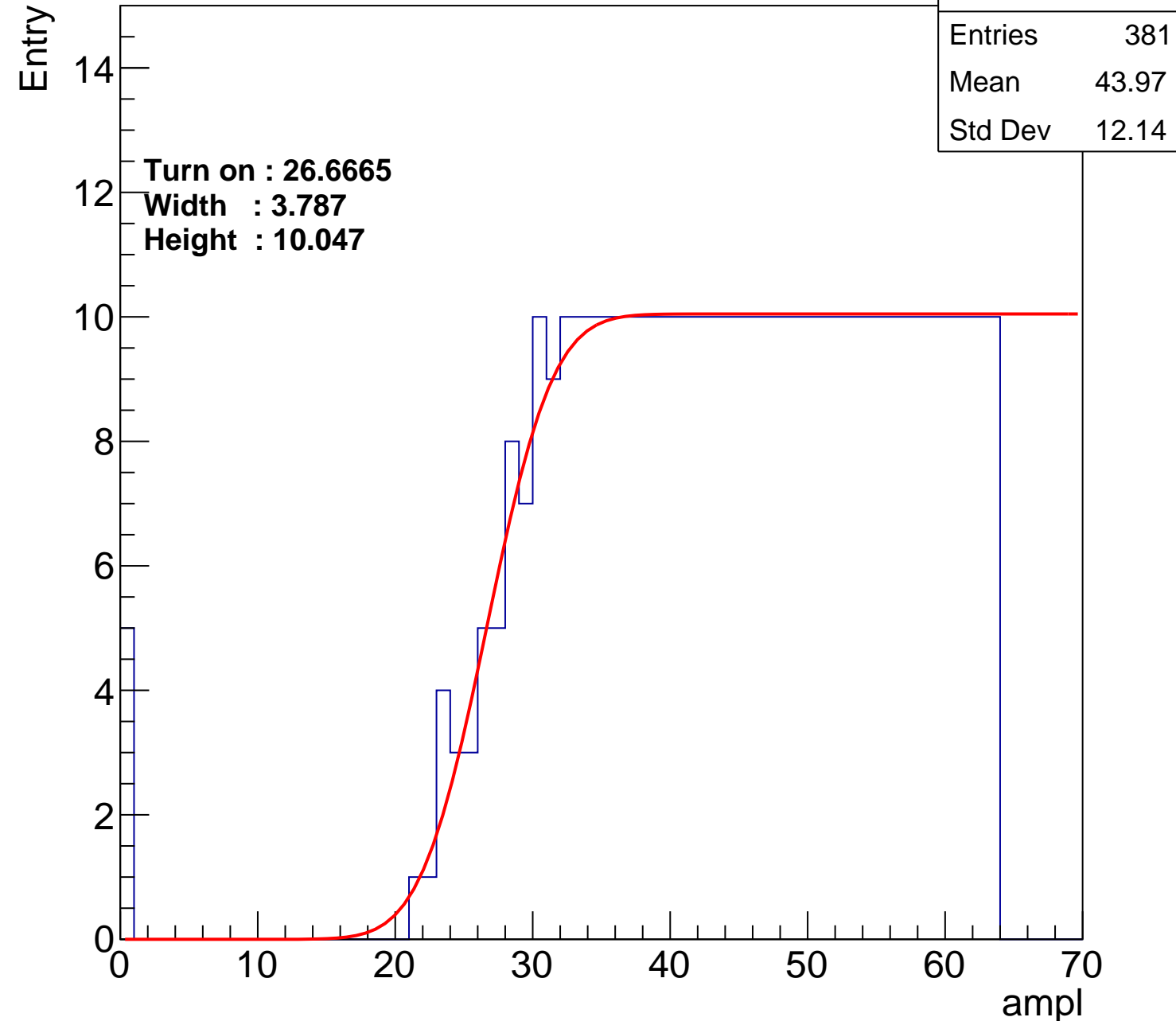
**Width : 3.787**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch50

calib\_packv5\_042523\_0143.root, FC#5, port B1

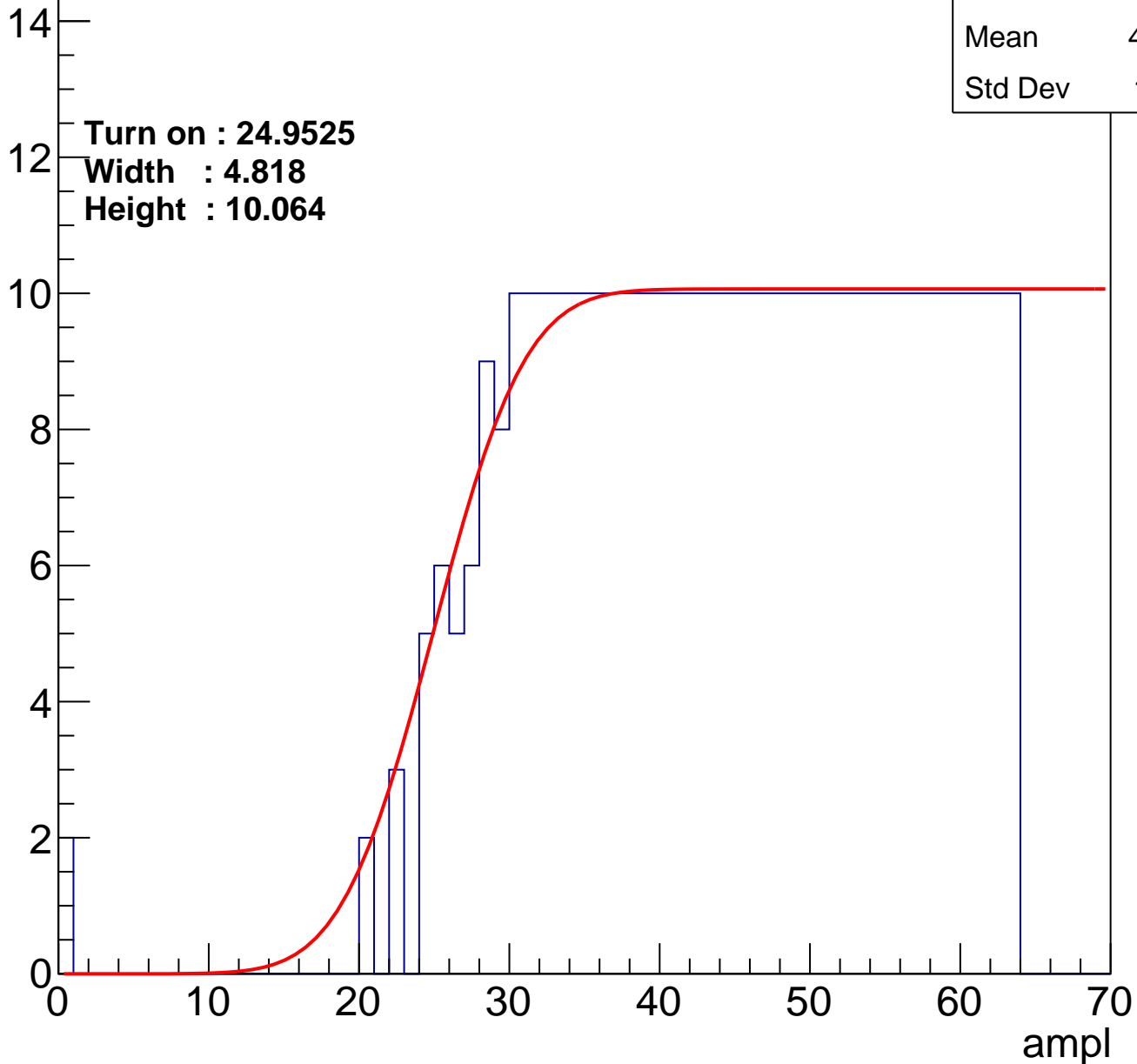
Entries	386
Mean	43.94
Std Dev	11.71

Turn on : 24.9525

Width : 4.818

Height : 10.064

Entry



# B0L000S, U14-ch51

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.5
Std Dev	12.18

Turn on : 25.6819

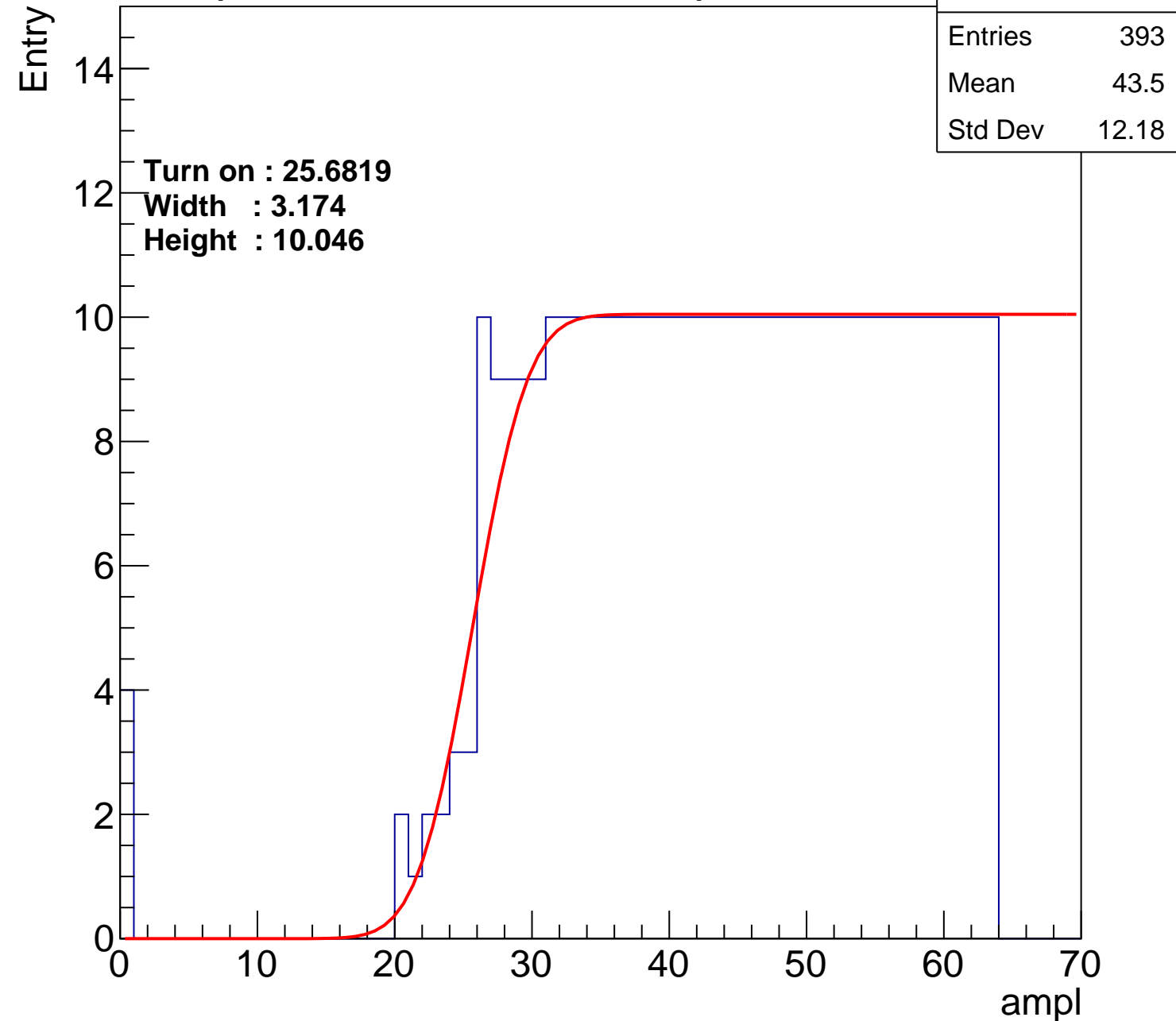
Width : 3.174

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch52

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.8820

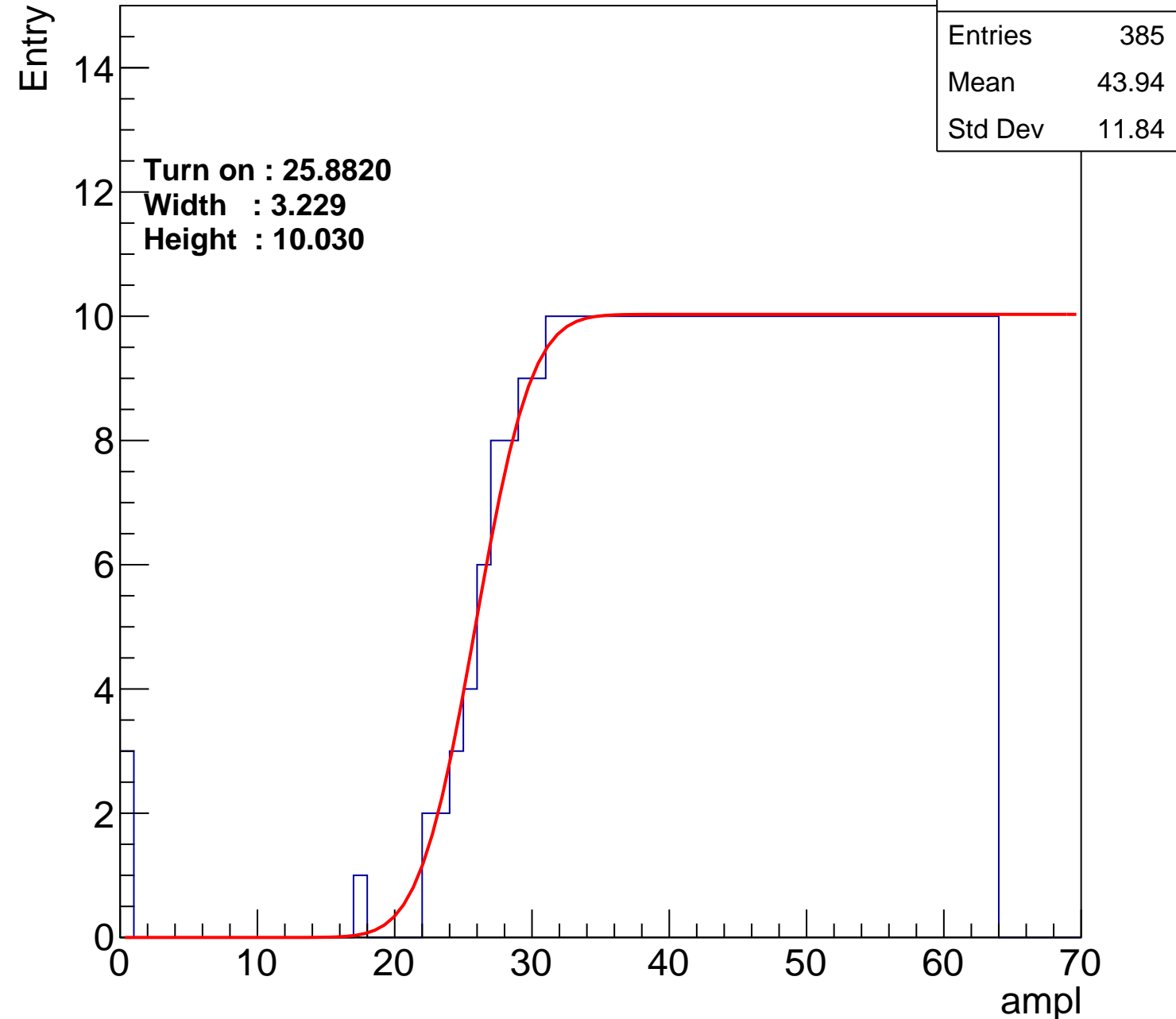
Width : 3.229

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch53

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.16
Std Dev	11.75

Turn on : 25.9671

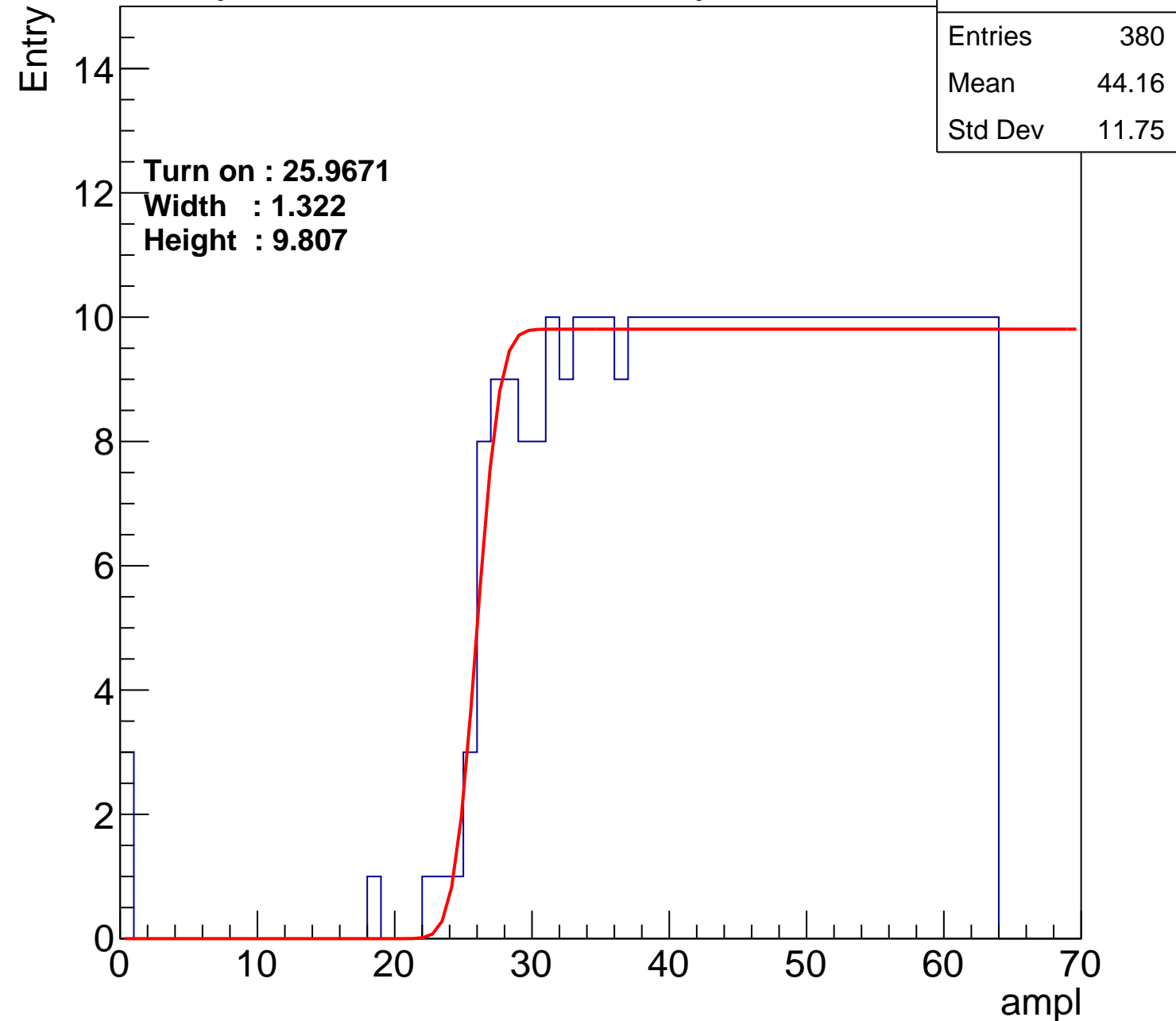
Width : 1.322

Height : 9.807

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch54

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.39
Std Dev	11.35

Turn on : 27.1403

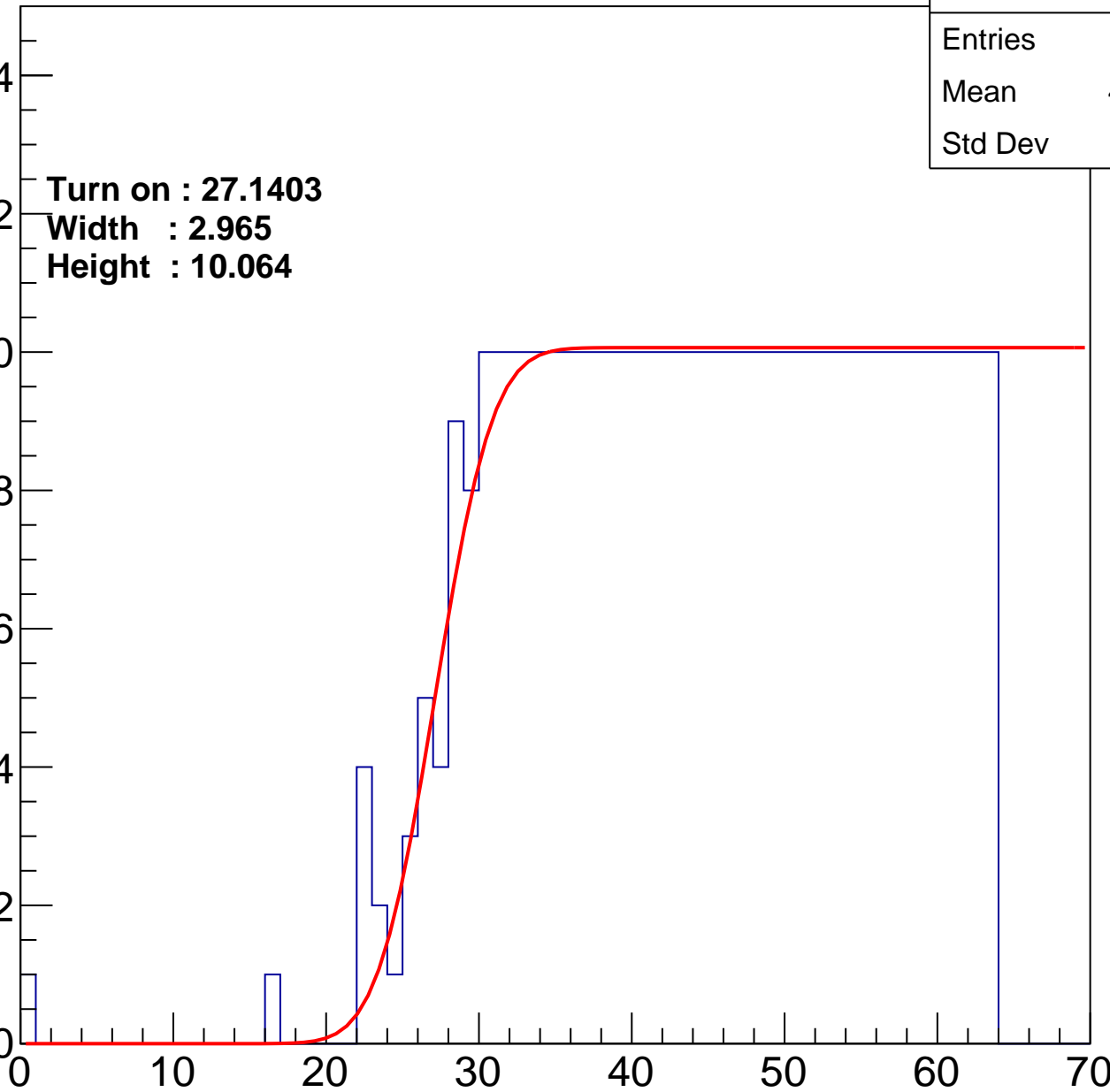
Width : 2.965

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch55

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.09
Std Dev	11.58

**Turn on : 25.8697**

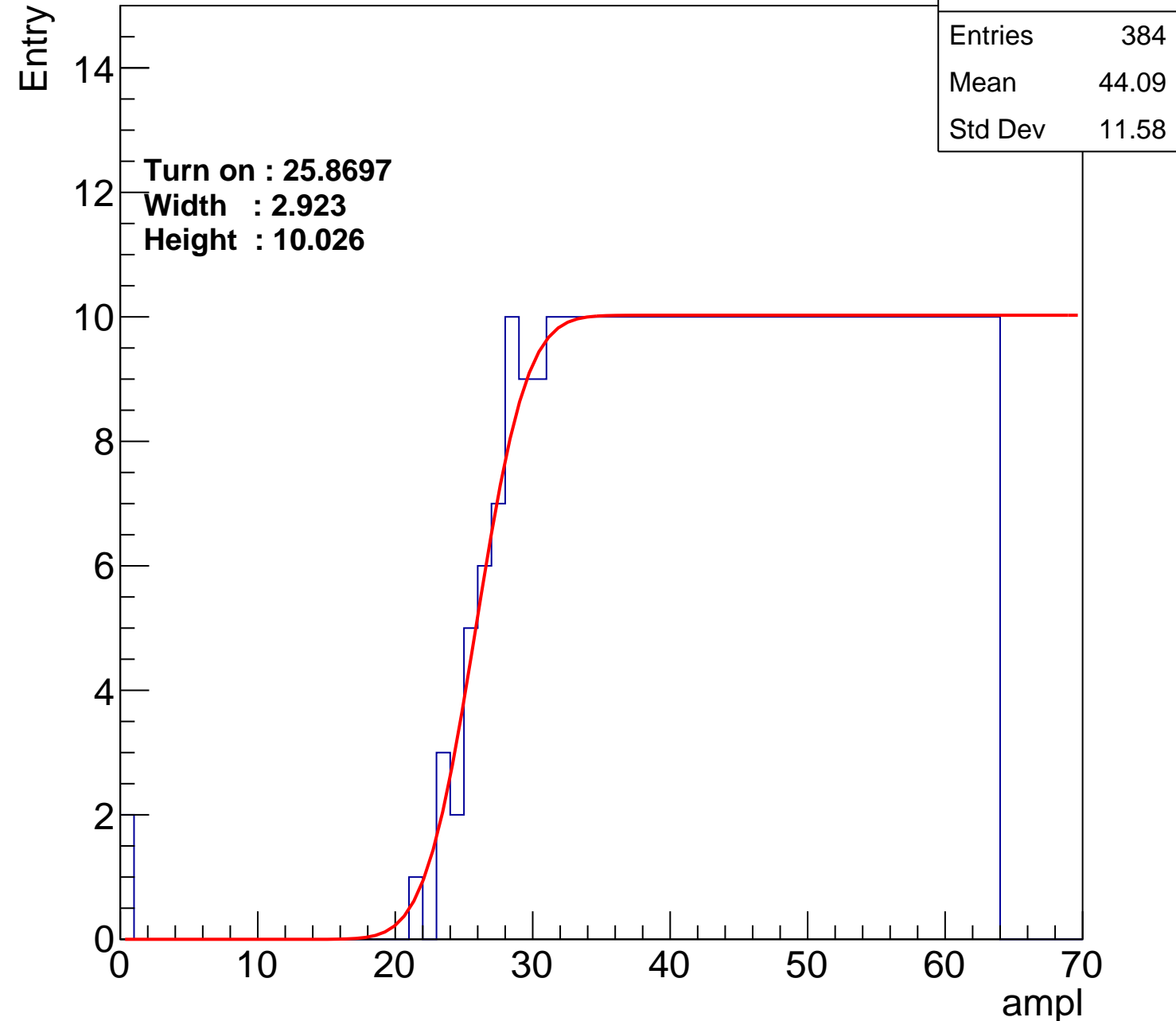
**Width : 2.923**

**Height : 10.026**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch56

calib\_packv5\_042523\_0143.root, FC#5, port B1

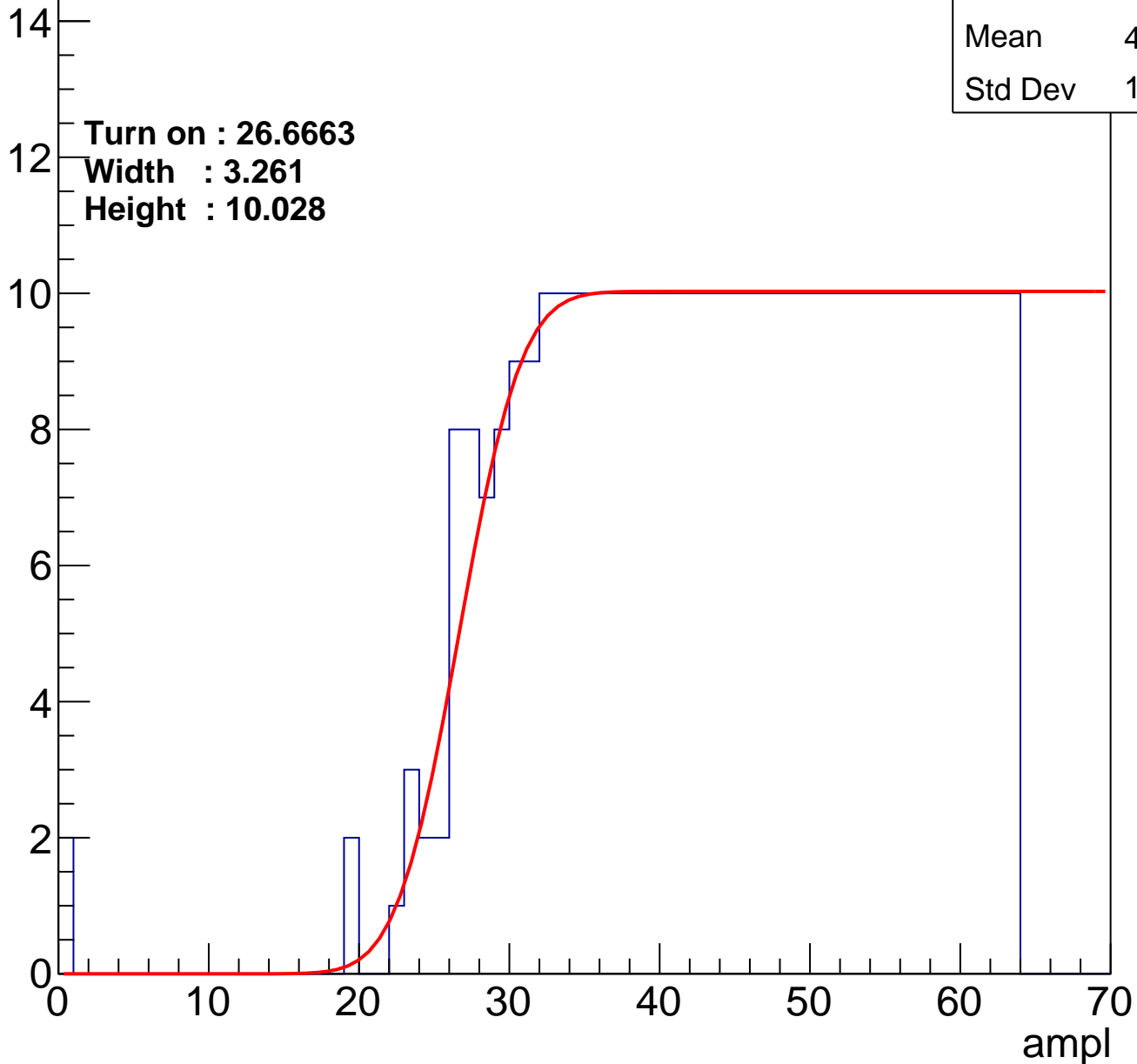
Entries	381
Mean	44.17
Std Dev	11.62

Turn on : 26.6663

Width : 3.261

Height : 10.028

Entry



B0L000S, U14-ch57

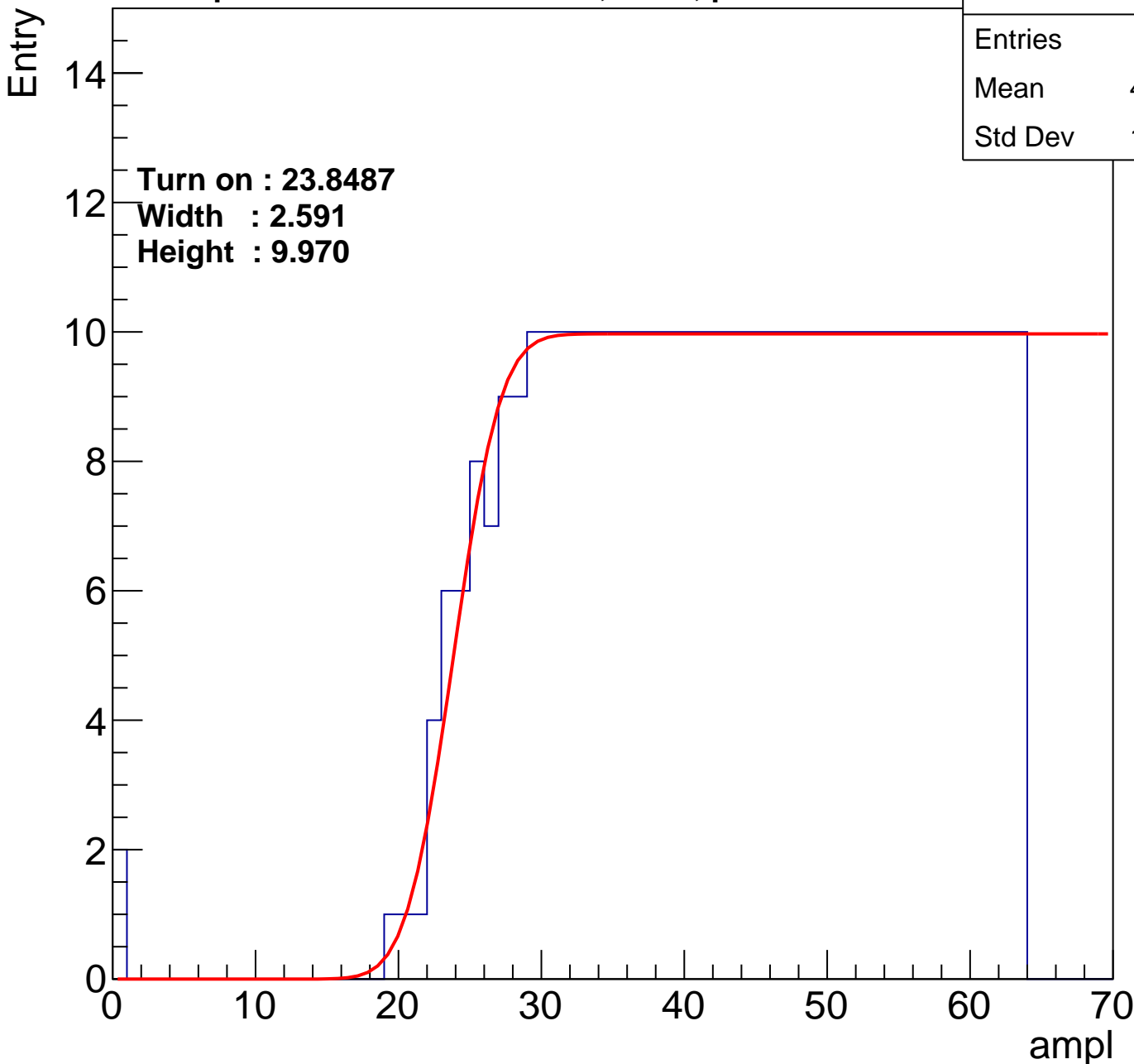
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	404
Mean	43.09
Std Dev	12.13

**Turn on : 23.8487**

**Width : 2.591**

**Height : 9.970**



# B0L000S, U14-ch58

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.85
Std Dev	11.63

Turn on : 25.5051

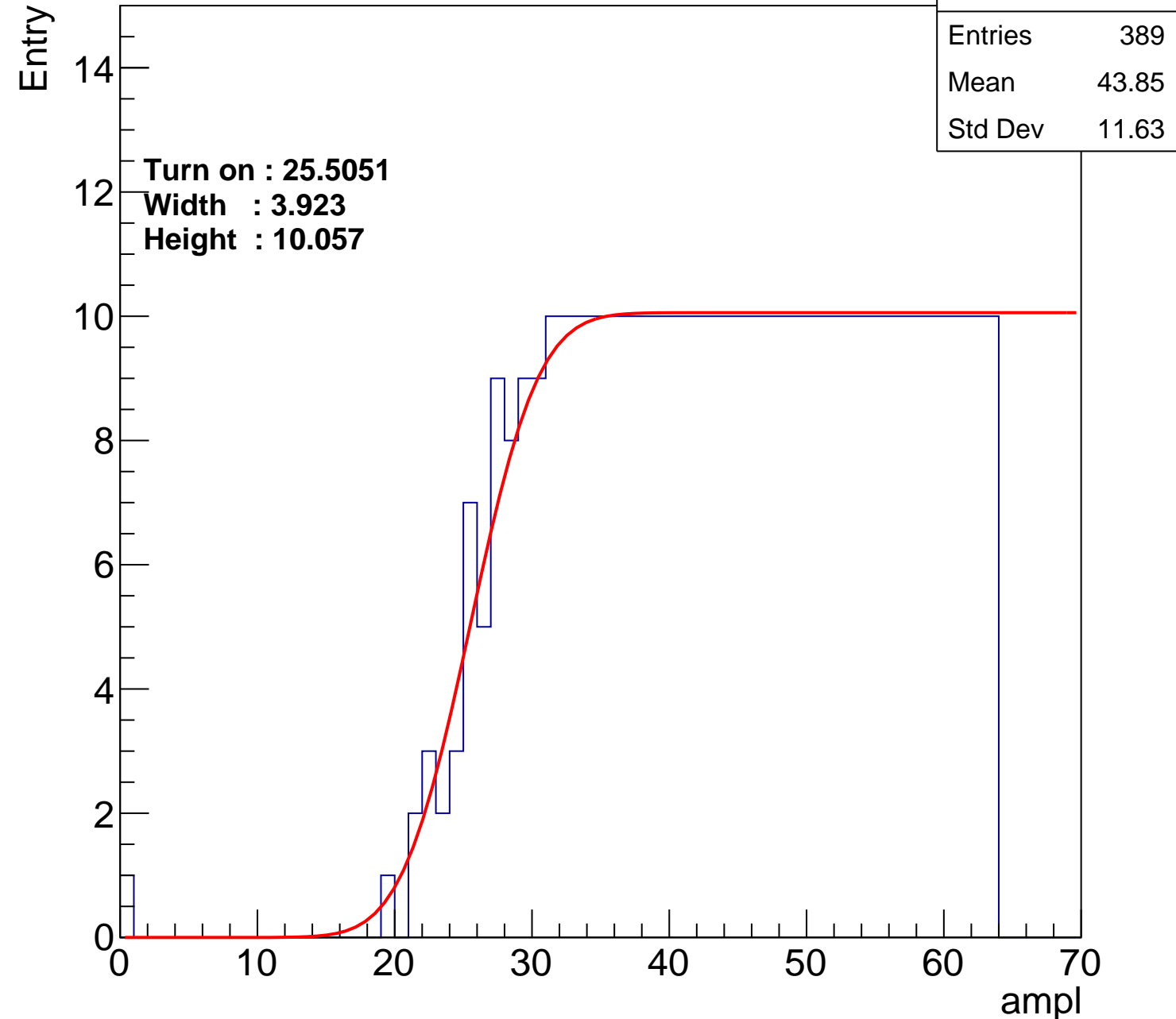
Width : 3.923

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch59

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.6629

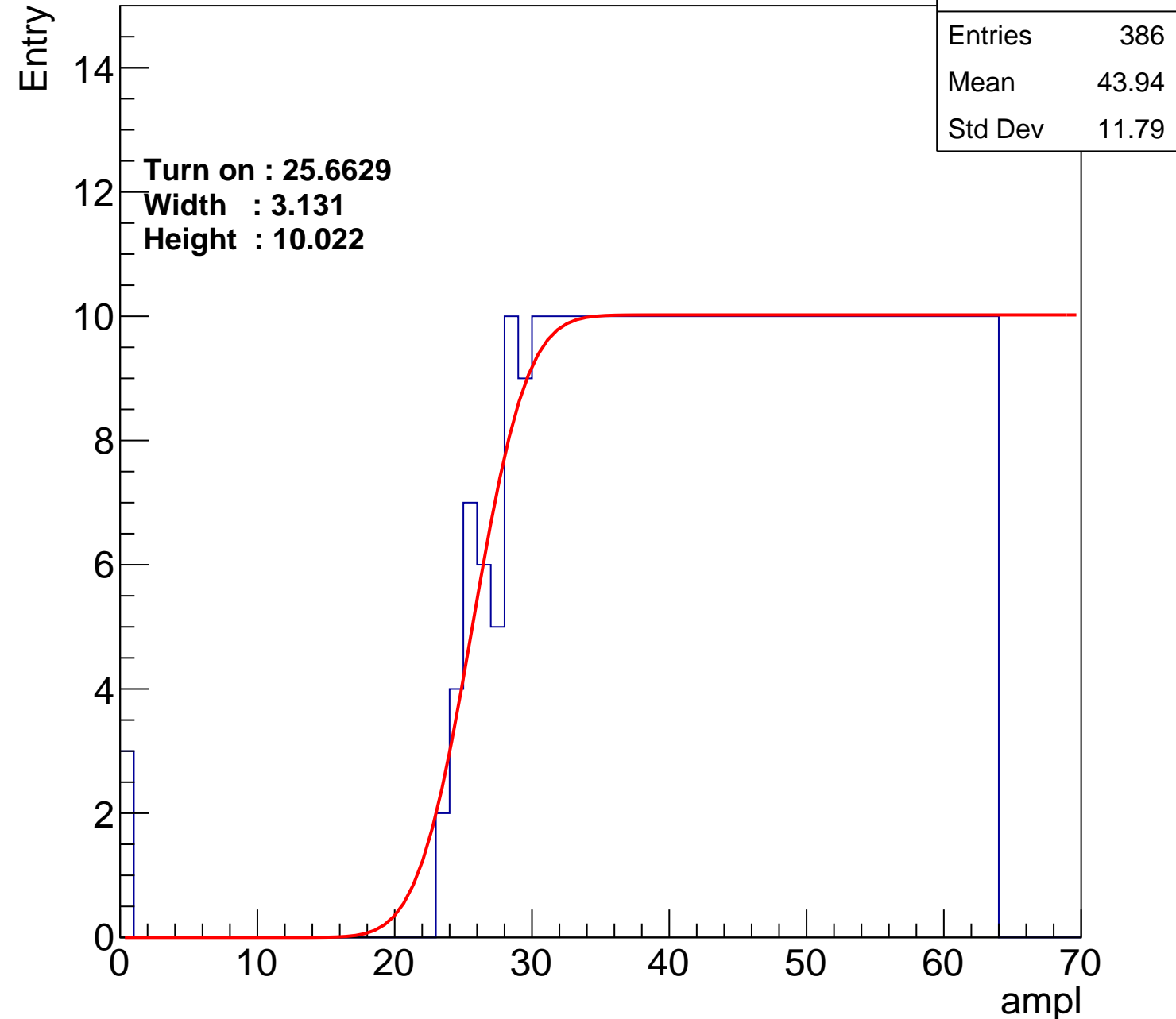
Width : 3.131

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch60

calib\_packv5\_042523\_0143.root, FC#5, port B1

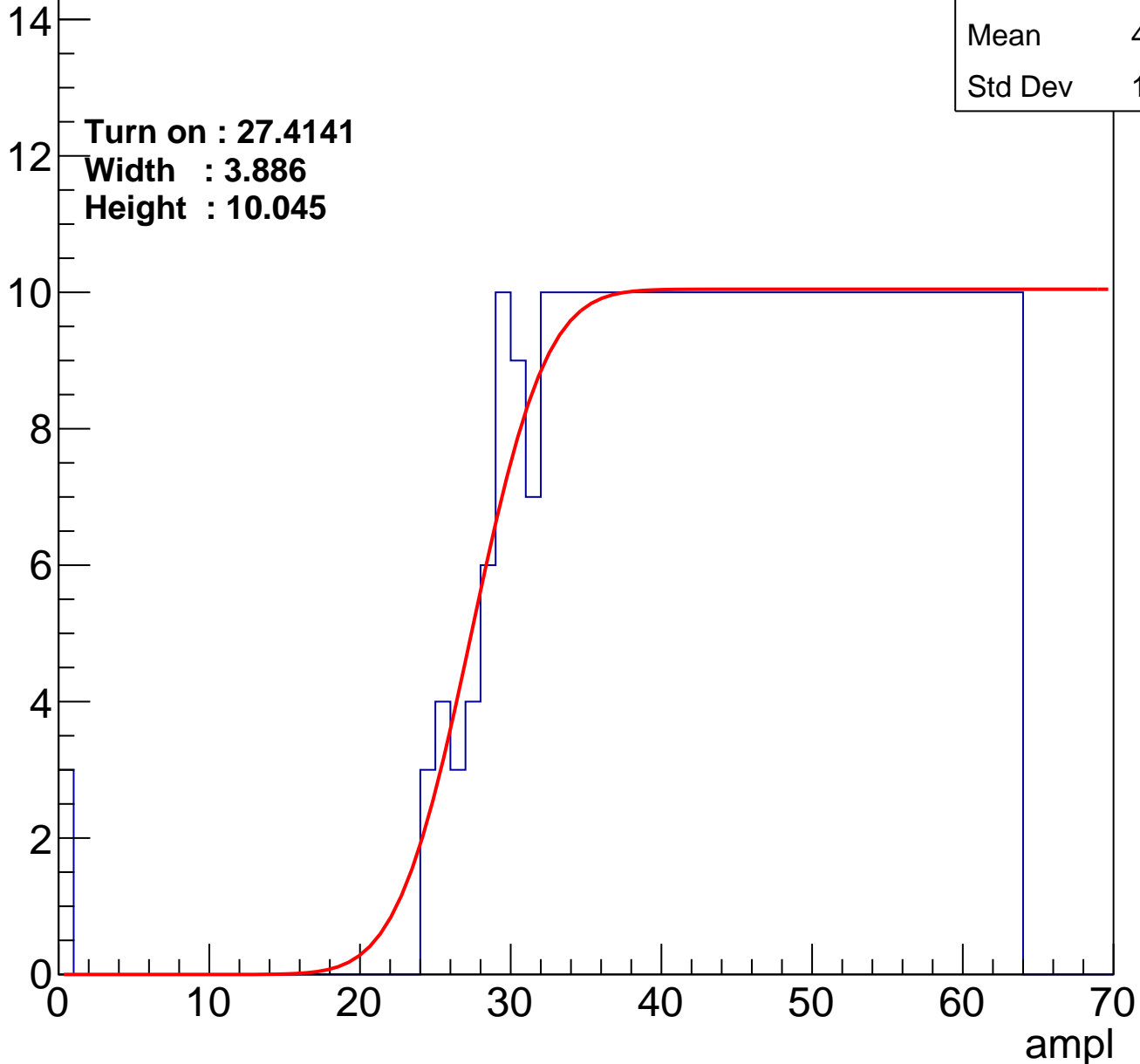
Entries	369
Mean	44.72
Std Dev	11.44

Turn on : 27.4141

Width : 3.886

Height : 10.045

Entry



# B0L000S, U14-ch61

calib\_packv5\_042523\_0143.root, FC#5, port B1

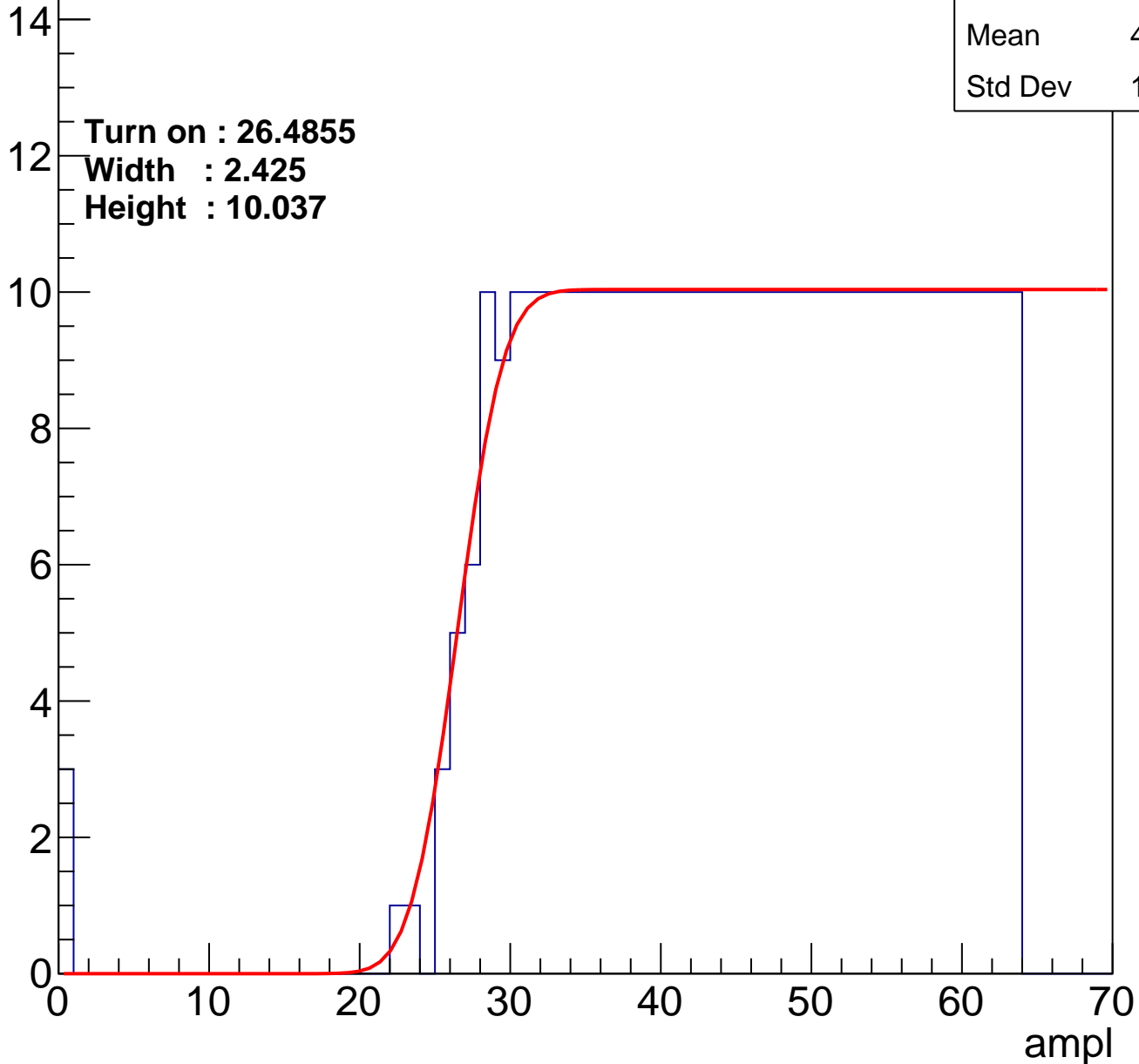
Entries	378
Mean	44.35
Std Dev	11.57

**Turn on : 26.4855**

**Width : 2.425**

**Height : 10.037**

Entry



# B0L000S, U14-ch62

calib\_packv5\_042523\_0143.root, FC#5, port B1

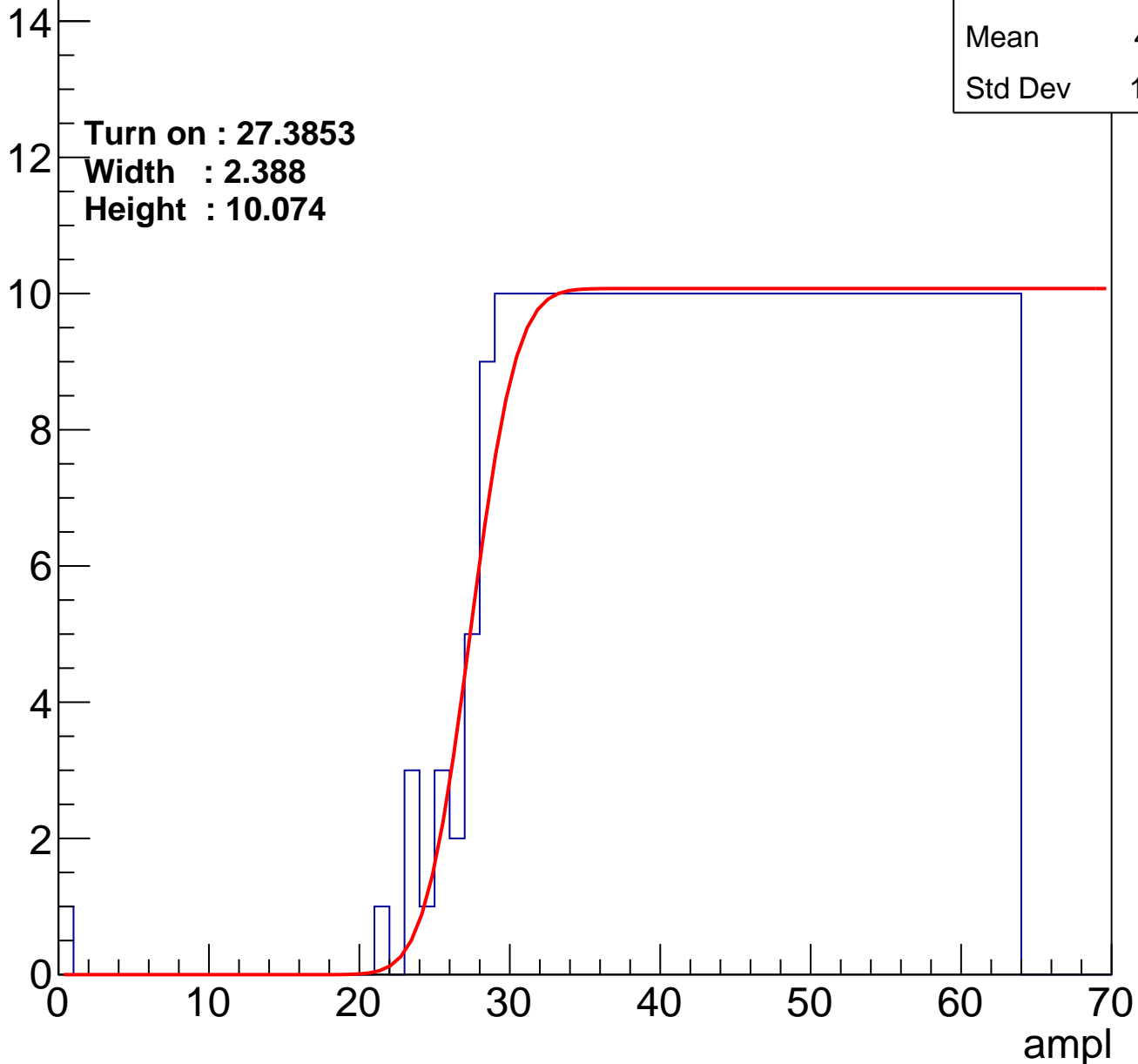
Entries	375
Mean	44.61
Std Dev	11.15

**Turn on : 27.3853**

**Width : 2.388**

**Height : 10.074**

Entry





# B0L000S, U14-ch63

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.76
Std Dev	12.19

Turn on : 25.7759

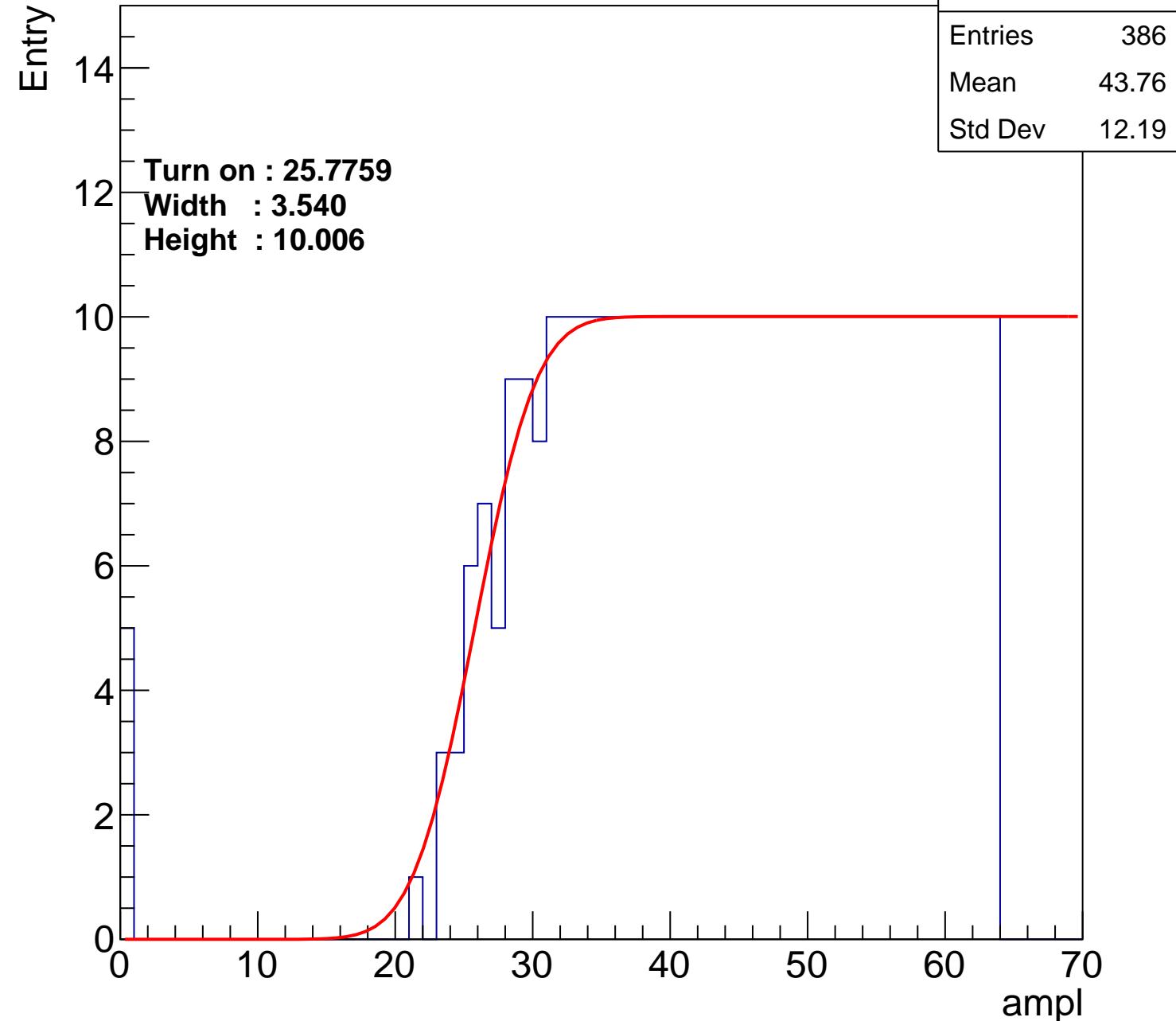
Width : 3.540

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch64

calib\_packv5\_042523\_0143.root, FC#5, port B1

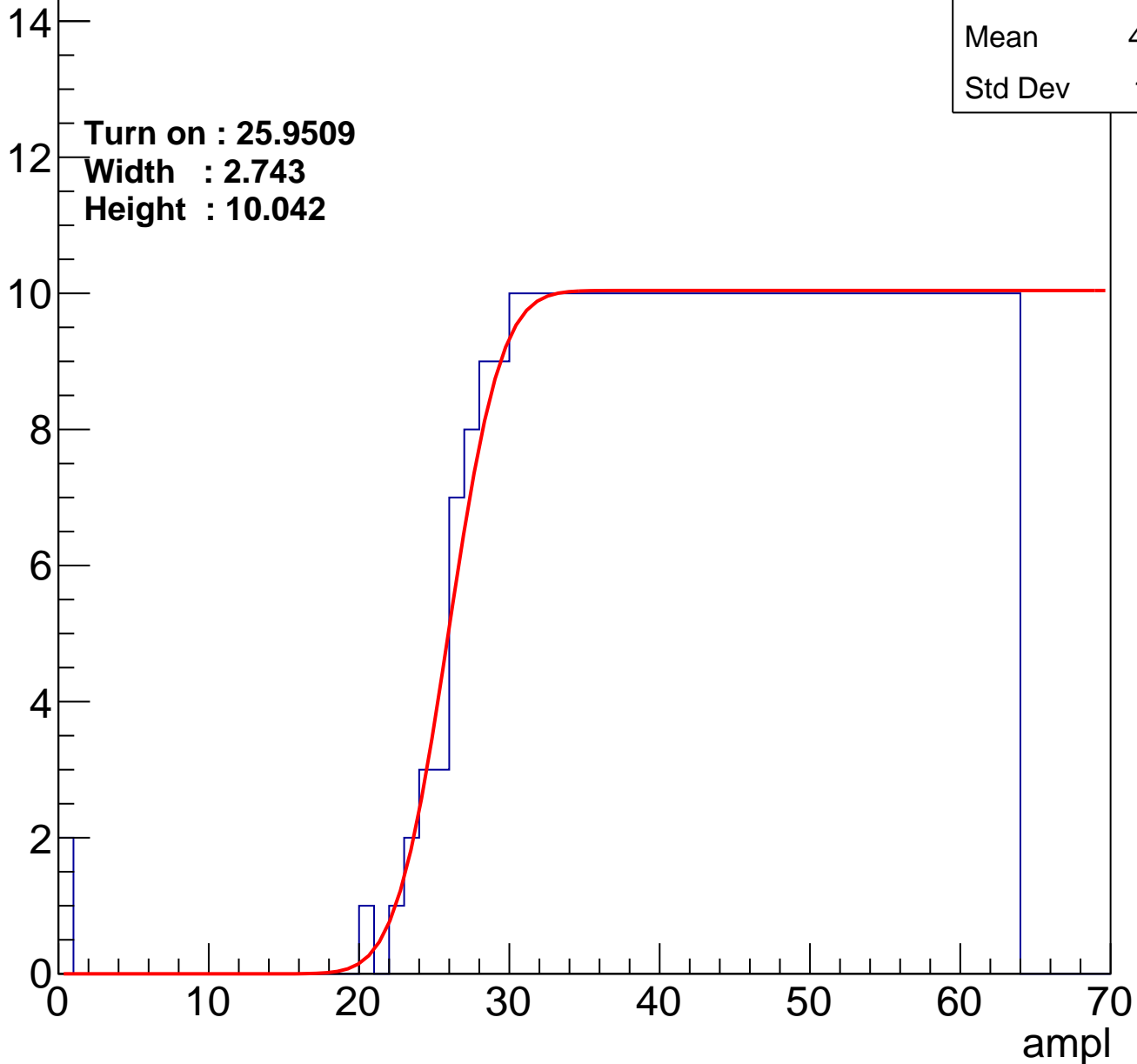
Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.9509

Width : 2.743

Height : 10.042

Entry



# B0L000S, U14-ch65

calib\_packv5\_042523\_0143.root, FC#5, port B1

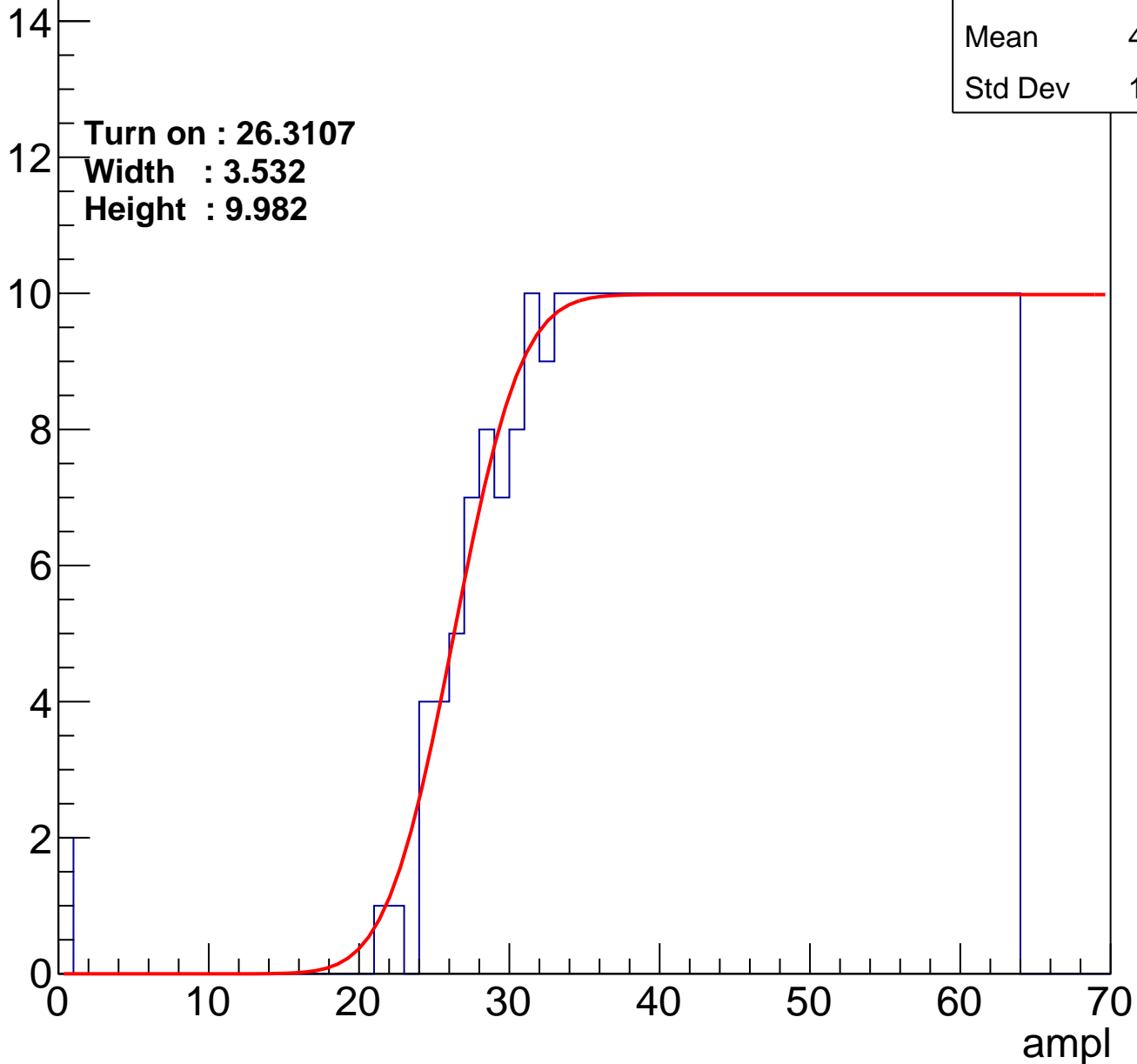
Entries	376
Mean	44.42
Std Dev	11.47

**Turn on : 26.3107**

**Width : 3.532**

**Height : 9.982**

Entry



# B0L000S, U14-ch66

calib\_packv5\_042523\_0143.root, FC#5, port B1

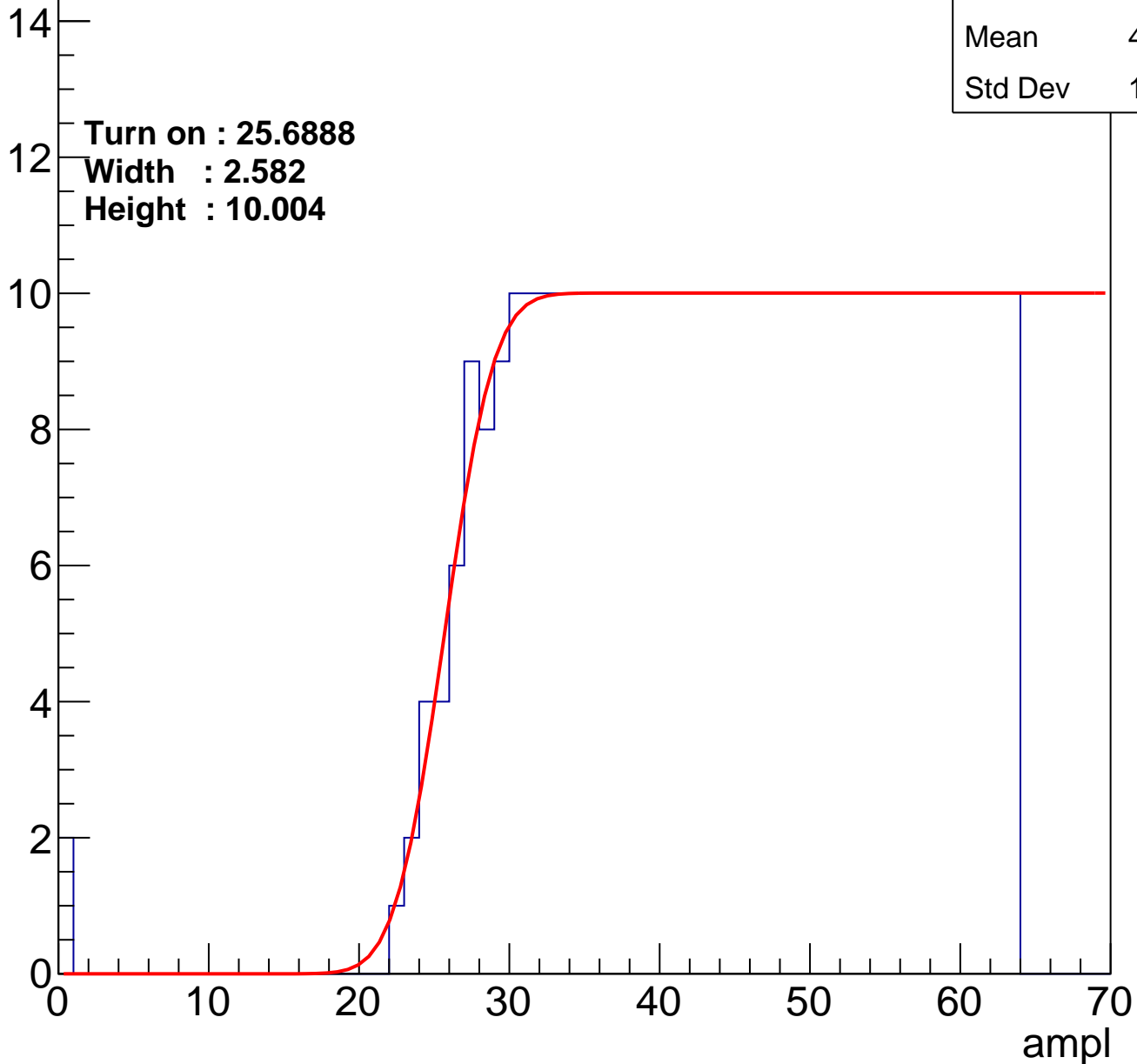
Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.6888

Width : 2.582

Height : 10.004

Entry



# B0L000S, U14-ch67

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.81
Std Dev	11.41

Turn on : 27.3331

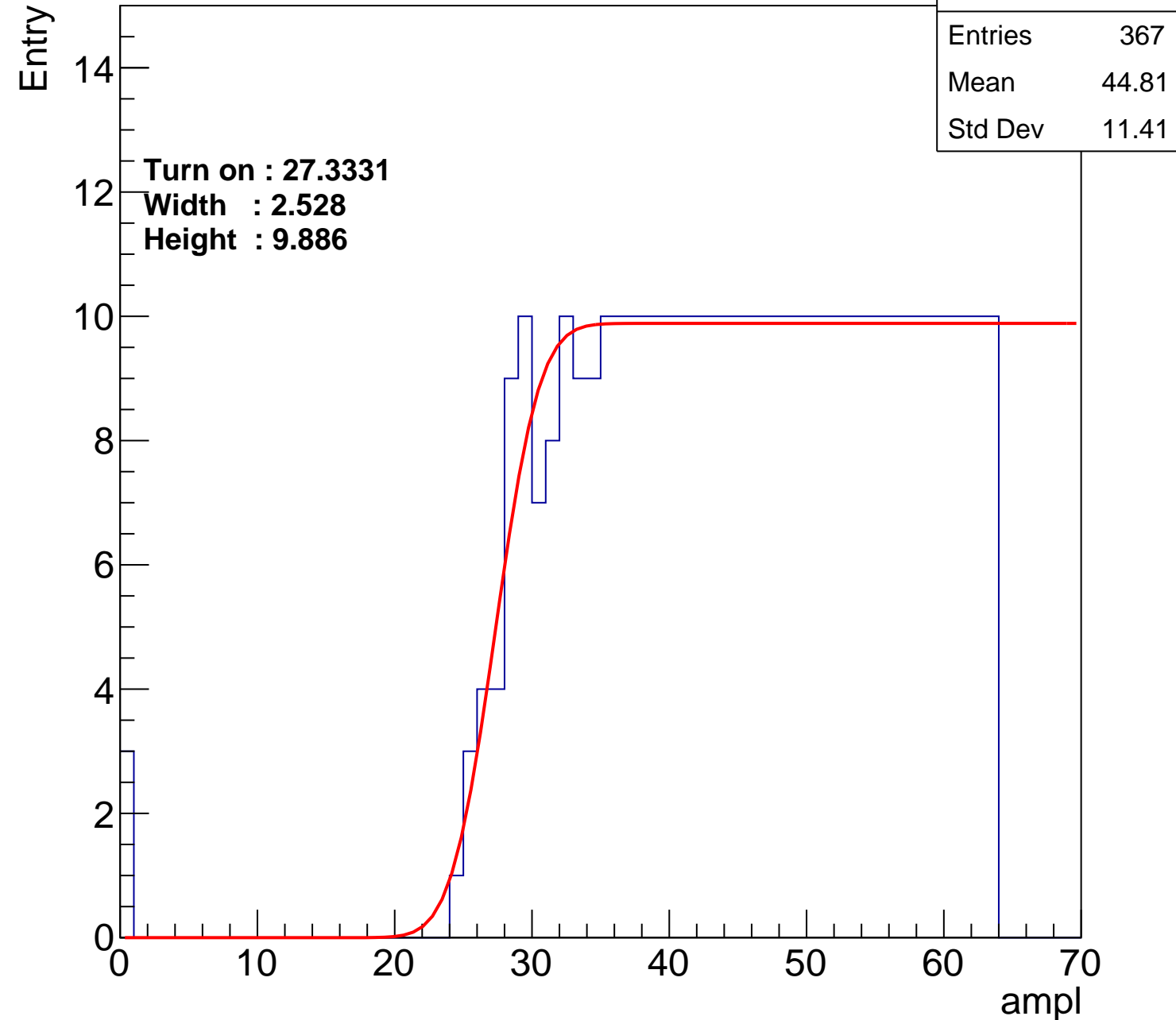
Width : 2.528

Height : 9.886

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch68

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.2276

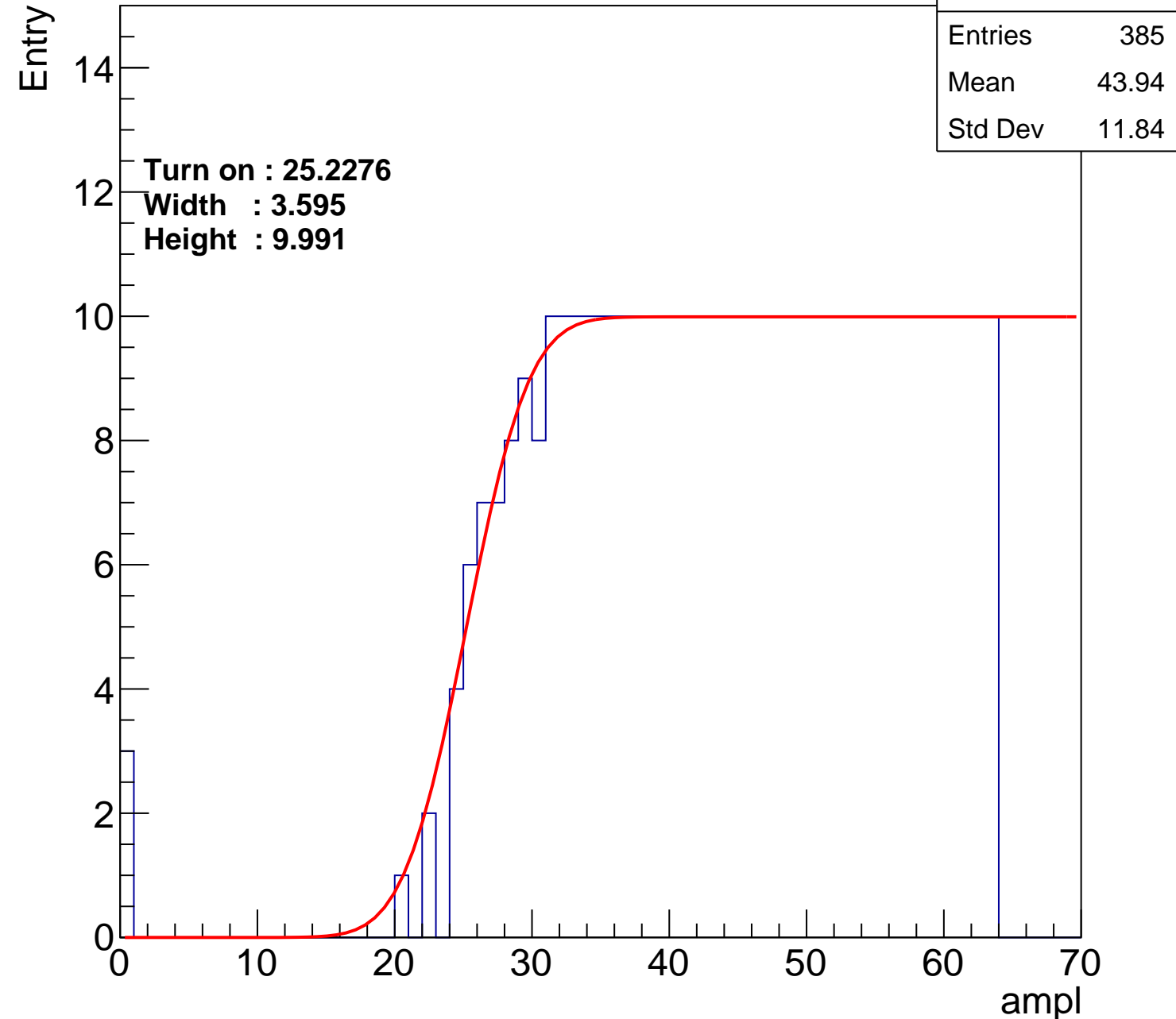
Width : 3.595

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch69

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.23
Std Dev	11.89

Turn on : 27.3314

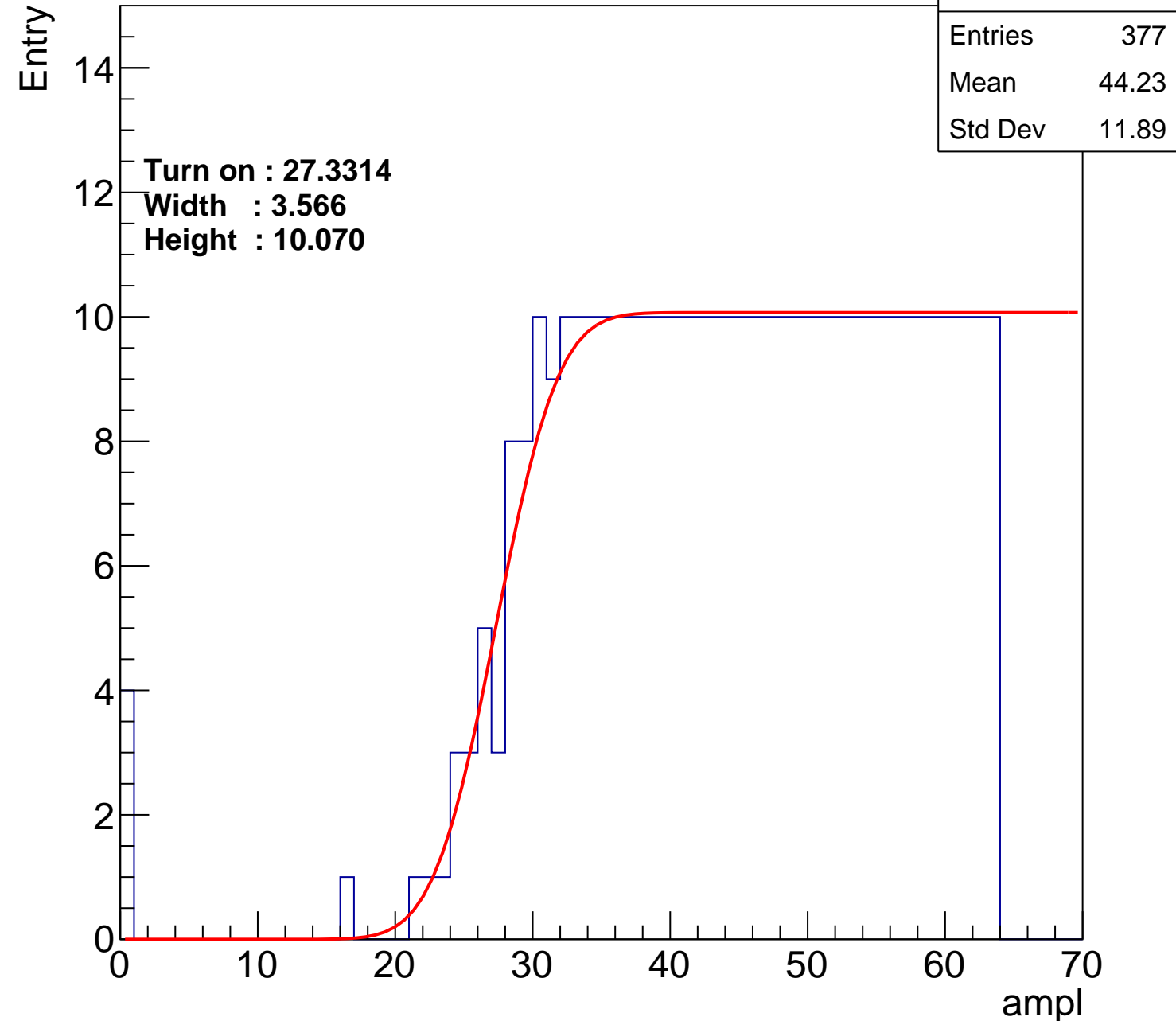
Width : 3.566

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch70

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.59
Std Dev	11.83

**Turn on : 27.4659**

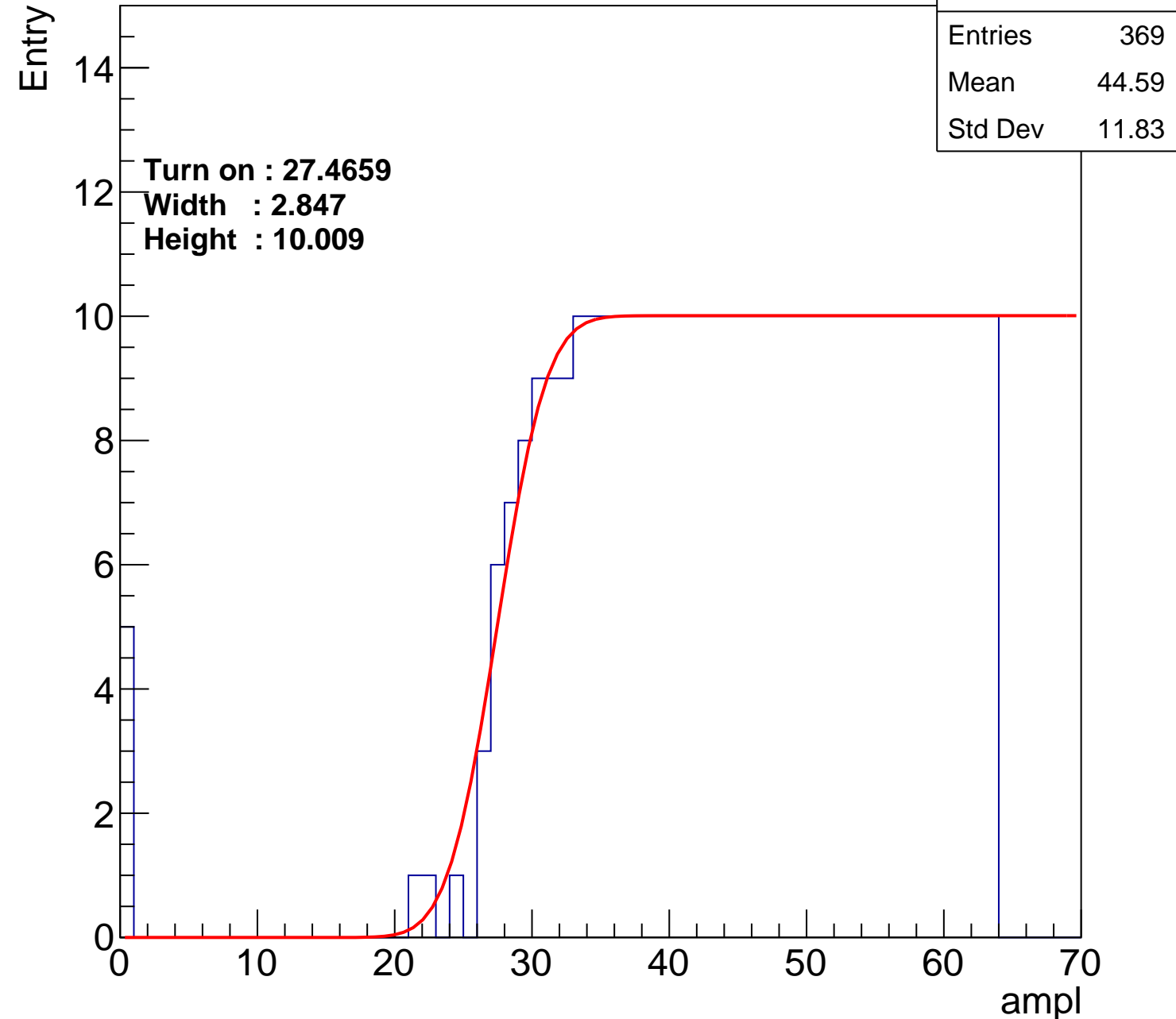
**Width : 2.847**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch71

calib\_packv5\_042523\_0143.root, FC#5, port B1

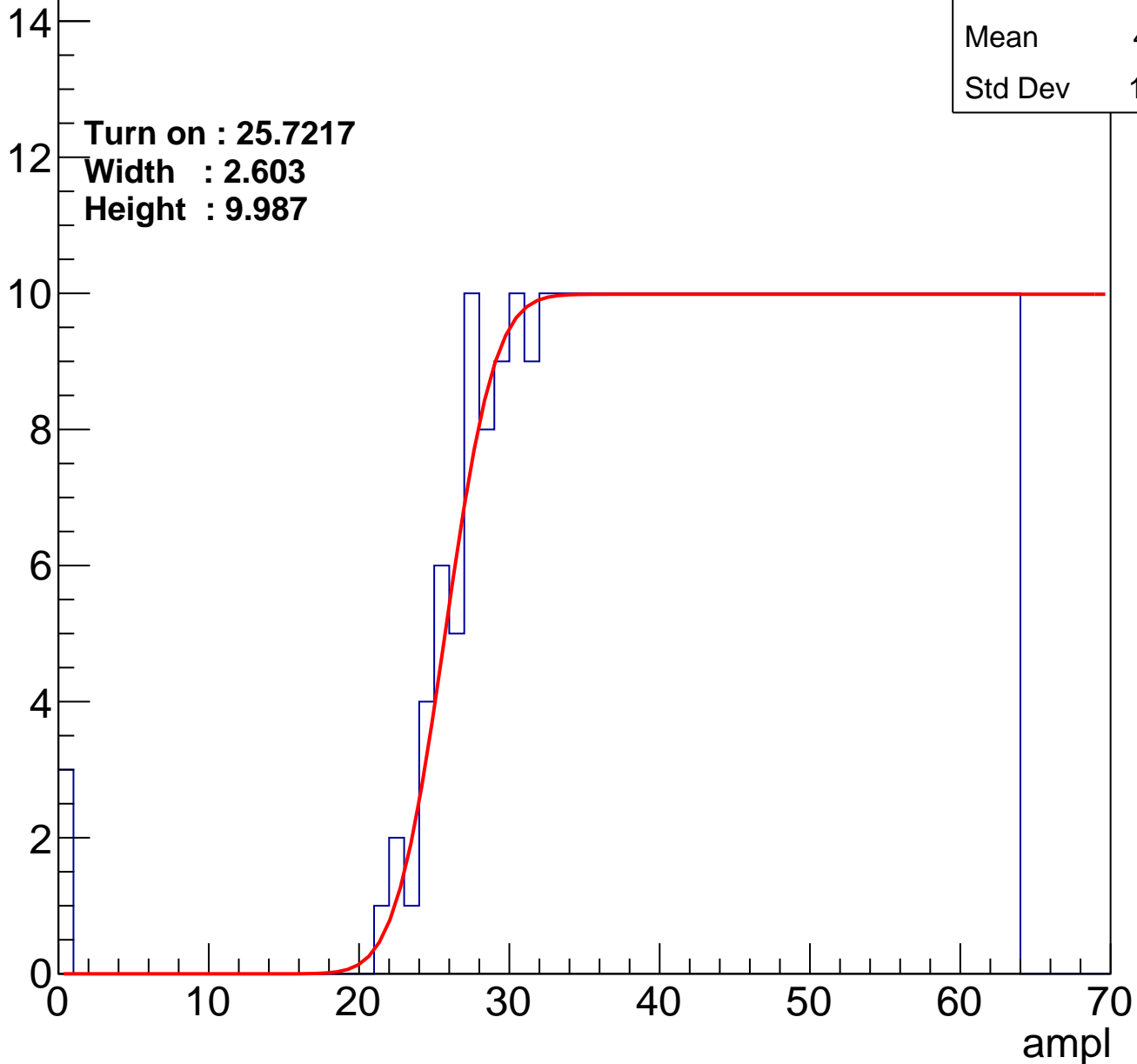
Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.7217

Width : 2.603

Height : 9.987

Entry



# B0L000S, U14-ch72

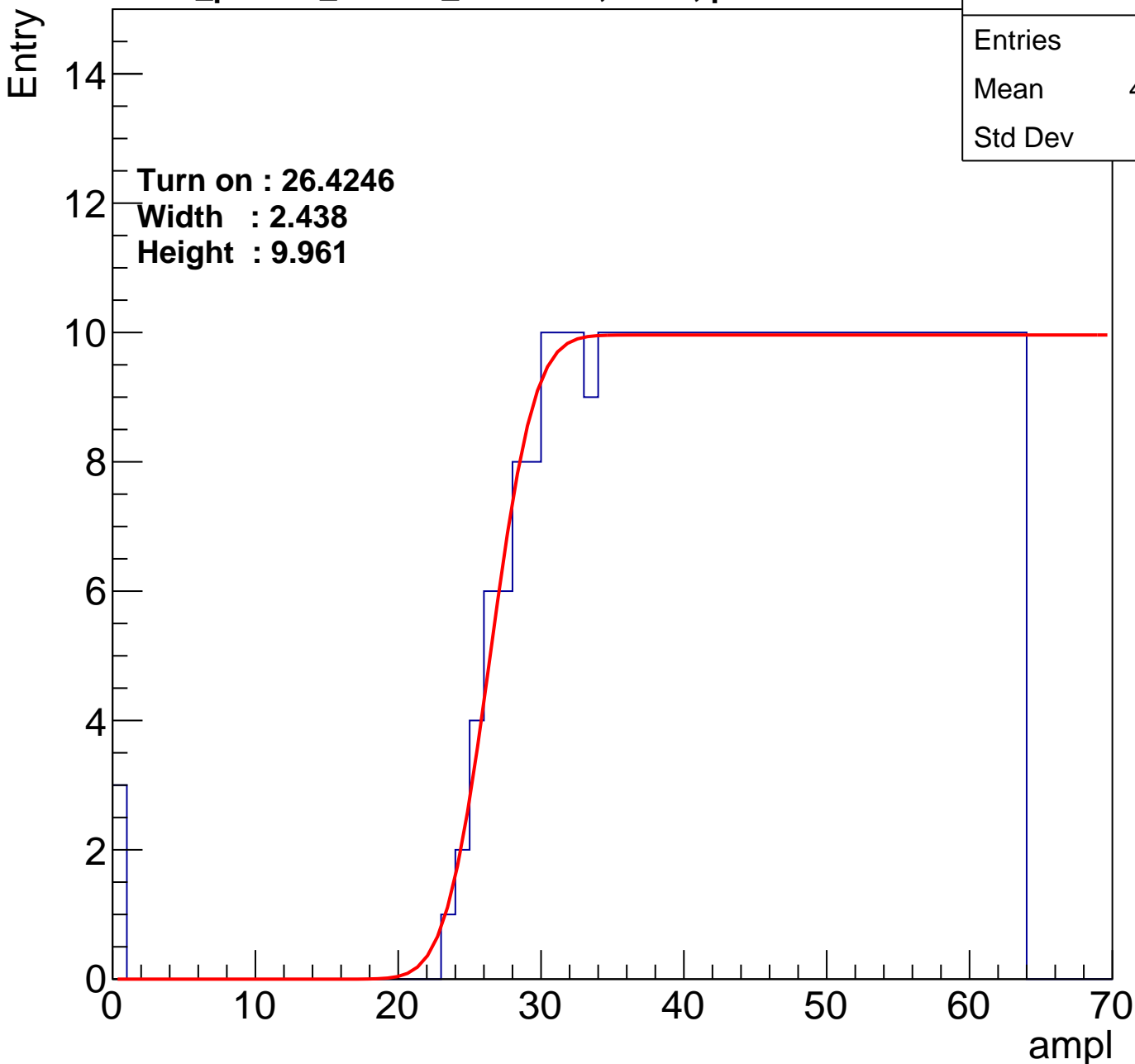
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	377
Mean	44.36
Std Dev	11.6

**Turn on : 26.4246**

**Width : 2.438**

**Height : 9.961**



# B0L000S, U14-ch73

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.34
Std Dev	12.48

Turn on : 25.4288

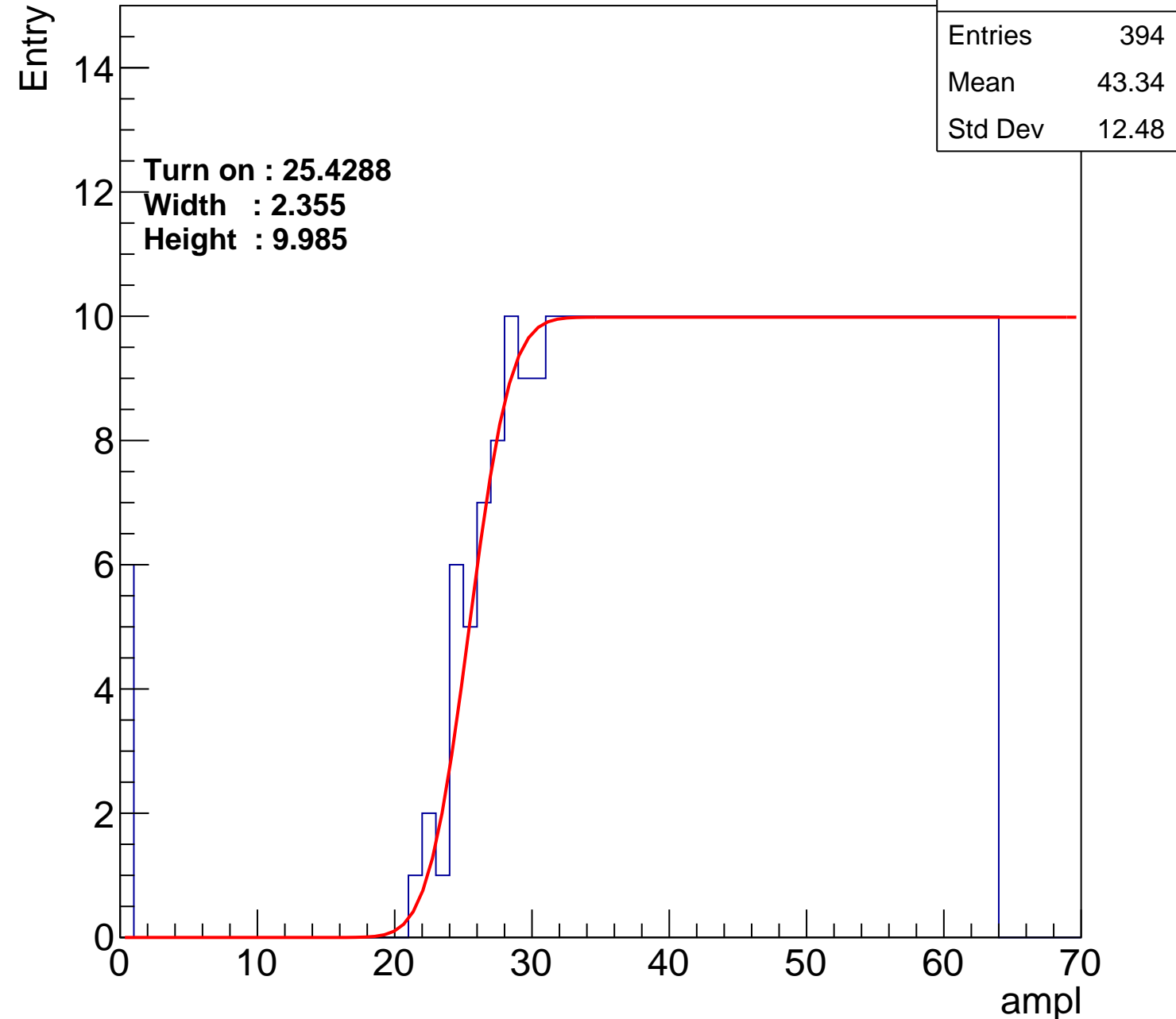
Width : 2.355

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch74

calib\_packv5\_042523\_0143.root, FC#5, port B1

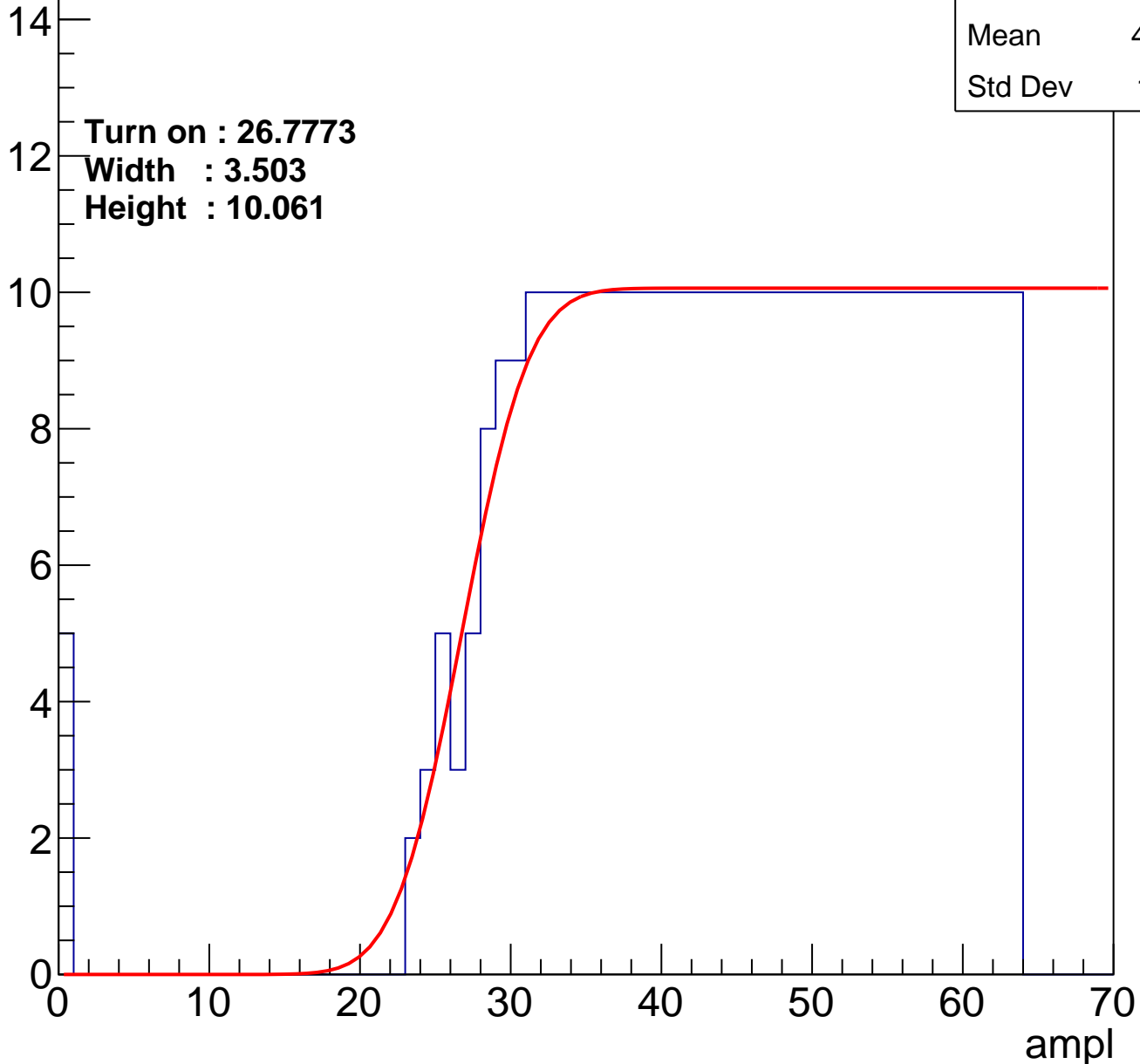
Entries	379
Mean	44.12
Std Dev	12.01

Turn on : 26.7773

Width : 3.503

Height : 10.061

Entry



# B0L000S, U14-ch75

calib\_packv5\_042523\_0143.root, FC#5, port B1

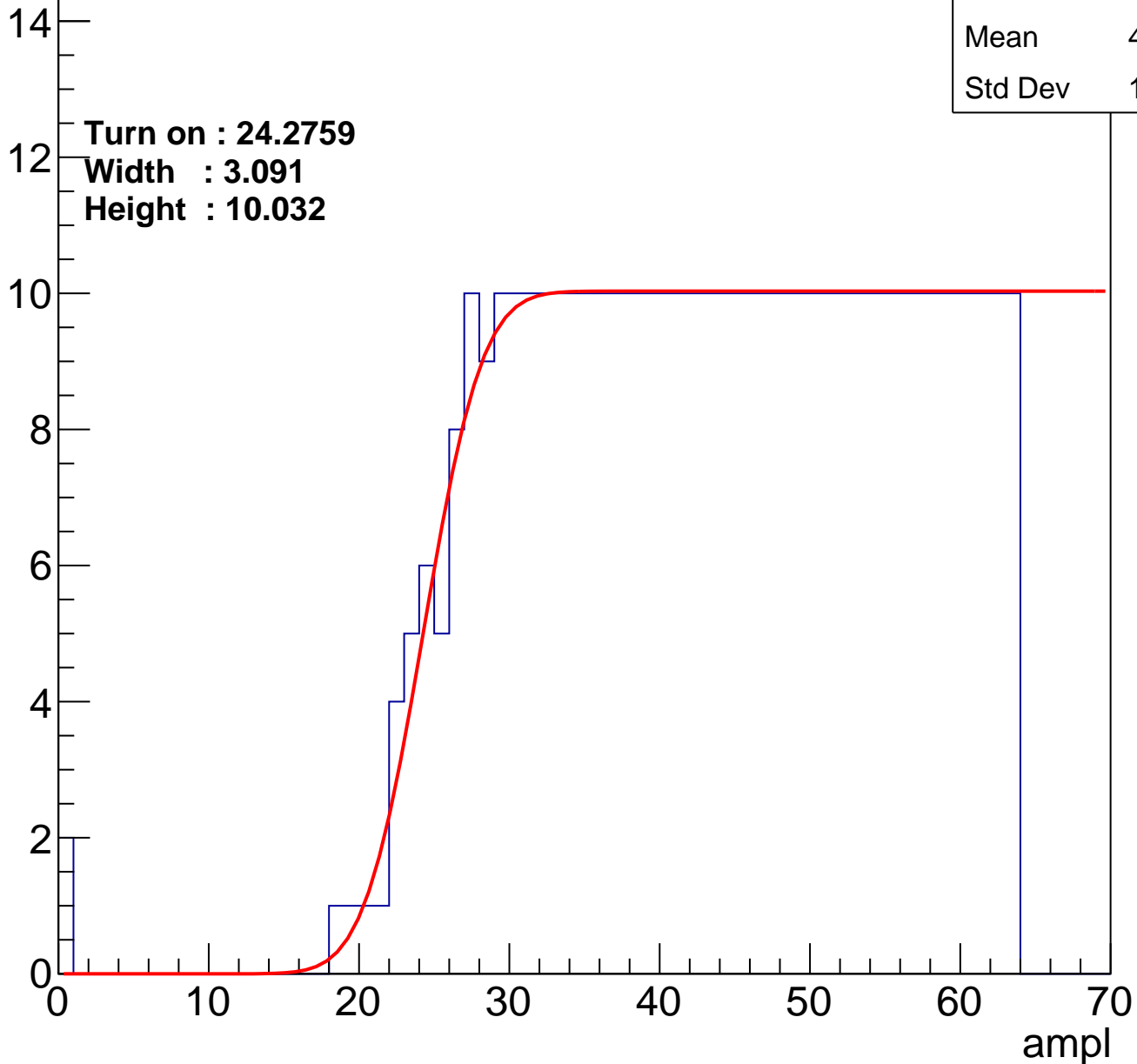
Entries	403
Mean	43.13
Std Dev	12.12

Turn on : 24.2759

Width : 3.091

Height : 10.032

Entry



# B0L000S, U14-ch76

calib\_packv5\_042523\_0143.root, FC#5, port B1

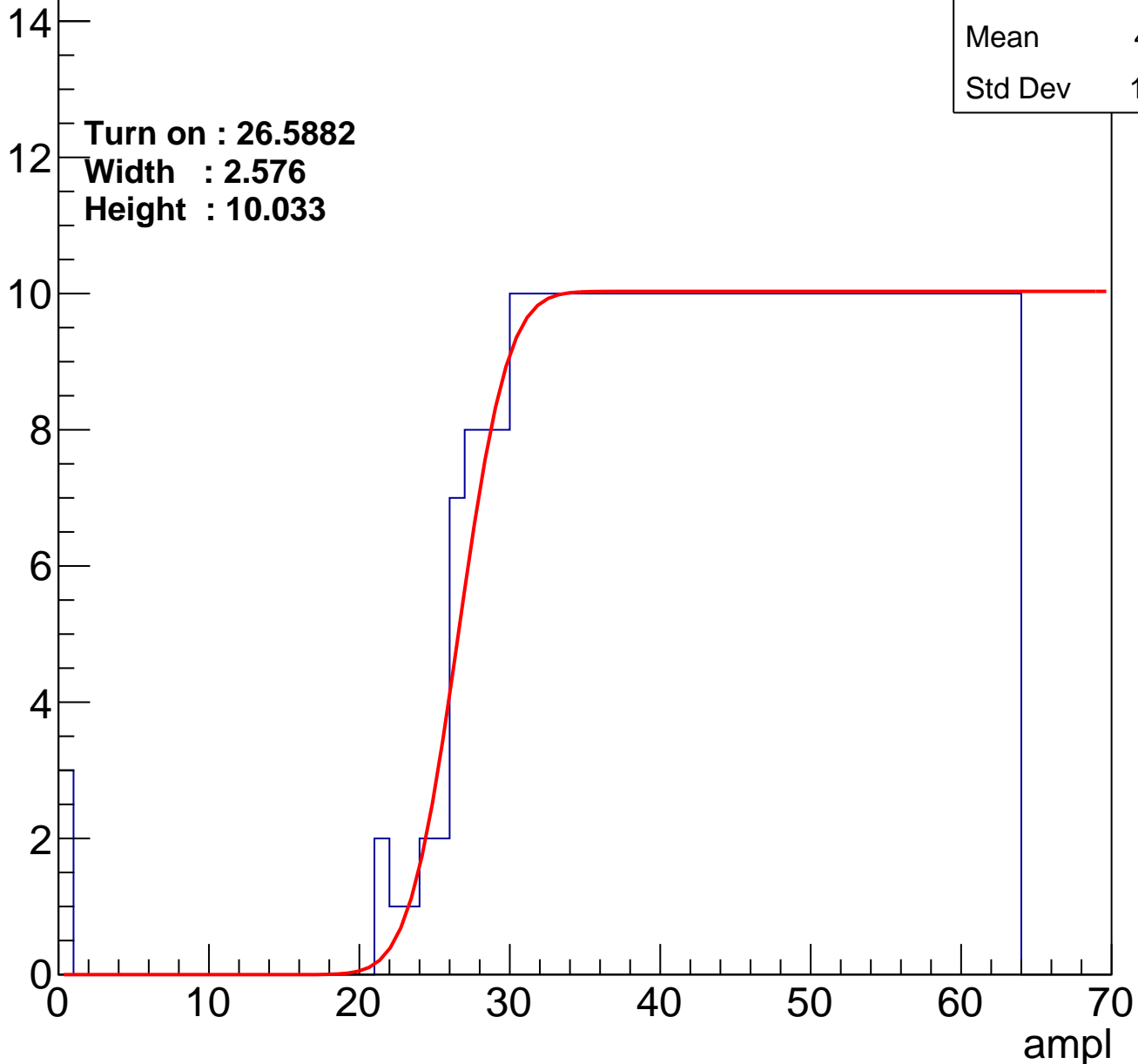
Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.5882

Width : 2.576

Height : 10.033

Entry



# B0L000S, U14-ch77

calib\_packv5\_042523\_0143.root, FC#5, port B1

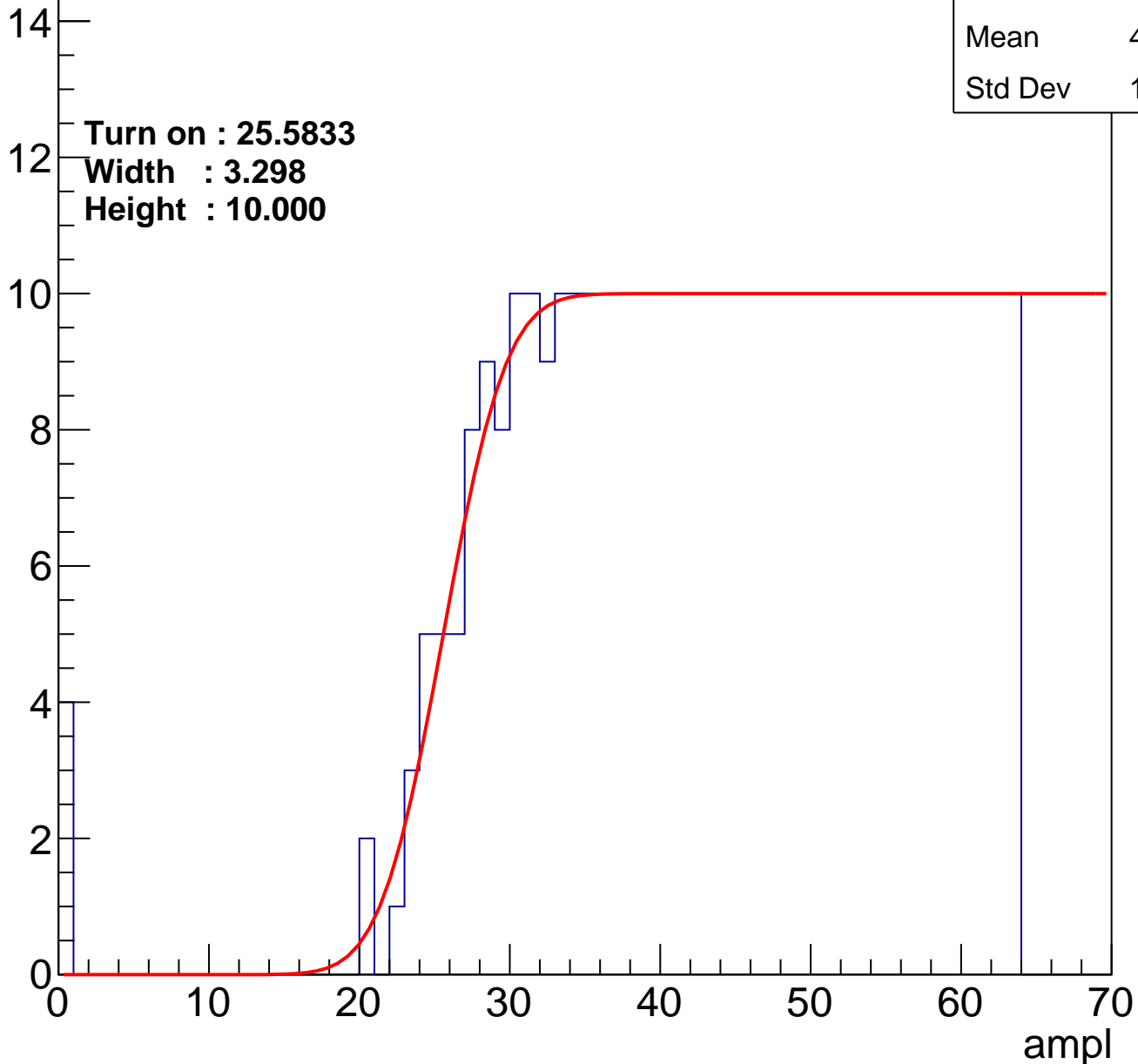
Entries	389
Mean	43.66
Std Dev	12.13

Turn on : 25.5833

Width : 3.298

Height : 10.000

Entry



# B0L000S, U14-ch78

calib\_packv5\_042523\_0143.root, FC#5, port B1

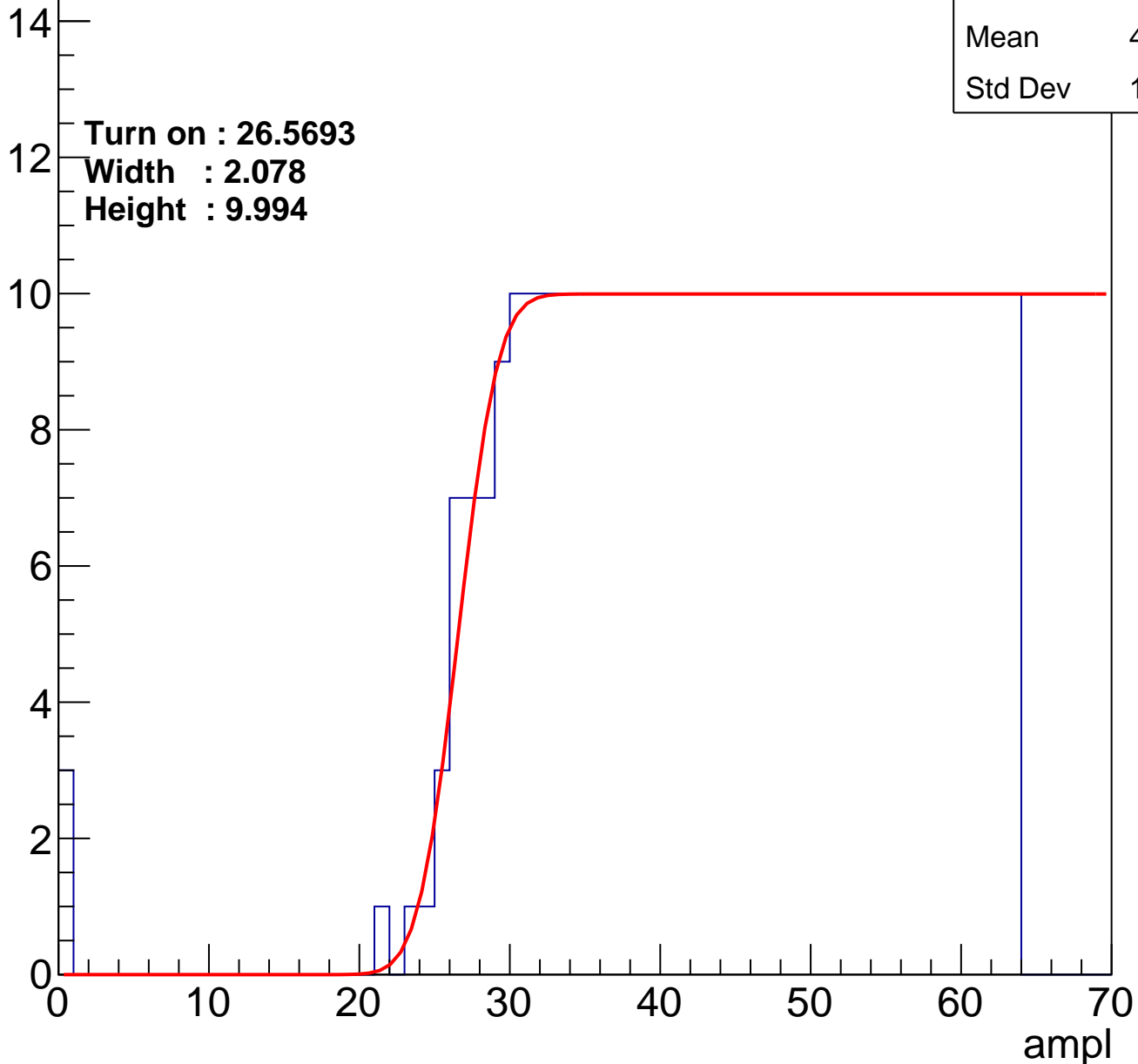
Entries	379
Mean	44.28
Std Dev	11.62

Turn on : 26.5693

Width : 2.078

Height : 9.994

Entry





# B0L000S, U14-ch79

calib\_packv5\_042523\_0143.root, FC#5, port B1

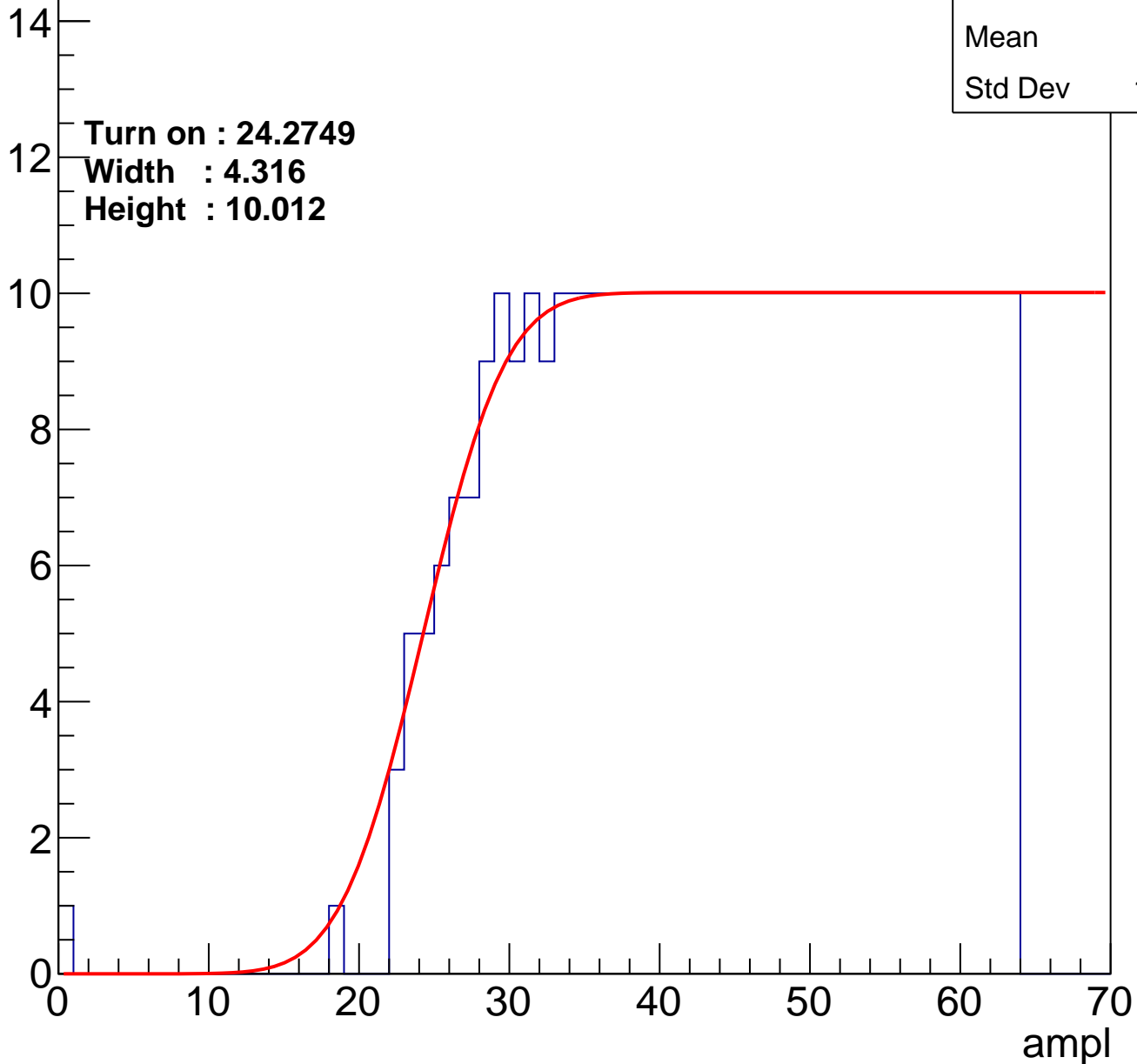
Entries	392
Mean	43.7
Std Dev	11.71

Turn on : 24.2749

Width : 4.316

Height : 10.012

Entry



# B0L000S, U14-ch80

calib\_packv5\_042523\_0143.root, FC#5, port B1

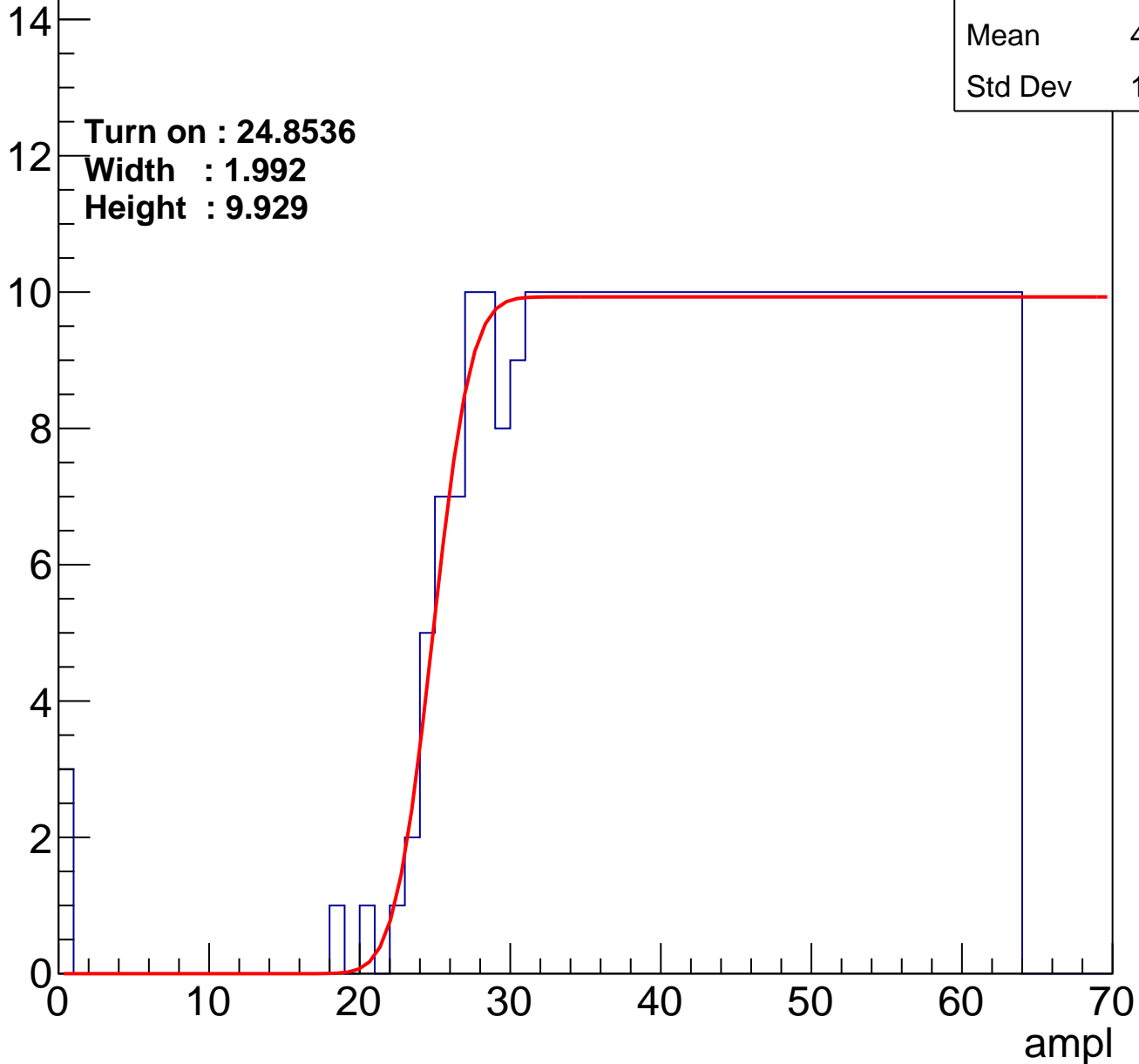
Entries	394
Mean	43.52
Std Dev	12.03

Turn on : 24.8536

Width : 1.992

Height : 9.929

Entry



# B0L000S, U14-ch81

calib\_packv5\_042523\_0143.root, FC#5, port B1

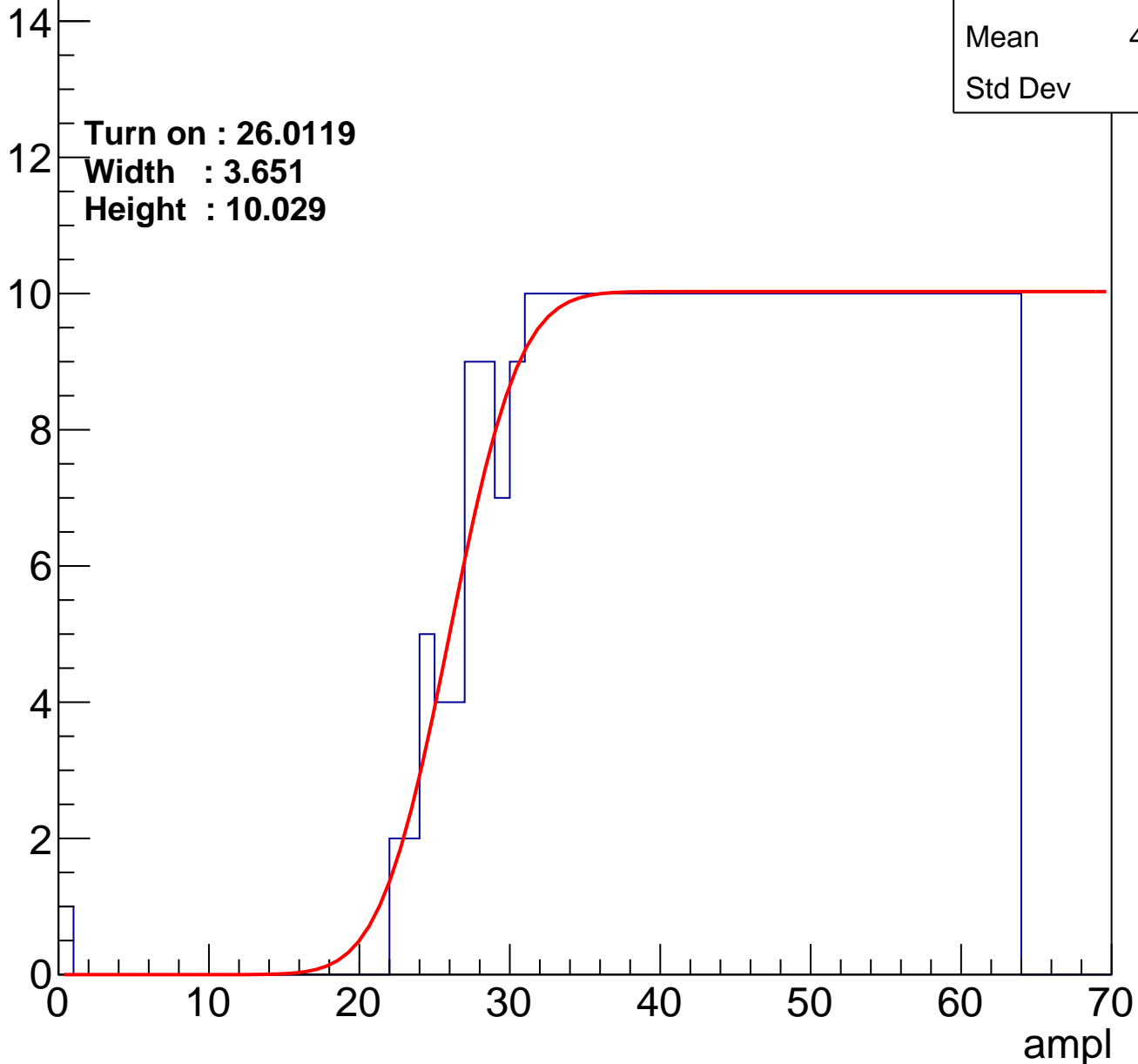
Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 26.0119

Width : 3.651

Height : 10.029

Entry



# B0L000S, U14-ch82

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	398
Mean	43.3
Std Dev	12.17

**Turn on : 24.5659**

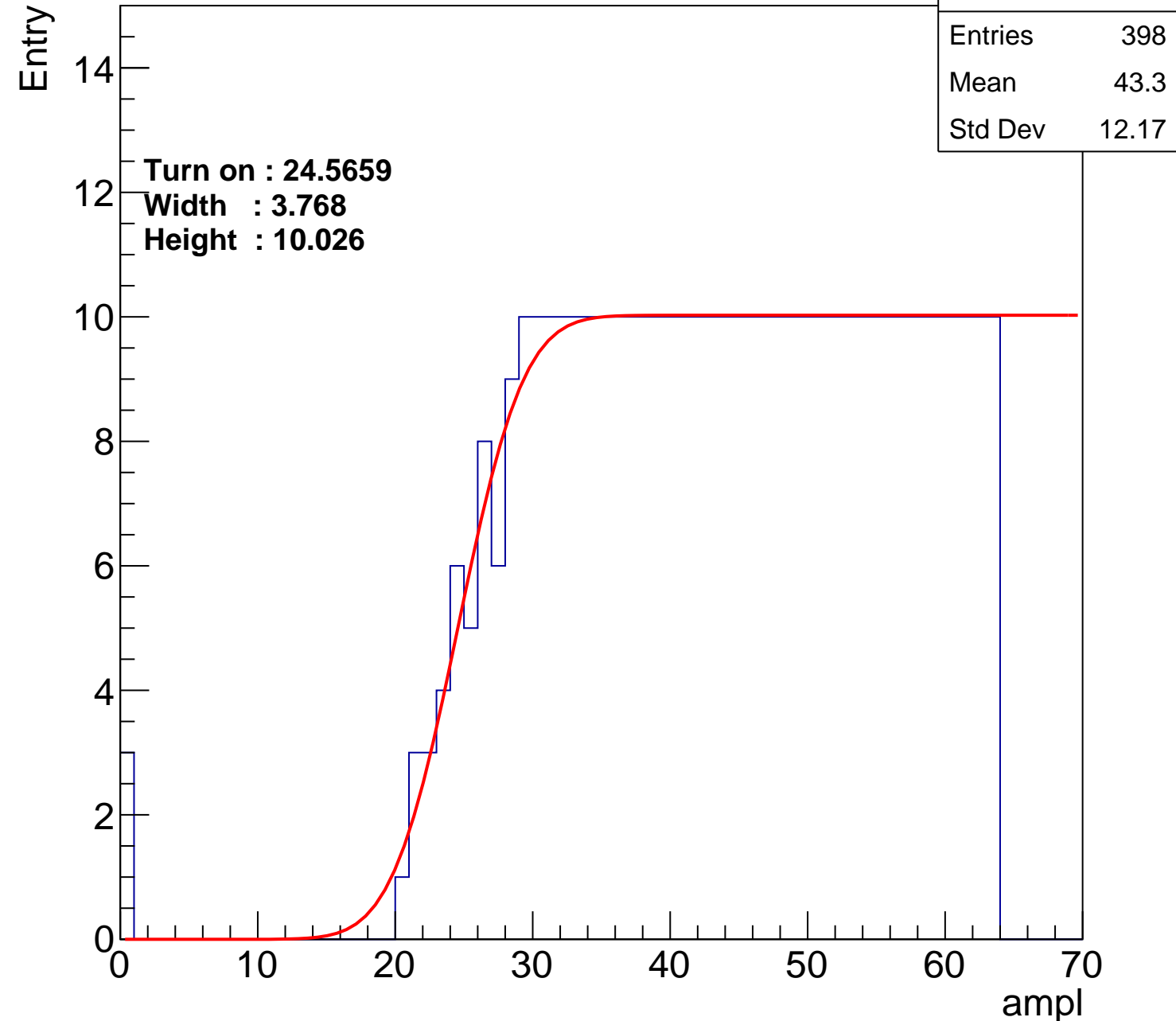
**Width : 3.768**

**Height : 10.026**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch83

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.8
Std Dev	11.56

**Turn on : 28.1107**

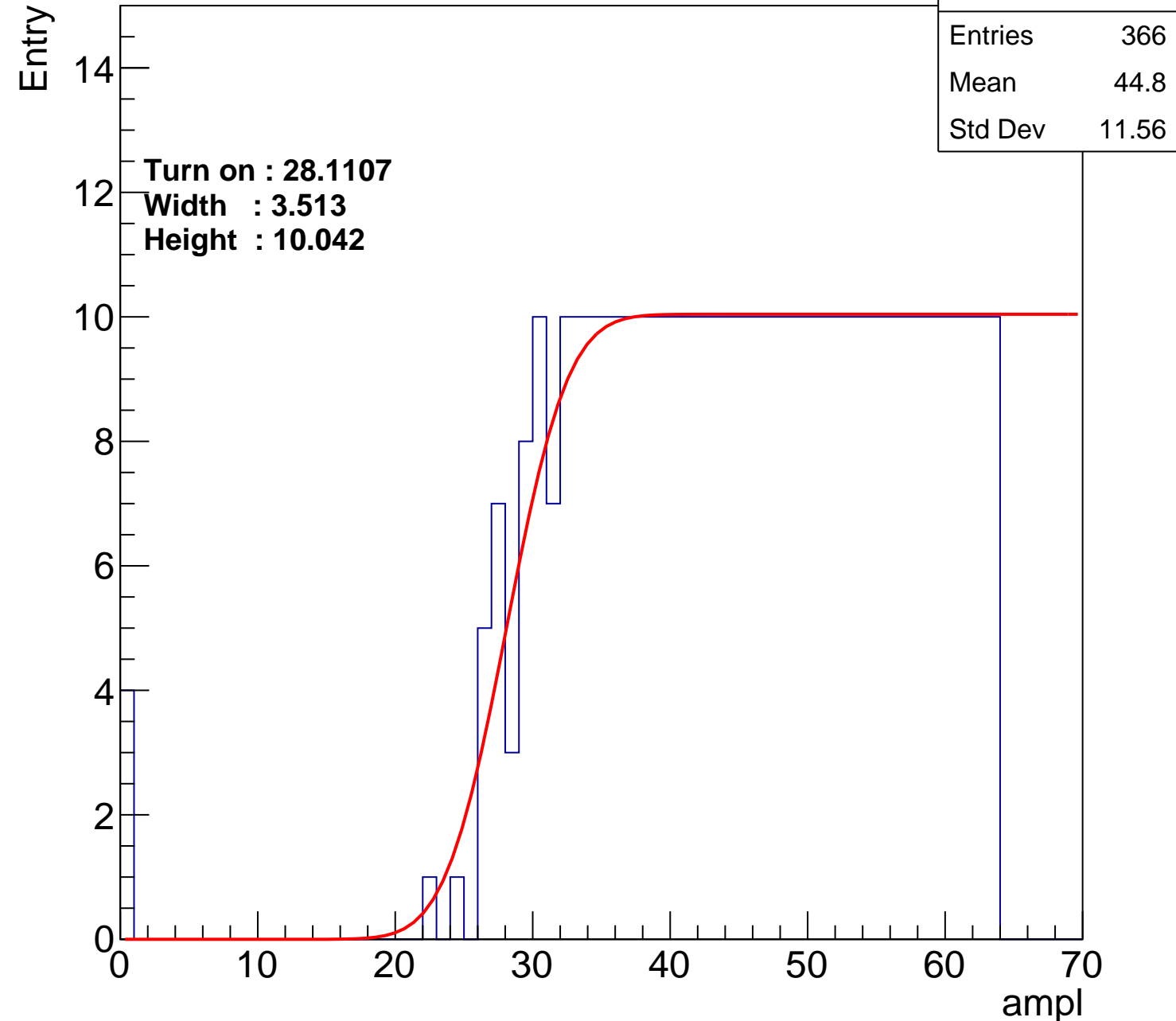
**Width : 3.513**

**Height : 10.042**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 26.7988  
Width : 4.285  
Height : 10.058



# B0L000S, U14-ch85

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.39
Std Dev	11.6

Turn on : 26.7104

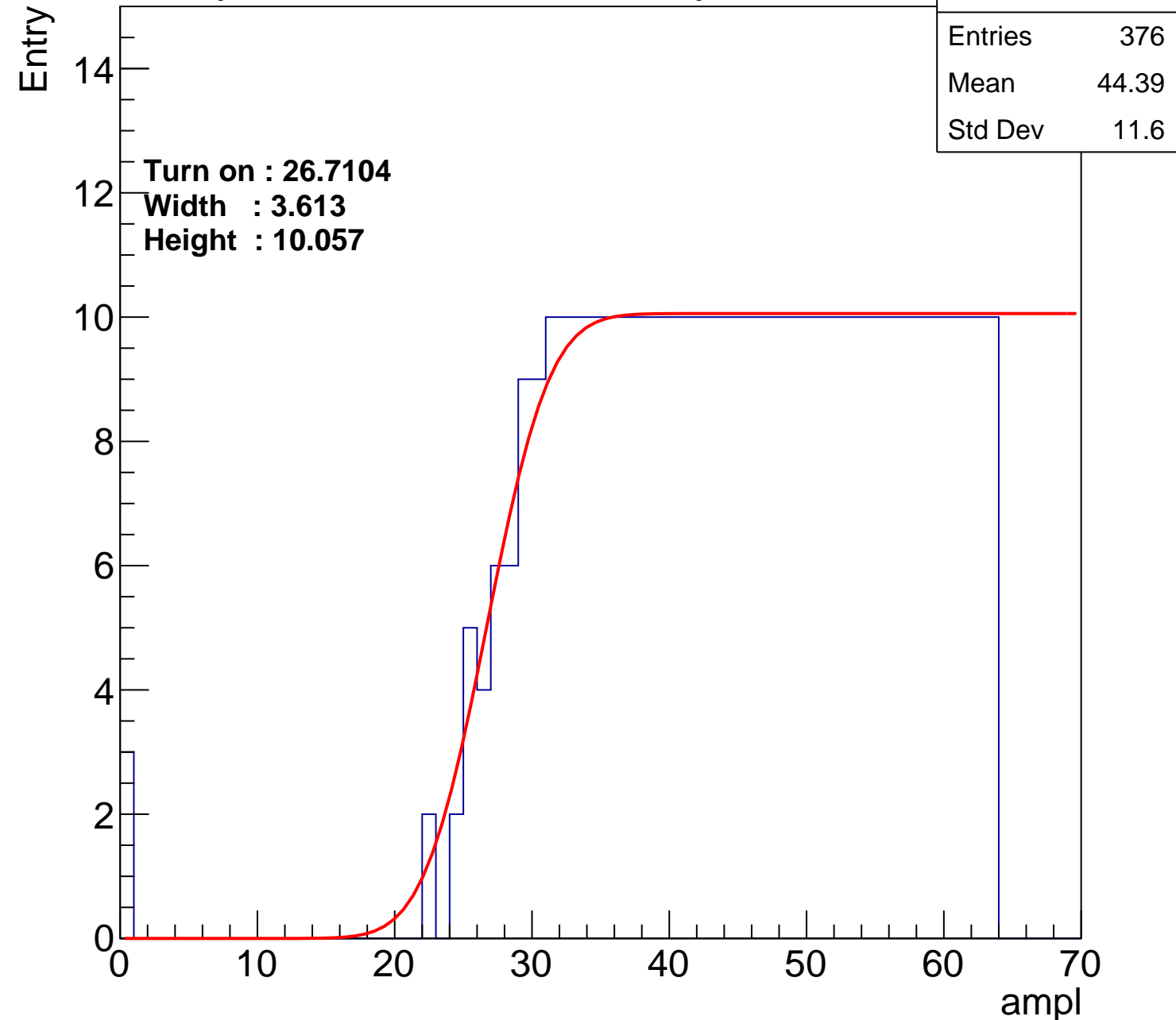
Width : 3.613

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch86

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	396
Mean	43.45
Std Dev	11.96

Turn on : 23.9413

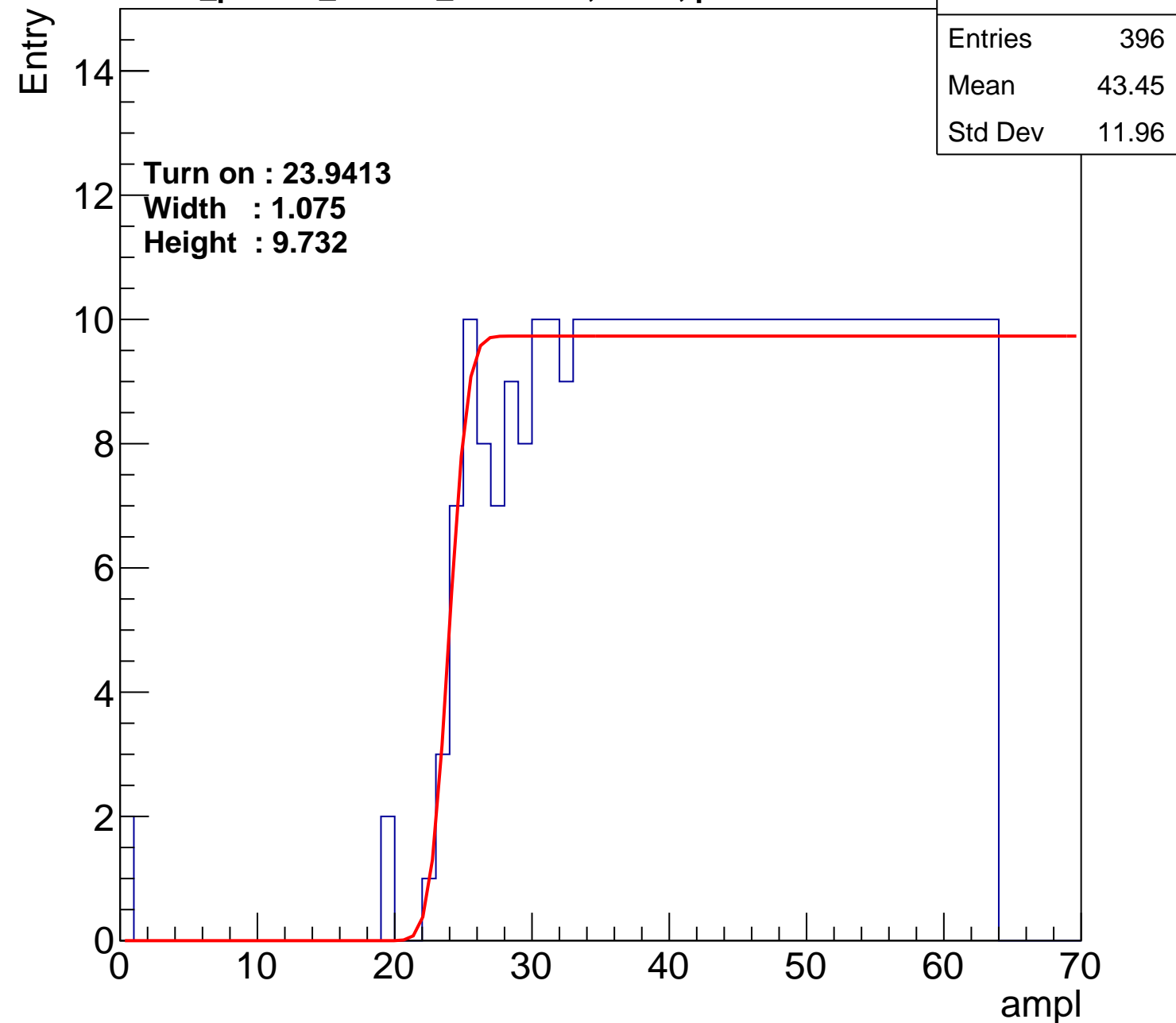
Width : 1.075

Height : 9.732

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch87

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.83
Std Dev	11.11

Turn on : 27.9096

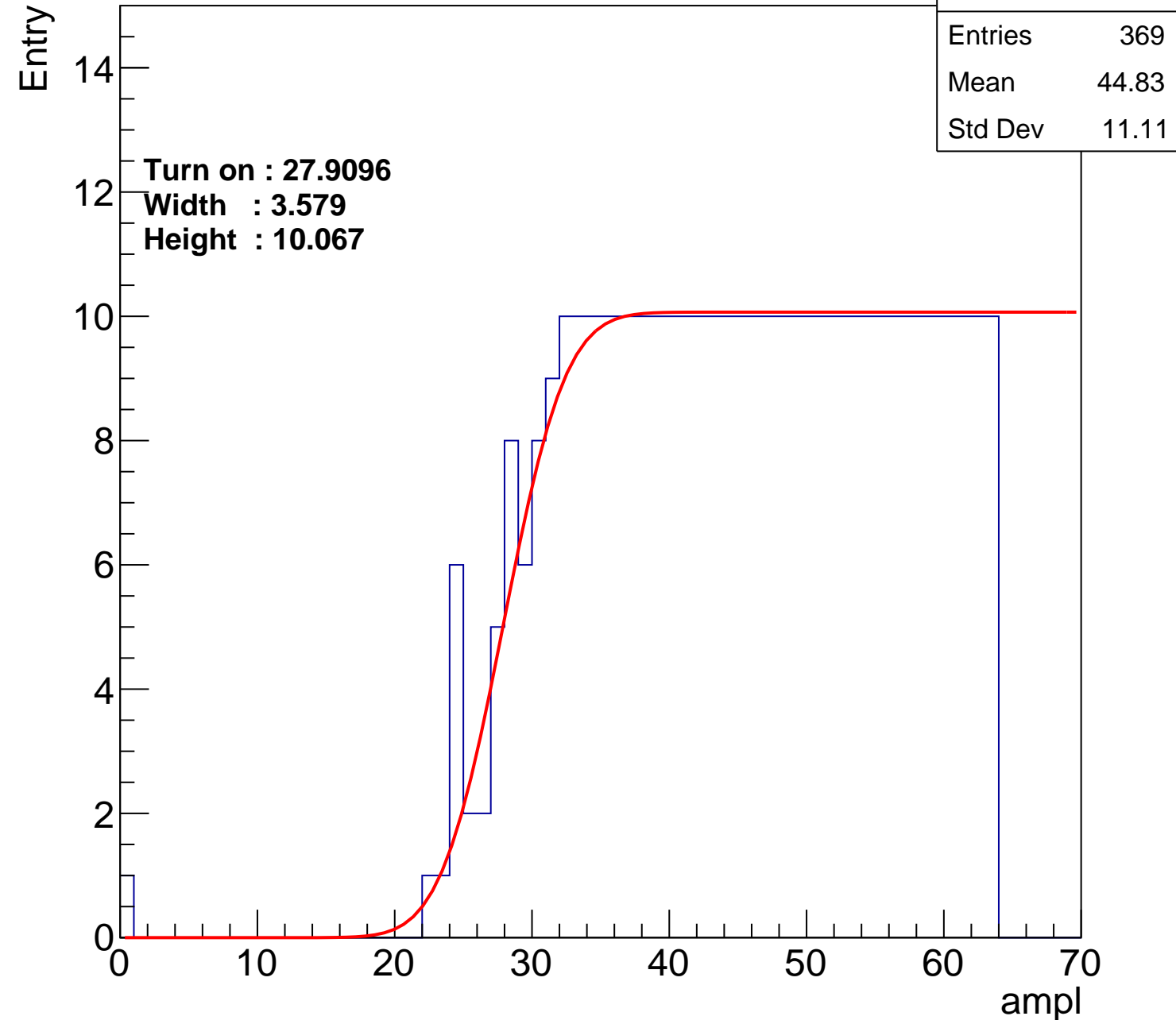
Width : 3.579

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch88

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	392
Mean	43.51
Std Dev	12.28

Turn on : 25.6219

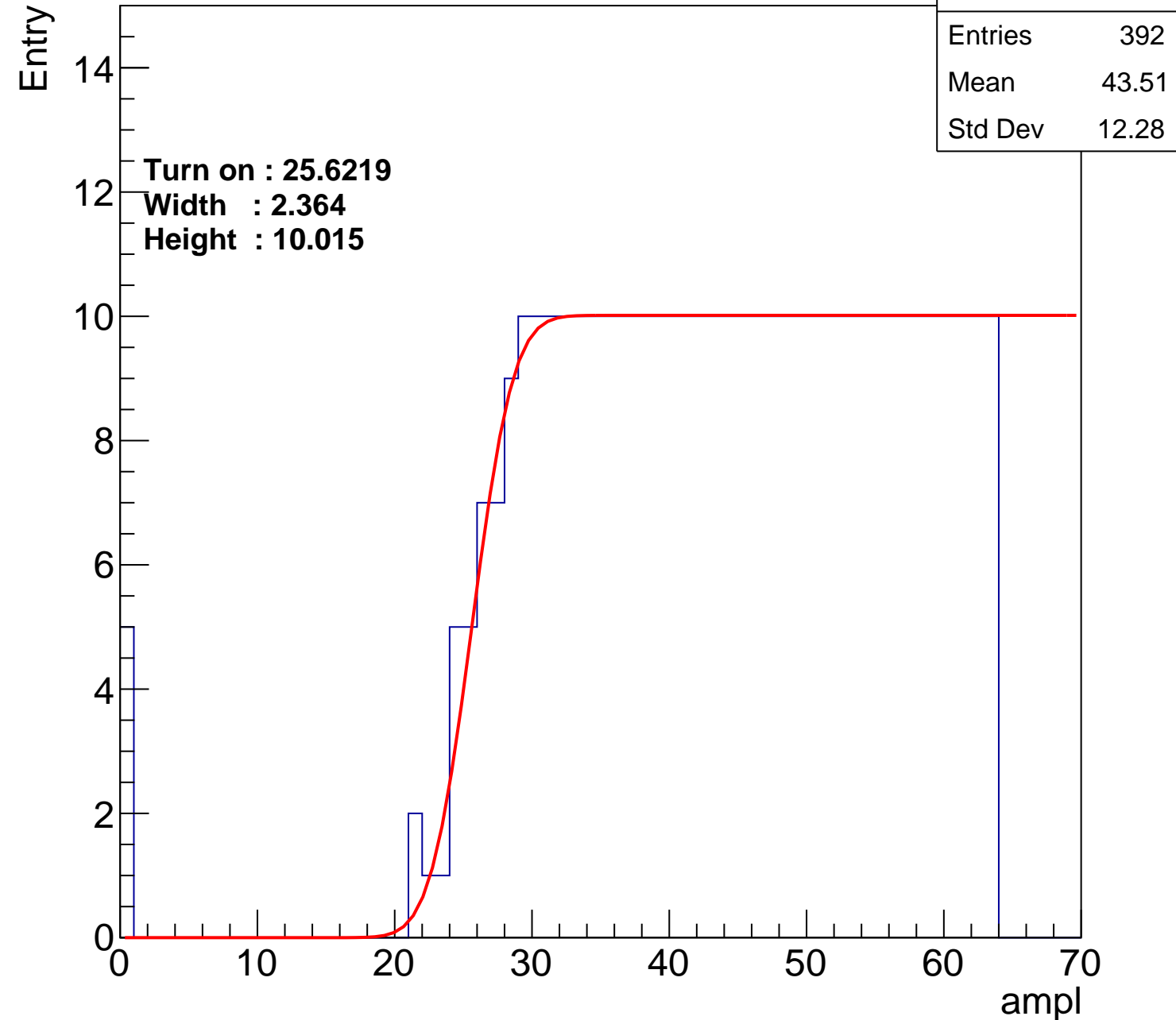
Width : 2.364

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch89

calib\_packv5\_042523\_0143.root, FC#5, port B1

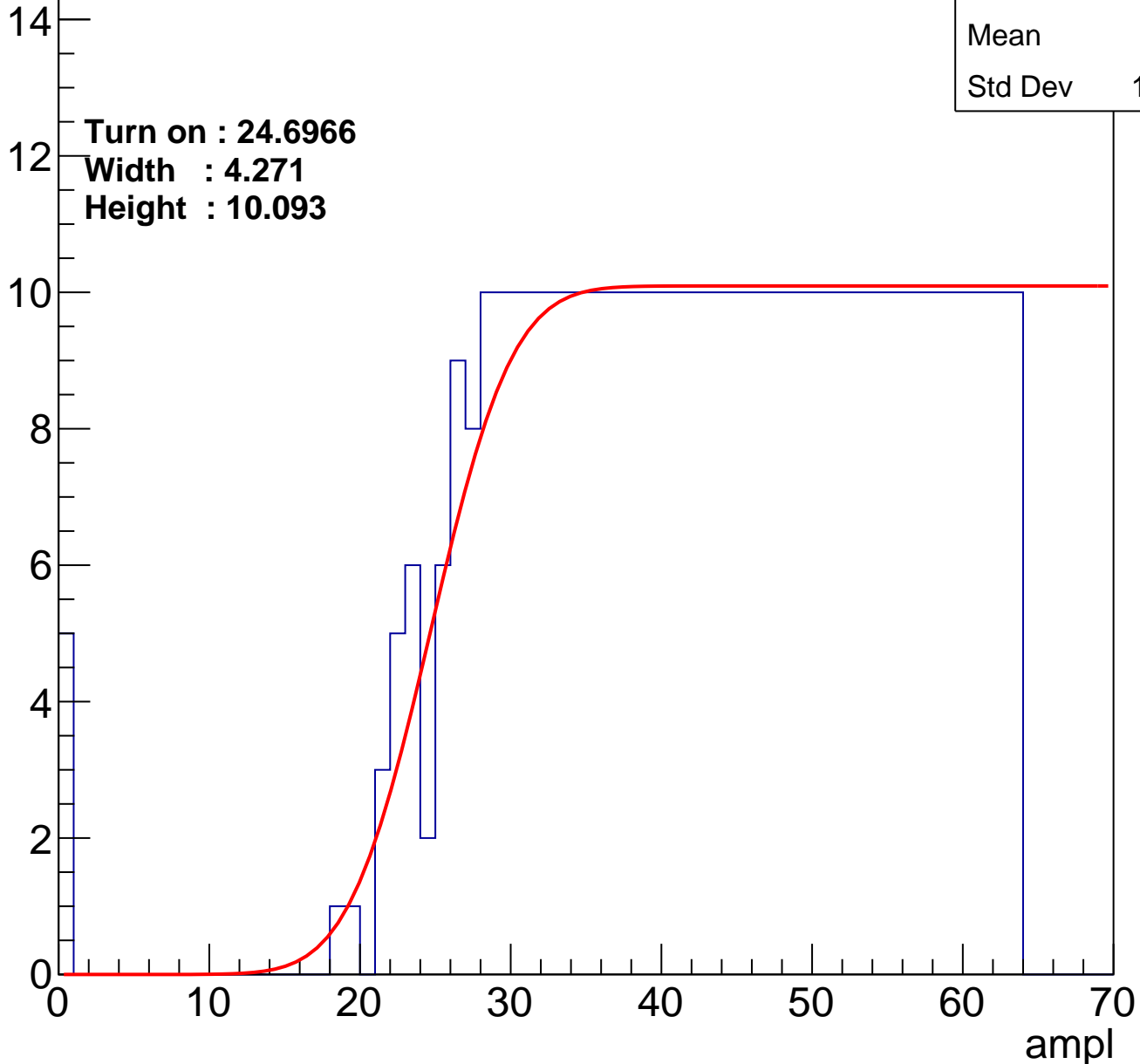
Entries	406
Mean	42.8
Std Dev	12.64

Turn on : 24.6966

Width : 4.271

Height : 10.093

Entry



# B0L000S, U14-ch90

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.51
Std Dev	12.16

**Turn on : 25.6857**

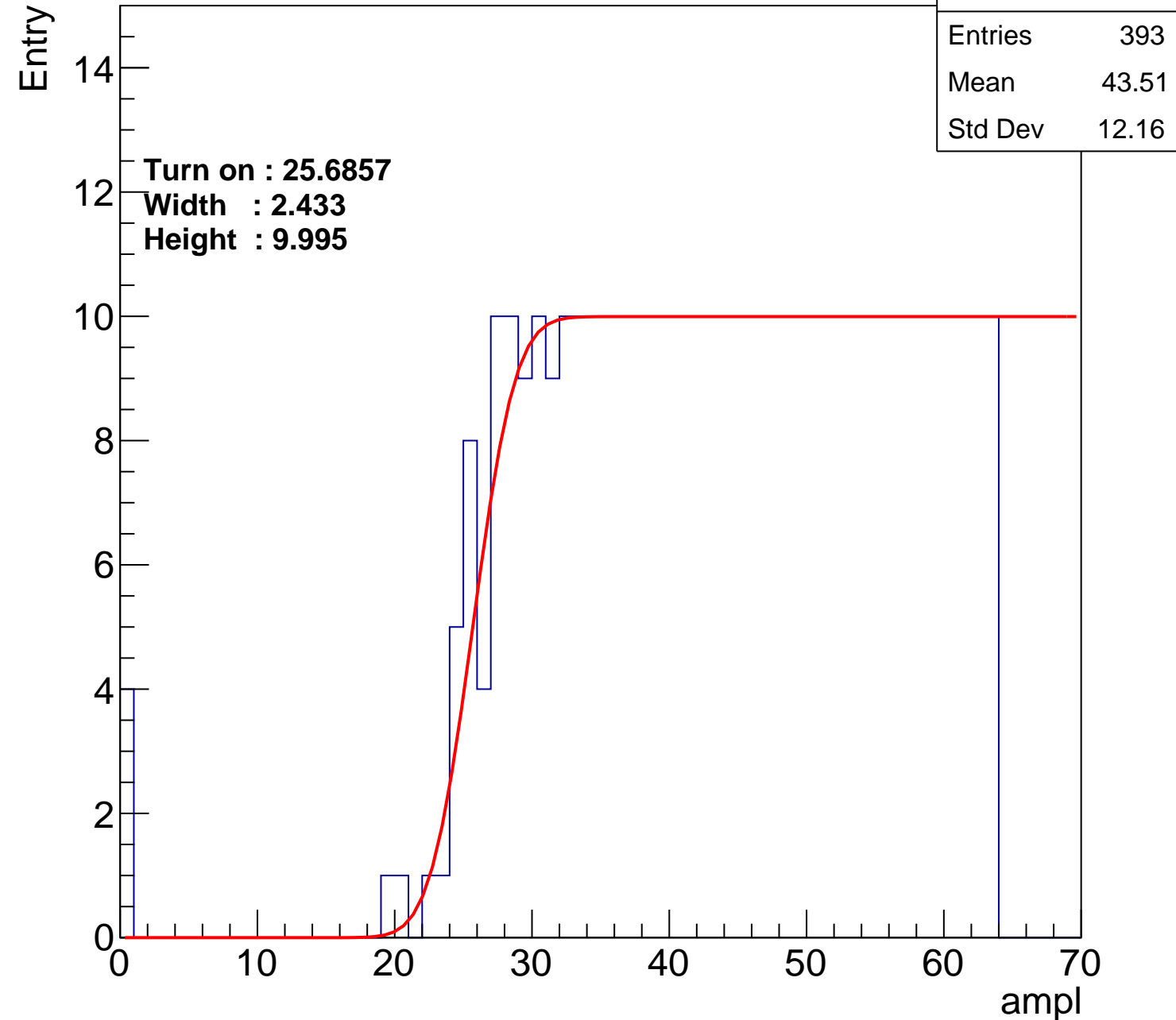
**Width : 2.433**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch91

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.84
Std Dev	11.6

**Turn on : 25.6089**

**Width : 3.768**

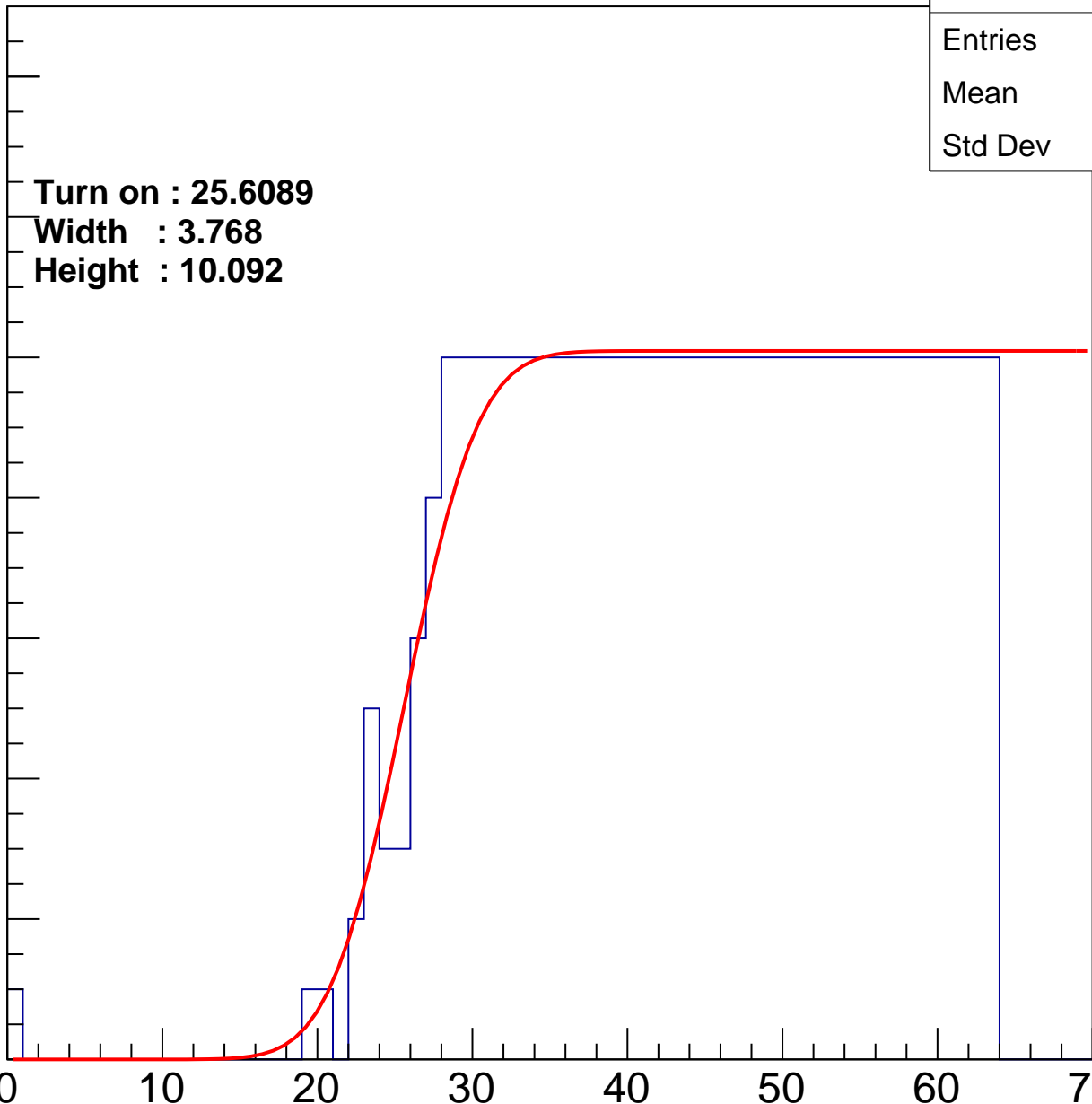
**Height : 10.092**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U14-ch92

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.64
Std Dev	12.1

Turn on : 25.4356

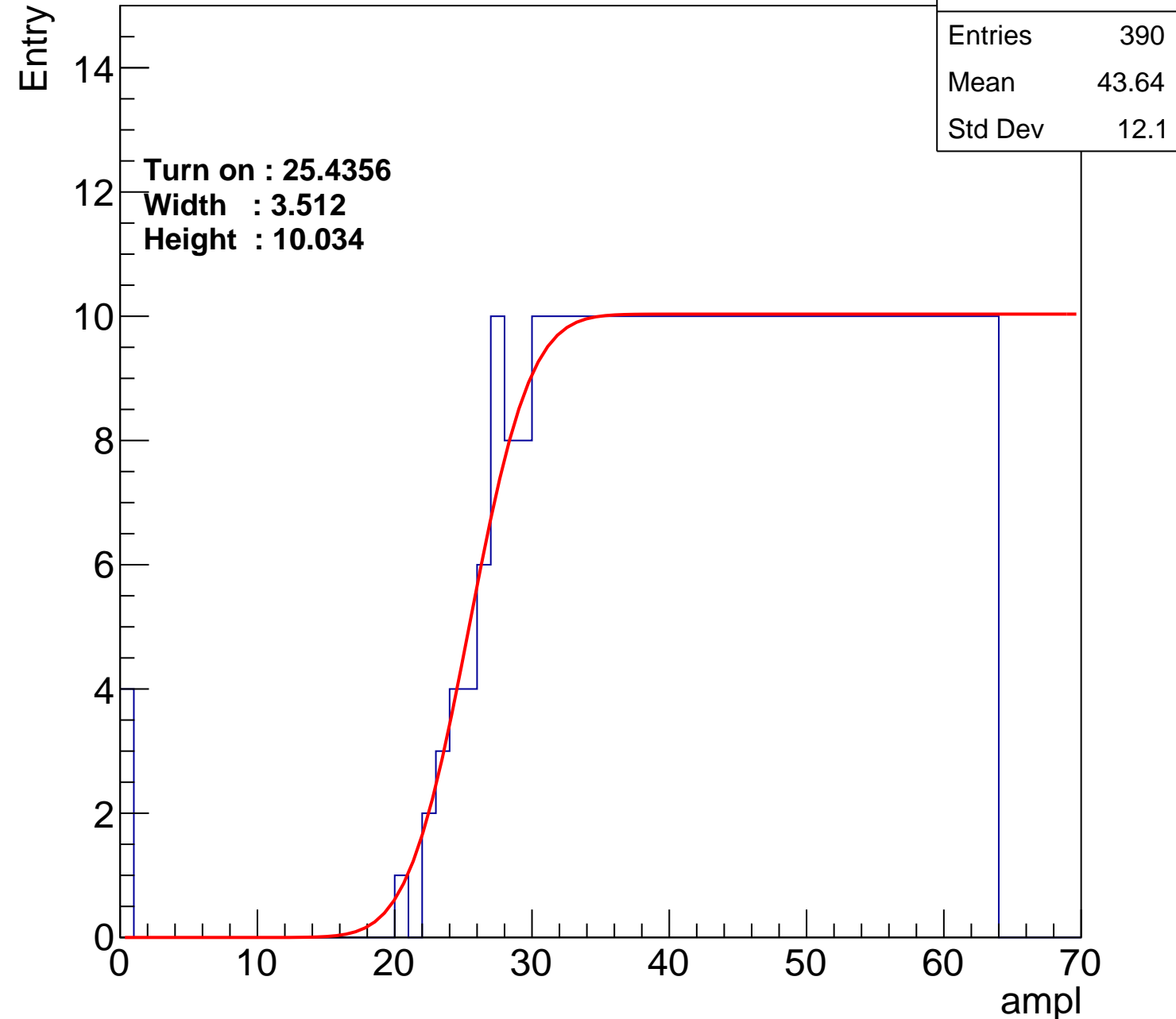
Width : 3.512

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch93

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.7704

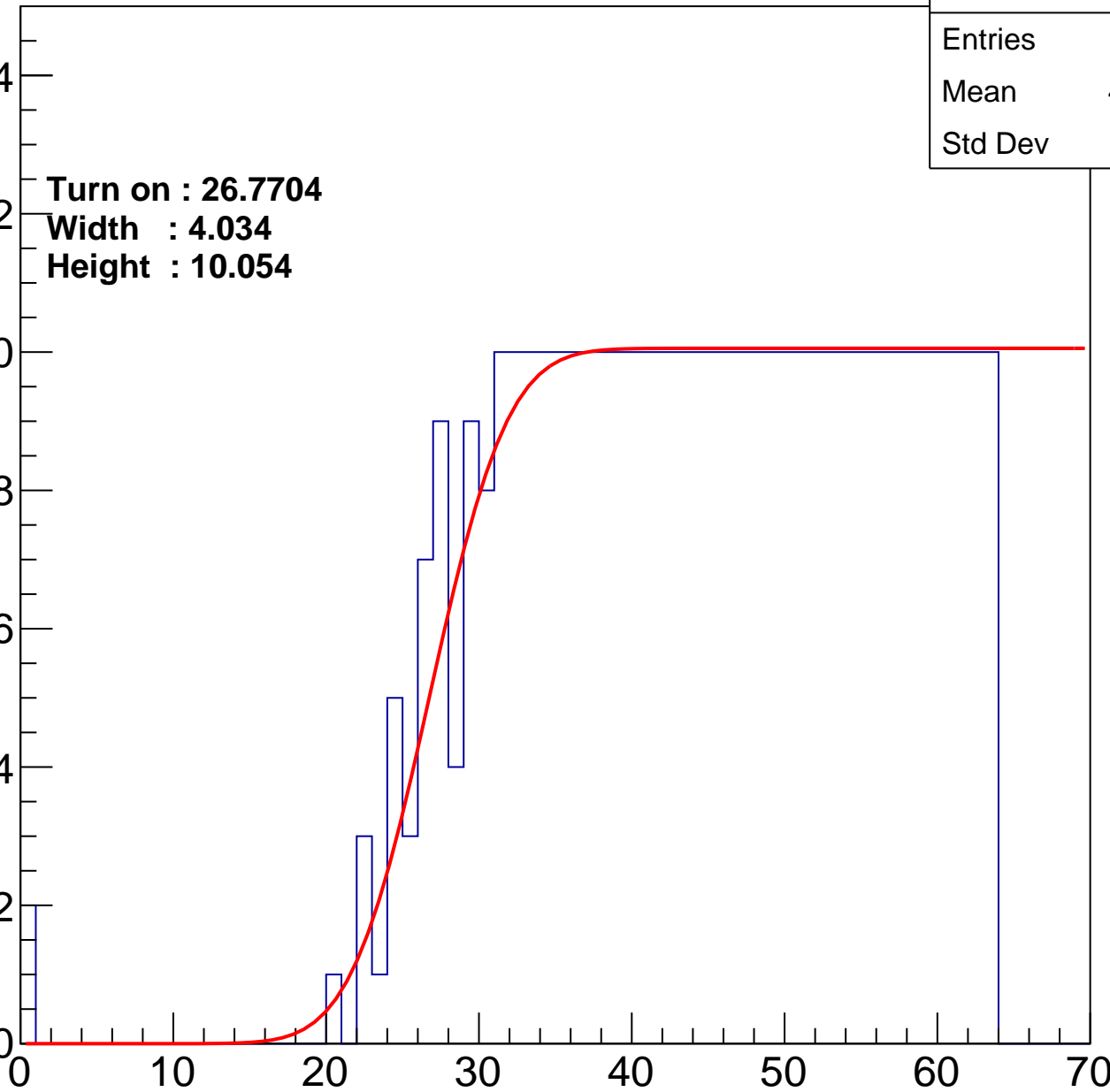
Width : 4.034

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch94

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.06
Std Dev	11.93

Turn on : 26.7651

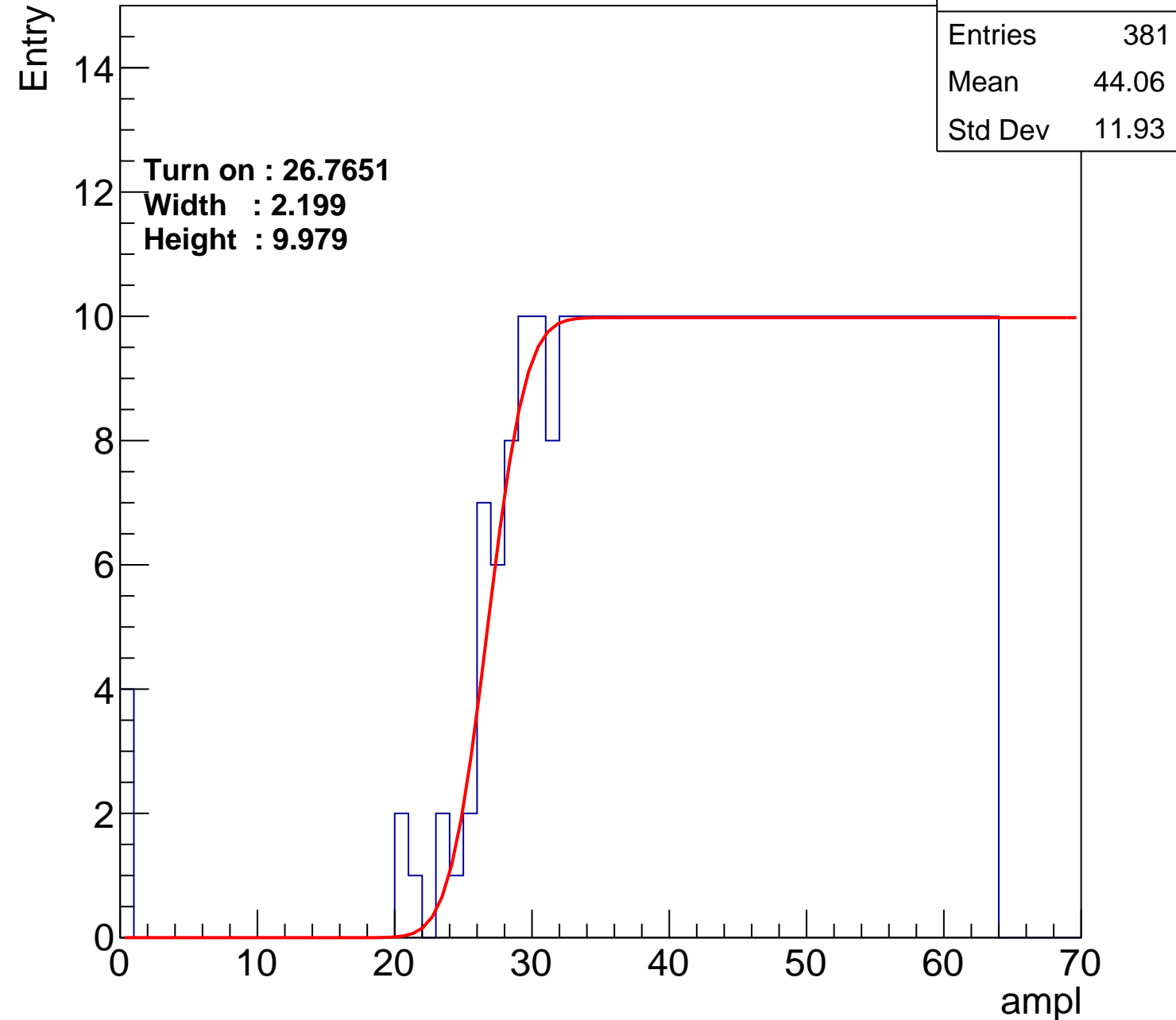
Width : 2.199

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch95

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.19
Std Dev	11.95

**Turn on : 26.7260**

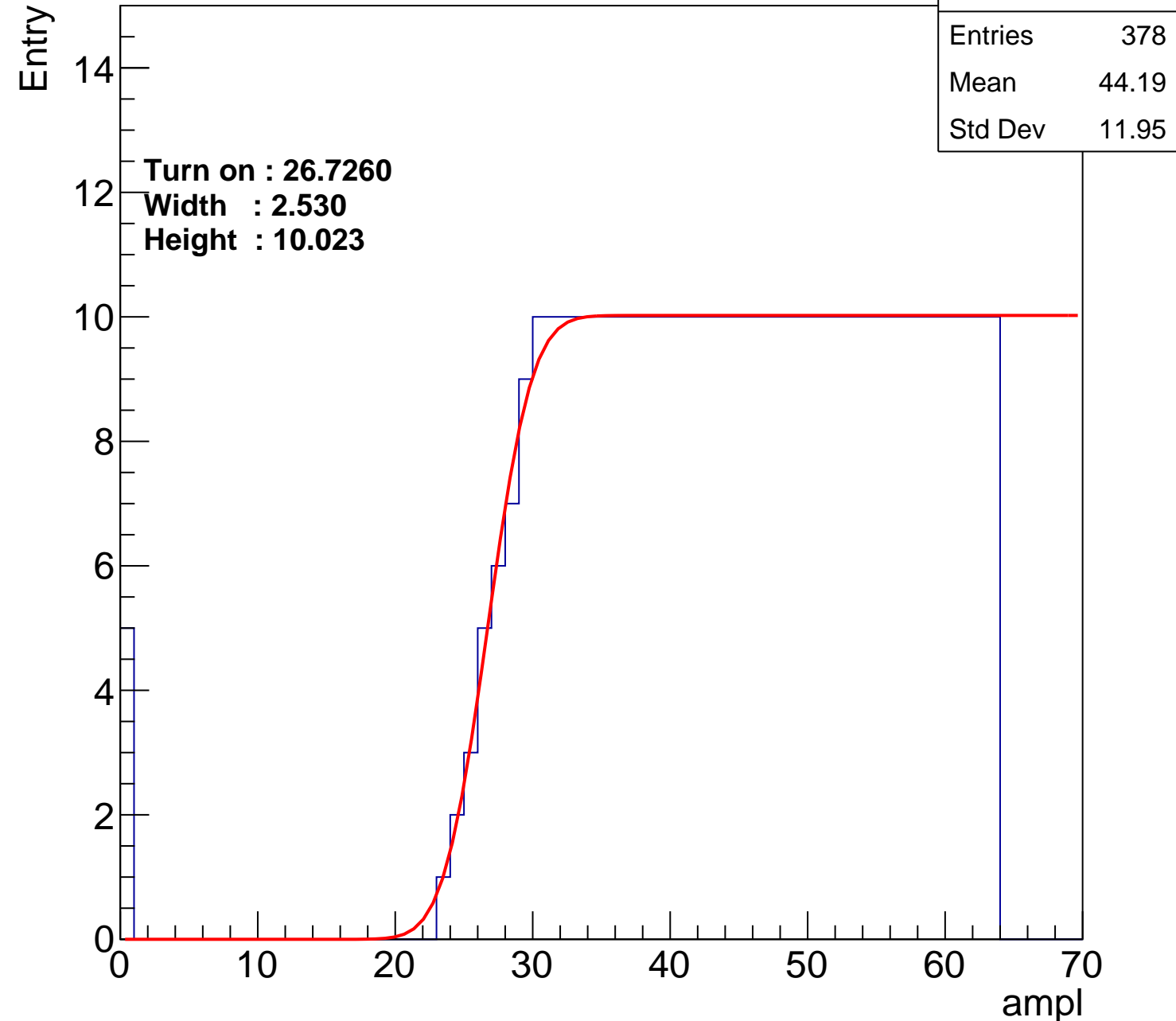
**Width : 2.530**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch96

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.18
Std Dev	12.27

Turn on : 27.2541

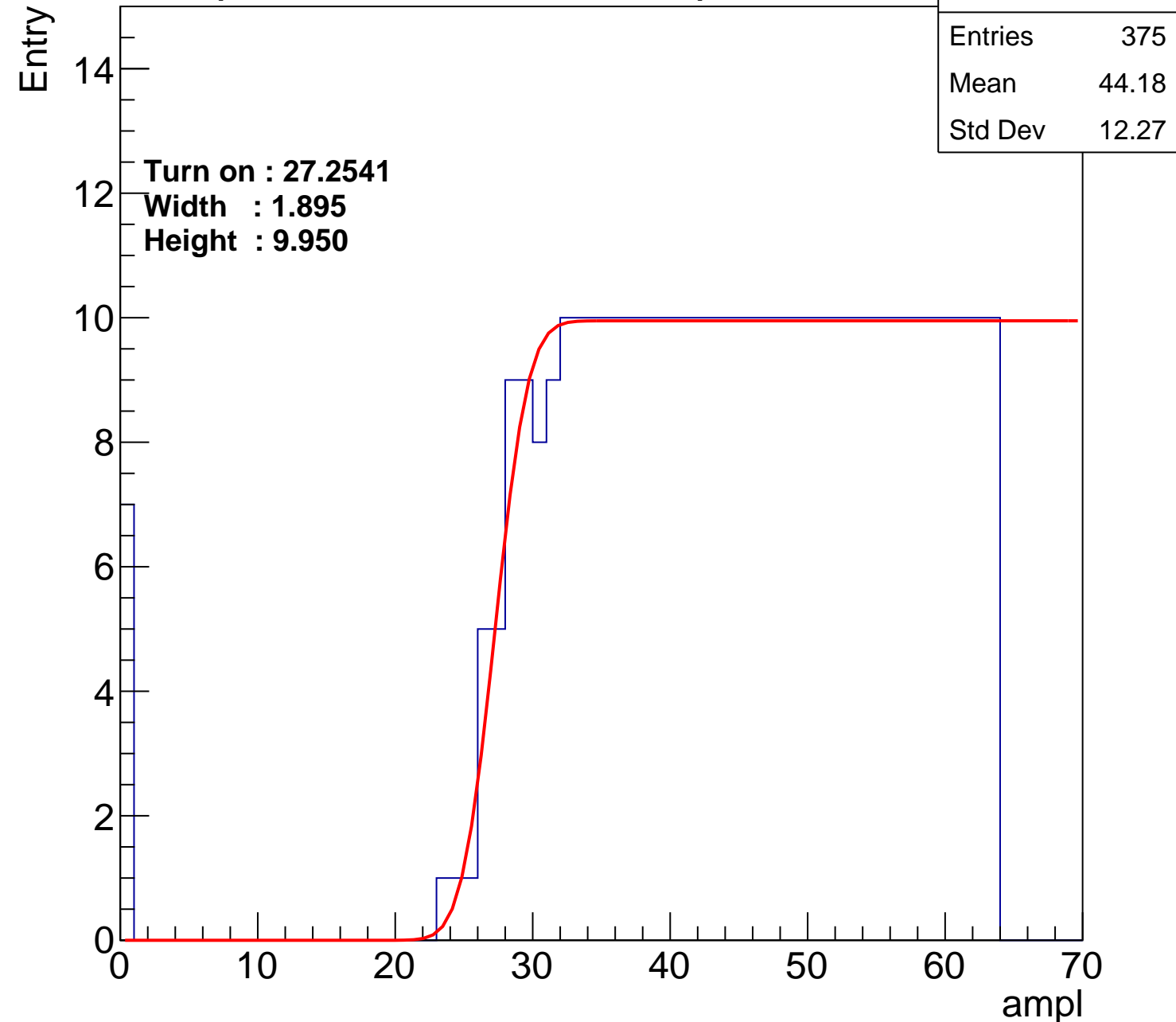
Width : 1.895

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch97

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	397
Mean	43.31
Std Dev	12.26

**Turn on : 25.0363**

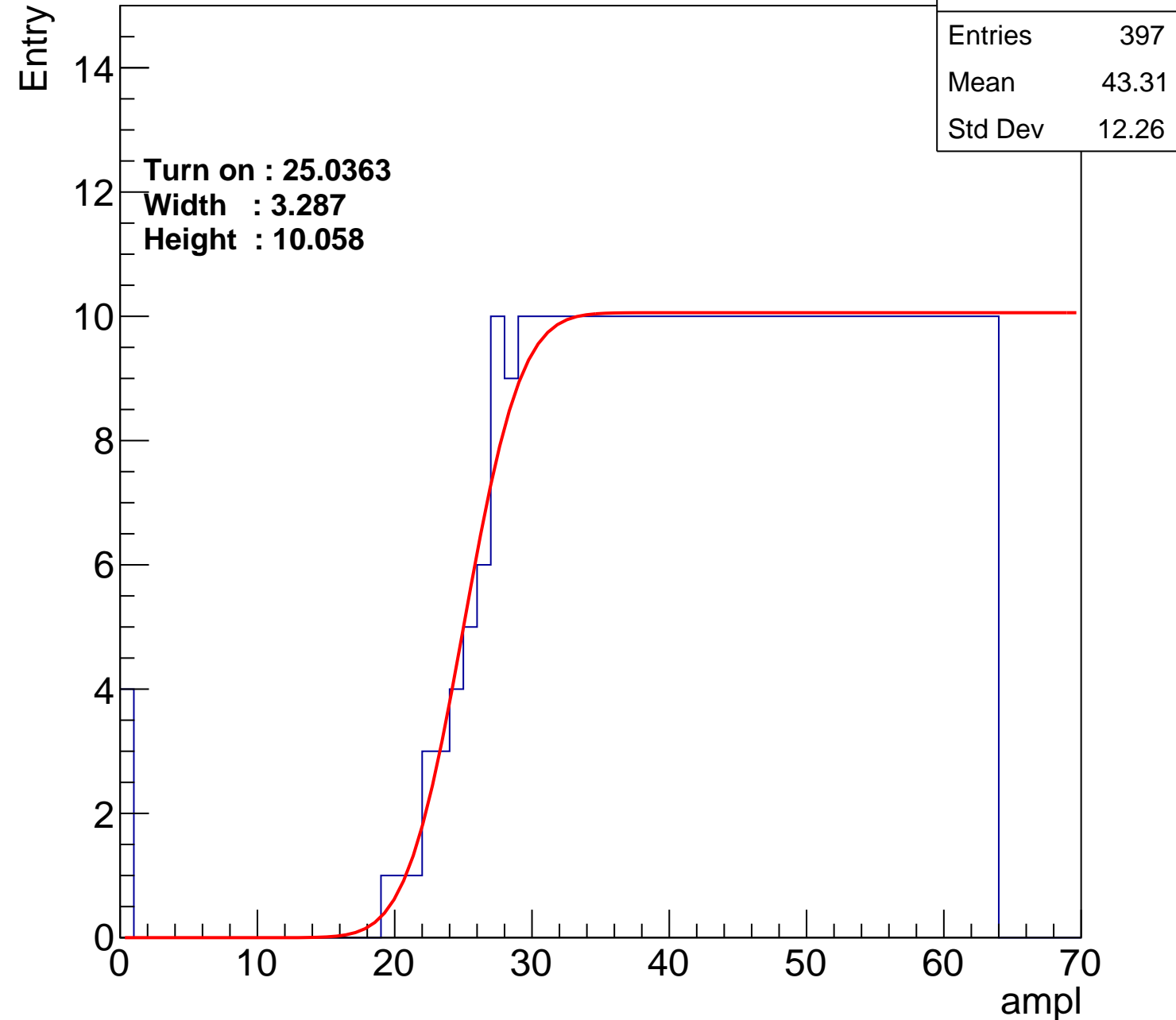
**Width : 3.287**

**Height : 10.058**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch98

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	43.97
Std Dev	11.91

**Turn on : 26.3767**

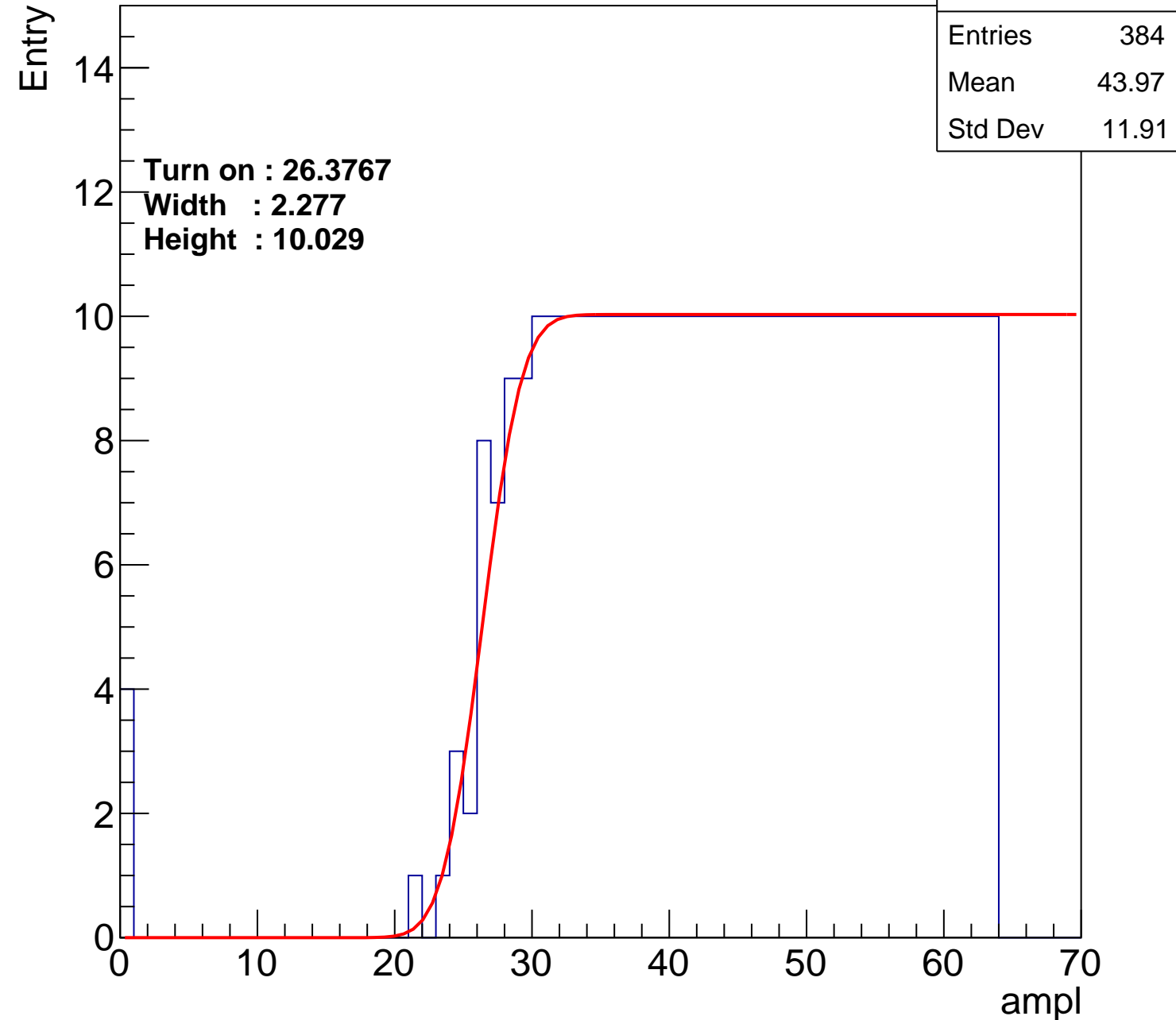
**Width : 2.277**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch99

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.49
Std Dev	12.07

Turn on : 25.3870

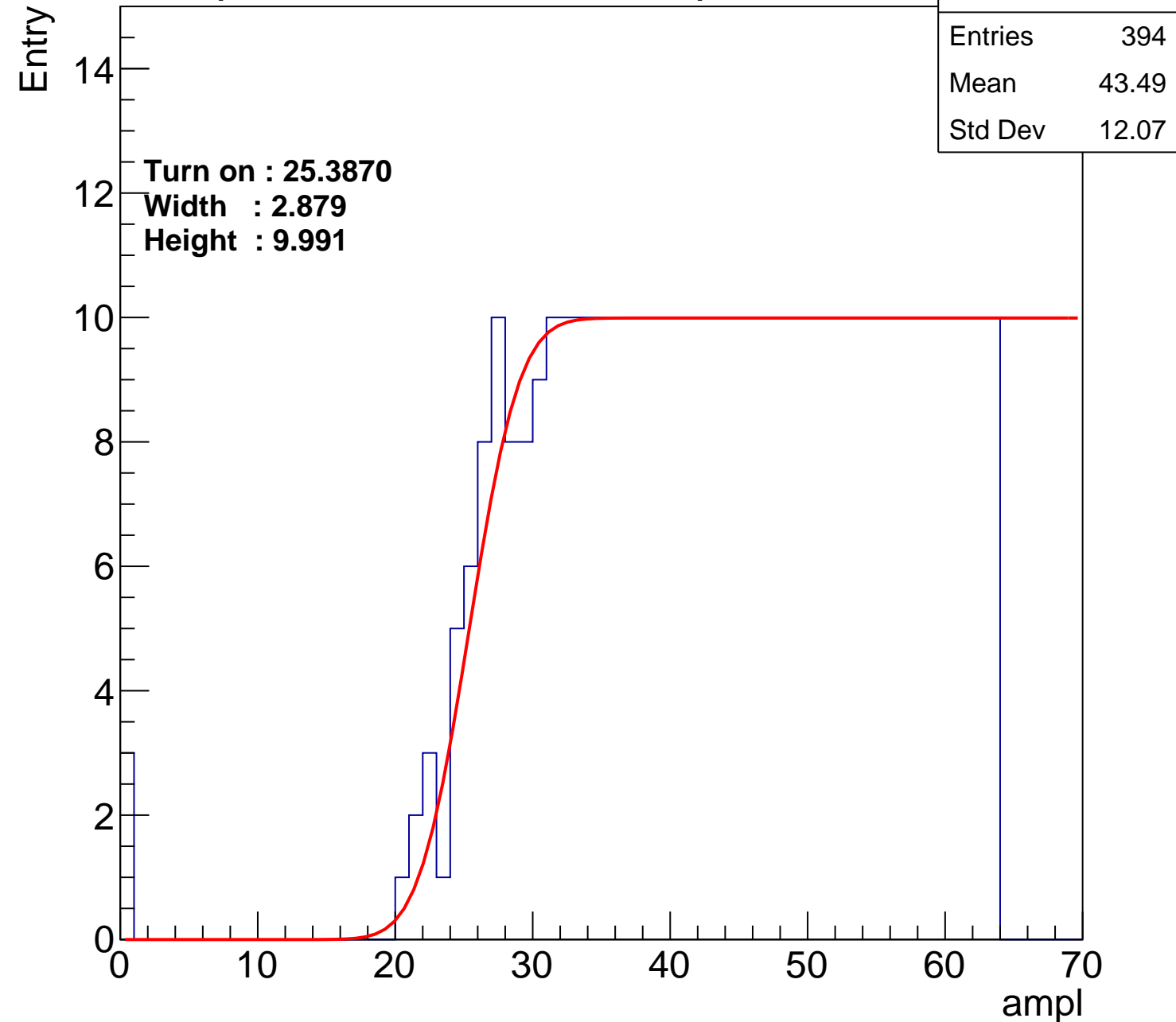
Width : 2.879

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch100

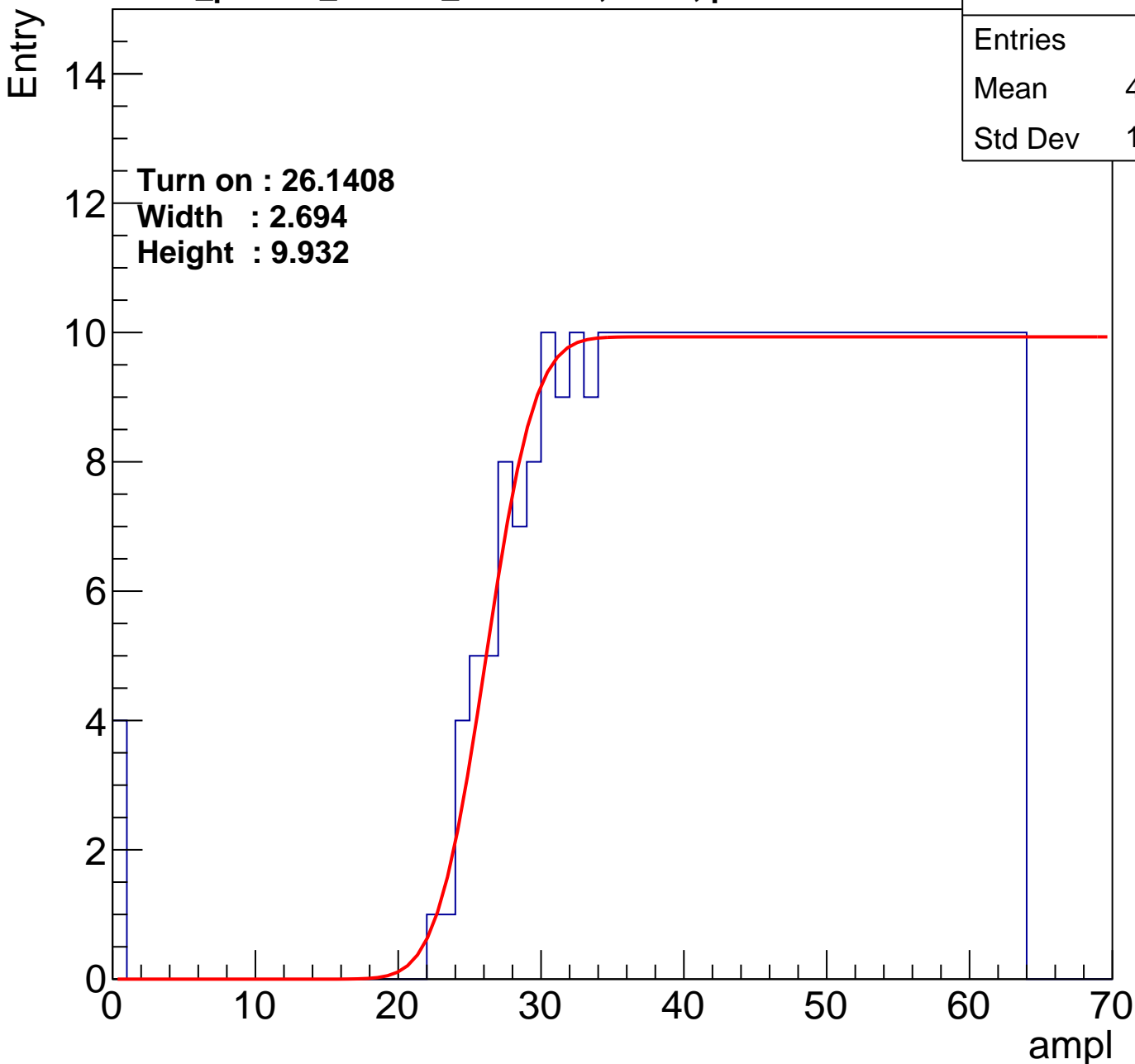
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.06
Std Dev	11.92

Turn on : 26.1408

Width : 2.694

Height : 9.932



# B0L000S, U14-ch101

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.81
Std Dev	11.23

**Turn on : 27.2650**

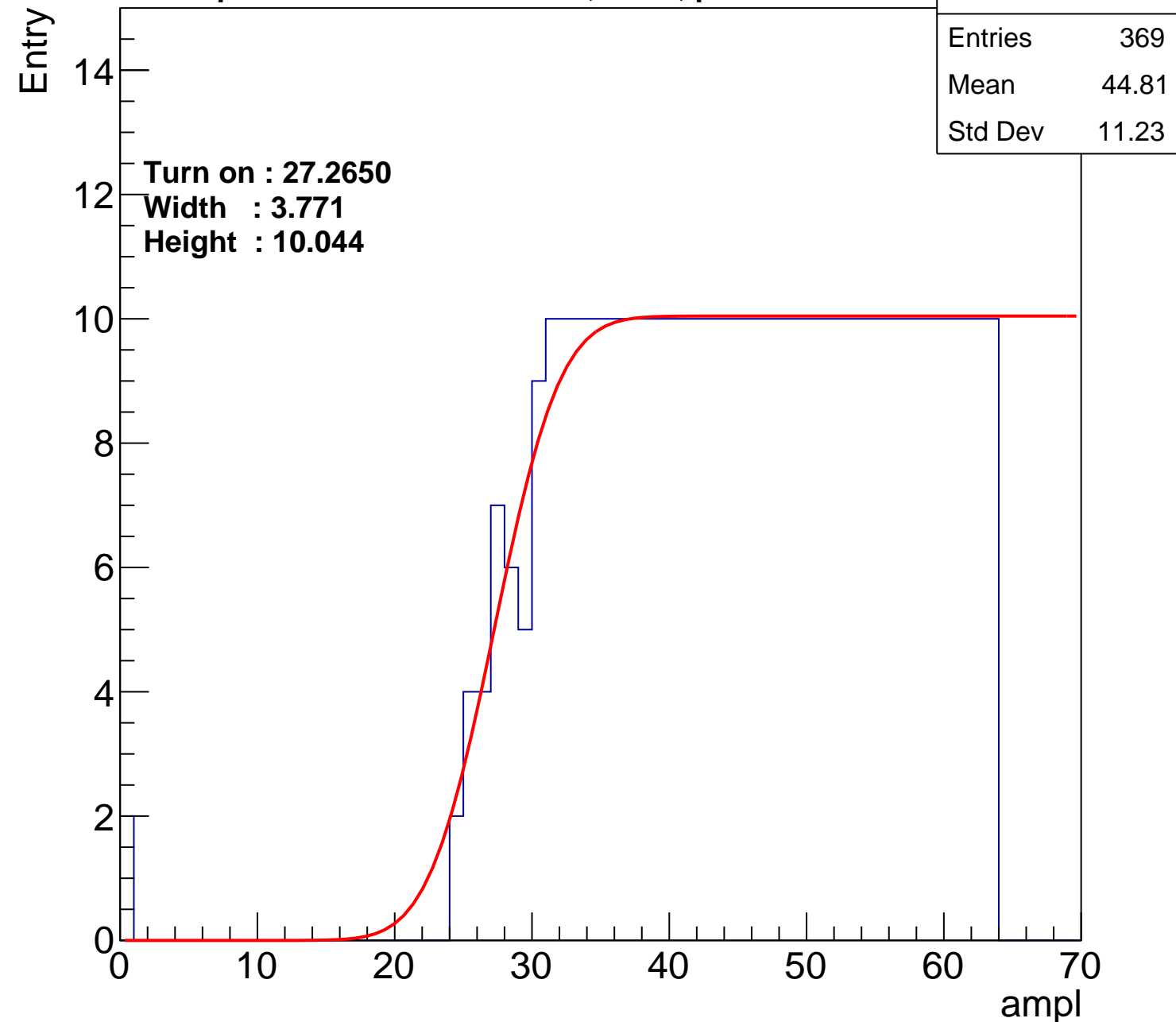
**Width : 3.771**

**Height : 10.044**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch102

calib\_packv5\_042523\_0143.root, FC#5, port B1

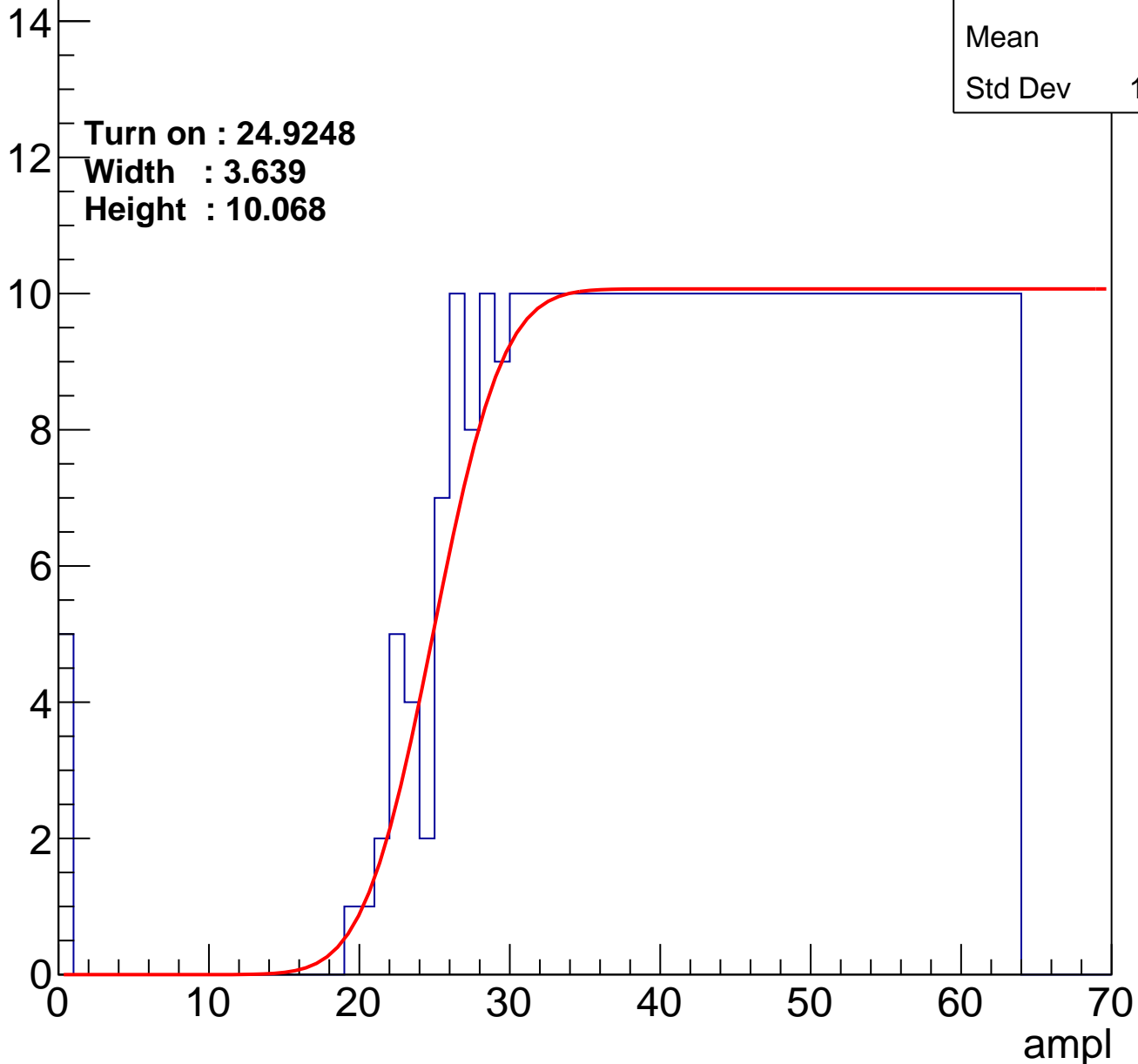
Entries	404
Mean	42.9
Std Dev	12.58

**Turn on : 24.9248**

**Width : 3.639**

**Height : 10.068**

Entry





# B0L000S, U14-ch103

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.8
Std Dev	11.44

**Turn on : 28.2345**

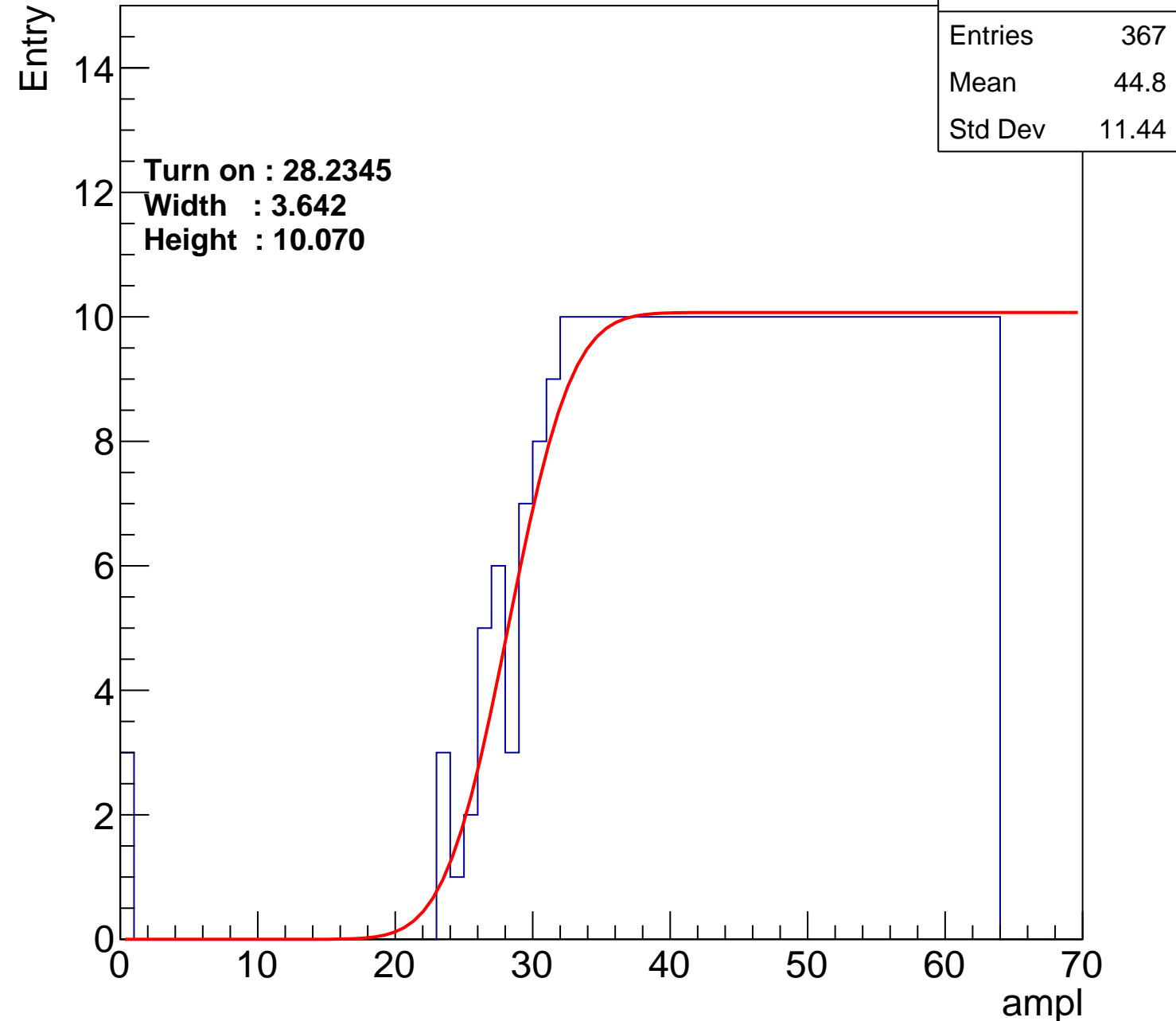
**Width : 3.642**

**Height : 10.070**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch104

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	397
Mean	43.33
Std Dev	12.16

Turn on : 25.0831

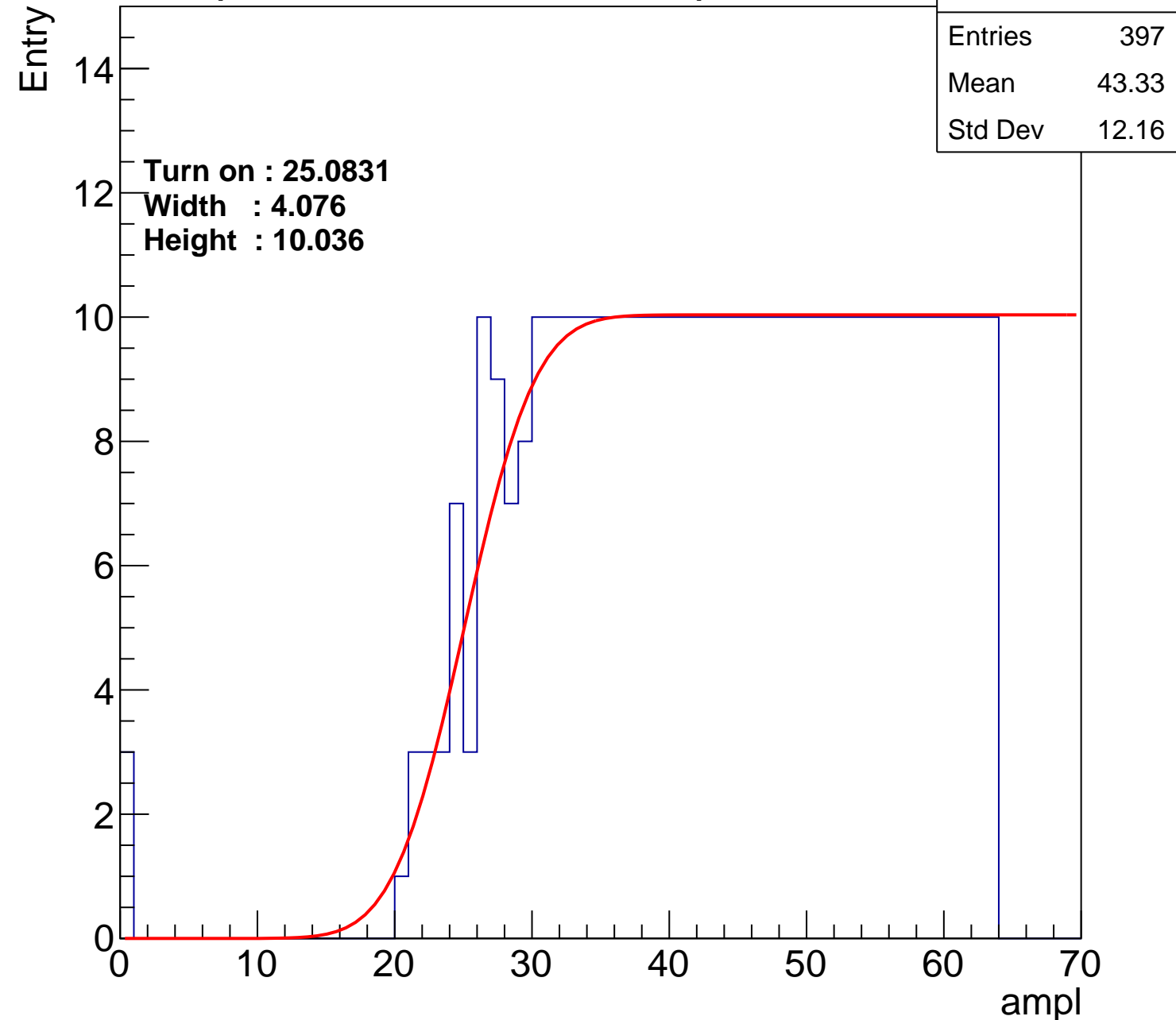
Width : 4.076

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch105

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	392
Mean	43.61
Std Dev	11.91

Turn on : 24.6596

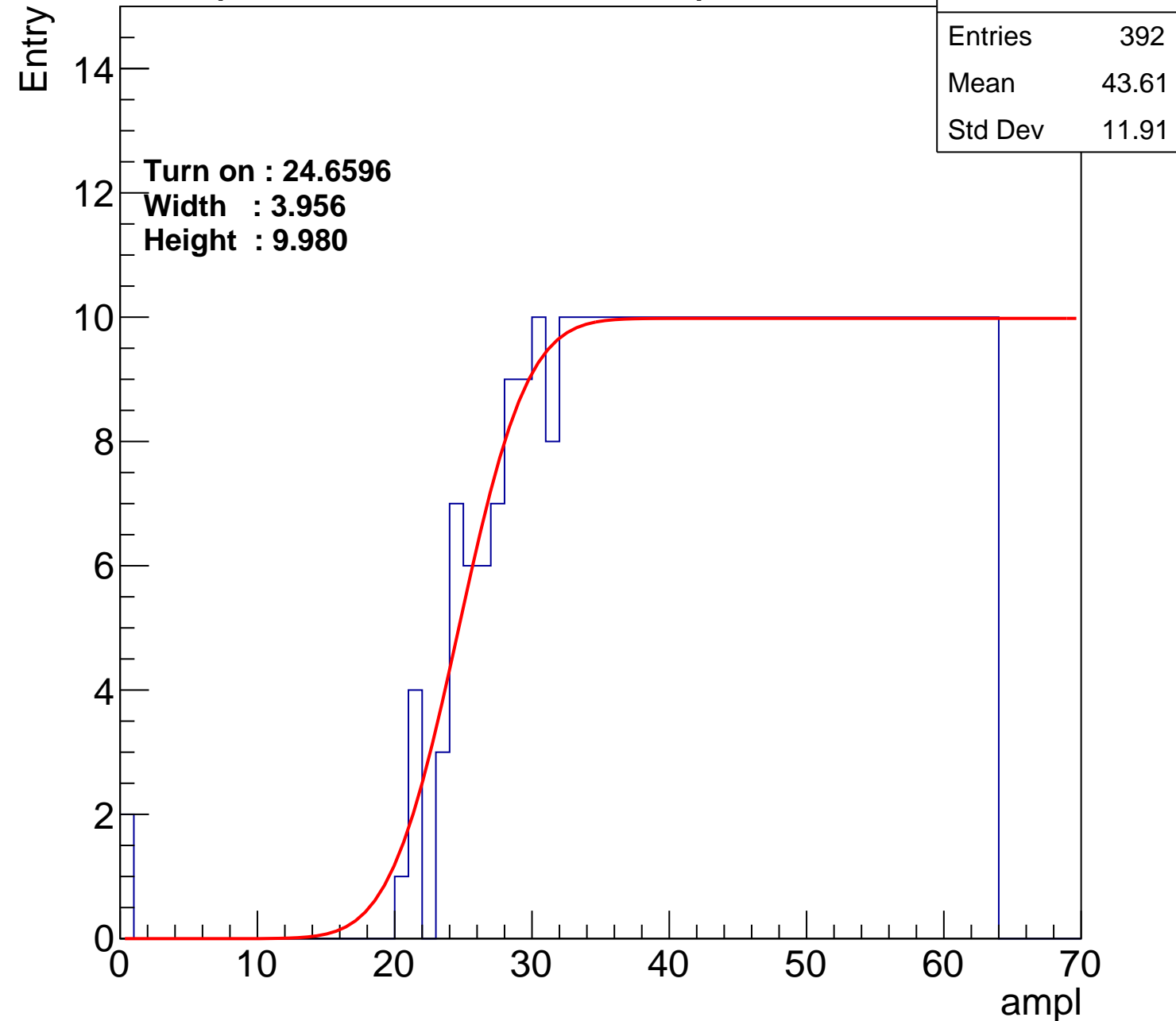
Width : 3.956

Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch106

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.11
Std Dev	11.61

Turn on : 26.1563

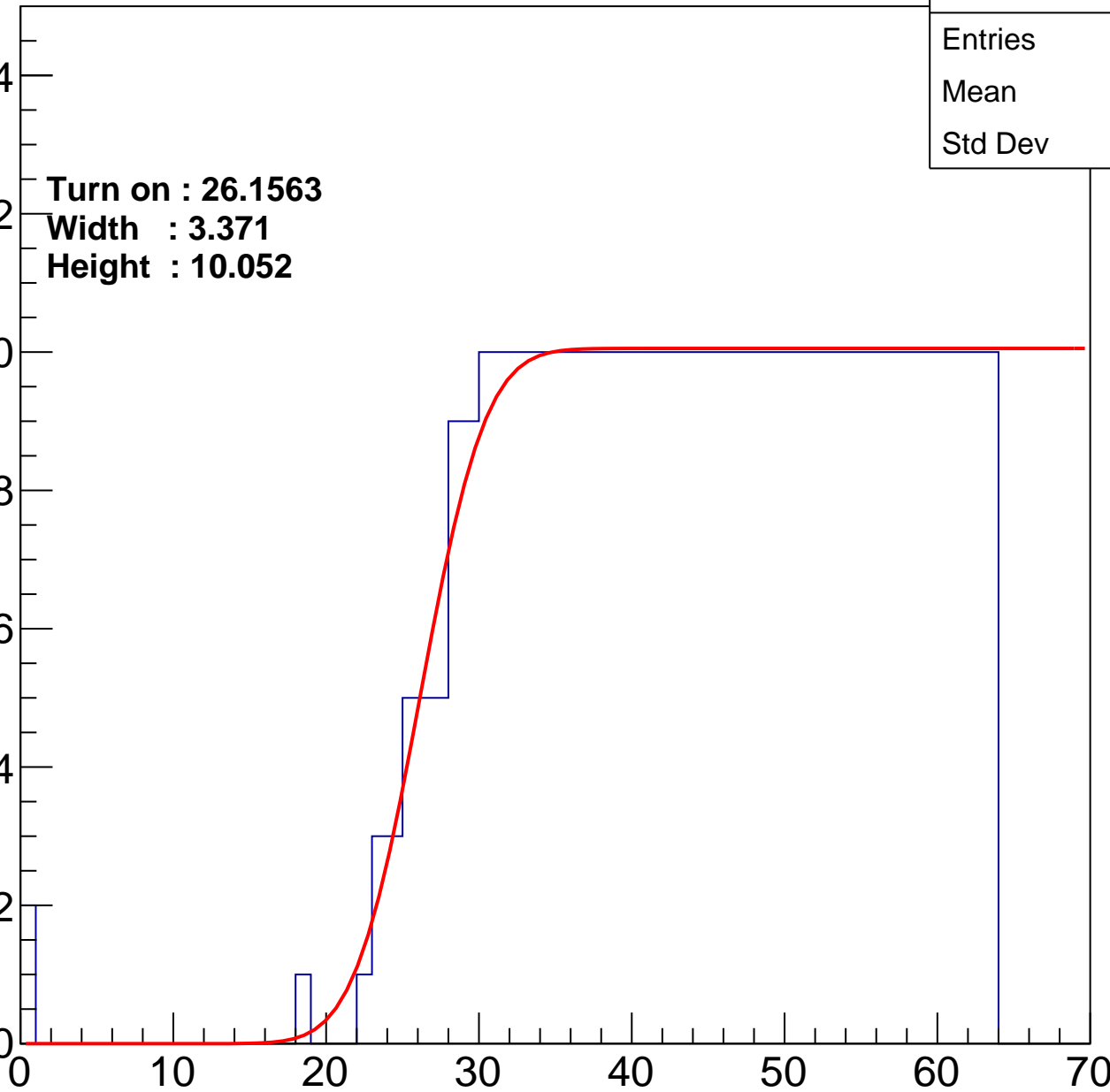
Width : 3.371

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch107

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.05
Std Dev	11.58

**Turn on : 25.4555**

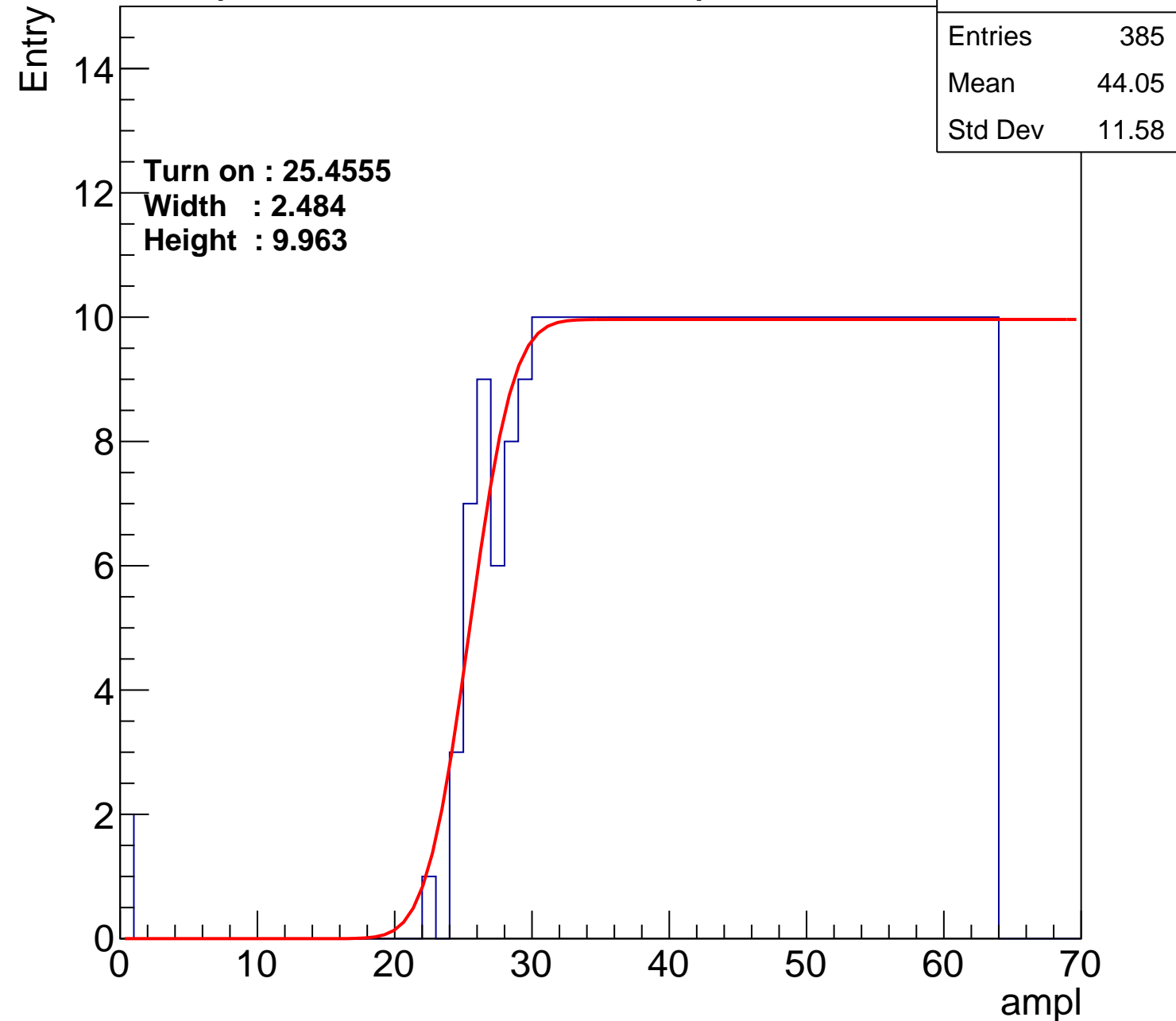
**Width : 2.484**

**Height : 9.963**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch108

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.02
Std Dev	11.87

Turn on : 26.2526

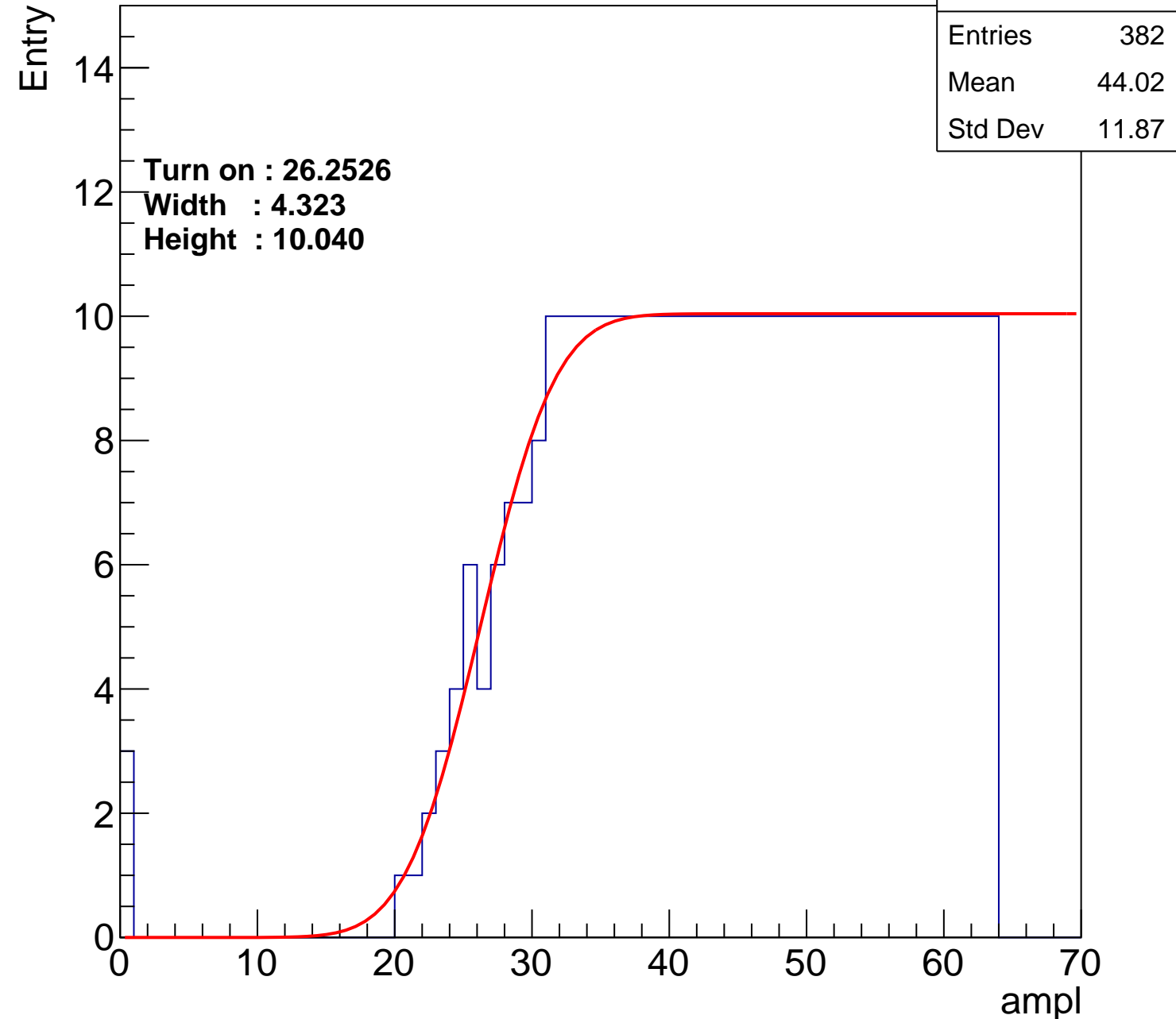
Width : 4.323

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch109

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	410
Mean	42.77
Std Dev	12.37

**Turn on : 23.6745**

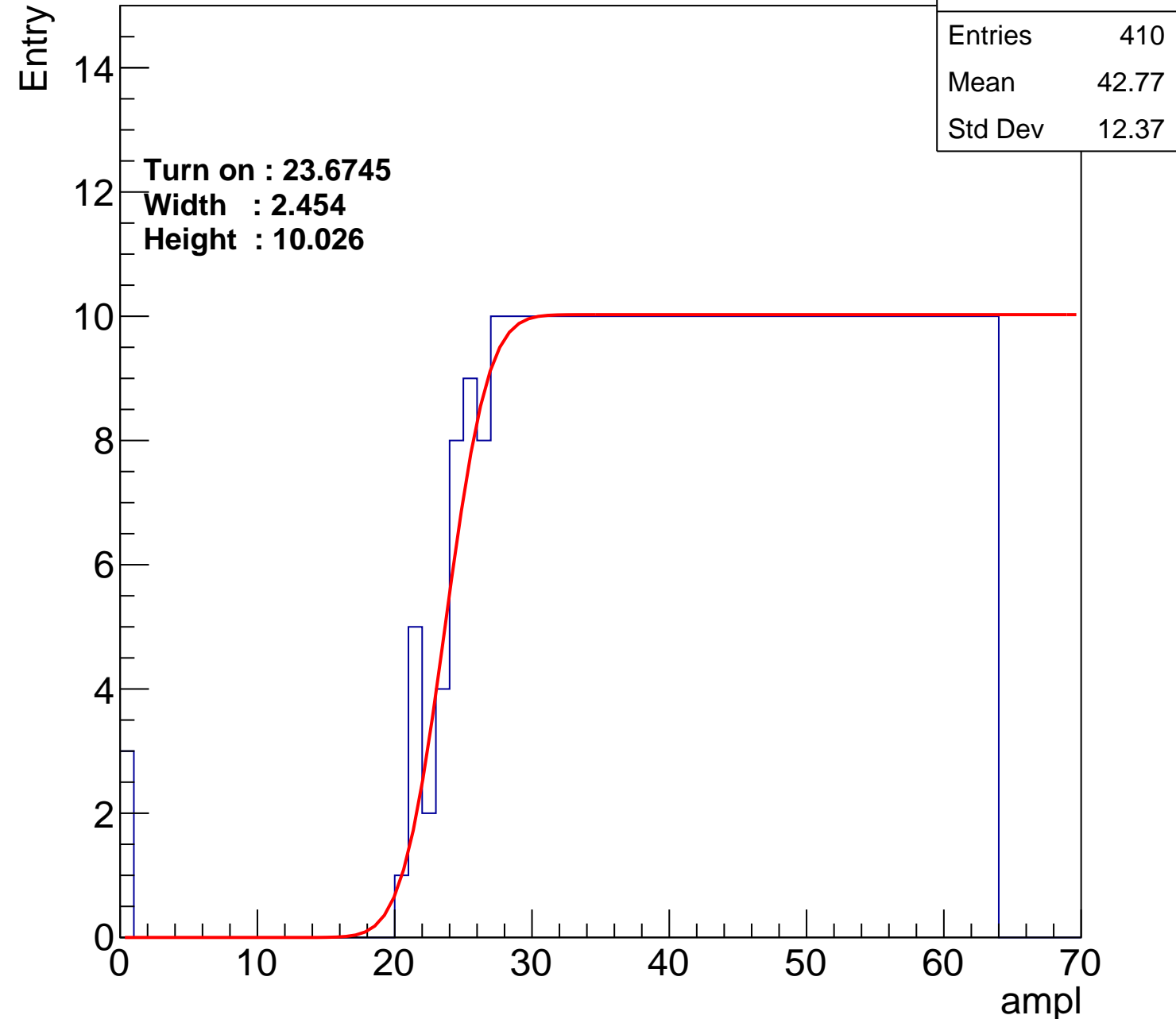
**Width : 2.454**

**Height : 10.026**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch110

calib\_packv5\_042523\_0143.root, FC#5, port B1

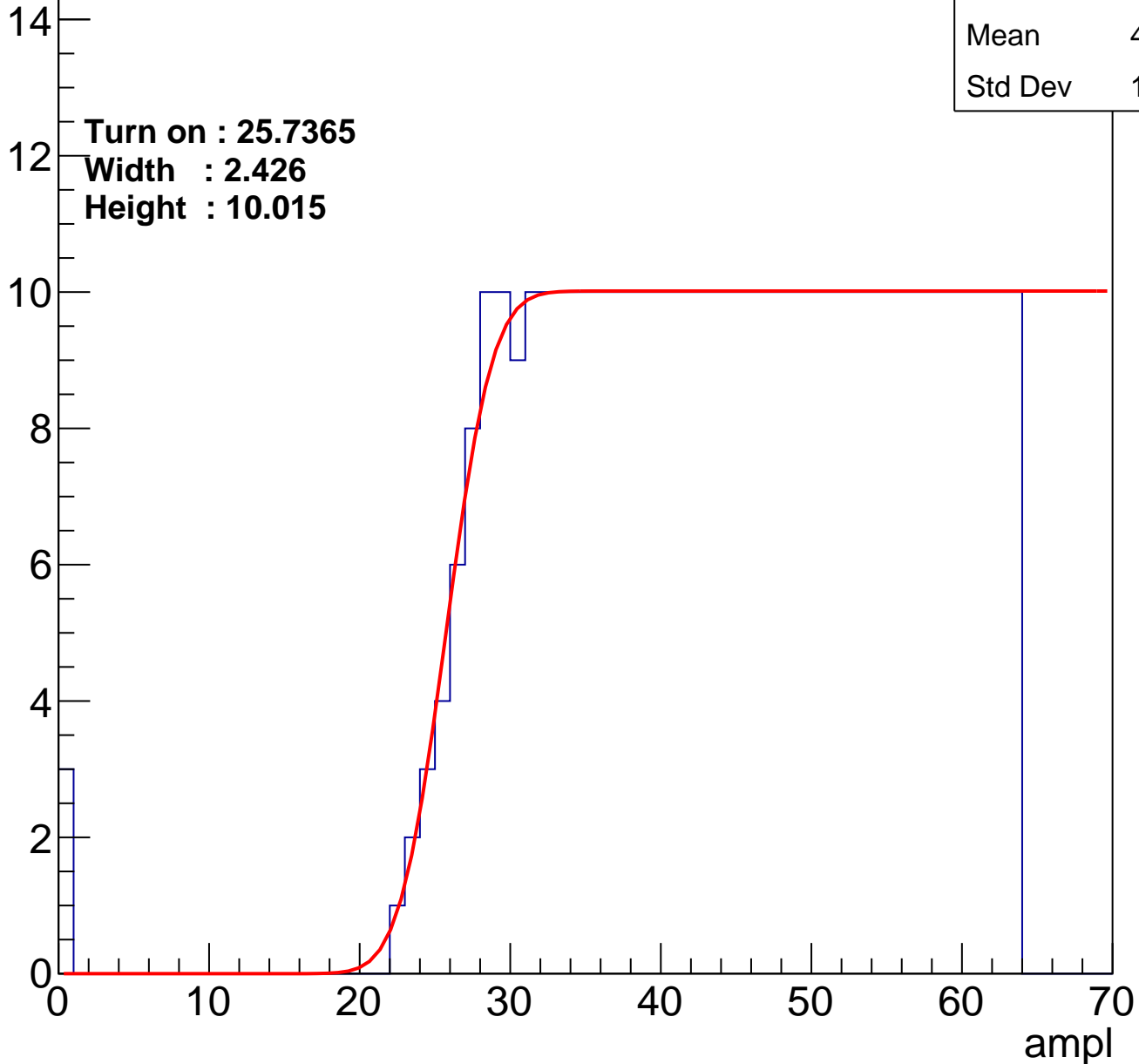
Entries	386
Mean	43.94
Std Dev	11.78

**Turn on : 25.7365**

**Width : 2.426**

**Height : 10.015**

Entry





# B0L000S, U14-ch111

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.57
Std Dev	12.17

**Turn on : 24.8298**

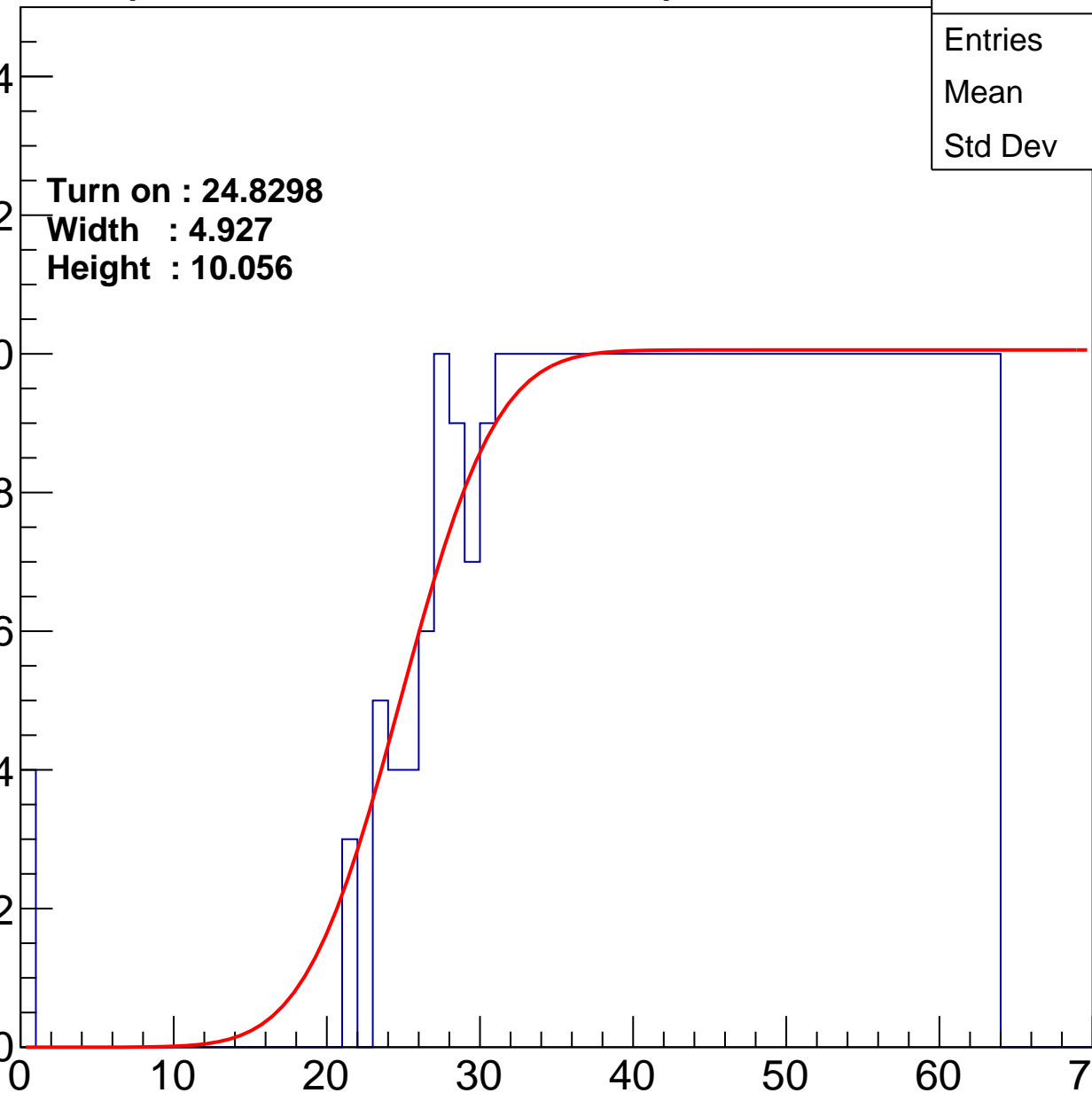
**Width : 4.927**

**Height : 10.056**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch112

calib\_packv5\_042523\_0143.root, FC#5, port B1

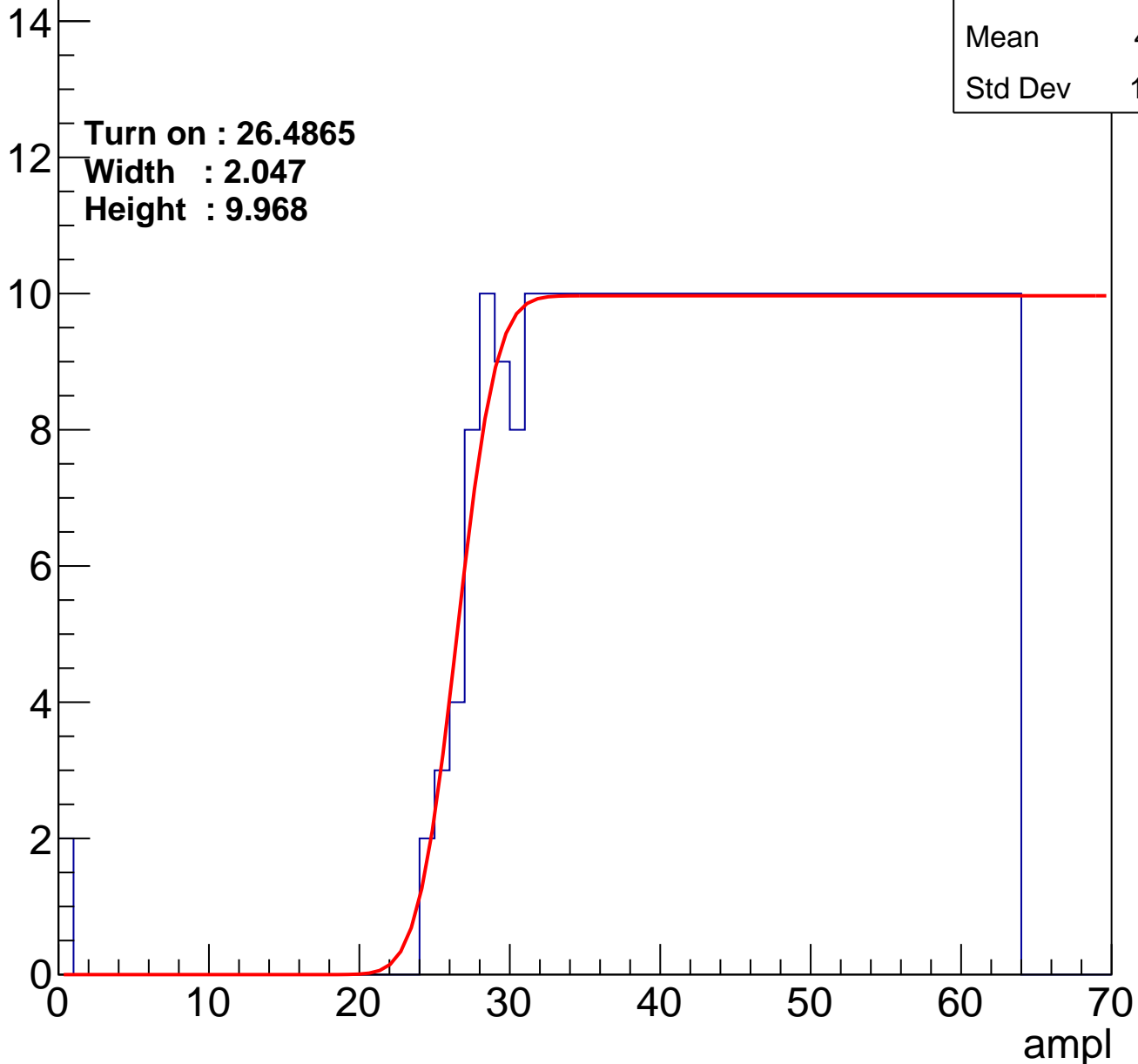
Entries	376
Mean	44.51
Std Dev	11.34

**Turn on : 26.4865**

**Width : 2.047**

**Height : 9.968**

Entry



# B0L000S, U14-ch113

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.51
Std Dev	11.58

Turn on : 27.3616

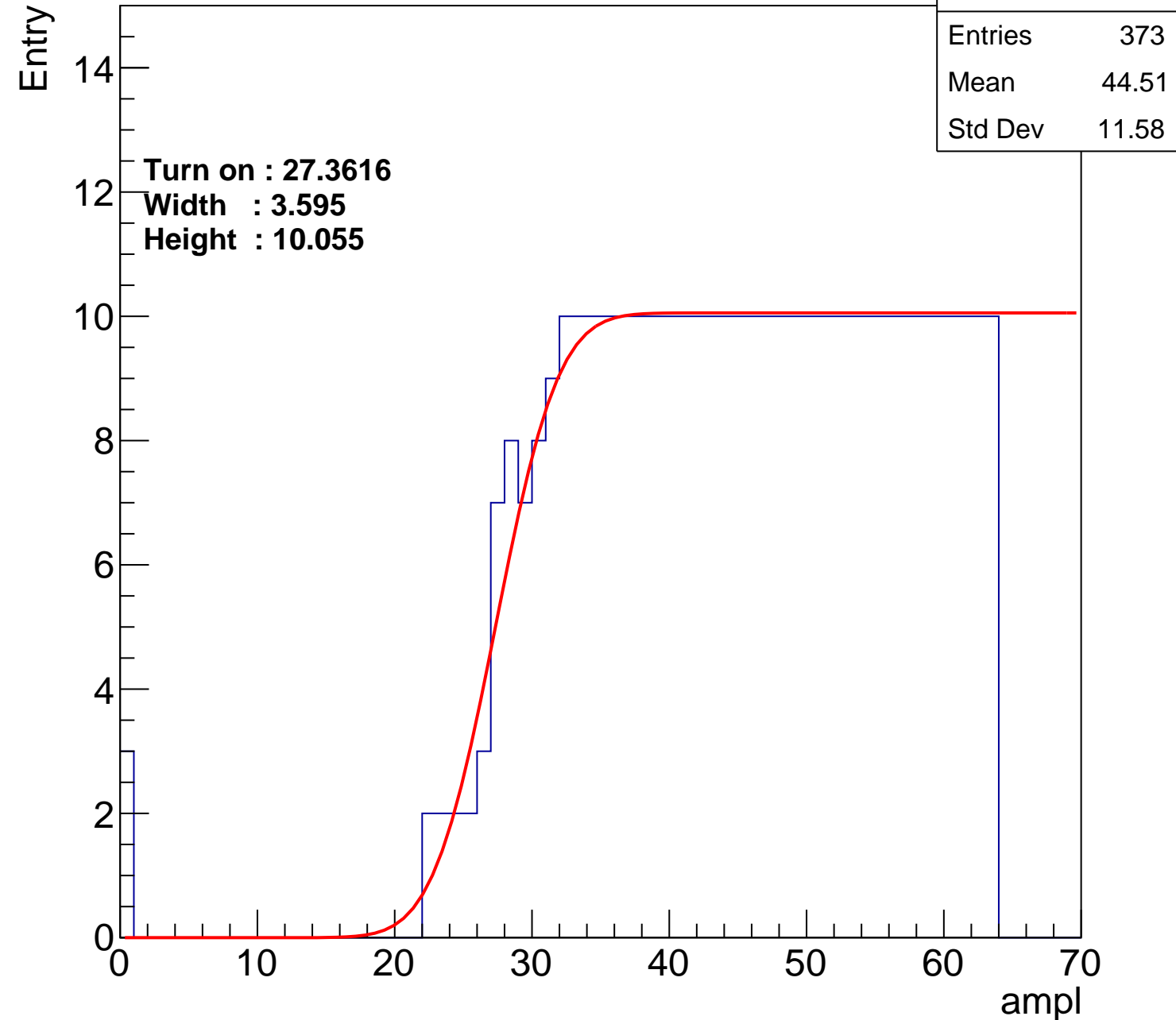
Width : 3.595

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch114

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.5
Std Dev	11.39

Turn on : 26.3041

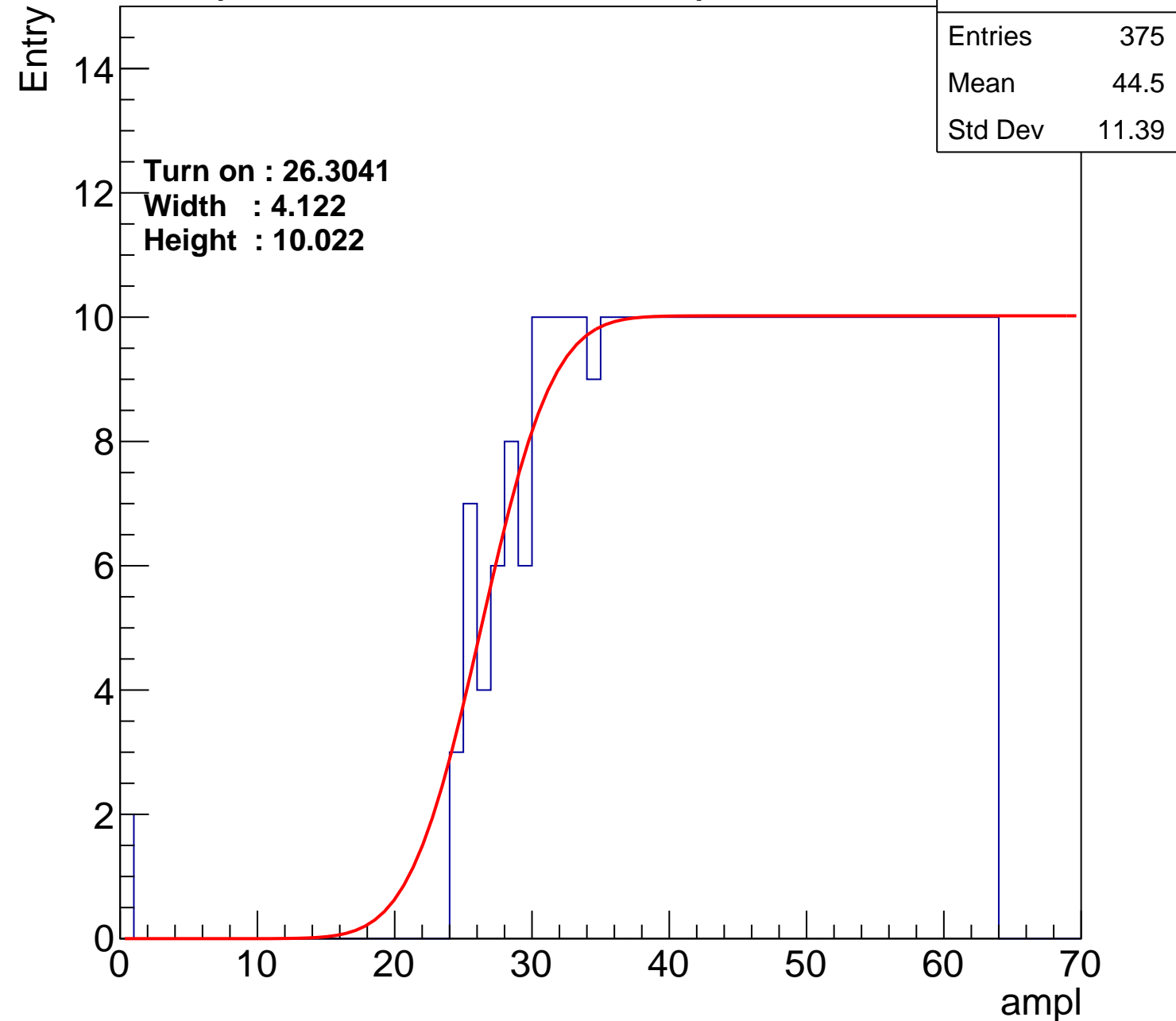
Width : 4.122

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch115

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	44.01
Std Dev	11.52

**Turn on : 25.6632**

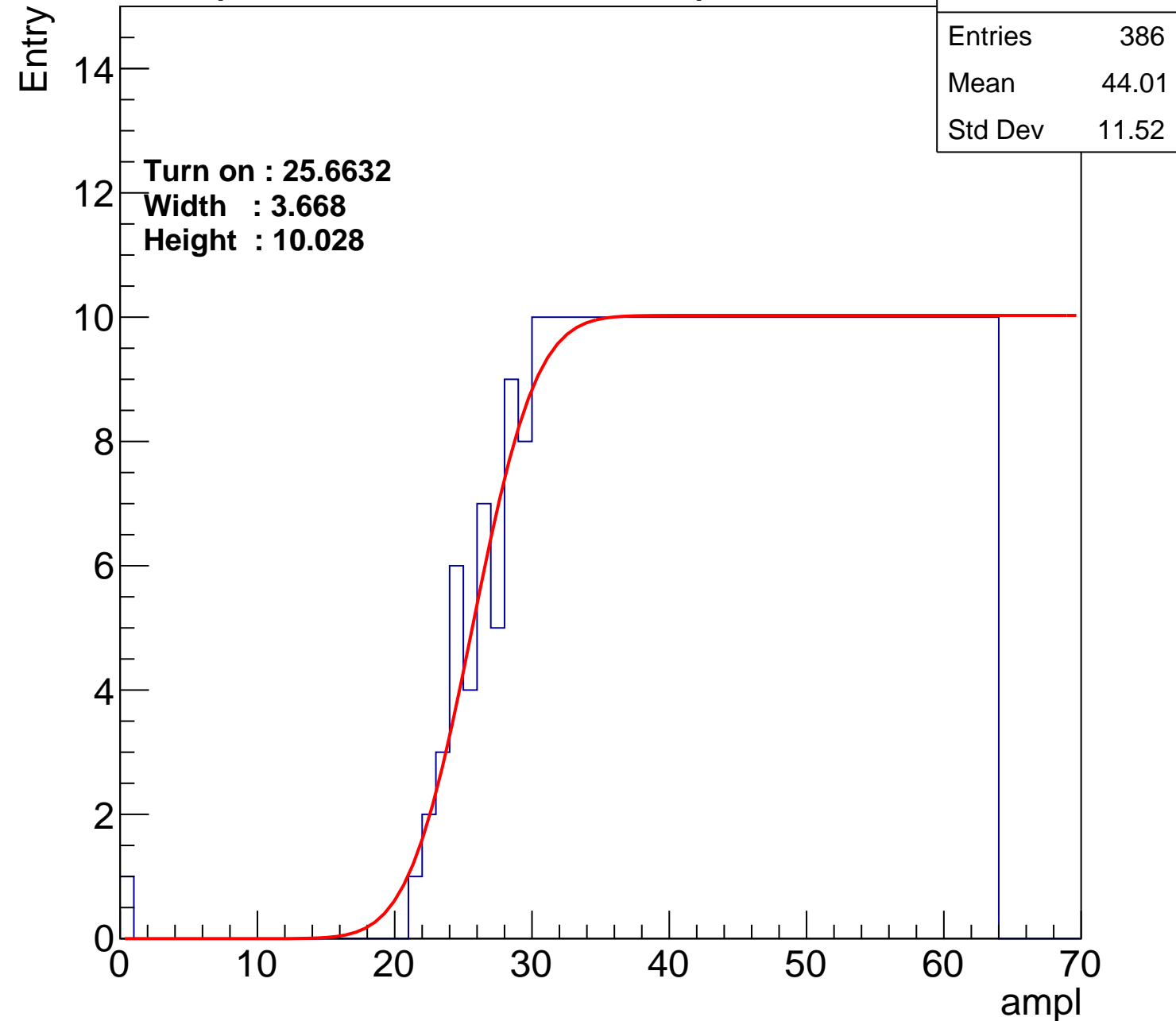
**Width : 3.668**

**Height : 10.028**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch116

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.45
Std Dev	11.3

**Turn on : 26.9388**

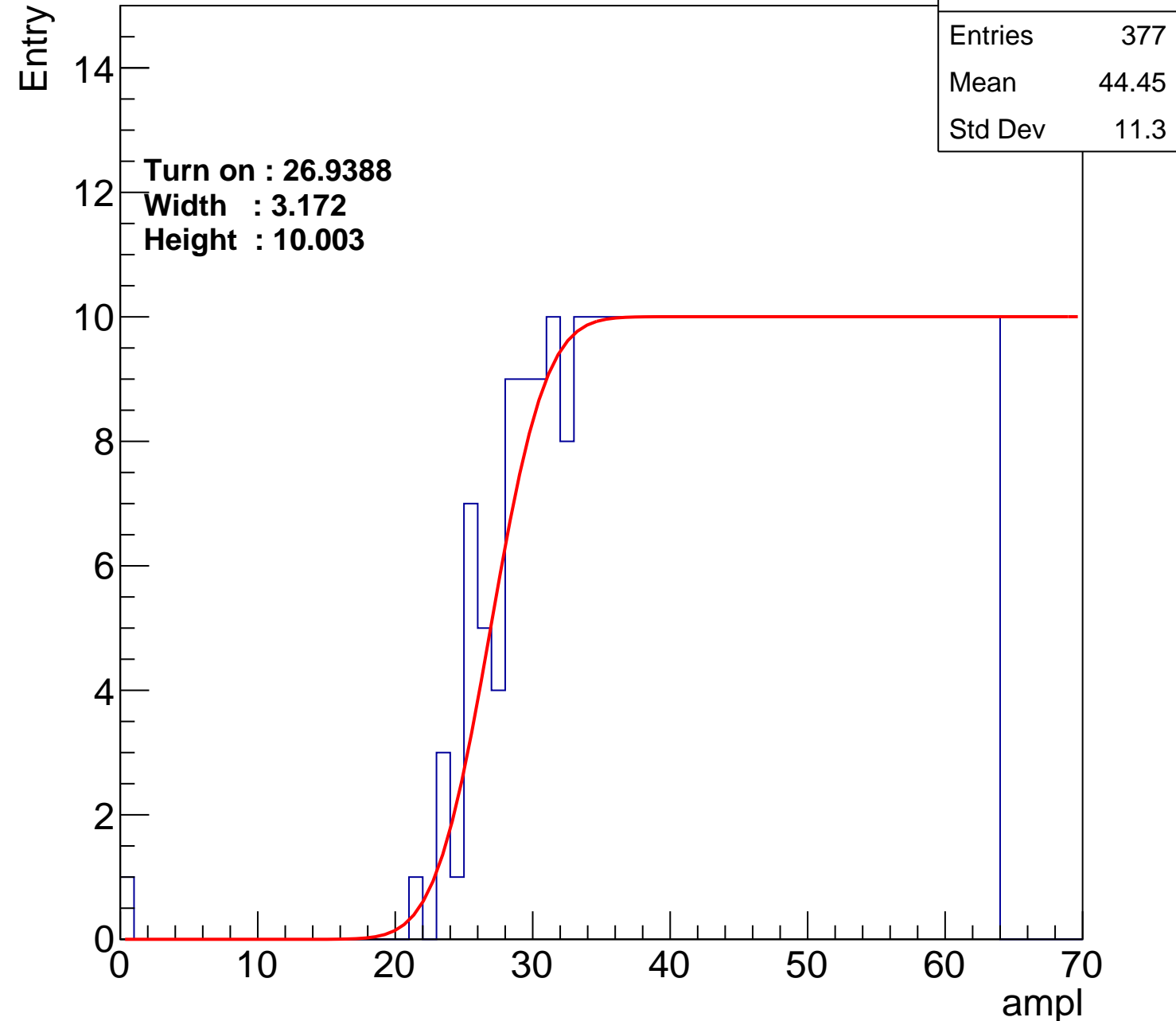
**Width : 3.172**

**Height : 10.003**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch117

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.73
Std Dev	11.78

**Turn on : 25.1975**

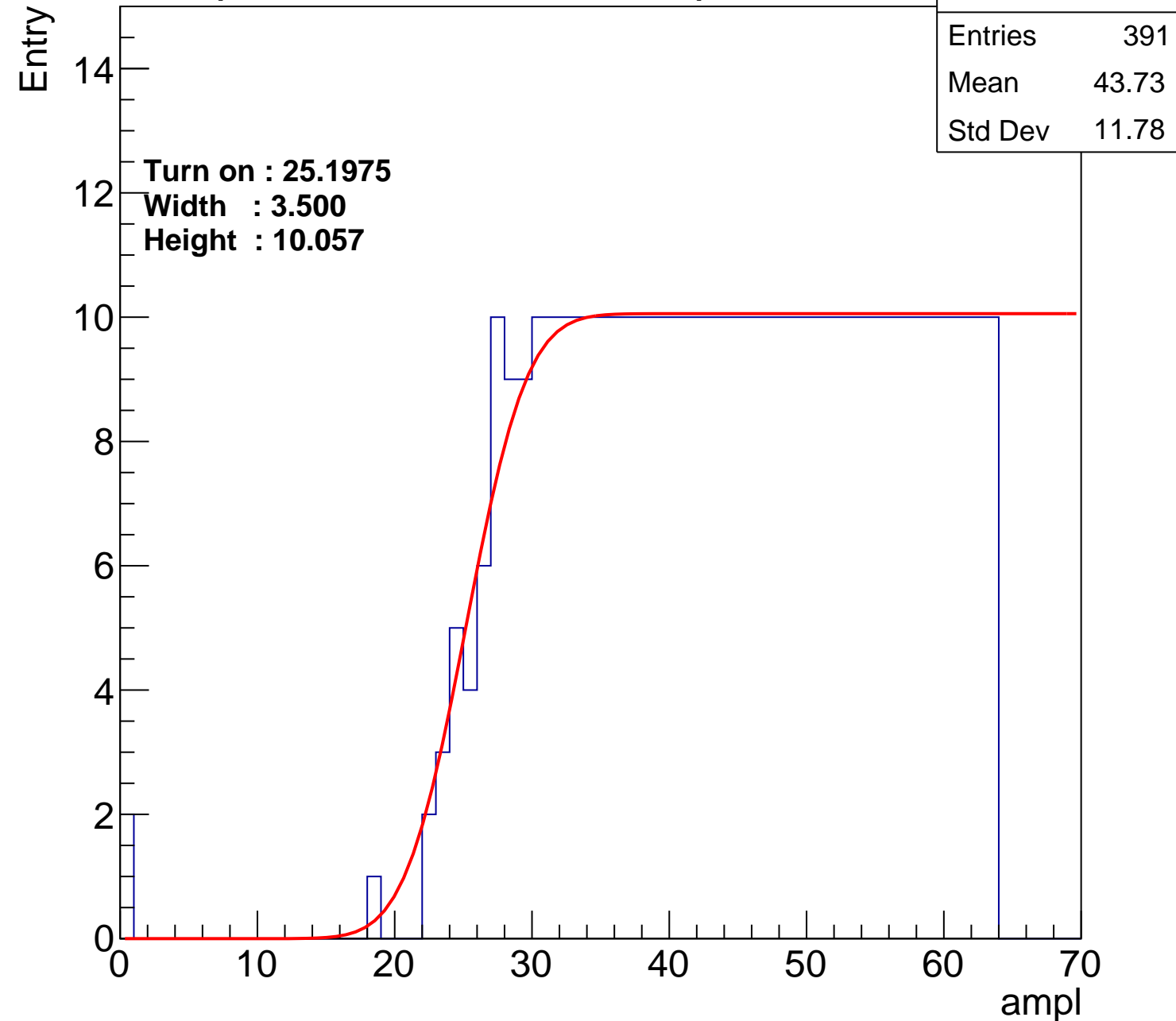
**Width : 3.500**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch118

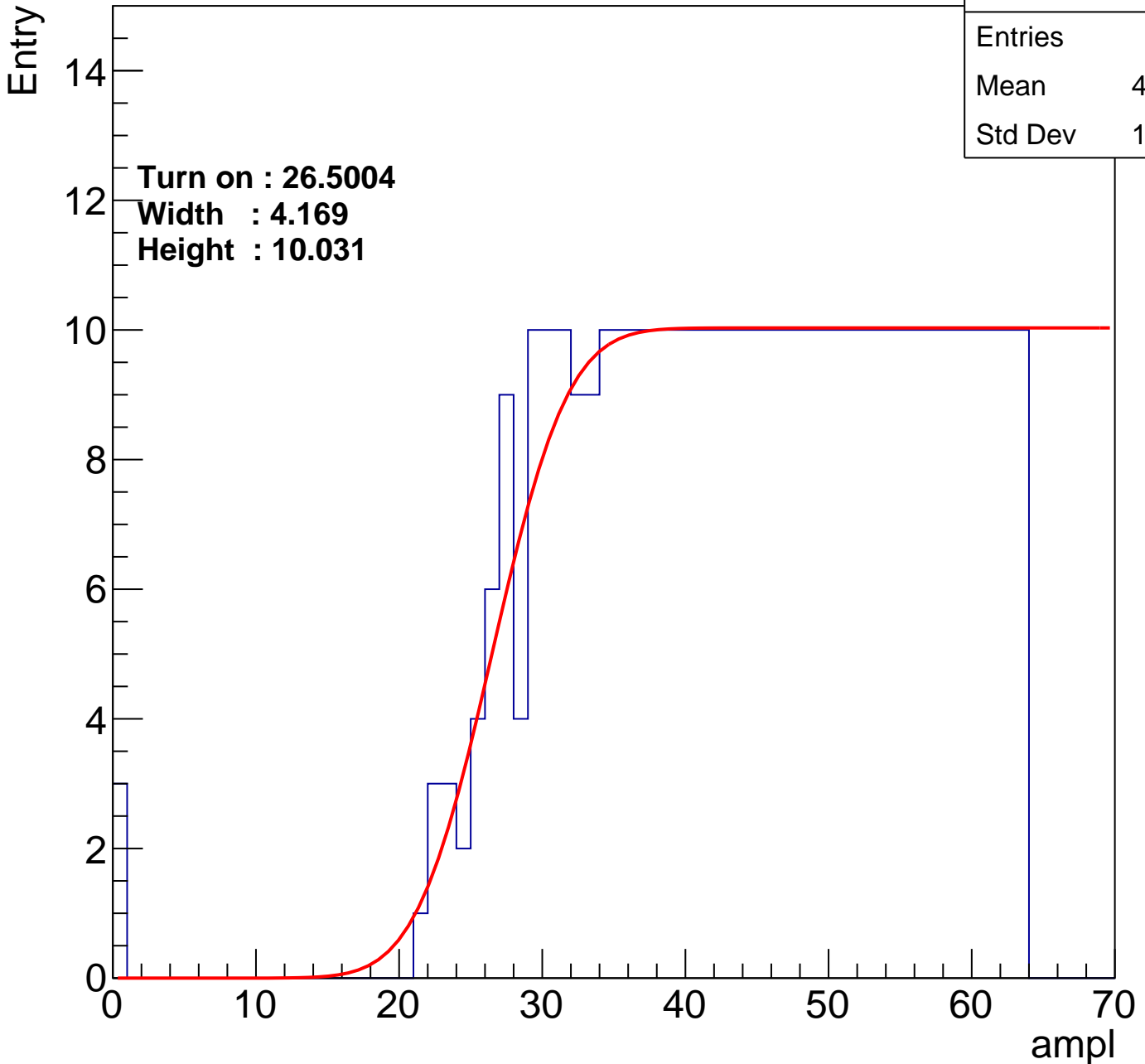
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	43.99
Std Dev	11.85

**Turn on : 26.5004**

**Width : 4.169**

**Height : 10.031**





# B0L000S, U14-ch119

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.21
Std Dev	11.72

Turn on : 26.8489

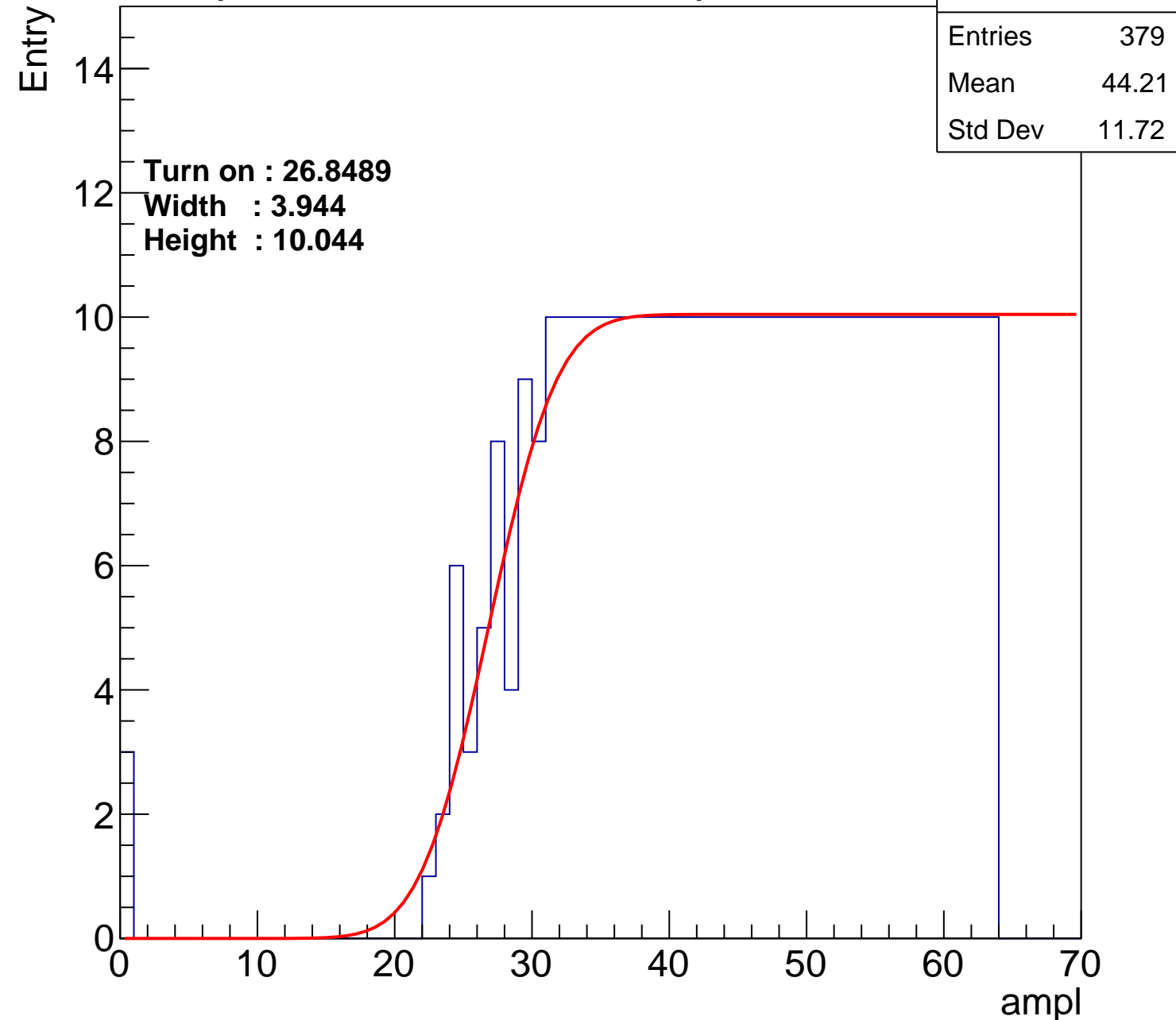
Width : 3.944

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch120

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.81
Std Dev	11.36

**Turn on : 27.5714**

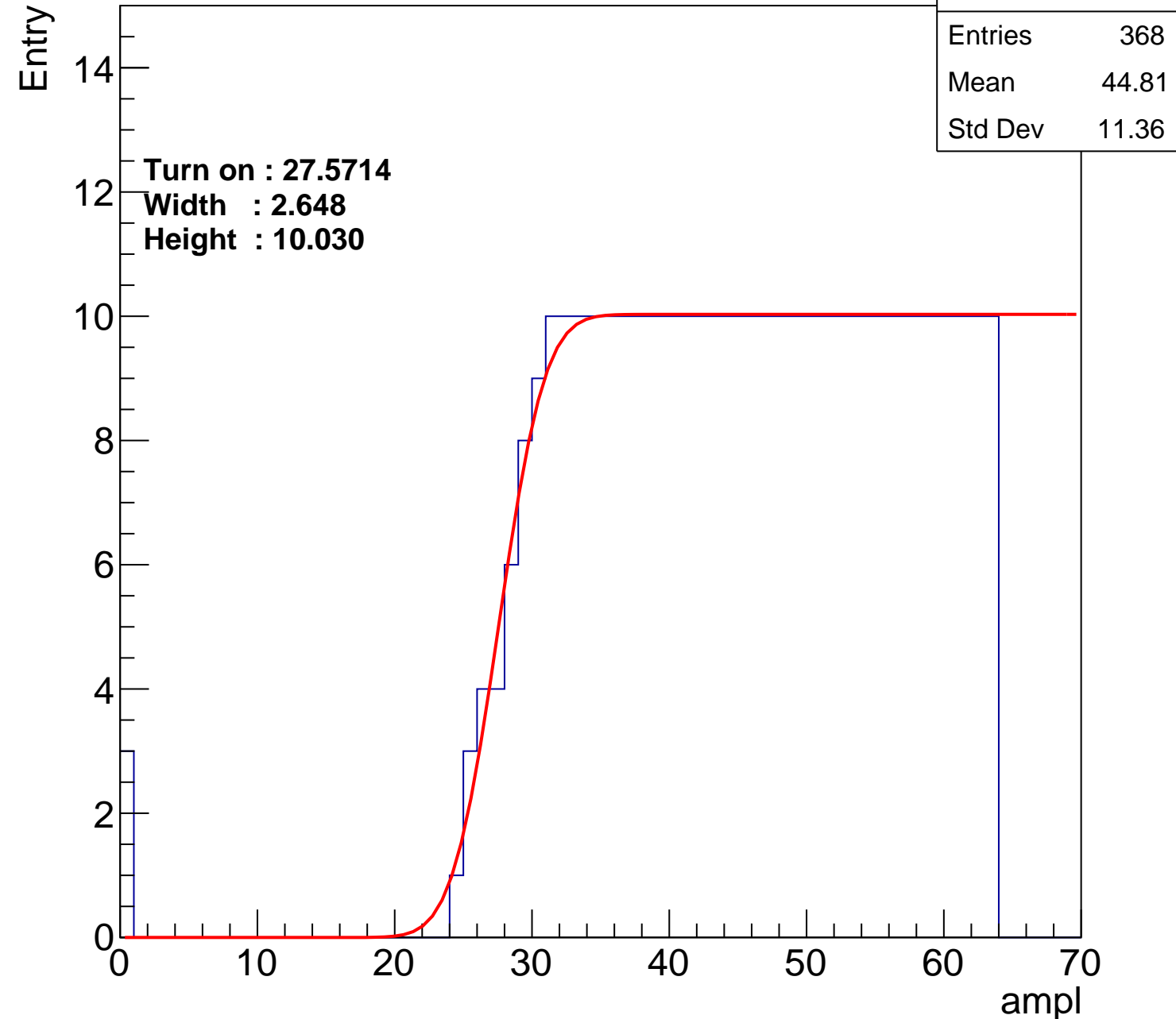
**Width : 2.648**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch121

calib\_packv5\_042523\_0143.root, FC#5, port B1

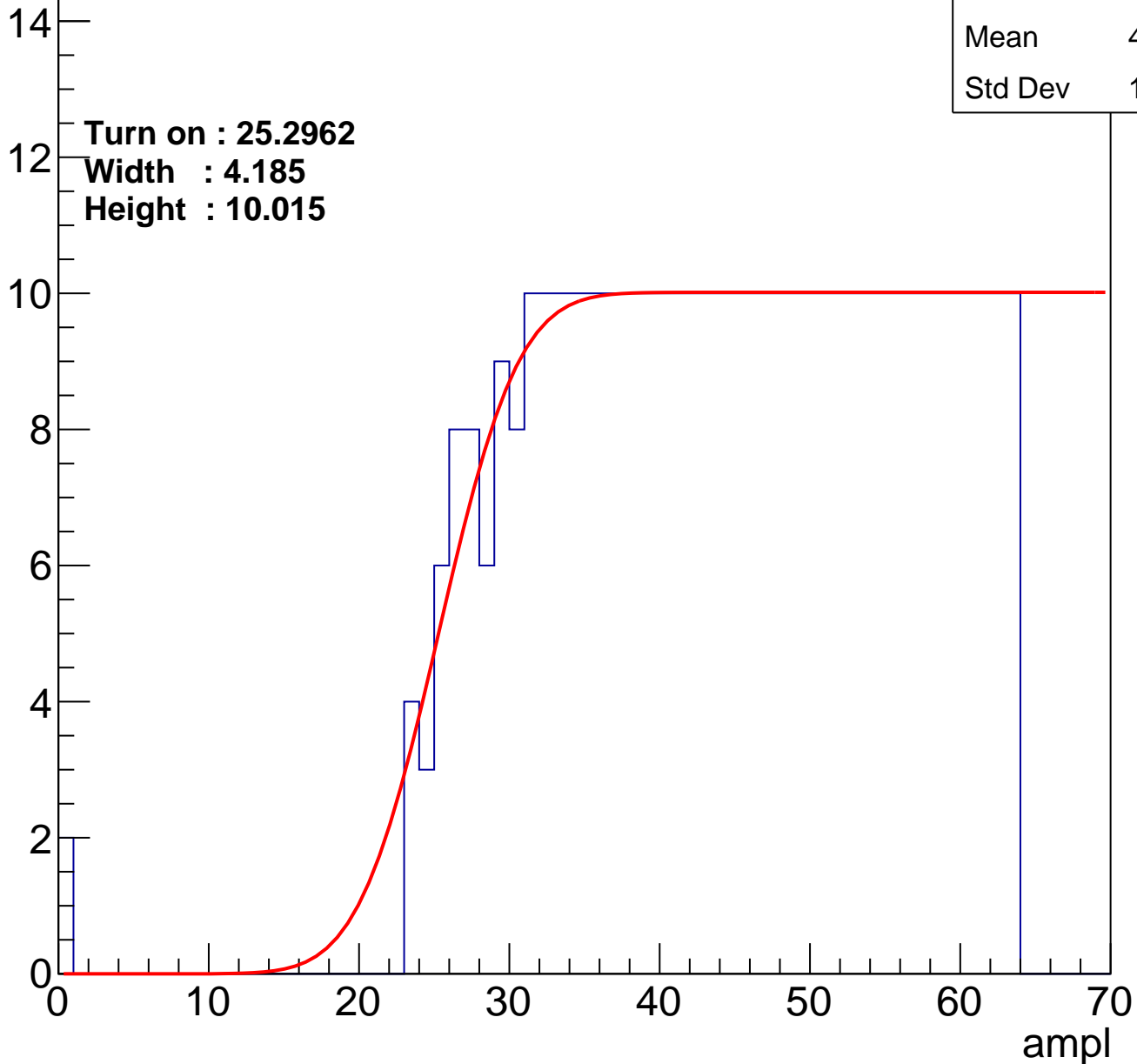
Entries	384
Mean	44.05
Std Dev	11.63

**Turn on : 25.2962**

**Width : 4.185**

**Height : 10.015**

Entry



# B0L000S, U14-ch122

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.53
Std Dev	11.72

**Turn on : 27.1098**

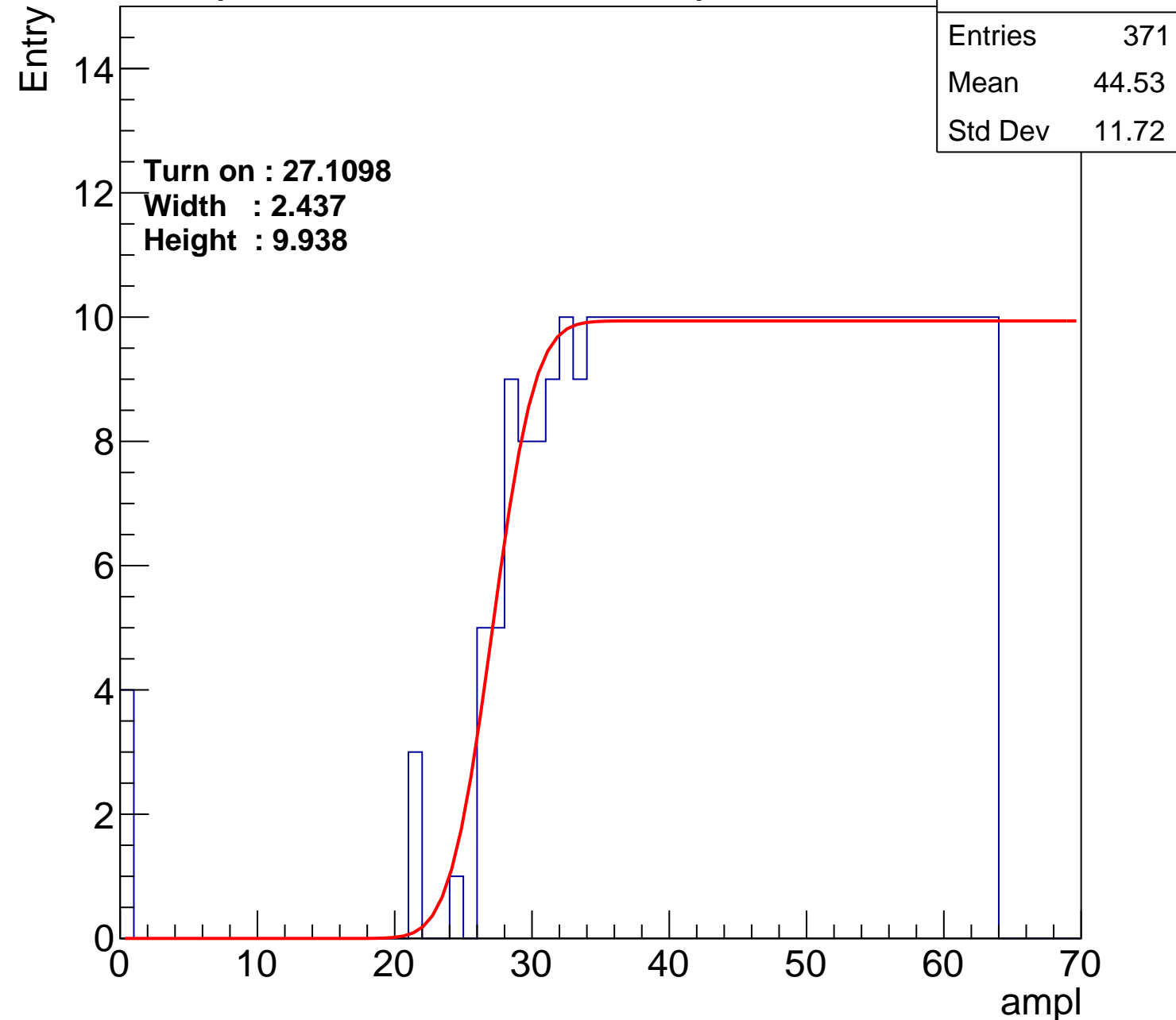
**Width : 2.437**

**Height : 9.938**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch123

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.16
Std Dev	11.29

**Turn on : 29.2339**

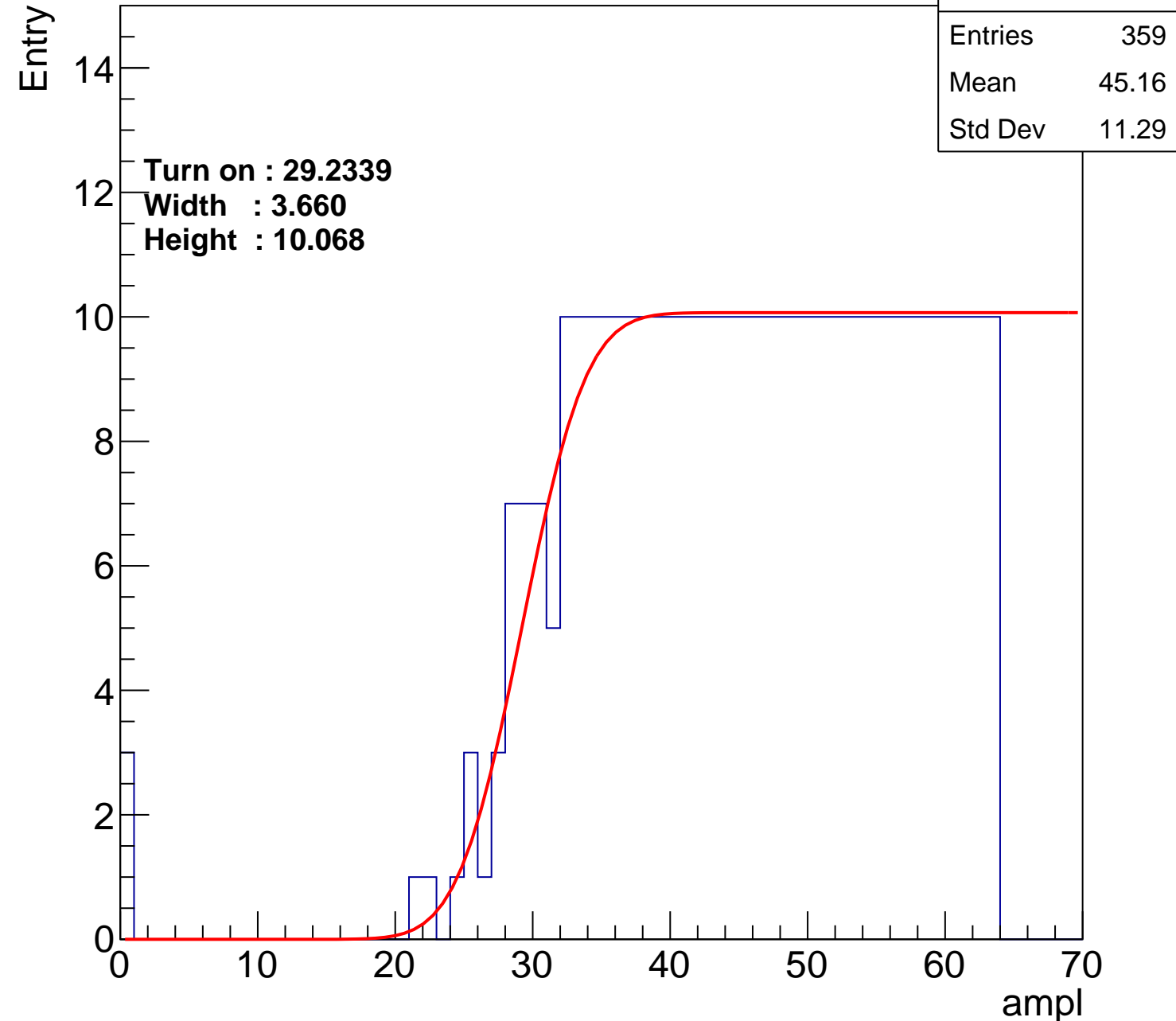
**Width : 3.660**

**Height : 10.068**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch124

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.82
Std Dev	11.62

Turn on : 25.6183

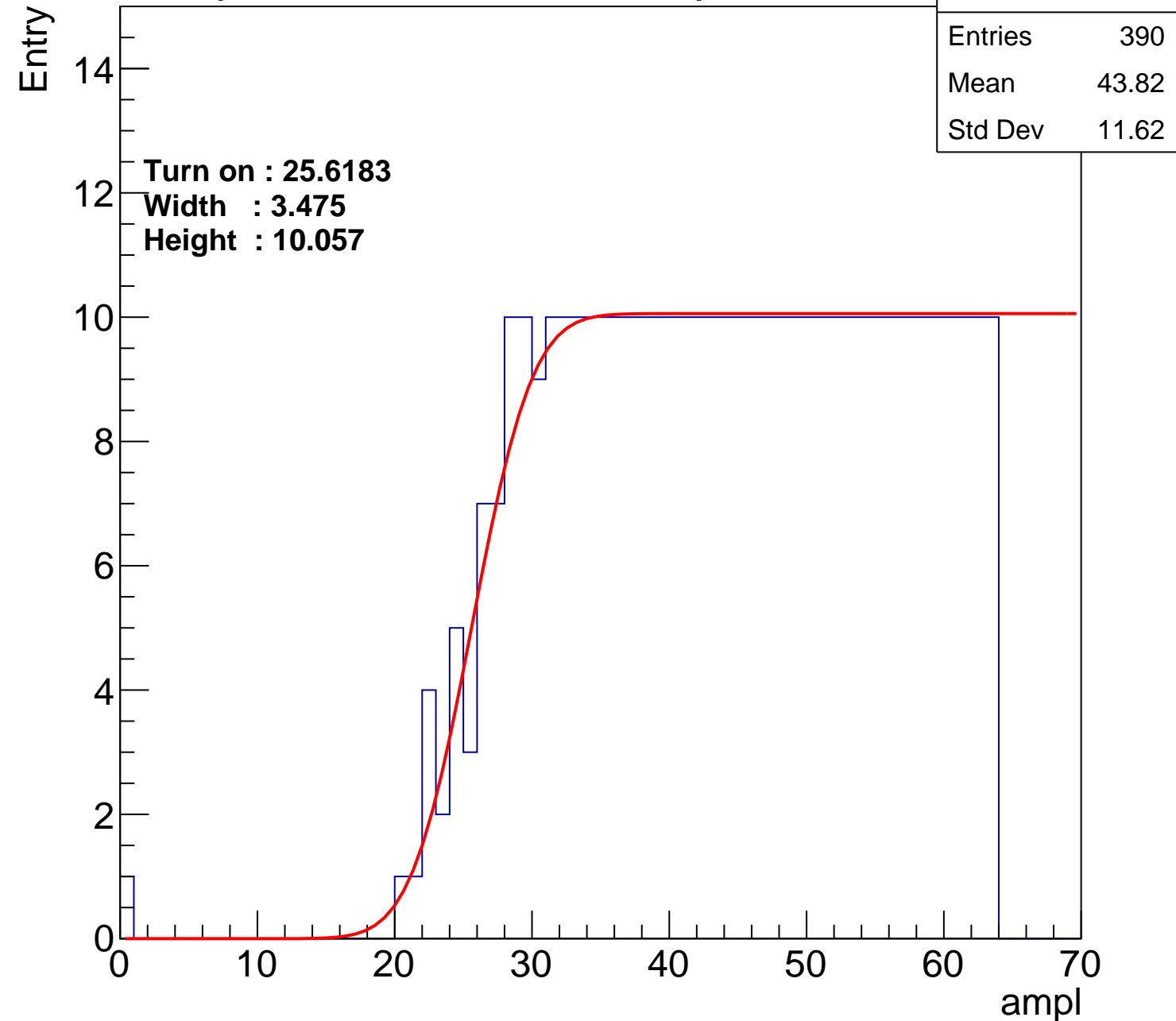
Width : 3.475

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch125

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	396
Mean	43.49
Std Dev	11.91

Turn on : 24.7801

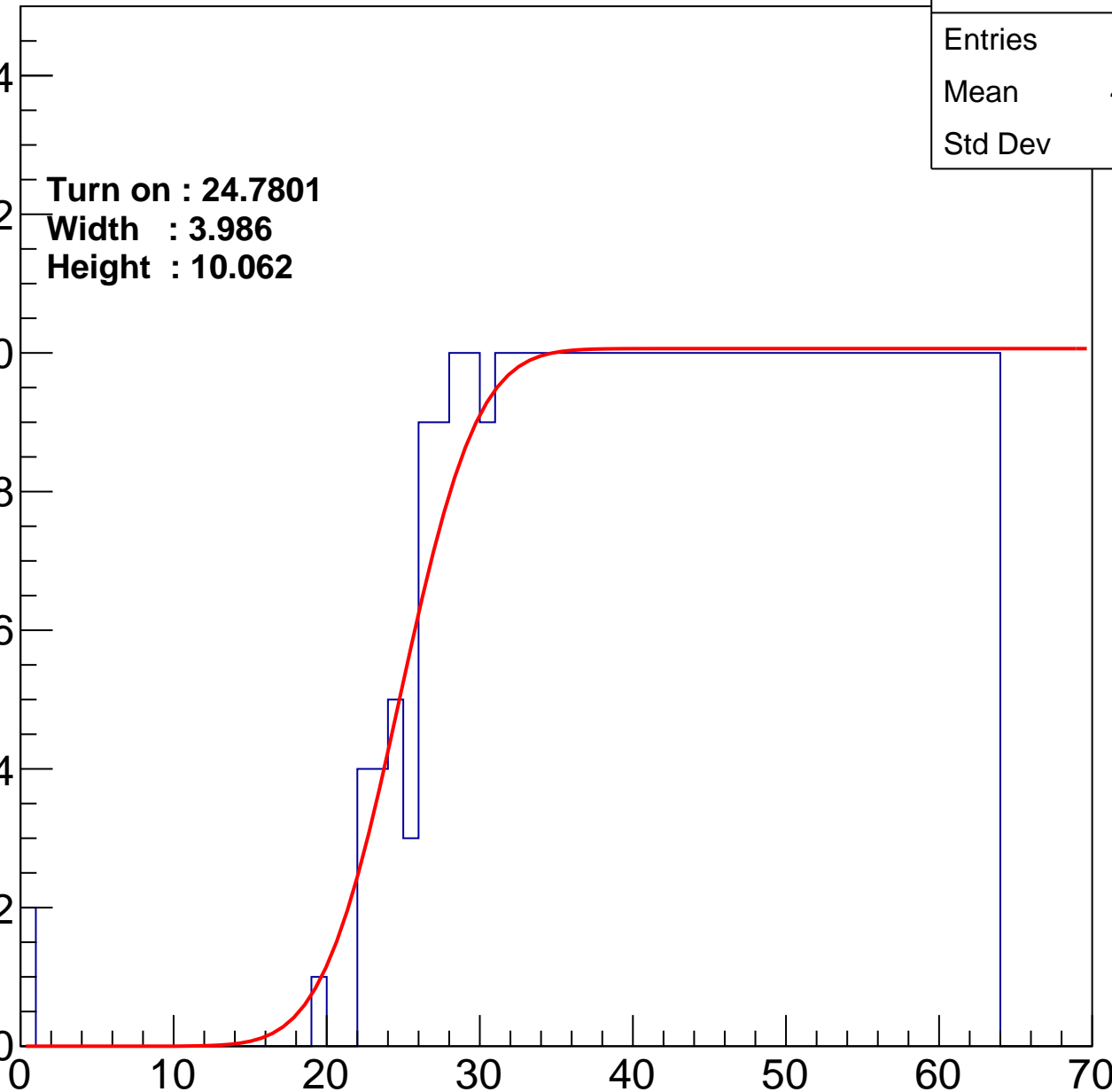
Width : 3.986

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch126

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.28
Std Dev	11.54

Turn on : 26.8051

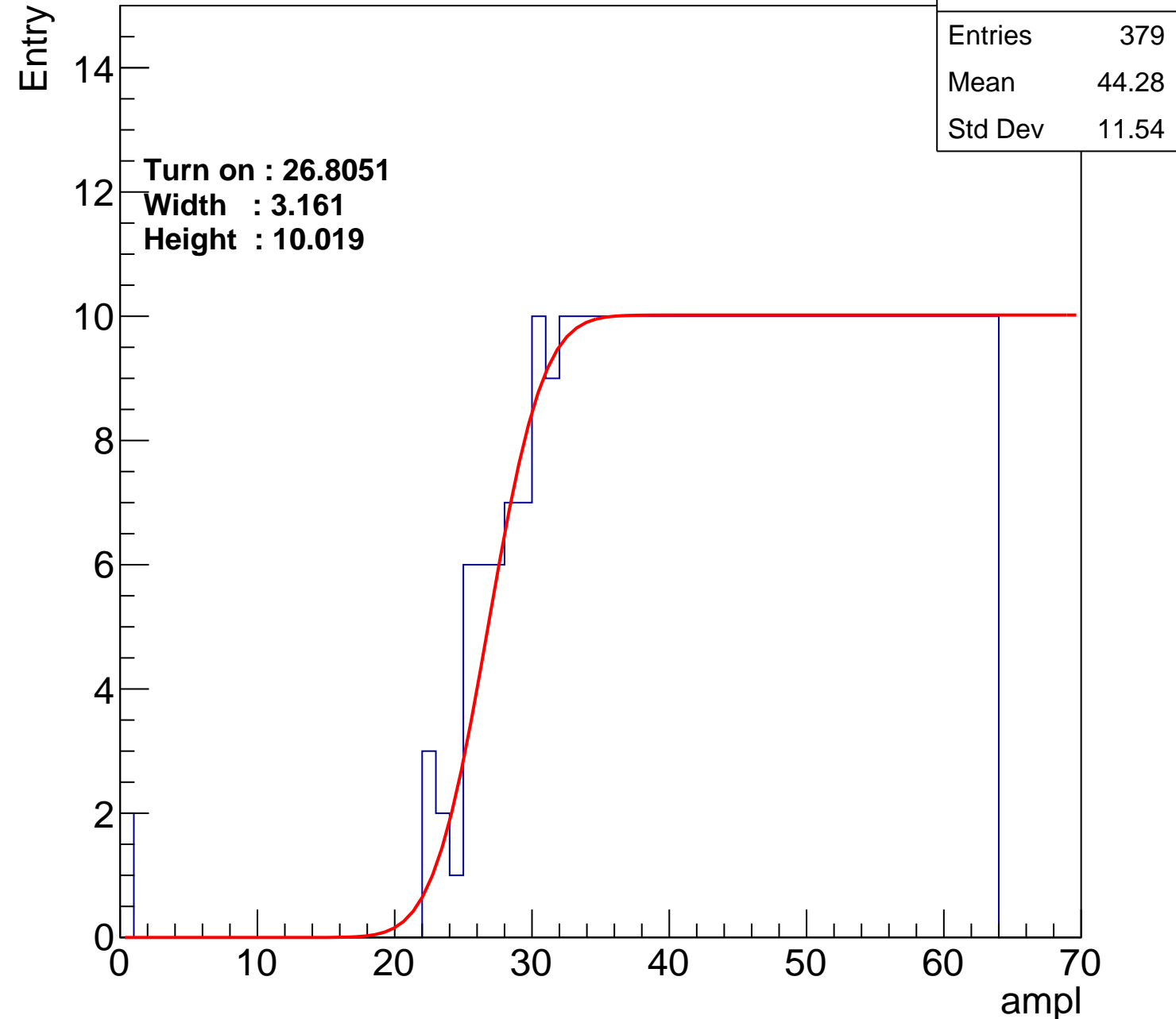
Width : 3.161

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U14-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.19
Std Dev	11.76

Turn on : 26.2084

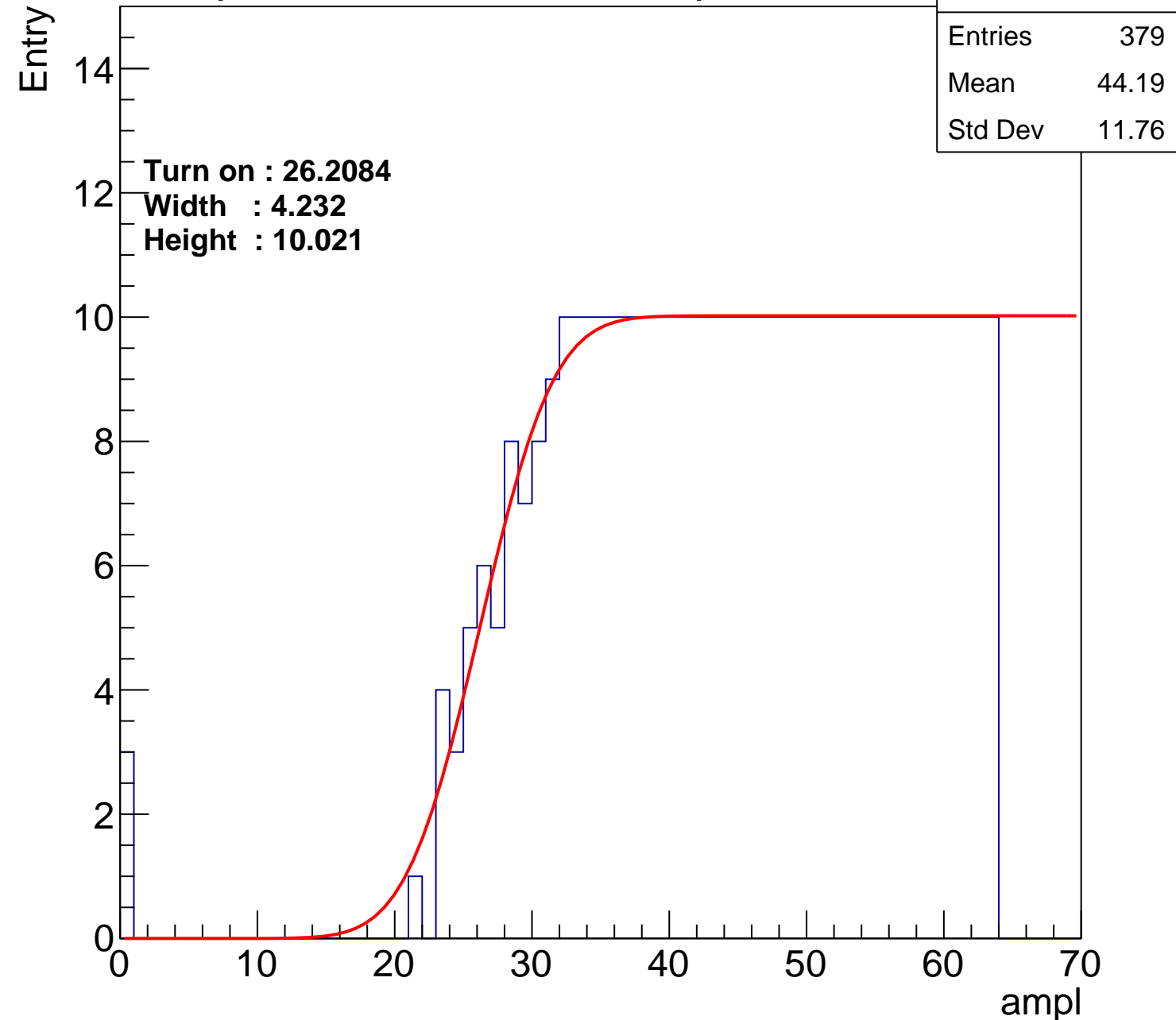
Width : 4.232

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U14-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.19
Std Dev	11.76

Turn on : 26.2084

Width : 4.232

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

